

IBM POUGHKEEPSIE
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Diagnostic Engineering Publications

001

1410/7010

Subject: Diagnostic Program M014B-1410/7010-1401 TOPSY
Compatibility
Sequence Number 291
Replaces M014A

- I. System and Channel One Cards 2 Cards 001-002
- II. Program also includes an 86 card reader test deck for 1402 reader tests. This deck is not punched with a sequence number field. Description of the deck is provided in Section 2.00.08.0 of the program write-up.
- III. This program corrects certain errors that existed in the previous level
 - a. The previous level tested a second tape on channel 1 only when loc 1292 contained a 1. Since loc. 1292 is not a standard 1410/7010 control to indicate the availability of a second drive on channel 1 a modification has been made to test only loc. 1291 to determine the availability of channel 1 tapes. The second drive will be tested if found to be ready after loading while program is still in 1410/7010 mode.
 - b. The previous level failed to reset an index register within Routine 20 allowing tape to write continuously when tape was the only I/O attachment being tested.

Enclosures: 62 Pages
 Card Deck for CARD ONLY SYSTEMS (as punched by UP51)
 8 Cards - Card Loader (1-7) and 1 Core Clear
 147 Cards No. 001-147 Data Cards
 1 Card Execute Card

Distribution: X 1410
 X 7010
 Other

002
M014

c03
M014
Page 001
4/14/64

M014B
TOPSY PROGRAM
FOR
1410/7010 - 1401 COMPATIBILITY
4/14/64

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2.00.00.0 TEST DESCRIPTION

00.1 MODIFICATIONS

This is a new program.

00.2 DESCRIPTION

This program is designed to test the reliability of 1410/7010 while operating in 1401 mode. Routines included within this program provide tests of both CPU and I/O to supplement tests made in previous 1410/7010 - 1401 compatibility programs. Routines are executed in the following sequence:

Routines 1 - 8	CPU Tests
Routines 9 - 13	Printer Tests
Routines 14 - 15	Punch Tests
Routines 16 - 19	Routines to test card-tape, tape-tape, tape-punch and tape-print operations.
Routines 20 - 46	Scramble overlap I/O tests.

Note: An 86 card reader test deck is required for reader tests.
See Section 2.00.08.0 for list of reader test cards.

As with all 1410/7010 - 1401 compatibility programs, the system is assumed to be functioning properly while in 1410/7010 mode. The program therefore tests only those areas affected by 1401 compatibility circuits. The following programs should be run before testing with M014.

M011 - 1410/7010-1401 CPU Compatibility
M012 - 1410/7010-1401 I/O Compatibility

All test routines communicate with two common control routines to test for inquiry and to test TAD locations for looping routines, indicating errors and halting on error. Errors will normally be indicated by a six character typeout as follows:

ERR XXX *

* XXX indicates the three-digit representation of the five-digit error address.

Reference to the error address in the program listing will provide an explanation for the error.

00.2 DESCRIPTION (continued)

The program will normally make one complete pass of all CPU routines and all I/O routines for which ready units have been indicated as available in control set up before typing PASS and testing TAD3 for repeat of entire program. If TAD3 is not 1, the program will halt to change mode back to 1410/7010. Pressing computer reset and start will call in the next program. If TAD3 is a 1, program will halt to allow set up of I/O for next pass. Pressing computer reset and start will begin execution of the next pass.

Note: Immediately after the loading of the program and while the system is still in 1410/7010 mode, the units indicated as available in control area will be tested for ready status. The control area will be modified to bypass tests for non-ready devices. If tapes are to be tested, the two lowest numbered ready drives, excluding drive 0, will be used.

00.3 EQUIPMENT REQUIRED

CPU, console printer; optional units are 1402 Reader-Punch, 1403 Printer and 729 or 7330 tapes.

00.4 CARD DECK

7	Cards	Load Program
1	Card	Core Clear Card
	Cards numbered 001-147	Program
	Card numbered 001	Is Standard system control card
	Card numbered 002	Is Standard Channel 1 control card
1	Card	Execute Card (Branch to 02000)

00.5 MACHINE E. C. LEVEL

00.6 PASS LENGTH

Approximately 1/2 min. assuming a full system with bypass of manual routines.

2.00.01.0 LOADING PROCEDURE

01.1 FROM CARDS

A. 7010 - 1410 without Load Button

1. Clear memory
2. Display memory location 00000
3. Alter to
 $\begin{matrix} \vee & \vee & & \vee \\ \text{RL}\%1100011\$ & & & \end{matrix}$ for channel 1 reader
 $\begin{matrix} \vee & \vee & & \vee \\ \text{XL}\%1100011\$ & & & \end{matrix}$ for channel 2 reader
4. Set to Run, Computer Reset, Start.

B. 7010 with Load Button

1. Clear memory
2. Computer reset
3. Depress Load button

01.2 FROM TAPE (80 Character Master or Memory Dump Tape)

A. 7010 - 1410 without Load Button

1. Clear memory
2. Display memory location 00000
3. Alter to -
 $\begin{matrix} \vee & \vee & & \vee \\ \text{RL}\%B000011\$ & & & \end{matrix}$ for channel 1 tape drive
 $\begin{matrix} \vee & \vee & & \vee \\ \text{XL}\%B000011\$ & & & \end{matrix}$ for channel 2 tape drive
4. Set to Run, Computer Reset, Start.

B. 7010 with Load Button

1. Clear memory
2. Computer reset
3. Depress tape Load button

2.00.02.0 OPERATING PROCEDURE

Load Program.

Program will type the following:

M014B
SET SENSE SW A ON
SET I/O CK STOP SW OFF
SET COMPATIBILITY SW TO 1401
PRESS START

A normal program halt will occur at 02008 to allow the operator to set switches as indicated in the typeout. The control area specifying units to be tested and/or TAD locations may also be modified at this time if desired. The control area will already have been modified at this point to bypass tests for non-ready devices indicated as available. To include a previously non-ready device, make it ready and alter control area accordingly before pressing Start. To include tapes, it will be necessary to alter locations 7991 and/or 7992 to the numbered drives to be used along with altering 1291 to 1.

Note: An 86 card reader test deck is required for reader tests.
See Section 02.00.08.0 for list of reader test cards.

The following are control locations that are tested by the program:

Location 1291 Test 729 or 7330 tape unit specified in location 7991 if this location is at 1 (used as read tape).

Location 7992 Test 729 or 7330 tape unit specified in loc. 7992 if this location is non blank (used as write tape).

Location 1301 Test 1402 reader if this location is an R.

Location 1303 Test 1402 punch if this location is a P.

Location 1305 Test 1403 printer if this location is a P.

2.00.02.0 OPERATING PROCEDURE (continued)

Location 1306 If N, print only numeric data for numeric chain. If A, print data for alpha chain.

Location 1257 Program tests this location for 0 to determine 10K memory. If not 0, greater than 10K memory is assumed.

Under normal conditions (all TADS 0 and no errors encountered) program will make one complete pass without stopping and then test TAD3 for repeat or continue. If it is desired to execute the manual routines along with the normal routines, it will be necessary to alter TAD4 (location 1004) to a 1. Manual routines are those that require manual intervention for proper execution such as disabling of print hammer, setting of switches, etc. Required steps of manual intervention will be indicated by a console printer typeout.

Normal program operations may be altered by using the Console Printer Inquiry routine to set one or several of the following TAD locations to "1."

<u>TAD</u>	<u>Address</u>	<u>If Not 1 (Normal)</u>	<u>If Set to 1</u>
0	01000 (#00)	Normal typeouts	Bypass all typeouts for scoping
1	01001 (#01)	No loops	Loop on present routine
2	01002 (#02)	No halts	Halt on error
3	01003 (#03)	1 pass only	Cycle program indefinitely
4	01004 (#04)	Bypass manual routines	Execute manual routines
5	01005 (#05)	No loops on same data	Loop routine using same data

The Console Printer Inquiry routine mentioned above may be used to alter TADS. To alter TADS do the following:

2.00.02.0 OPERATING PROCEDURE (continued)

Depress Inquiry Request Key

Note: If program is stopped when this key is depressed, it will be necessary to press computer start to branch on inquiry. Machine should type an I, make a space and unlock the keyboard for insertion of characters (1's or 0's) beginning at location 01000.

Key in the six numbers (0's and 1's) for desired set up of TAD0 - TAD5 (location 01000 - 01005).

Note: The program requires that the six digits always be altered even though it may be desired to change only TAD3 (location 01003). If an error is made during the key-in, the inquiry cancel key may be depressed to terminate the inquiry and branch program back to the same read console printer instruction.

Depress the inquiry release key to resume running.

2.00.03.0 OPERATING HINTS AND COMMENTS

1. Post restart for all routines is contained in locations 1901 - 1904. Locations 0001 - 0004 will contain a branch to 1901 to allow restart of any routine by depressing computer reset and start.
2. If a routine is causing an alarm failure and it is desired to loop the routine for scoping, do the following:
 - a. Alter TAD1 to 1 to loop the routine.
 - b. Turn the check control switch to RESET and RESTART mode.
 - c. If failure is occurring within a reader test, it may be desirable to duplicate the cards being used with the failing routine to allow for continuous looping.

Note: Altering TAD1 to 1 is desired for intermittent alarm failures to insure that the program will stay in the failing routine.

2.00.03.0 OPERATING HINTS AND COMMENTS (continued)

3. Normal print output for print test routines will include three types as follows:

- Type 1 100 positions containing all 64 characters and beginning with BZ01
- Type 2 26 lines of 20 positions (F-Z & 0-4)
- Type 3 132 positions of PRBUSYTEST or PRTERTEST

If printer chain is numeric, types 1 and 3 above will appear as 0123456789 and only 0-4 will print in type 2. Any standard carriage tape may be used. The program will call for a skip to one during test for ready units while in 1410/7010 mode.

4. The routine to force punch errors allows ten cards to be punched and then reinserted in the punch feed, 9 edge first face down, to cause hole count checks. Almost any prepunched cards may be used for this test. Mention of this is made to allow for the processing of a larger card deck for the purpose of looping this routine.

5. Normal tape operations with tapes of sufficient lengths will not cause the encountering of end of reel with tape write instructions. Tape rewinds within the program are never bypassed, so that only several feet of tape will be used. If end of reel is encountered during tape writes, the program may rewind the tape prematurely or "END OF REEL" may type without rewinding. Either of these results may cause errors to occur such as non-compare when checking the data written, etc. They merely provide indication that End of Reel was encountered and it is suggested that longer reels of tape be used unless the branch on EOR appears to be erroneous.

6. Tape errors resulting in other than scramble overlap routines (21-46) will be indicated only after ten successive retries have been made. Within routines 21-46, however, a single read or write tape error will cause an error typeout. Within these routines a check for tape error is not made until the tape operation along with the associated I/O operation is completed. A few tape errors, therefore, may be tolerated during a pass of the program but should not be consistent.

2.00.03.0 OPERATING HINTS AND COMMENTS (continued)

7. If printouts are not inhibited, routine No. 4 to cause system check error with move of location containing no bits will result in two error printouts, one for channel A error and one for channel B error before typing message to restore CK control switch to normal.

2.00.04.0 PROGRAM STOPS AND RESTARTS

- | | | |
|---|-------|--|
| N | 02008 | Normal halt while in 1410/7010 mode following typeout of program ID and instructions for setting switches. Set switches and press Start. |
| N | 02223 | Normal halt following instruction message for altering location 7800. Alter this location to no bits (hold shift, depress 8 key), set CK control switch to restart and press Start.. |
| N | 02261 | Normal halt following message to restore CK control switch to normal. Set switch to normal and press Start. |
| N | 02772 | Normal halt following message to disable print hammer. Disable print hammer and press Start. |
| N | 02990 | Normal halt following message to restore print hammer. After restoring the print hammer, press Start.. |
| N | 03070 | Normal halt following message to insert cards in punch hopper. Insert last ten cards punched 9 edge first face down followed by blank cards in punch and press Start. |
| N | 05951 | Normal halt following message to set compatibility switch to 1410/7010. Set switch and press computer reset and start to continue. |

2.00.04.0 PROGRAM STOPS AND RESTARTS (continued)

- N 05961 Normal halt following completion of one program pass when TAD3 is set to 1. Depress computer reset and start for next pass.
- 06544 Halt following typeout indicating tape write error when TAD2 is set to 1. Press Start to attempt write again.
- 06689 Tape Read Error halt - occurs following typeout of tape read error message when TAD2 is set to 1. Press Start to continue.
- 06823 Halt following typeout indicating false TP EOF when TAD2 is set to 1. Press start to continue.
- 06907 Error halt - occurs following error typeout when TAD2 is set to 1. Press start to continue.

2.00.05.0 TYPEOUTS

05.1 NON-ERROR TYPEOUTS

M014B
SET SENSE SW A ON
SET I/O CK STOP SW OFF
SET COMPATIBILITY SW TO 1401
PRESS START

This typeout occurs after program is loaded while system is still in 1410/7010 mode.

ALTER LOC 7800 TO NO BITS SET CK CONTROL SW TO RESTART AND PRESS START

SET CK CONTROL SW TO NORMAL PRESS START

These typeouts occur in routine 4 to force system check error with move of location containing no bits (will occur only when TAD4 is set to 1).

2.00.05.0 TYPEOUTS (continued)

DISABLE 1403 PRINT HAMMER PRESS START

RESTORE 1403 PRINT HAMMER TO NORMAL STATUS
PRESS START

These typeouts occur in routine to force printer error
(will occur only when TAD4 is set to 1).

READY 10 CARDS JUST PUNCHED IN
PUNCH 9 EDGE FIRST FACE DOWN
FOLLOWED BY BLANK CARDS PRESS START

This typeout occurs following punching of ten cards to be
used in force punch error routines (will occur only when
TAD 4 is set to 1).

SET COMPATIBILITY SW TO 1410/7010 PRESS COMPUTER
RESET AND START

This typeout occurs at end of program pass if TAD3 is not 1.

PASS

Occurs after one complete pass of the program.

05.2 ERROR TYPEOUTS

TP WR ERR XXX
TP RD ERR XXX

These typeouts will occur when ten successive tries to read
or write a record on tape in other than scramble overlap
routines have failed. XXX will be the three-digit represen-
tation of the five-position error address. See address
conversion chart. (These typeouts can occur only when
TAD0 does not contain a 1.)

END OF REEL

This typeout occurs whenever END OF REEL is sensed
when writing tape in other than scramble overlap routines.
(Can occur only when TAD0 does not contain a 1.)

05.2 ERROR TYPEOUTS (continued)

FALSE TP EOF XXX

This typeout occurs whenever a false end of file is detected when reading tape. XXX is the three-digit representation of the five-position error address; occurs only when TAD0 does not contain a 1.

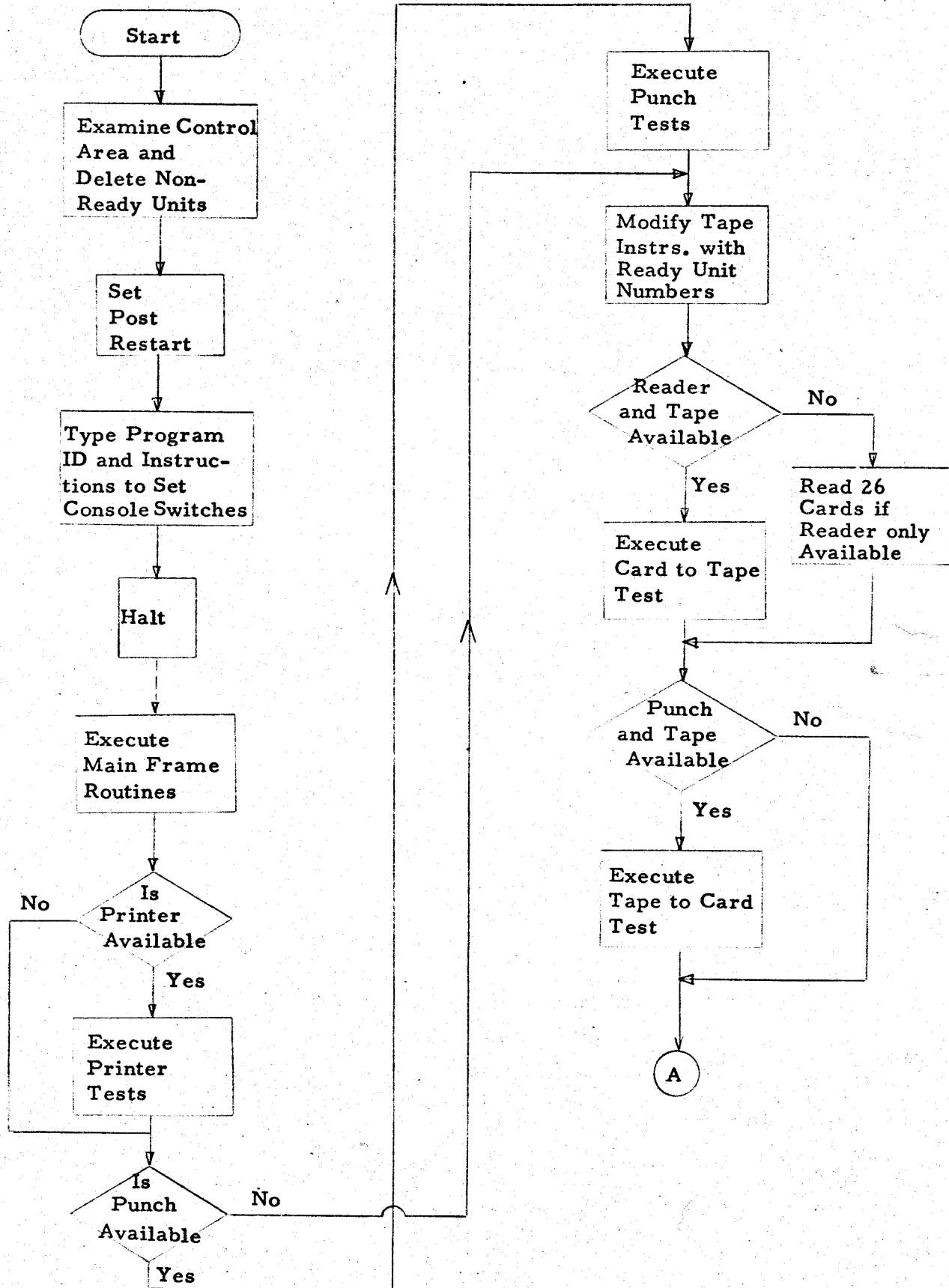
ERR XXX

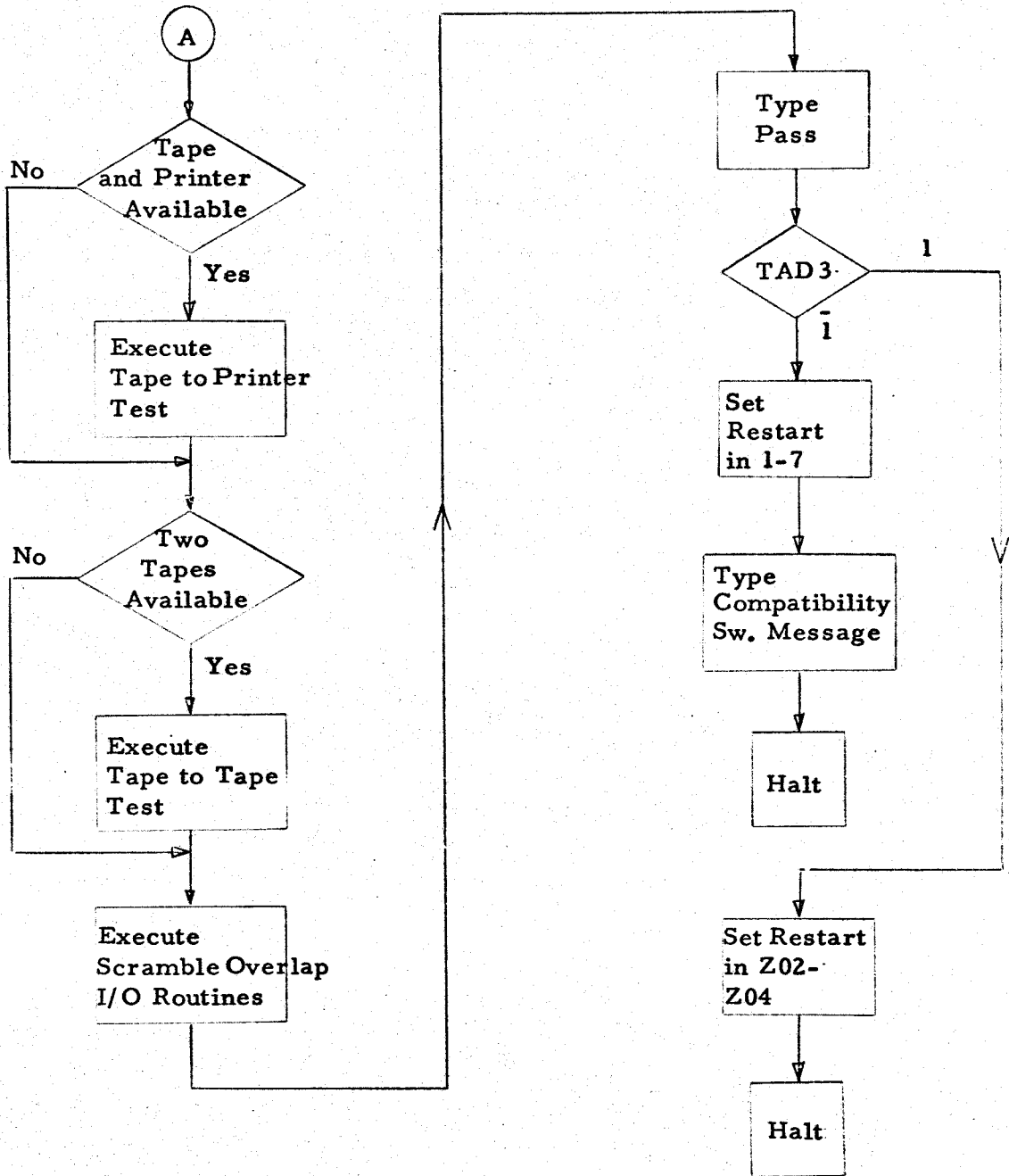
This typeout occurs whenever an error is detected within a test routine and TAD0 does not contain a 1. XXX is the three-digit representation of the five-position error address. Error addresses may be deciphered as follows:

B = 2	B = 8
A = 1	A = 4
0 0	0
Hundredths	Units

EX. ERR P2S

	B	A	
P2S =	7	2	= 06722





ACTUAL ADDRESSES	ZONE BITS OVER HUNDREDS POSITION	ZONE BITS OVER UNITS POSITION	3-CHARACTER ADDRESSES
0000 to 0999 1000 to 1999 2000 to 2999 3000 to 3999	No Zone Bits A-Bit (Zero-Zone) B-Bit (11-Zone) AB-Bits (12-Zone)	No Zone Bits No Zone Bits No Zone Bits No Zone Bits	000 to 999 #00 to Z99 !00 to R99 ?00 to I99
4000 to 4999 5000 to 5999 6000 to 6999 7000 to 7999	No Zone Bits A-Bit (Zero-Zone) B-Bit (11-Zone) AB-Bits (12-Zone)	A-Bit (Zero-Zone) A-Bit (Zero-Zone) A-Bit (Zero-Zone) A-Bit (Zero-Zone)	00† to 99Z †0† to Z9Z #0† to R9Z ?0† to I9Z
8000 to 8999 9000 to 9999 10000 to 10999 11000 to 11999	No Zone Bits A-Bit (Zero-Zone) B-Bit (11-Zone) AB-Bits (12-Zone)	B-Bit (11-Zone) B-Bit (11-Zone) B-Bit (11-Zone) B-Bit (11-Zone)	00! to 99R #0! to Z9R !0! to R9R ?0! to I9R
12000 to 12999 13000 to 13999 14000 to 14999 15000 to 15999	No Zone Bits A-Bit (Zero-Zone) B-Bit (11-Zone) AB-Bits (12-Zone)	AB-Bits (12-Zone) AB-Bits (12-Zone) AB-Bits (12-Zone) AB-Bits (12-Zone)	00? to 99I †0? to Z9I !0? to R9I ?0? to I9I

2.00.08.0 READER TEST DECK

M014 READER TEST DECK

0 1 2 3 4 5 6 7 8
1.....0.....0.....0.....0.....0.....0.....0.....0

BZ01 AAAAAAAAAAAAAAAAAA
BZ01 BBBBBBBBBBBBBBBBBB
BZ01 CCCCCCCCCCCCCCCCCC
BZ01 DDDDDDDDDDDDDDDDD
BZ01 EEEEEEEEEEEEEEEEEEE
BZ01 FFFFFFFFFFFFFFFFFFFF
BZ01 GGGGGGGGGGGGGGGGGG
BZ01 HHHHHHHHHHHHHHHHHH
BZ01 IIIIIIIIIIIIIIIIIII
BZ01 JJJJJJJJJJJJJJJJJJ
BZ01 KKKKKKKKKKKKKKKKKK
BZ01 LLLLLLLLLLLLLLLLLLLL
BZ01 MMMMMMMMMMMMMMMMMM
BZ01 NNNNNNNNNNNNNNNNNN
BZ01 OOOOOOOOOOOOOOOOOO
BZ01 PPPPPPPPPPPPPPPPPP
BZ01 QQQQQQQQQQQQQQQQQQ
BZ01 RRRRRRRRRRRRRRRRRR
BZ01 SSSSSSSSSSSSSSSSSS
BZ01 TTTTTTTTTTTTTTTTTTT
BZ01 UUUUUUUUUUUUUUUUUU
BZ01 VVVVVVVVVVVVVVVVVV
BZ01 WWWWWWWWWWWWWWWWWW
BZ01 XXXXXXXXXXXXXXXXXXXX
BZ01 YYYYYYYYYYYYYYYYYYY
BZ01 ZZZZZZZZZZZZZZZZZZ

CARDS 27-36 PUNCHED AS FOLLOWS

+++++ ----- 0000 0
34567 34567 456734567+-2
BZ01 FGH IJKLMN OPQRST UVWX*Z012345678988888+88888-/*888888888008 ABCDEFGHIJK*MNOPQ
* IN COLS 25, 50 AND 75 INDICATE 3, 4, 5, 7, AND 8 PUNCHES

CARDS 37-86 PUNCHED AS FOLLOWS

+++++ ----- 00000 0
34567 34567 3456734567+-2
BZ01 FGH IJKLMN OPQRST UVWXYZ012345678988888+88888-/*888888888008 ABCDEFGHIJKLMNCPQ

020
M014

1410/7010-1401 TOPSY COMPATIBILITY TEST
 SFX CT LDCV INSTRUCTION

1410/7010-1401 TOPSY COMPATIBILITY TEST

SEQ PG LIN LABEL OP OPERANDS

SEQ	PG	LIN	LABEL	OP	OPERANDS	SFX	CT	LDCV	INSTRUCTION
101	AA	00	000	JOB	1410/7010-1401 TOPSY COMPATIBILITY TEST				
102	AA	01	CTL	CTL	461111				
103	AA	03							
104	AA	04							
105	AA	06							
106	AA	07							
107	AA	08							
108	AA	09							
109	AA	10	TADO	EQU	1000				1000
110	AA	11	TADI	EQU	1001				1001
111	AA	12	TAD2	EQU	1002				1002
112	AA	13	TAD3	EQU	1003				1003
113	AA	14	TAD4	EQU	1004				1004
114	AA	15	TAD5	EQU	1005				1005
115	AA	16	SYSI	EQU	1256				1256
116	AA	17	CHN1	EQU	1289				1289
117	AA	18	START	EQU	2000				2000
118	AA	19							
119	AA	20							
120	AA	21	ORG	SYSI					1255
121	AA	22	DC	a					32 1287
122	AA	23	DC	a a					1 1288
123	AA	24							
124	AA	25	ORG	1239					1239
125	AA	26	DCW	a1J8X60291-9a					11 1249
126	AA	27	DCW	aM0148a					5 1254
127	AA	28	DCW	a a					1 1255
128	AA	29							
129	AA	30	ORG	1000					1000
130	AA	31	DC	a000000a					6 1005
131	AA	32	DCW	a a					1 1006
132	AA	33							
133	AA	34	ORG	CHN1					1289
134	AA	35	DC	a					32 1320
135	AA	36	DC	a					25 1345

SFX CT LDCV INSTRUCTION

136 AA 38 JOB 14107010-1401 TOPSY COMPATIBILITY TEST
 137 AA 40 ORG 1439
 138 AA 41
 139 AA 42
 140 AA 43
 141 AA 44
 142 AA 45
 143 AA 46
 144 AA 47
 145 AA 48

LOOP CHECK ROUTINE
 THIS ROUTINE IS ENTERED AT
 COMPLETION OF TEST ROUTINE
 TO CK FOR INQUIRY AND LOOP

LOOPCK SBR LPEX6003 SET ROJTIVE EXIT 4 1439 4 J59
 BIN ALTER,Q CK FOR INQUIRY 5 1443 8 J68 J
 BCE POST-003,TAD1,1 TEST FOR LOOP 8 1448 3 Z01 #01 1
 LPEX 0000 ROUTINE EXIT 4 1456 8 000
 ERRLOC DCH @ERR @ 7 1466
 DCH @ @ 1 1457

CONSOLE PRINTER INQUIRY ROUTINE

ALTER SBR ALTEX6003 SET ROUTINE EXIT 4 1458 4 U88
 MCH @T0,1000,R READ CON. PRTR. 8 1472 4 #T0 #00 3
 BIN *-012,* CK FOR INQ ERR 5 1480 8 J72 *
 ALTEX 0000 ROUTINE EXIT 4 1485 3 000

ROUTINE TO TEST TADS FOR
 LOOP ON SAME DATA AND
 TO TEST FOR INQUIRY

TAD5CK SBR T05EX6003 SET ROJTIVE EXIT 4 1489 4 V09
 BIN ALTER,Q CK FOR INQUIRY 5 1493 3 J68 J
 BCE 0001,TAD5,1 TEST FOR LOOP 8 1498 3 001 #05 1
 T05EX 0000 ROUTINE EXIT 4 1506 8 000

1410/7010-1401 TOPSY COMPATIBILITY TEST M014
SFX CT LDCN INSTRUCTION

SEQ PG LIN LABEL OP OPERANDS

171 AA 75 JOB 1410/7010-1401 TOPSY COMPATIBILITY TEST
 172 AA 77 ORG 2000
 173 AA 78
 174 AA 79 DCW 2J08500 2
 175 AA 80
 176 AA 81
 177 AA 82
 178 AA 83
 179 AA 84
 180 AA 85
 181 AA 86
 182 AA 87
 183 AA 88
 184 AA 89
 185 AA 90
 186 AA 91
 187 AA 92
 188 AA 93
 189 AA 94
 190 AA 95
 191 AA 96
 192 AA 97
 193 AA 98
 194 AA 99
 195 AB 00
 196 AB 01
 197 AB 02
 198 AB 03
 199 AB 04
 200 AB 05
 201 AB 06
 202 AB 07
 203 AB 08
 204 AB 09
 205 AB 10
 206 AB 11
 207 AB 12
 208 AB 13
 209 AB 14
 210 AB 15
 211 AB 16
 212 AB 17
 213 AB 18
 214 AB 19
 215 AB 20
 216 AB 21
 217 AB 22
 218 AB 23
 219 AB 24
 220 AB 25

GO TO 8500 TO
 DELETE NON READY
 DEVICES
 SET RESTART AND
 TYPE PROGRAM ID
 1 2007
 HALT TO SET COMP
 SW TO 1401
 PRESS START
 SET RESTART
 7 2008 L 81X 005

ROUTINE NO. 1
 EXECUTE BRANCH IF CHAR EQUAL
 INSTRUCTION WITH INSTRUCTION
 LENGTHS GREATER THAN 8
 EXECUTE ROUTINE 5 TIMES

*8005
 NOP POST
 SAR BCE,TAD5,1
 LCA DMS,BCE8015
 CW BCE8008
 LCA ZER3,0089
 SW BCE8008&X1
 CW BCE8006&X1
 MCH BCE8007&X1,CHTEST
 BCE BROK,CHTEST,A
 DCW 2BNCNDNE2
 B TYP1-031
 BROK TAD5CK
 A TWO,0089
 C 0089,Z10
 BU VX
 B LOOPCK

ROUTINE NO. 2
 EXECUTE TWO ADDRESS SBR INSTR
 H XXX YYY

*8005
 NOP SET ROUT. START
 4 2111 V J19

SET ROUT. START
 4 2111 V J19

SEQ PG LIN LABEL OP OPERANDS SFK CT LOCN INSTRUCTION

SEQ	PG	LIN	LABEL	OP	OPERANDS	SFK	CT	LOCN	INSTRUCTION
221	AB	26		SAR	POST	4		2115	2 Z04
222	AB	27		LCA	ZER3,0089	7		2119	L E3/ J89
223	AB	28		LCA	XXX,TSBR	7		2126	L E4S E3Z
224	AB	29		SBR	0089,TSBR	7		2133	H 089 E3Z
225	AB	30		C	0089,TSBRAN	7		2140	C 089 E4V
226	AB	31		BU	TYPI	5		2147	B J4Z /
227	AB	32		C	TSBR,XXX	7		2152	C E3Z E4S
228	AB	33		BU	TYPI	5		2159	B J4Z /
229	AB	34		B	LOOPCK	4		2154	B J39
230	AB	35							
231	AB	36							
232	AB	37							
233	AB	38							
234	AB	39							
235	AB	40							
236	AB	41		NOP	*E005	4		2158	V J75
237	AB	42		SAR	POST	4		2172	Z Z04
238	AB	43		LCA	8KCYC,0089	7		2176	L Z8S J89
239	AB	44		NOP	1000EX1,1000EX1	7		2183	V #0 #0
240	AB	45		B	LOOPCK	4		2190	B J39
241	AB	46							
242	AB	47							
243	AB	48							
244	AB	49							
245	AB	50							
246	AB	51							
247	AB	52							
248	AB	53							
249	AB	54							
250	AB	55							
251	AB	56		BCE	EX8,TAD4,1	8		2194	B K05 #04 1
252	AB	57		B	RN5	4		2202	B K65
253	AB	58	EX8	NOP	MKSTP	4		2206	V K27
254	AB	59		SAR	POST	4		2210	Z Z04
255	AB	60		MCW	#T0,PROCMS-031,W	8		2214	V #T0 C4S W
256	AB	61		H		1		2222	.
257	AB	62							
258	AB	63							
259	AB	64							
260	AB	65		SW	7801	4		2223	V H0/
261	AB	66	MKSTP	MCW	7800,7801	7		2227	V H0# -10/
262	AB	67		BIN	ROK8,%	5		2234	B K43 %
263	AB	68		B	TYPI-031	4		2239	B J1V
264	AB	69							
265	AB	70		BIN	TYPI,%	5		2243	B J4Z %
266	AB	71	ROK8						
267	AB	72		B	LOOPCK	4		2248	B J39
268	AB	73		MCW	#T0,RESCK-031,W	8		2252	V #T0 A1X W
269	AB	74		H		1		2250	.
270	AB	75							

ROUTINE NO. 3
EXECUTE INDEXED NOP INSTRUCTION

ROUTINE NO. 4
FORCE SYSTEM CHECK ERROR
WITH MOVE OF LOC. CONTAINING
EVEN NUMBER OF BITS
TEST BRANCH ON PROCESS ERROR
#LOC ALTERED TO EVEN BITS
FROM CONSOLE TYPEWRITER

SET ROUT START
ADDR IV Z02-Z04
LOAD XRI - 19E
EXECUTE NOP

CK FOR MANL TEST
BYPASS ROUTINE
SET ROUT START
ADDR IV Z02-Z04
TYPE MESSAGE
HALT TO ALTER
LOC 7800
HOLD SHIFT AND
DEPRESS KEY B
SET WM
EXEC MOVE OP
CK FOR P30C ERR
PRGGRAM FAILED
TO BR ON PROCESS
ERROR
ERR LATCH DID
NOT RESET
CK FOR LOOP
TYPE MESSAGE
HALT TO RESTORE

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SFX CT LOCN INSTRUCION

SEQ PG LIN LABEL OP OPERANDS

SEQ	PG	LIN	LABEL	OP	OPERANDS	SFX	CT	LOCN	INSTRUCION
271	AB	76		CS	7801				
272	AB	77						2261	/ H0/
273	AB	78							
274	AB	79							
275	AB	80							
276	AB	81							
277	AB	82							
278	AB	83							
279	AB	84							
280	AB	85							
281	AB	86							
282	AB	87							
283	AB	88							
284	AB	89							
285	AB	90							
286	AB	91							
287	AB	92							
288	AB	93							
289	AB	94							
290	AB	95							
291	AB	96							
292	AB	97							
293	AB	98							
294	AB	99							
295	AC	00							
296	AC	01							
297	AC	02							
298	AC	03							
299	AC	04							
300	AC	05							
301	AC	06							
302	AC	07							
303	AC	08							
304	AC	09							
305	AC	10							
306	AC	11							
307	AC	12							
308	AC	13							
309	AC	14							
310	AC	15							
311	AC	16							
312	AC	17							
313	AC	18							
314	AC	19							
315	AC	20							
316	AC	21							
317	AC	22							
318	AC	23							
319	AC	24							
320	AC	25							

CK CONTROL SW
CLEAR WORK AREA

ROUTINE NO. 5
ADD 99 TO I1 AND CHECK
FOR /0 RESULT WITH OVERFLOW

SET ROUT START
ADDR IN Z02-Z04
EXECUTE ADD
CK RESULT
RESULT OF ADD
IS INCORRECT
CK FOR OVERFLOW
DID NOT GET OVFL
CK FOR LOOP

ROUTINE NO. 6
ADD 99 TO /1 AND CHECK
FOR /0 RESULT WITH OVERFLOW

SET ROUT START
ADDR IN Z02-Z04
EXECUTE ADD
CK RESULT
RESULT OF ADD
IS INCORRECT
CK FOR OVERFLOW
DID NOT GET OVFL
CK FOR LOOP

ROUTINE NO. 7
ADD 99 TO J1 AND CHECK
FOR /0 RESULT WITH OVERFLOW

SET ROUT START
ADDR IN Z02-Z04
EXECUTE ADD
CK RESULT
RESULT OF ADD
IS INCORRECT
CK FOR OVERFLOW
DID NOT GET OVFL
CK FOR LOOP

321 AC 26
 322 AC 27
 323 AC 28
 324 AC 29
 325 AC 30
 326 AC 31
 327 AC 32
 328 AC 33
 329 AC 34
 330 AC 35
 331 AC 36
 332 AC 37
 333 AC 38
 334 AC 39
 335 AC 40
 336 AC 41
 337 AC 42
 338 AC 43
 339 AC 44
 340 AC 45
 341 AC 46
 342 AC 47
 343 AC 48
 344 AC 49
 345 AC 50
 346 AC 51
 347 AC 52
 348 AC 53
 349 AC 54
 350 AC 55
 351 AC 56
 352 AC 57
 353 AC 58
 354 AC 59
 355 AC 60
 356 AC 61
 357 AC 62
 358 AC 63
 359 AC 64
 360 AC 65
 361 AC 66
 362 AC 67
 363 AC 68
 364 AC 69
 365 AC 70
 366 AC 71
 367 AC 72
 368 AC 73
 369 AC 74
 370 AC 75

ROUTINE NO. 8
 ADD 99 TO A1 AND CHECK
 FOR 10 RESULT WITH OVERFLOW

NOP	*E005	SET ROJLT START	4	2427	V	M15
SAR	POST	ADDR IN Z02-Z04	4	2411	Z	Z04
LCA	AA1E006,ADAREA		7	2415	L	B4Y 35Y
A	VINT9,ADAREA	EXECUTE ADD	7	2422	A	B6# 35Y
C	ADAREA,ADAV&006	CK RESULT	7	2429	C	B5Y 35M
BU	TYPI	RESULT OF ADD	5	2436	B	Z4Z /
BAV	*E005	IS INCORRECT	5	2441	B	M5J Z
B	TYPI-031	CK FOR OVERFLOW	4	2446	B	Z1Y
B	LOOPCK	DID NOT GET OVFL	4	2450	B	U39
CS	0332	CK FOR LOOP	4	2454	/	332
CS		CLEAR	4	2454	/	332
CS		PRINT AREA	1	2458	/	

ROUTINE NO. 9
 PRINT 10 LINES AND
 TEST BRANCH ON PRINTER BUSY

BCE	EX9,13051P	CK FOR PRINTER	8	2459	B	M71 T05 P
B	RN14	BYPASS PRT TESTS	4	2457	B	R95
NOP	*E005	SET ROUT. START	4	2471	V	M79
SAR	POST	ADDR IN Z02-Z04	4	2475	Z	Z04
BCE	*E012,1306,N	CK FOR NUM CHAIN	8	2479	B	M98 T05 V
MCM	PRBSEG,0332	MV DATA	7	2487	M	B2M 332
B	*E008		4	2494	B	V05
MCM	PRBNSG,0332	MV DATA	7	2498	M	E9J 332
SW	0201	TJ PRINT AND	4	2505	P	201
MCM	0332,0322	SPREAD IT OUT	7	2509	M	332 322
LCA	ZZZ,0089	RESET XR 1	7	2516	L	J3K 089
LCA	ZZZZ,CYCNT	RESET COUNTER	7	2523	L	B3J B3#
W		PRINT LINE	1	2530	Z	
BPB	CKBUSY	CK FOR PRINT BUSY	5	2531	B	Z5Y P
C	CYCNT,ZZZZ	CK CNT	7	2536	C	33# 33J
BE	TYPI	PRG FAILED TO	5	2543	B	Z4Z S
BIN	TYPI, #	BR ON BUSY	5	2548	B	Z4Z #
A	DNE,0089	CK FOR PRINT ERR	7	2553	A	ADV 089
C	0089,Z10	UP XR 1	7	2550	C	089 03M
BU	PTZ	CK FOR 10 LINES	5	2557	B	V23 /
B	LOOPCK	PRINT NEXT LINE	4	2572	B	J39
CS	0332	CK FOR LOOP	4	2576	/	332
CS		CLEAR	4	2576	/	332
CS		PRINT AREA	1	2580	/	

ROUTINE NO. 10
 PRINT 10 LINES AND TEST

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SFK CT LOCN INSTRUCTION

JPERANDS

SEQ PG LIN LABEL OP

SEQ	PG	LIN	LABEL	OP	JPERANDS	SFK	CT	LOCN	INSTRUCTION
371	AC	76				4	2581	V	N 189
372	AC	77				4	2585	2	Z04
373	AC	78				8	2589	B	J08 T05 V
374	AC	79				7	2597	M	B2M 332
375	AC	80				4	2604	B	J15
376	AC	81				7	2608	M	G9J 332
377	AC	82				4	2615	*	201
378	AC	83				7	2619	M	332 322
379	AC	84				7	2626	L	B3X 089
380	AC	85				7	2633	L	B3J 83*
381	AC	86				1	2640	2	
382	AC	87				5	2641	3	Q4Z *
383	AC	88				5	2646	3	J4Z P
384	AC	89							
385	AC	90							
386	AC	91							
387	AC	92							
388	AC	93							
389	AC	94							
390	AC	95							
391	AC	96				7	2651	A	ADV 089
392	AC	97				7	2658	C	089 83M
393	AC	98				5	2655	B	J33 /
394	AC	99				4	2670	B	J39
395	AD	00				4	2674	/	332
396	AD	01				1	2678	/	
397	AD	02							
398	AD	03							
399	AD	04							
400	AD	05							
401	AD	06							
402	AD	07							
403	AD	08				4	2679	V	J87
404	AD	09				4	2683	2	Z04
405	AD	10				7	2697	L	B3X 089
406	AD	11				7	2694	L	B3J 83*
407	AD	12				2	2701	F	L
408	AD	13				5	2703	B	Z5Y 2
409	AD	14				7	2708	C	B3* 83J
410	AD	15				5	2715	B	J4Z S
411	AD	16							
412	AD	17							
413	AD	18				7	2720	A	ADV 089
414	AD	19				7	2727	C	089 81M
415	AD	20				5	2734	B	J94 /
416	AD	21				4	2739	B	U39
417	AD	22							
418	AD	23							
419	AD	24							
420	AD	25							

FOR SYSTEM INTERLOCK EXECUTING
BR ON PRINT ERROR BEFORE
BR ON PRINT BUSY

*E005
POST
*E012,1306,N
PRBSEG,0332
*E008
PRBNSG,0332
0201
0332,0322
LCA ZZZ,0089
LCA ZZZZ,CYCNT

TYPI,*
TYPI

ONE,0089
0089,Z10
PT3
LOOPCK
0332

ROUTINE NO. 11
EXECUTE CARRIAGE CONTROL
UPS AND TEST FOR
PRINTER CARRIAGE BUSY

*E005
POST
ZZZ,0089
ZZZZ,CYCNT
L
CKBUSY
CYCNT,ZZZZ
TYPI

ONE,0089
0089,ZZ5
PT4
LOOPCK

ROUTINE NO 12

1410/7010-1401 TOPSY COMPATIBILITY TEST

SEQ PG LIN LABEL OP OPERANDS SFX CT LOCN INSTRUCTION

421 AD 26 NOP
 422 AD 27 SAR
 423 AD 28 BCE
 424 AD 29 B
 425 AD 30 MCH
 426 AD 31 H
 427 AD 32 LCA
 428 AD 33 BCE
 429 AD 34 MCH
 430 AD 35 SW
 431 AD 36 MCH
 432 AD 37 MCH
 433 AD 38 MCH
 434 AD 39 SW
 435 AD 40 MCH
 436 AD 41 MCH
 437 AD 42 SW
 438 AD 43 MCH
 439 AD 44 MCH
 440 AD 45 W
 441 AD 46 BIN
 442 AD 47 B
 443 AD 48 CKR
 444 AD 49 A
 445 AD 50 C
 446 AD 51 BU
 447 AD 52 B
 448 AD 53 B
 449 AD 54
 450 AD 55
 451 AD 56
 452 AD 57
 453 AD 58
 454 AD 59
 455 AD 60
 456 AD 61
 457 AD 62
 458 AD 63
 459 AD 64
 460 AD 65
 461 AD 66
 462 AD 67
 463 AD 68
 464 AD 69
 465 AD 70
 466 AD 71
 467 AD 72
 468 AD 73
 469 AD 74
 470 AD 75

FORCE PRINTER ERROR
 HALT TO DISABLE PRINT HAMMER
 PRINT 10 LINES AND TEST
 BRANCH ON PRINT ERROR

EXX12&009
 POST
 EXX12,TAD4,1
 RN13
 &T0,PRTHAN-031,W
 ZZZ,0089
 *&012,1306,N
 IMSEG,0332
 *&008
 PRBNSG,0332
 0201
 0332,0322
 CKR,*
 TYPL-031
 TYPL,*
 ONE,0089
 0089,Z10
 PT
 LOOPCK

SET ROUT. START
 ADDR IN 202-204
 CK FOR MANL TEST
 GO TO NEXT RJUT.
 PRINT MESSAGE
 HALT TO DISABLE
 PRINT HAMMER
 RESET XR 1
 CK FOR NUM C-CHAIN
 MV IN IMAGE SEGM
 MV DATA
 SET WM
 SPREAD RECORD
 PRINT LINE
 CK FOR PRINT ERR
 FAILED TO BRANCH
 ON PRINT ERROR
 CK FOR RESET
 UP XR 1
 CK FOR 10 LINES
 PRINT NEXT LINE
 CK FOR LOOP

ROUTINE NO. 13

WITH PRINT HAMMER DISABLED
 FROM PREVIOUS ROUTINE CHECK
 FOR NO RESET OF PRINT ERROR
 LATCH WITH BRANCH INSTR HAVING
 REC MARK IN UNITS PSN OF ADDRESS

421 AD 26 NOP
 422 AD 27 SAR
 423 AD 28 BCE
 424 AD 29 B
 425 AD 30 MCH
 426 AD 31 H
 427 AD 32 LCA
 428 AD 33 BCE
 429 AD 34 MCH
 430 AD 35 SW
 431 AD 36 MCH
 432 AD 37 MCH
 433 AD 38 MCH
 434 AD 39 SW
 435 AD 40 MCH
 436 AD 41 MCH
 437 AD 42 SW
 438 AD 43 MCH
 439 AD 44 MCH
 440 AD 45 W
 441 AD 46 BIN
 442 AD 47 B
 443 AD 48 CKR
 444 AD 49 A
 445 AD 50 C
 446 AD 51 BU
 447 AD 52 B
 448 AD 53 B
 449 AD 54
 450 AD 55
 451 AD 56
 452 AD 57
 453 AD 58
 454 AD 59
 455 AD 60
 456 AD 61
 457 AD 62
 458 AD 63
 459 AD 64
 460 AD 65
 461 AD 66
 462 AD 67
 463 AD 68
 464 AD 69
 465 AD 70
 466 AD 71
 467 AD 72
 468 AD 73
 469 AD 74
 470 AD 75

PT1
 POST
 EXX13,TAD4,1
 RN14
 FRTHOU,0094
 SVLOC,0099
 SVRES
 BRBK004,4004
 ZZZ,0089
 4000
 CKR1,*

SET ROUT. START
 ADDR IN 202-204
 CK FOR MANL TEST
 BYPASS ROUTINE
 SAVE
 4000 AREA
 SAVE 4000 AREA
 LOAD BR INSTR
 RESET XR 1
 PRINT LINE
 BRANCH TO 4000
 CK FOR PRINT ERR

1410/7010-1401 TOPSY COMPATIBILITY TEST

SFX CT LDCN INSTRUCTION

SEQ PG LIN LABEL OP OPERANDS

SEQ PG LIN	LABEL	OP	OPERANDS	SFX	CT	LDCN	INSTRUCTION
471 AD 76		B	TYPI-031	4	2916	B	QIV
472 AD 77							BRANCH INSTR
473 AD 78							CAUSED PRT ERRJR
474 AD 79	CKER1	BIN	TYPI,*	5	2920	B	34Z *
475 AD 80							LATCH TO RESET
476 AD 81							PREVIOUS BR ON
477 AD 82		A	ONE,0089	7	2925	A	ADV 089
478 AD 83		C	0089,Z10	7	2932	C	089 83M
479 AD 84		BU	PRI	5	2939	B	205 /
480 AD 85		B	LOOPCK	4	2944	B	J39
481 AD 86		LCA	FRTHOU,0099	7	2948	L	AOY 099
482 AD 87		LCA	SVLOC,0094	7	2955	L	ALW 094
483 AD 88		CW	4000	4	2962	B	00*
484 AD 89		B	SVRES	4	2966	B	30X
485 AD 90		LCA	PRBSEG-005,SAVA	7	2970	L	32/ ALT
486 AD 91		CW	SAVA-004	4	2977	B	A0Z
487 AD 92		MCH	3TO,RESHAM-031,W	8	2981	M	3TO 45Y W
488 AD 93		H		1	2989	.	.
489 AD 94							HALT TO RESTORE
490 AD 95		CS	0332	4	2990	/	332
491 AD 96		CS		1	2994	/	
492 AD 97							PRINT AREA
493 AD 98							
494 AD 99							
495 AE 00							
496 AE 01							
497 AE 02							
498 AE 03							
499 AE 04							
500 AE 05							
501 AE 06							
502 AE 07							
503 AE 08							
504 AE 09							
505 AE 10							
506 AE 11							
507 AE 12							
508 AE 13							
509 AE 14							
510 AE 15							
511 AE 16							
512 AE 17							
513 AE 18							
514 AE 19							
515 AE 20							
516 AE 21							
517 AE 22							
518 AE 23							
519 AE 24							
520 AE 25							

ROUTINE NO. 14

PUNCH 10 CARDS TO BE USED
IN BR ON PUNCH ERROR TEST
READY THESE CARDS IN PUNCH
FEED AND FORCE BRANCH ON
PUNCH ERR BY PUNCHING INTO
PRE PUNCHED CARDS

SEQ PG LIN	LABEL	OP	OPERANDS	SFX	CT	LDCN	INSTRUCTION
502 AE 07	RN14	BCE	EXB,1303,P	8	2995	B	807 T03 P
503 AE 08		B	TESTP	4	3003	B	854
504 AE 09	EXB	BCE	EX14,TAD4,1	8	3007	B	819 804 1
505 AE 10		B	TESTP	4	3015	B	854
506 AE 11	EX14	NOP	PN14	4	3019	V	870
507 AE 12		SAR	POST	4	3023	2	Z04
508 AE 13		LCA	ZZZ,0089	7	3027	L	93X 089
509 AE 14		LCA	RDCOMP003,0180	7	3034	L	C4* 180
510 AE 15		P		1	3041	4	
511 AE 16	PNDK1	A	ONE,0089	7	3042	A	ADV 089
512 AE 17		C	0089,Z10	7	3049	C	089 83M
513 AE 18		BU	PNDK1	5	3056	B	841 /
514 AE 19		MCH	3TO,PNRMS-031,W	8	3051	M	3TO DIJ W
515 AE 20		H		1	3059	.	.
516 AE 21							
517 AE 22							
518 AE 23							
519 AE 24	PN14	LCA	ZZZ,0089	7	3070	L	83X 089
520 AE 25		CS	0180	4	3077	/	180

CK FOR PUNCH
CK FOR MANUAL TS
SET ROJT START
ADDR IV Z02-Z04
LOAD DATA
PUNCH
UP XR 1
PUNCH
10 CARDS
TYPE MESSAGE
HALT TO READY
PUNCHED CARDS
IN PUNCH HOPPER
RESET XR 1
CLEAR PUNCH AREA

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SFX CT LDCN INSTRUCTION

SEQ PG LIN LABEL OP OPERANDS

SEQ	PG	LIN	LABEL	OP	OPERANDS
571	AE	76	OPCK	BWZ	CKPERU,0000&X1,1
572	AE	77	GOO	MA	ZZ1,0089
573	AE	78		C	0089,0094
574	AE	79		BU	OPCK
575	AE	80		B	RESTPX
576	AE	81	CKPERU	BCE	CKTT,0000&X1,M
577	AE	82		BCE	CKTT,0000&X1,L
578	AE	83		BCE	CKTT,0000&X1,U
579	AE	84		B	GOO
580	AE	85	CKTT	C	0002&X1,TPINS1
581	AE	86		BE	CKNI
582	AE	87		C	0002&X1,TPINS2
583	AE	88		BE	CKNI
584	AE	89		B	GOO
585	AE	90	CKN1	BCE	FIX1,0003&X1,1
586	AE	91	CKN2	BCE	FIX2,0003&X1,2
587	AE	92	FIX1	MN	RTX,0003&X1
588	AE	93		B	GOO
589	AE	94	FIX2	MN	WTX,0003&X1
590	AE	95		B	GOO
591	AE	96	RESTPX	MN	RTX,CKNI&007
592	AE	97		MN	WTX,CKN2&007
593	AE	98		B	RN16
594	AE	99			
595	AF	00			
596	AF	01			
597	AF	02			
598	AF	03			
599	AF	04			
600	AF	05			
601	AF	06			
602	AF	07	RN16	BCE	EX16,1301,R
603	AF	08		B	RN17
604	AF	09	EX16	NOP	*&005
605	AF	10		SAR	POST
606	AF	11		LCA	ZZZ,0089
607	AF	12		BLC	TYPI
608	AF	13			
609	AF	14			
610	AF	15			
611	AF	16			
612	AF	17			
613	AF	18			
614	AF	19			
615	AF	20			
616	AF	21			
617	AF	22			
618	AF	23	WTAP3	MCM	WTAP3,0001,M
619	AF	24		NOP	0000
620	AF	25		BEF	ENREEL

ROUTINE NO. 16
 CARD TO TAPE TEST
 READ 26 CARDS AND WRITE
 CARD DATA ON TAPE 1
 BACKSPACE TP1 AND READ
 RECORDS TO CHECK DATA WRITTEV

SFX	CT	LDCN	INSTRUCTION
8	3277	V	COB 0#0 1
7	3285	#	62# 089
7	3292	C	089 094
5	3299	B	B77 /
4	3304	B	D02
8	3308	B	C36 0#0 M
8	3316	B	C36 0#0 L
8	3324	B	C36 0#0 J
4	3332	B	885
7	3336	C	0#2 18W
5	3343	B	C64 S
7	3348	C	0#2 18Y
5	3355	B	C64 S
4	3350	B	885
8	3354	B	C80 0#3 1
8	3372	B	C91 0#3 2
7	3380	D	I9/ 0#3
4	3387	B	885
7	3391	D	I9S 0#3
4	3398	B	885
7	3402	D	I9/ C71
7	3409	D	I9S C79
4	3416	B	D20
8	3420	B	D32 T01 3
4	3428	B	E97
4	3432	V	D40
4	3436	Q	Z04
7	3440	L	B3X 089
5	3447	B	Q4Z A
1	3452	I	
5	3453	B	Q4Z 6
7	3458	L	68U 025
8	3455	B	D77 S91 1
4	3473	B	E74
4	3477	B	D91
5	3481	J	XU1 B
5	3486	U	XU1 E
8	3491	M	XU1 001 M
4	3499	V	000
5	3503	B	P2S K

CK FOR READER
 BYPASS ROUTINE
 SET ROUT. START
 ADDR IN 202-204
 RESET XR 1
 CK FOR LAST CARD
 ERR TYPE HERE
 INDS FALSE EOF
 READ CARD
 INVALID READ
 SET GMMH
 CK FOR TAPE
 GO TO WRITE TAPE
 BACKSPACE
 AND SKIP
 WRITE TAPE
 EXTRA INSTR
 CK FOR EDR

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SFX CT LOCN INSTRUCTION

SEQ PG LIN LABEL OP OPERANDS

SEQ	PG	LIN	LABEL	OP	OPERANDS	SFX	CT	LOCN	INSTRUCTION
621	AF	26		BER	TPWRR				CK FOR WR ERROR
622	AF	27							ERR TYPE HERE
623	AF	28							INDS THAT 10
624	AF	29							TRYS USING BKSP-
625	AF	30							SKIP HAVE BEEN
626	AF	31							MADE TO WR REC
627	AF	32							RESET ERR CNTR
628	AF	33							BACKSPACE REC
629	AF	34		LCA	ZZ,WRCNT	7	3513	L	E2V E2X
630	AF	35		CU	%U1,B	5	3520	J	%U1 B
631	AF	36		CS	WKAREA%025	4	3525	/	H2V
632	AF	37		MCW	%U1,WKAREA,R	8	3529	M	%U1 HJ# R
633	AF	38		NOP	0000	4	3537	V	000
634	AF	39		BEF	EOF1	5	3541	B	P4V K
635	AF	40							
636	AF	41		BER	TPRDER	5	3546	B	V9T L
637	AF	42							
638	AF	43							
639	AF	44							
640	AF	45		LCA	ZZ,RDCNT	7	3551	L	E2V E2Z
641	AF	46		C	WKAREA%023,0024	7	3558	C	H2T 024
642	AF	47		BE	RDK16	5	3555	B	E74 S
643	AF	48		B	TYPI-031	4	3570	B	Q1Y
644	AF	49							
645	AF	50							
646	AF	51	RDK16	A	DNE,0089	7	3574	A	ADV 089
647	AF	52		C	0089,Z26	7	3581	C	089 E35
648	AF	53		BU	EX16%015	5	3588	B	D47 /
649	AF	54		B	LOOPCK	4	3593	B	D39
650	AF	55							
651	AF	56							
652	AF	57							
653	AF	58							
654	AF	59							
655	AF	60							
656	AF	61							
657	AF	62							
658	AF	63		BCE	CKT2,1303,P	8	3597	B	F09 T03 P
659	AF	64		B	RN18	4	3605	B	G07
660	AF	65	CKT2	BCE	EX17,1291,1	8	3609	B	F21 S91 1
661	AF	66		B	RN18	4	3617	B	G07
662	AF	67	EX17	B	PRETP1	4	3621	B	L0+
663	AF	68		NOP	*%005	4	3625	V	F33
664	AF	69		SAR	POST	4	3629	Q	Z04
665	AF	70		CU	%U1,R	4	3633	J	%U1 R
666	AF	71		LCA	ZZZ,0089	5	3638	L	B3X 089
667	AF	72		CS	0180	7	3645	/	180
668	AF	73		MCW	%U1,0101,R	4	3649	M	%U1 101 R
669	AF	74		NOP	0000	8	3657	V	000
670	AF	75		BEF	EOF1	4	3651	B	P4V K

ROUTINE NO. 17
 TAPE TO CARD TEST
 WRITE 26 RECS ON TAPE 1
 REWIND TAPE - READ AND
 PUNCH THE 26 RECORDS

1410/7010-1401 TOPSY COMPATIBILITY TEST

SEQ PG	LIN	LABEL	OP	OPERANDS	SFX	CT	LOCN	INSTRUCTION
671	AF	76						ERR TYPE HERC
672	AF	77						INDS FALSE EOF
673	AF	78	BER	TPRDR	5	3656	B N9T L	CK FOR READ ERR
674	AF	79						ERR TYPE HERE
675	AF	80						INDS THAT IO
676	AF	81						TRYS HAVE BEEN M
677	AF	82						MADE TO READ REC
678	AF	83	LCA	ZZ,RDCNT	7	3671	L E2V EZZ	RESET ERR CNTR
679	AF	84	P		1	3678	4	PUNCH CARD
680	AF	85	BIN	TYPL,-	5	3679	B Q4Z -	PUNCH ERR
681	AF	86	A	DNE,0089	7	3684	A ADV 089	UP XR 1
682	AF	87	C	0089,Z26	7	3691	C 089 E3S	CK FDR 26 RECS
683	AF	88	BU	NX17	5	3698	B F45 /	READ NEXT REC
684	AF	89	B	LODPCK	4	3703	B U39	CK FOR LOOP
685	AF	90						
686	AF	91						
687	AF	92						
688	AF	93						
689	AF	94						
690	AF	95						
691	AF	96						
692	AF	97						
693	AF	98						
694	AF	99						
695	AG	00	RN18	CKT3,1305,P	8	3707	B G19 T05 P	CK FOR PRINTER
696	AG	01	B	RN19	4	3715	B H41	BYPASS ROUTINE
697	AG	02	B	EX18,1291,1	8	3719	B G31 S91 I	CK FOR TAPES
698	AG	03	B	RN19	4	3727	B H41	BYPASS ROUTINE
699	AG	04	NOP	PRETP1	4	3731	B L0#	WRITE TP 1
700	AG	05	SAR	*E005	4	3735	V G43	SET ROUT START
701	AG	06	CU	POST	4	3739	Q Z04	ADDR IN Z02-Z04
702	AG	07	LCA	XU1,R	5	3743	J XU1 R	REWIND TAPE 1
703	AG	08	CS	ZZZ,0089	7	3748	L B3X 089	RESET XR 1
704	AG	09	MCH	0299	4	3755	/ 299	CLEAR PUNCH
705	AG	10	NOP	XU1,0201,R	8	3759	V XU1 Z01 R	READ TAPE REC
706	AG	11	BEF	0000	4	3767	V 000	EXTRA INSTR
707	AG	12		EOF1	5	3771	B P4V K	CK FOR EOF
708	AG	13						ERR TYPE HERE
709	AG	14	BER	TPRDR	5	3776	B N9T L	INDS FALSE EOF
710	AG	15						ERR TYPE HERE
711	AG	16						INDS THAT IO
712	AG	17						TRYS HAVE BEEN
713	AG	18						MADE TO READ REC
714	AG	19	LCA	ZZ,RDCNT	7	3781	L E2V EZZ	RESET ERR CNTR
715	AG	20	BCE	CKZN18,1306,N	8	3788	B H01 T06 V	CK FOR NUM CHAIN
716	AG	21	B	CK18	1	3796	Z	PRINT
717	AG	22	B	PR18,0201,2	4	3797	B H13	GO TO CHECK
718	AG	23	B	CK18E005	8	3801	V G96 Z01 2	CK FOR NO ZONE
719	AG	24	B	TYPL,+	4	3809	B H18	BYPASS RECDRD
720	AG	25	A	DNE,0089	5	3813	B Q4Z +	PRINT ERROR
					7	3818	A ADV 089	UP XR 1

ROUTINE NO. 18
 TAPE TO PRINTER TEST
 WRITE 26 RECS ON TAPE 1
 REWIND TAPE - READ AND
 PRINT THE 26 RECORDS

034

SEQ PG LIN LABEL OP OPERANDS SFX CT LOCN INSTRUCTION

721 AG 26 C 0089,Z26 CK FOR 26 RECS
 722 AG 27 BU NX18 READ NEXT REC
 723 AG 28 B LOOPCK CK FOR LOOP

ROUTINE NO. 19

TAPE TO TAPE TEST
 WRITE 26 TWENTY CHAR RECRD
 ON TAPE 1 - REWIND TAPE 1 AND
 TRANSFER RECS TO TAPE 2

RN19 8CE TESNUM,1291,1 CK FOR TAPES
 B RN20 BYPASS TEST
 TESNUM BCE RN20,7992, CK FOR 2 TAPES
 EX19 B PRETP1 WRITE TAPE 1
 NOP *6005 SET ROUT START
 SAR POST ADDR IN Z02-Z04
 CU ZU1,R REWIND TAPE 1
 LCA ZZZ,0089 RESET XR 1
 LCA GMM,WKAREA&051 LOAD GMM
 CS WKAREA&050 READ RECORD
 MCM ZU1,WKAREA&021,R SET WM
 SH WKAREA&041 CK FOR ERF ERR
 BEF EOF1 TYPE HERE INDS

747 AG 52 BER TPRDR CK FOR RD ERRR
 748 AG 53 FALSE EOF
 750 AG 55 ERR TYPE HERE
 751 AG 56 INDS THAT 10
 752 AG 57 TRYS HAVE BEEN
 753 AG 58 MADE TO READ REC
 754 AG 59 RESET ERR CNTR
 755 AG 60 GO TO WRITE TP2
 756 AG 61 BACKSPACE
 757 AG 62 & SKIP
 758 AG 63 MCH ZU2,WKAREA&021,W WRITE TAPE
 759 AG 64 NOP 0000 EXTRA INSTR
 760 AG 65 BEF ENREEL CK FOR EDR
 761 AG 66 BER TPMRER CK FOR WRITE ERR
 762 AG 67 ERR TYPE HERE
 763 AG 68 INDS THAT 10
 764 AG 69 TRYS USING BKSP-
 765 AG 70 SKIP HAVE BEEN
 766 AG 71 MADE TO WR REC
 767 AG 72 LCA ZZ,WRCNT RESET ERR CNTR
 768 AG 73 CU ZU2,B BACKSPACE TP 2
 769 AG 74 CS WKAREA&020 CLEAR STORAGE
 770 AG 75 MCH ZU2,WKAREA,R READ RECORD

8	3841	B	153	S91	1	CK FOR TAPES
4	3849	B	05+			BYPASS TEST
4	3853	B	05+	19S		CK FOR 2 TAPES
1	3851	V				WRITE TAPE 1
4	3852	B	L0+			SET ROUT START
4	3856	V	174			ADDR IN Z02-Z04
4	3870	J	Z04			REWIND TAPE 1
5	3874	U	ZU1	R		RESET XR 1
7	3879	L	B3X	089		LOAD GMM
7	3886	L	B8J	H5/		READ RECORD
4	3893	/	H5+			SET WM
8	3897	M	ZU1	H2/	R	CK FOR ERF ERR
4	3905	.	H4/			TYPE HERE INDS
5	3909	B	P4V	K		FALSE EOF
5	3914	B	V9T	L		ERR TYPE HERE
7	3919	L	E2V	E2Z		INDS THAT 10
4	3926	B	I4J			TRYS HAVE BEEN
5	3930	J	ZU2	B		MADE TO READ REC
5	3935	J	ZU2	E		RESET ERR CNTR
8	3940	M	ZU2	H2/	M	GO TO WRITE TP2
4	3948	V	000			BACKSPACE
5	3952	B	P2S	K		& SKIP
5	3957	B	M4Y	L		WRITE TAPE
7	3962	L	E2V	E2X		EXTRA INSTR
5	3969	U	ZU2	B		CK FOR EDR
4	3974	/	H2+			CK FOR WRITE ERR
8	3978	M	ZU2	H0+	R	ERR TYPE HERE

1410/7010-1401 TOPSY COMPATIBILITY TEST

SFX CT LOCN INSTRUCTION

OPERANDS

LABEL OP

SEQ PG LIN

SEQ PG LIN	LABEL	OP	OPERANDS	SFX	CT	LOCN	INSTRUCTION
821 AH 26		B	LOOPCK	4	4147	B J39	
822 AH 27	SPP	BCE	NUMC2,1306,N	8	4151	B 17# T06 V	
823 AH 28		LCA	RDCOMP&003,0280	7	4159	L C4# 230	
824 AH 29		B	*6019	4	4166	B 18Y	
825 AH 30	NUMC2	MCH	PRBNSG,0280	7	4170	M 89J 280	
826 AH 31		SW	0201	4	4177	* 201	
827 AH 32		MCH	0280,0270	7	4181	M 280 270	
828 AH 33		LCA	GMWM,0281	7	4188	L 88J 291	
829 AH 34		LCA	RDCOMP&003,0180	7	4195	L C4# 180	
830 AH 35		LCA	GMWM,0181	7	4202	L 88J 181	
831 AH 36		LCA	RDCOMP&003,WKAREA&079	7	4209	L C4# 17Z	
832 AH 37		LCA	GMWM,WKAREA&080	7	4216	L 88J H8#	
833 AH 38		LCA	RDCOMP&003,0080	7	4223	L C4# 380	
834 AH 39		LCA	GMWM,0081	7	4230	L 88J 381	
835 AH 40							
836 AH 41							
837 AH 42							
838 AH 43							
839 AH 44							
840 AH 45							
841 AH 46							
842 AH 47							
843 AH 48							
844 AH 49							
845 AH 50	EXAA	NOP	*6005	4	4249	V 25K	
846 AH 51		SAR	POST	4	4253	Q Z04	
847 AH 52		LCA	ZZZ,0089	7	4257	L 83X 389	
848 AH 53		MCH	TPRINA,GDRT&006	7	4254	M R9/ -5V	
849 AH 54	TRES2	SS	.	2	4271	K .	
850 AH 55		R		1	4273	1	
851 AH 56		BCE	*6005,1291,1	8	4274	B 28W S91 1	
852 AH 57		B	*6005	4	4282	B 29#	
853 AH 58		B	TROX	4	4286	B -4I	
854 AH 59		BIN	ERO2,6	5	4290	B 29Z 6	
855 AH 60		B	TYPI-031	4	4295	B 21Y	
856 AH 61							
857 AH 62	ERO2	B	UPX1	4	4299	B 28I	
858 AH 63		B	TRES2	4	4303	B 27I	
859 AH 64		B	LOOPCK	4	4307	B J39	
860 AH 65							
861 AH 66							
862 AH 67							
863 AH 68							
864 AH 69							
865 AH 70							
866 AH 71							
867 AH 72							
868 AH 73							
869 AH 74							
870 AH 75							

ROUTINE NO. 21
TEST BRANCH ON READ ERROR
WITH NORMAL READ INV CARD
FOLLOWED BY OVLP TAPE READ
AND THEN B&I06

ROUTINE NO. 22
TEST BRANCH ON READ ERROR
WITH OVLP READ INV CARD FOLLOWED
BY OVERLAP TAPE READ
AND THEN B&I06

SET ROUT START
ADDR IN Z02-Z04
RESET XR 1

1410/7010-1401 TOPSY COMPATIBILITY TEST

M014

SFX CT LDCN INSTRUCTION

SEQ PG LIN LABEL OP OPERANDS

SEQ	PG	LIN	LABEL	OP	OPERANDS	SFX	CT	LDCN	INSTRUCTION
871	AH	76			TPRINA, GORT&006				
872	AH	77	TRES1	B	CROX		7	4326	M R9/ -5V
873	AH	78		B	TROX		4	4333	B J0H
874	AH	79		B	ER01, &		4	4337	B -4I
875	AH	80		B	TYPI-031		5	4341	B 35+ &
876	AH	81		B	UPX1		4	4346	B Q1V
877	AH	82	ER01	B	TRES1		4	4350	B Z8I
878	AH	83		B	LOOPCK		4	4354	B 33I
879	AH	84		B			4	4358	B J39
880	AH	85							
881	AH	86							

ALTER TP RD INST
 READ CARD
 READ TAPE
 CK FOR CD RD ERR
 FAILED TO BR ON
 READ ERROR
 UP XR 1
 EXECUTE AGAIN
 CK FOR LOOP

1410/7010-1401 TOPSY COMPATIBILITY TEST

SEQ PG	LIN	LABEL	OP	OPERANDS	SFX	CT	LOCN	INSTRUCTION
882	AH 88		JOB	1410/7010-1401 TOPSY COMPATIBILITY TEST				
883	AH 90			ROUTINE NO. 23				
884	AH 91			TEST OVERLAP TAPE WRITE				
885	AH 92			FOLLOWED BY OVERLAP TAPE READ				
886	AH 93							
887	AH 94	RN23	NOP	*E005				
888	AH 95		SAR	POST	4		4352	V 37+
889	AH 96		LCA	ZZZ,0089	4		4356	Q Z04
890	AH 97		MCM	CC2,GOCKTR&003	7		4370	L B3X 089
891	AH 98		MCM	CC1,GORTE&006	7		4377	M 60T <4/
892	AH 99		MCM	CC1,GOUT&006	7		4384	M 60+ -5V
893	AI 00		MCM	CC2,GOCKTME019	7		4391	M 60# -9V
894	AI 01		B	TWOX	7		4398	M 60T KOV
895	AI 02	TWTR	B	TROX	4		4405	B -7M
896	AI 03		BER	TYPI	4		4409	B -4T
897	AI 04				5		4413	B Q4Z L
898	AI 05							
899	AI 06							
900	AI 07							
901	AI 08							
902	AI 09							
903	AI 10							
904	AI 11		B	CKTW	4		4418	B J7T
905	AI 12		B	TYPI-031	4		4422	B Q1Y
906	AI 13		B	CKTR	4		4426	B K2S
907	AI 14		B	TYPI-031	4		4430	B Q1Y
908	AI 15		B	UPX1	4		4434	B Z8T
909	AI 16		B	TWTR	4		4438	B 40V
910	AI 17		B	LOOPCK	4		4442	B J39
911	AI 18							
912	AI 19							
913	AI 20							
914	AI 21							
915	AI 22							
916	AI 23							
917	AI 24							
918	AI 25							
919	AI 26							
920	AI 27							
921	AI 28							
922	AI 29							
923	AI 30	TWCR	B	TWOX	4		4475	B -7M
924	AI 31		B	CROX	4		4479	B J04
925	AI 32		BER	TYPI	5		4483	B Q4Z L
926	AI 33		BIN	TYPI,6	5		4488	B Q4Z 6
927	AI 34		B	CKTW	4		4493	B J7T
928	AI 35		B	TYPI-031	4		4497	B Q1Y
929	AI 36		B	CKCR	4		4501	B K6/
930	AI 37		B	TYPI-031	4		4505	B Q1Y
931	AI 38		B	UPX1	4		4509	B Z8T

ROUTINE NO. 24
TEST OVERLAP TAPE WRITE
FOLLOWED BY OVERLAP CARD READ

917	AI 24		NOP	*E005				
918	AI 25		SAR	POST	4		4446	V 45U
919	AI 26		LCA	ZZZ,0089	4		4450	Q Z04
920	AI 27		MCM	TPRINA,GOUT&006	7		4454	L B3X 089
921	AI 28		MCM	CC2,GOCKTME019	7		4451	M R9/ -9V
922	AI 29		B	TWOX	7		4458	M 60T KOV
923	AI 30		B	CROX	4		4475	B -7M
924	AI 31		BER	TYPI	4		4479	B J04
925	AI 32		BIN	TYPI,6	5		4483	B Q4Z L
926	AI 33		B	CKTW	5		4488	B Q4Z 6
927	AI 34		B	TYPI-031	4		4493	B J7T
928	AI 35		B	REC WRITTEN DOES	4		4497	B Q1Y
929	AI 36		B	NOT COMPARE				
930	AI 37		B	CK CARD READ	4		4501	B K6/
931	AI 38		B	INCORRECT READ	4		4505	B Q1Y
			B	UP XR 1	4		4509	B Z8T

1410/7010-1401 TOPSY COMPATIBILITY TEST

SFX CT LOCN INSTRUCTION

SEQ PG LIN LABEL OP OPERANDS

982	AI	89	NOP	*6005	SET ROUT START	4	4655	V	56T
983	AI	90	SAR	POST	ADDR IN Z02-Z04	4	4659	Z	Z04
984	AI	91	LCA	ZZZ,0089	RESET XR 1	7	4653	L	83X 089
985	AI	92	MCH	CC1,GOUT&006	ALTER TP WR INST	7	4670	M	60# -9V
986	AI	93	MCH	CC2,GOCKTME019		7	4677	M	60T <DV
987	AI	94	B	TWOX	WRITE TAPE	4	4684	B	-7M
988	AI	95	B	TWOX	WRITE TAPE	4	4688	B	-7M
989	AI	96	BER	TYPI	TAPE WRITE ERR	5	4692	B	Q4Z L
990	AI	97	B	CKTW	CK WRITE	4	4697	B	J7T
991	AI	98	B	TYPI-031	2ND REC WRITTEN	4	4701	B	Q1Y
992	AI	99	B	UPXI	DOES NOT COMPARE	4	4705	B	Z8T
993	AJ	00	B	TMTW	UP XR 1	4	4709	B	58J
994	AJ	01	B	LOOPCK	EXECUTE AGAIN	4	4713	B	J39
995	AJ	02	B		CK FOR LOOP	4			
996	AJ	03							
997	AJ	04							
998	AJ	05							
999	AJ	06							
1000	AJ	07							

ROUTINE NO. 28
TEST OVERLAP TAPE READ
FOLLOWED BY OVERLAP TAPE WRITE

1001	AJ	08	NOP	*6005	SET ROUT. START	4	4717	V	72V
1002	AJ	09	SAR	POST	ADDR IN Z02-Z04	4	4721	Z	Z04
1003	AJ	10	LCA	ZZZ,0089	RESET XR 1	7	4725	L	83X 089
1004	AJ	11	MCH	CC1,GORT&006	ALTER TP RD INST	7	4732	M	60# -5V
1005	AJ	12	MCH	CC1,GOUT&006	ALTER TP WR INST	7	4739	M	60# -9V
1006	AJ	13	MCH	CC2,GOCKTR&003	ALTER CK INSTR	7	4746	M	60T K4/
1007	AJ	14	MCH	CC2,GOCKTME019		7	4753	M	60T K0V
1008	AJ	15	B	TROX	READ TAPE	4	4760	B	-4T
1009	AJ	16	B	TWOX	WRITE TAPE	4	4764	B	-7M
1010	AJ	17	BER	TYPI	CK FOR TAPE ERR	4	4758	B	Q4Z L
1011	AJ	18			ERR TYPE HERE	5			
1012	AJ	19			WILL NORMALLY				
1013	AJ	20			IND TP WR ERROR				
1014	AJ	21			THIS WILL BE TP				
1015	AJ	22			RD ERR IF WRITE				
1016	AJ	23			TP IS NOT AVAIL				
1017	AJ	24			CK READ	4	4773	B	K2S
1018	AJ	25	B	CKTR	INCORRECT READ	4	4777	B	Q1Y
1019	AJ	26	B	TYPI-031	CK WRITE	4	4781	B	J7T
1020	AJ	27	B	CKTW	REC WRITTEN DOES	4	4785	B	Q1Y
1021	AJ	28	B	TYPI-031	NOT COMPARE				
1022	AJ	29			UP XR 1	4	4789	B	Z8T
1023	AJ	30	B	UPXI	EXECUTE AGAIN	4	4793	B	76F
1024	AJ	31	B	TRTW	CK FOR LOOP	4	4797	B	J39
1025	AJ	32	B	LOOPCK					
1026	AJ	33							
1027	AJ	34							
1028	AJ	35							
1029	AJ	36							
1030	AJ	37							
1031	AJ	38							

ROUTINE NO. 29
TEST OVERLAP TAPE READ
FOLLOWED BY OVERLAP CARD READ

1410/7010-1401 TOPSY COMPATIBILITY TEST

SFX CT LOCN INSTRUCTION

SEQ PG LIN LABEL OP OPERANDS

SEQ	PG	LIN	LABEL	OP	OPERANDS	SFX	CT	LOCN	INSTRUCTION
1032	AJ	39		NOP	*E005			4801	V 80Z
1033	AJ	40		SAR	POST			4805	Q Z04
1034	AJ	41		LCA	ZZZ,0089			4809	L B3X 089
1035	AJ	42		MCW	TPRINA,GORT&006			4816	M R9J -5V
1036	AJ	43	TRCR	B	TROX			4823	B -4I
1037	AJ	44		B	CROX			4827	B JOW
1038	AJ	45		BER	TYPI			4831	B Q4Z L
1039	AJ	46		BIN	TYPI,6			4836	B Q4Z E
1040	AJ	47		B	CKCR			4841	B K6I
1041	AJ	48		B	TYPI-031			4845	B Q1Y
1042	AJ	49		B	UPXI			4849	B Z8I
1043	AJ	50		B	TRCR			4853	B B2I
1044	AJ	51		B	LOOPCK			4857	B U39
1045	AJ	52							
1046	AJ	53							
1047	AJ	54							
1048	AJ	55							
1049	AJ	56							
1050	AJ	57							
1051	AJ	58							
1052	AJ	59							
1053	AJ	60							
1054	AJ	61							
1055	AJ	62							
1056	AJ	63							
1057	AJ	64							
1058	AJ	65							
1059	AJ	66							
1060	AJ	67							
1061	AJ	68							
1062	AJ	69							
1063	AJ	70							
1064	AJ	71							
1065	AJ	72							
1066	AJ	73							
1067	AJ	74							
1068	AJ	75							
1069	AJ	76							
1070	AJ	77							
1071	AJ	78							
1072	AJ	79							
1073	AJ	80							
1074	AJ	81							
1075	AJ	82							
1076	AJ	83							
1077	AJ	84	NUMC1	MCW	CC1,GORT&006			4923	V 93I
1078	AJ	85	TRPF	B	CC2,GOCKTR&003			4927	Q Z04
1079	AJ	86		B	TROX			4931	L B3X 089
1080	AJ	87		BER	PFOX			4938	B 96U T06 V
1081	AJ	88		B	TYPI			4946	M R9X -5V
					CKTR			4953	M E1S K4I
								4950	B 97Y
								4954	M E0I -5V
								4971	M E0I K4I
								4978	B -4I
								4982	B J5S
								4986	B Q4Z L
								4991	B K2S

ROUTINE NO. 30
TEST OVERLAP TAPE READ
FOLLOWED BY OVERLAP CARD PUNCH

ROUTINE NO. 31
TEST OVERLAP TAPE READ
FOLLOWED BY PRINT

SFX CT LDCN INSTRUCTION

OP ERANDS

SEQ PG LIN LABEL OP

1082 AJ 89 B TYP1-031
 1083 AJ 90 B UPX1
 1084 AJ 91 B TRPF
 1085 AJ 92 B LOOPCK
 1086 AJ 93
 1087 AJ 94
 1088 AJ 95
 1089 AJ 96
 1090 AJ 97
 1091 AJ 98
 1092 AJ 99
 1093 AK 00
 1094 AK 01
 1095 AK 02
 1096 AK 03
 1097 AK 04
 1098 AK 05
 1099 AK 06
 1100 AK 07
 1101 AK 08
 1102 AK 09
 1103 AK 10
 1104 AK 11
 1105 AK 12
 1106 AK 13
 1107 AK 14
 1108 AK 15
 1109 AK 16
 1110 AK 17
 1111 AK 18
 1112 AK 19
 1113 AK 20
 1114 AK 21
 1115 AK 22
 1116 AK 23
 1117 AK 24
 1118 AK 25
 1119 AK 26
 1120 AK 27
 1121 AK 28
 1122 AK 29
 1123 AK 30
 1124 AK 31
 1125 AK 32
 1126 AK 33
 1127 AK 34
 1128 AK 35
 1129 AK 36
 1130 AK 37
 1131 AK 38

ROUTINE NO. 32
 TEST OVERLAP TAPE READ
 FOLLOWED BY OVERLAP TAPE READ

*E005
 NOP POST
 SAR ZZZ,0089
 LCA CCI,GORT&006
 MCM CC2,GCKTR&003
 B TRTR
 B TROX
 B TROX
 BER TYP1
 B CKTR
 B TYP1-031
 B UPX1
 B TRTR
 B LOOPCK

INCORRECT TP RD
 UP XR 1
 EXECUTE AGAIN
 CK FOR LOOP

SET ROUT. START
 ADDR IN Z02-Z04
 RESET XR 1
 ALTER TP RD INSTR
 ALTER CK INSTR
 READ TAPE
 READ TAPE
 RD ERR ON 2ND RD
 CK READ
 INCORRECT READ
 ON 2ND TAPE READ
 UP XR 1
 EXECUTE AGAIN
 CK FOR LOOP

4 4995 B Q1Y
 4 4999 B Z8T
 4 5003 B 97Y
 4 5007 B J39
 4 5011 V #1Z
 4 5015 Q Z04
 7 5019 L B3X 089
 7 5026 M E04 -5V
 7 5033 M E0T K47
 4 5040 B -4T
 4 5044 B -4T
 5 5048 B Q4Z L
 4 5053 B K2S
 4 5057 B Q1Y
 4 5051 B Z8T
 4 5055 B #44
 4 5059 B J39

ROUTINE NO. 33
 TEST OVERLAP CARD READ
 FOLLOWED BY OVERLAP TAPE WRITE

*E005
 NOP POST
 SAR TPRINA,GOUT&006
 MCM TPOTA,GCKTWE019
 LCA ZZZ,0089
 B CRTM
 B YMOX
 BIN TYP1,G
 BER TYP1
 B CKCR
 B TYP1-031
 B CKTH
 B TYP1-031
 B UPX1
 B CRTM
 B LOOPCK

SET ROUT. START
 ADDR IN Z02-Z04
 ALTER TP WR INSTR
 ALTER CK INSTR
 RESET XR 1
 READ CARD
 WRITE TAPE
 CD READ ERROR
 TP WRITE ERROR
 CK CD READ
 INCORRECT CD RD
 CK TP WRITE
 REC WRITTEN DOES
 NOT COMPARE
 UP XR 1
 EXECUTE AGAIN
 CK FOR LOOP

4 5073 V #8/
 4 5077 Q Z04
 7 5081 M R9/ -9V
 7 5088 M E0M K0V
 7 5095 L B3X 089
 4 5102 B J0M
 4 5106 B -7H
 5 5110 B Q4Z E
 5 5115 B Q4Z L
 4 5120 B K67
 4 5124 B Q1Y
 4 5128 B J7T
 4 5132 B Q1Y
 4 5136 B Z8T
 4 5140 B 70S
 4 5144 B J39

ROUTINE NO. 34

1410/7010-1401 TOPSY COMPATIBILITY TEST

SFX CT LOCN INSTRUCTION

SEQ PG LIN LABEL OP OPERANDS

1132 AK 39
1133 AK 40
1134 AK 41
1135 AK 42
1136 AK 43
1137 AK 44
1138 AK 45
1139 AK 46
1140 AK 47
1141 AK 48
1142 AK 49
1143 AK 50
1144 AK 51
1145 AK 52
1146 AK 53
1147 AK 54
1148 AK 55
1149 AK 56
1150 AK 57
1151 AK 58
1152 AK 59
1153 AK 60
1154 AK 61
1155 AK 62
1156 AK 63
1157 AK 64
1158 AK 65
1159 AK 66
1160 AK 67
1161 AK 68
1162 AK 69
1163 AK 70
1164 AK 71
1165 AK 72
1166 AK 73
1167 AK 74
1168 AK 75
1169 AK 76
1170 AK 77
1171 AK 78
1172 AK 79
1173 AK 80
1174 AK 81
1175 AK 82
1176 AK 83
1177 AK 84
1178 AK 85
1179 AK 86
1180 AK 87
1181 AK 88

TEST OVERLAP CARD READ
FOLLOWED BY OVERLAP TAPE READ

*E005
NOP POST
SAR ZZZ,0089
LCA TPRINA,GORT&006
MCW TPOITA,GCKTR&003
MCW
B
CRTR
B
B
BIN TYP1,&
BER TYP1
B CKTR
B TYP1-031
B UPX1
B CRTR
B LOOPCK

SET ROUT. START
ADDR IN Z02-Z04
RESET XR 1
ALTER TP RD INSTR
ALTER CK INSTR
READ CARD
READ TAPE
CD READ ERROR
TP READ ERROR
CK TP READ
INCORRECT TP RD
UP XR 1
EXECUTE AGAIN
CK FOR LOOP

4 5148 V /5W
4 5152 Q Z04
7 5156 L B3X 089
7 5153 M R9/ -5V
7 5170 M 60W K47
4 5177 B J0W
4 5181 B -4T
5 5185 B Q4Z &
5 5190 B Q4Z L
4 5195 B K2S
4 5199 B Q1Y
4 5203 B Z8T
4 5207 B /7X
4 5211 B U39

ROUTINE NO. 35
TEST OVERLAP CARD READ
FOLLOWED BY OVERLAP CARD PUNCH

1154 AK 61
1155 AK 62
1156 AK 63
1157 AK 64
1158 AK 65
1159 AK 66
1160 AK 67
1161 AK 68
1162 AK 69
1163 AK 70
1164 AK 71
1165 AK 72
1166 AK 73
1167 AK 74
1168 AK 75
1169 AK 76
1170 AK 77
1171 AK 78
1172 AK 79
1173 AK 80
1174 AK 81
1175 AK 82
1176 AK 83
1177 AK 84
1178 AK 85
1179 AK 86
1180 AK 87
1181 AK 88

ROUTINE NO. 35
TEST OVERLAP CARD READ
FOLLOWED BY OVERLAP CARD PUNCH

*E005
NOP POST
SAR ZZZ,0089
LCA
B
CRPC
B
B
BIN TYP1,&
BIN TYP1,-
BIN TYP1,-
B CKCR
B TYP1-031
B UPX1
B CRPC
B LOOPCK

SET ROUT. START
ADDR IN Z02-Z04
RESET XR 1
READ CARD
PUNCH CARD
CD READ ERR
PD
PUNCH ERROR
CK READ
INCORRECT CD RD
UP XR 1
EXECUTE AGAIN
CK FOR LOOP

4 5215 V S2T
4 5219 Q Z04
7 5223 L B3X 089
4 5230 B J0W
4 5234 B J2Z &
5 5238 B J4Z &
5 5243 B J4Z -
5 5248 B J4Z -
4 5253 B K6/
4 5257 B Q1Y
4 5251 B Z8T
4 5255 B S3#
4 5259 B U39

ROUTINE NO. 36
TEST OVERLAP CARD READ
FOLLOWED BY PRINT

1173 AK 80
1174 AK 81
1175 AK 82
1176 AK 83
1177 AK 84
1178 AK 85
1179 AK 86
1180 AK 87
1181 AK 88

ROUTINE NO. 36
TEST OVERLAP CARD READ
FOLLOWED BY PRINT

*E005
NOP POST
SAR ZZZ,0089
LCA
B
CRPF
B
B
BIN TYP1,&
BIN TYP1,#
B CKCR

SET ROUT. START
ADDR IN Z02-Z04
RESET XR 1
READ CARD
PRINT
CD RD ERROR
PRINT ERROR
CK CD READ

4 5273 V S8/
4 5277 Q Z04
7 5281 L B3X 089
4 5288 B J0W
4 5292 B J5S
5 5296 B Q4Z &
5 5301 B Q4Z #
4 5306 B K6/

SEQ PG LIN LABEL OP OPERANDS SFX CT LOCN INSTRUCTION

1182 AK 89 8 TYP1-031 INCORRECT CD RD 4 5310 B Q1Y
 1183 AK 90 8 UPX1 UP XR 1 4 5314 B Z8T
 1184 AK 91 8 CRPF EXECUTE AGAIN 4 5318 B S8Y
 1185 AK 92 8 LOOPCK CK FOR LOOP 4 5322 B J39

ROUTINE NO. 37
 TEST OVERLAP CARD READ
 FOLLOWED BY OVERLAP CARD READ

1191 AK 98 8 NOP *E005 SET ROUT. START 4 5326 V T3J
 1192 AK 99 8 SAR POST ADDR IN Z02-Z04 4 5330 Q Z04
 1193 AL 00 8 LCA ZZZ,0089 RESET XR 1 7 5334 L B3X 089
 1194 AL 01 8 CRCR 8 CROX READ CARD 4 5341 B J0W
 1195 AL 02 8 8 CROX READ CARD 4 5345 B J0W
 1196 AL 03 8 BIN TYP1,& RD ERR OV 2ND RD 5 5349 B Q4Z 8
 1197 AL 04 8 8 CKCR CK READ 4 5354 B K67
 1198 AL 05 8 8 TYP1-031 INCORRECT READ 4 5358 B Q1Y
 1200 AL 07 8 8 UPX1 ON 2ND CARD READ 4 5362 B Z8T
 1201 AL 08 8 8 CRCR EXECUTE AGAIN 4 5356 B I47
 1202 AL 09 8 8 LOOPCK CK FOR LOOP 4 5370 B J39

ROUTINE NO. 38
 TEST OVERLAP CARD PUNCH
 FOLLOWED BY OVERLAP TAPE WRITE

1209 AL 16 8 NOP *E005 SET ROUT. START 4 5374 V T8S
 1210 AL 17 8 SAR POST ADDR IN Z02-Z04 4 5378 Q Z04
 1211 AL 18 8 LCA ZZZ,0089 RESET XR 1 7 5382 L B3X 089
 1212 AL 19 8 MCH TPTB,GOCKTWE019 ALTER CK INSTR 7 5389 V 60Z QDV
 1213 AL 20 8 MCH IPRINB,GOUT&006 ALTER TP WR INST 7 5396 V R9U -9V
 1214 AL 21 8 8 PCOX PUNCH CARD 4 5403 B J2Z
 1215 AL 22 8 8 TMOX WRITE TAPE 4 5407 B -7M
 1216 AL 23 8 BIN PUNCH ERROR 5 5411 B Q4Z -
 1217 AL 24 8 BER TYP1, TAPE WRITE ERR 5 5416 B Q4Z L
 1218 AL 25 8 8 CKTW CK TP WRITE 4 5421 B J7T
 1219 AL 26 8 8 TYP1-031 REC WRITTEN DOES 4 5425 B Q1Y
 1220 AL 27 8 8 NOT COMPARE
 1221 AL 28 8 8 UPX1 UP XR 1 4 5429 B Z8T
 1222 AL 29 8 8 PCTW EXECUTE AGAIN 4 5433 B U0T
 1223 AL 30 8 8 LOOPCK CK FOR LOOP 4 5437 B J39
 1224 AL 31
 1225 AL 32
 1226 AL 33
 1227 AL 34
 1228 AL 35
 1229 AL 36
 1230 AL 37
 1231 AL 38 8 NOP *E005 SET ROUT. START 4 5441 V U4Z

ROUTINE NO. 39
 TEST OVERLAP CARD PUNCH
 FOLLOWED BY OVERLAP TAPE READ

1410/7010-1401 TOPSY COMPATIBILITY TEST

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SFX CT LOCN INSTRUCTION

OPERANDS

LABEL OP

SEQ PG LIN

1232	AL	39	SAR	POST	ADDR IV Z02-Z04	4	5445	2	Z04
1233	AL	40	LCA	ZZZ,0089	RESET XR 1	7	5449	L	B3X 089
1234	AL	41	MCH	IPRINB,GORI&006	ALTER TP RD INST	7	5456	M	R9U -5V
1235	AL	42	MCH	TPDTB,GOCKTR&003	ALTER CK INSTR	7	5453	M	R0Z K47
1236	AL	43	B	PCOX	PUNCH CARD	4	5470	B	J2Z
1237	AL	44	B	TROX	READ TAPE	4	5474	B	-4T
1238	AL	45	BIN	TYPI,-	PUNCH ERROR	5	5478	B	Q4Z -
1239	AL	46	BER	TYPI	TAPE READ ERROR	5	5483	B	Q4Z L
1240	AL	47	B	CKTR	CK TP READ	4	5488	B	K2S
1241	AL	48	B	TYPI-031	INCORRECT TP RD	4	5492	B	Q1Y
1242	AL	49	B	UPXI	UP XR 1	4	5496	B	Z8T
1243	AL	50	B	PCTR	EXECUTE AGAIN	4	5500	B	J7#
1244	AL	51	B	LOOPCK	CK FOR LOOP	4	5504	B	J39

ROUTINE NO. 40
TEST OVERLAP CARD PUNCH
FOLLOWED BY OVERLAP CARD READ

1250	AL	57	NOP	*&005	SET ROUT. START	4	5508	V	V1W
1251	AL	58	SAR	POST	ADDR IV Z02-Z04	4	5512	2	Z04
1252	AL	59	LCA	ZZZ,0089	RESET XR 1	7	5516	L	B3X 089
1253	AL	60	B	PCOX	PUNCH CARD	4	5523	B	J2Z
1254	AL	61	B	CRDX	READ CARD	4	5527	B	J0W
1255	AL	62	BIN	TYPI,-	PUNCH ERROR	5	5531	B	Q4Z -
1256	AL	63	BIN	TYPI,&	CD READ ERROR	5	5536	B	Q4Z &
1257	AL	64	B	CKCR	CK CD READ	4	5541	B	K67
1258	AL	65	B	TYPI-031	INCORRECT CD RD	4	5545	B	Q1Y
1259	AL	66	B	UPXI	UP XR 1	4	5549	B	Z8T
1260	AL	67	B	PCCR	EXECUTE AGAIN	4	5553	B	V2T
1261	AL	68	B	LOOPCK	CK FOR LOOP	4	5557	B	J39

ROUTINE NO. 41
TEST OVERLAP CARD PUNCH
FOLLOWED BY PRINT

1268	AL	75	NOP	*&005	SET ROUT. START	4	5561	V	V6Z
1269	AL	76	SAR	POST	ADDR IN Z02-Z04	4	5565	2	Z04
1270	AL	77	LCA	ZZZ,0089	RESET XR 1	7	5569	L	B3X 089
1271	AL	78	B	PCOX	PUNCH CARD	4	5576	B	J2Z
1272	AL	79	B	PFOX	PRINT	4	5580	B	J5S
1273	AL	80	BIN	TYPI,-	PUNCH ERROR	5	5584	B	Q4Z -
1274	AL	81	BIN	TYPI,*	PRINT ERROR	5	5589	B	Q4Z +
1275	AL	82	B	UPXI	UP XR 1	4	5594	B	Z8T
1276	AL	83	B	PCPF	EXECUTE AGAIN	4	5598	B	V7W
1277	AL	84	B	LOOPCK	CK FOR LOOP	4	5602	B	U39

ROUTINE NO. 42

SEQ PG	LIN	LABEL	OP	OPERANDS	SFX	CT	LOCN	INSTRUCTION
TEST OVERLAP CARD PUNCH FOLLOWED BY OVERLAP CARD PUNCH								
1282	AL 89		NOP	*6005				
1283	AL 90		SAR	POST	4	5606	V W1J	SET ROUT. START
1284	AL 91		LCA	ZZZ,0089	4	5610	Q Z04	ADDR IN Z02-Z04
1285	AL 92	PCPC	B	PCOX	7	5614	L B3X 089	RESET XR 1
1286	AL 93		B	PCOX	4	5621	B J2Z	PUNCH CARD
1288	AL 94		BIN	TYPI,-	4	5625	B J2Z	PUNCH CARD
1289	AL 95		B	UPX1	5	5629	B Q4Z -	PUN ERR-2ND CD
1290	AL 96		B	PCPC	4	5634	B Z8T	UP XR 1
1291	AL 97		B	LOOPCK	4	5638	B W2/	EXECUTE AGAIN
1292	AL 98		B		4	5642	B U39	CK FOR LOOP
1294	AM 01							
1295	AM 02							
1296	AM 03							
1297	AM 04							
1298	AM 05							
1299	AM 06							
ROUTINE NO. 43 TEST PRINT FOLLOWED BY OVERLAP TAPE WRITE								
1300	AM 07		NOP	*6005				
1301	AM 08		SAR	POST	4	5646	V W5J	SET ROUT. START
1302	AM 09		LCA	ZZZ,0089	4	5650	Q Z04	ADDR IN Z02-Z04
1303	AM 10		MCM	TPRINC,GOOUT&006	7	5654	L B3X 089	RESET XR 1
1304	AM 11		MCM	TPDIC,GOCKT&019	7	5651	M R9X -9V	ALTER TP WR INSTR
1305	AM 12	PFTW	B	PFOX	7	5658	M E1S KDV	ALTER CK INSTR
1306	AM 13		B	TWOX	4	5675	B J5S	PRINT
1307	AM 14		BIN	TYPI,*	4	5679	B -7W	WRITE TAPE
1308	AM 15		BER	TYPI	5	5683	B Q4Z #	PRINT ERR
1309	AM 16		B	CKTW	5	5688	B Q4Z L	TP WR ERR
1310	AM 17		B	TYPI-031	4	5693	B J7T	CK TP WRITE
1311	AM 18		B		4	5697	B Q1Y	REC WRITTEN DOES
1312	AM 19		B	UPX1	4	5701	B Z8T	NOT COMPARE
1313	AM 20		B	PFTW	4	5705	B W7V	UP XR 1
1314	AM 21		B	LOOPCK	4	5709	B U39	EXECUTE AGAIN
1315	AM 22							CK FOR LOOP
1316	AM 23							
ROUTINE NO. 44 TEST PRINT FOLLOWED BY OVERLAP TAPE READ								
1321	AM 28		NOP	*6005				
1322	AM 29		SAR	POST	4	5713	V X2/	SET ROUT. START
1323	AM 30		LCA	ZZZ,0089	4	5717	Q Z04	ADDR IN Z02-Z04
1324	AM 31		BCE	NUMC3,1306,N	7	5721	L B3X 089	RESET XR 1
1325	AM 32		MCM	TPRINC,GORI&006	8	5728	B X5J T06 V	CK FOR NUM CHAIN
1326	AM 33		MCM	TPDIC,GOCKTR&003	7	5736	M R9X -5V	ALTER TP RD INSTR
1327	AM 34		B	PFTW	7	5743	M E1S K4/	ALTER CK INSTR
1328	AM 35	NUMC3	MCM	CCI,GORTE&006	4	5750	B X6Y	ALTER TP RD INSTR
1329	AM 36		MCM	CC2,GOCKTR&003	7	5754	M E04 -5V	ALTER CK INSTR
1330	AM 37	PFTW	B	PFOX	7	5761	M E0T K4/	PRINT
1331	AM 38		B	TROX	4	5768	B J5S	READ TAPE

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1410/7010-1401 TOPSY COMPATIBILITY TEST

SEQ PG LIN	LABEL	OP	OPERANDS	SFX	CT	LOCN	INSTRUCTION
1382 AM 89		BCE	REPEAT, IAD3, 1				
1383 AM 90		LCA	BIGM, 0007				
1384 AM 91		SW	0008				
1385 AM 92		MCM	KTD, MD1410-031, W				
1386 AM 93		H					
1387 AM 94							
1388 AM 95	BIGM	DCW	@J00400 @				
1389 AM 96	REPEAT	NOP	2008				
1390 AM 97		SAR	POST				
1391 AM 98		H					
1392 AM 99							
1393 AN 00							
1394 AN 01							

TEST FOR REPEAT	SFX	CT	LOCN	INSTRUCTION
	8		5914	B Z4Z #03 1
	7		5922	L Z4Y 007
	4		5929	P 008
TYPE MESSAGE	8		5933	W RTO E4/ W
HALT TO SET COMP	1		5941	.
SW TO 1410/7010				
SET FOR	7		5948	
RESTART	4		5949	V -08
HALT PRESS COMP	4		5953	Z Z04
RESET AND START				
FOR NEXT PASS	1		5957	.

1410/7010-1401 TOPSY COMPATIBILITY TEST

SEQ PG LIN	LABEL	OP	OPERANDS	SFX	CT	LOCN	INSTRUCTION
1395 AN 03							
1396 AN 05	CKBUSY	SBR	JOB				
1397 AN 06		A	CYCLEX&003				
1398 AN 07		MA	ONE,CYCNT				
1399 AN 08	CYCLEX	B	BKCYC,CYCLEX&003				
1400 AN 09	BKCYC	DCM	0000				
1401 AN 10			@I9E@				
1402 AN 11	UPX1	SBR	UPXEX&003				
1403 AN 12		A	ONE,0089				
1404 AN 13		C	0089,ZZ5				
1405 AN 14		BU	UPXEX				
1406 AN 15		MA	ZZ4,UPXEX&003				
1407 AN 16	UPXEX	B	0000				
1408 AN 17							
1409 AN 18	EDFF	SBR	EOFFEX&003				
1410 AN 19		CU	%U1,R				
1411 AN 20	EDFFEX	B	0000				
1412 AN 21							
1413 AN 22	EDRR	SBR	EDRREX&003				
1414 AN 23		CU	%U2,R				
1415 AN 24	EDRREX	B	0000				
1416 AN 25							
1417 AN 26							
1418 AN 27	TROX	SBR	TROEX&003				
1419 AN 28		BCE	GORT,1291,1				
1420 AN 29		B	TROEX				
1421 AN 30	GORT	MCH	\$\$,WKAREA,R				
1422 AN 31		BEF	EOFF				
1423 AN 32	TROEX	B	0000				
1424 AN 33							
1425 AN 34							
1426 AN 35	TWOX	SBR	TWOEX&003				
1427 AN 36		BCE	TWOEX,7992,				
1428 AN 37		NOP					
1429 AN 38	GOUT	MCH	\$\$,WKAREA,W				
1430 AN 39		BEF	EORR				
1431 AN 40	TWOEX	B	0000				
1432 AN 41							
1433 AN 42							
1434 AN 43	CROX	SBR	CROEX&003				
1435 AN 44		BCE	GORC,1301,R				
1436 AN 45		B	CROEX				
1437 AN 46	GORC	SS	\$				
1438 AN 47		R					
1439 AN 48	CROEX	B	0000				
1440 AN 49							
1441 AN 50							
1442 AN 51	PCOX	SBR	PCDEX&003				
1443 AN 52		BCE	GOPC,1303,P				
1444 AN 53		B	PCOEX				

1410/7010-1401 TOPSY COMPATIBILITY TEST

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SEQ PG LIN LABEL OP OPERANDS SFX CT LDCN INSTRUCTION

1495 AD 04 SW WKAREA
 1496 AD 05 MCH RDCOMP-0716X1,WKAREA&019
 1497 AD 06 MCH WKAREA&019,WKAREA&018
 1498 AD 07 LCA GMWM,WKAREA&020
 1499 AD 08 B WTAPA
 1500 AD 09 CU XUL,B
 1501 AD 10 CU XUL,E
 1502 AD 11 MCH XUL,WKAREA,W
 1503 AD 12 NOP 0000
 1504 AD 13 BEF ENREEL
 1505 AD 14 BER TPWRER
 1506 AD 15
 1507 AD 16
 1508 AD 17
 1509 AD 18
 1510 AD 19
 1511 AD 20
 1512 AD 21
 1513 AD 22
 1514 AD 23
 1515 AD 24
 1516 AD 25
 1517 AD 26
 1518 AD 27
 1519 AD 28
 1520 AD 29
 1521 AD 30
 1522 AD 31
 1523 AD 32
 1524 AD 33
 1525 AD 34
 1526 AD 35
 1527 AD 36
 1528 AD 37
 1529 AD 38
 1530 AD 39
 1531 AD 40
 1532 AD 41
 1533 AD 42
 1534 AD 43
 1535 AD 44
 1536 AD 45
 1537 AD 46
 1538 AD 47
 1539 AD 48
 1540 AD 49
 1541 AD 50
 1542 AD 51
 1543 AD 52
 1544 AD 53

4 6328 , H0#
 7 6332 M BMW H1Z
 7 6339 M H1Z H1Y
 7 6346 L 68J H2#
 4 6353 B L6X
 5 6357 J XUL B
 5 6362 J XUL E
 8 6357 M XUL H0# M
 4 6375 V 000
 5 6379 B P2S K
 5 6384 B 44Y L

SET WM
 SET RECORD
 FDR WRITING
 LOAD GM WM
 GO TO WRITE TAPE
 BACKSPACE
 & SKIP
 WRITE RECORD
 EXTRA INSTR
 CK FOR EDR
 CK FOR WRITE ERR
 ERR TYPE HERE
 INDS THAT IO
 TRYS USING BKSP-
 SKIP HAVE BEEN
 MADE TO WR REC
 RESET ERR CNTR
 UP XR 1
 CK FOR 26 RECS
 WR NEXT REC
 WRITE EOF
 REWIND TAPE
 ROJTIME EXIT

7 6389 L E2V E2X
 7 6396 A ADV 089
 7 6403 C 089 E3S
 5 6410 B L2U /
 5 6415 U XUL M
 5 6420 J XUL R
 4 6425 B 000

TAPE WRITE ERROR ROUTINE
 THIS ROUTINE IS ENTERED WHENEVER
 A TAPE WRITE ERROR IS
 ENCOUNTERED WITHIN TEST ROUTINE

1519 AD 28 SBR TWREX&003
 1520 AD 29 SBR REDA&003
 1521 AD 30 LCA BRANCH,MODIFY
 1522 AD 31 B *6009
 1523 AD 32 SBR TWREX&003
 1524 AD 33 SBR REDA&003
 1525 AD 34 BIN ALTER,Q
 1526 AD 35 A ONE,TWRCNT
 1527 AD 36 A ONE,WRCNT
 1528 AD 37 C WRCNT,TEN
 1529 AD 38 BU MODIFY
 1530 AD 39 LCA ZZ,WRCNT
 1531 AD 40 BCE MODIFY,TADO,1
 1532 AD 41 SW 0000
 1533 AD 42 REDA
 1534 AD 43 CH
 1535 AD 44 CH
 1536 AD 45 CH
 1537 AD 46 SAR
 1538 AD 47 MCH
 1539 AD 48
 1540 AD 49
 1541 AD 50
 1542 AD 51
 1543 AD 52
 1544 AD 53

4 6429 H V6X
 4 6433 H NOV
 7 6437 L FOV N3V
 4 6444 B M5W
 4 6448 H V6X
 4 6452 H NOV
 5 6456 B U68 Q
 7 6451 A ADV F1#
 7 6458 A ADV E2X
 7 6475 C E2X F1S
 5 6482 B N3V /
 7 6487 L E2V E2X
 8 6494 B V3V #00 1
 4 6502 B 000
 1 6506 B
 1 6507 B
 1 6508 B
 1 6509 B
 4 6510 Q F2V
 8 6514 M XTO FIT M

SET EXIT AND
 REDUCE INSTR
 SET SW TO BR
 SET ROUTINE EXIT
 SET REDUCE INSTR
 CK FOR INQUIRY
 ADD 1
 TO ERROR CNTRS
 CK FOR 10 TRYS
 NOT 10 TRY AGAIN
 RESET ERR CNTR
 BYPASS ERR IND
 REDUCE
 ADDRESS
 AND
 STORE
 IN
 ERRLO
 TYPE ERR LOC

SEQ PG LIN LABEL OP OPERANDS SFX CT LCN INSTRUCTION

1545	AD	54		BCE	ERHA,TAD2,1	8	6522	B	V3U	#02	1
1546	AD	55		B	MODIFY		4	6530	B	V3V	
1547	AD	56	ERHA	H			1	6534			
1548	AD	57	MODIFY	NOP			4	6535		V15*	
1549	AD	58									
1550	AD	59									
1551	AD	60		MA	BK32,TWREX#003	7	6539	#	V7*	V5X	
1552	AD	61		B	TWREX	4	6546	B	V6J		
1553	AD	62	OVLPMD	LCA	NOP,MODIFY	7	6550	L	F0W	V3V	
1554	AD	63		MA	BK56,TWREX#003	7	6557	#	N7T	N5X	
1555	AD	64	TWREX	B	0000	4	6564	B	000		
1556	AD	65									
1557	AD	66	BK32	DCW	#16H#	3	6570				
1558	AD	67	BK56	DCW	#14D#	3	6573				
1559	AD	68									
1560	AD	69									
1561	AD	70									
1562	AD	71									
1563	AD	72									
1564	AD	73									
1565	AD	74									
1566	AD	75									
1567	AD	76									
1568	AD	77									
1569	AD	78									
1570	AD	79	TPRDR	SBR	TRREX#003	4	6574	H	J8T		
1571	AD	80		SBR	REDAD#003	4	6578	H	J5*		
1572	AD	81		B	BRANCH,MODIF	7	6582	L	F0V	J8U	
1573	AD	82		A	*#009	4	6589	B	D0/		
1574	AD	83		A	ONE,TRDCNT	4	6593	H	D8T		
1575	AD	84		C	ONE,RDCNT	4	6597	H	J5*		
1576	AD	85		BU	RDCNT,TEN	5	6601	B	J68	J	
1577	AD	86		LCA	MODIF	7	6606	A	ADV	F4U	
1578	AD	87		BCE	ZZ,MDCNT	7	6613	A	ADV	E2Z	
1579	AD	88	REDAD	SW	TRREX,TAD0,1	5	6620	C	E2Z	F1S	
1580	AD	89			0000	7	6627	B	J8J	/	
1581	AD	90									
1582	AD	91									
1583	AD	92									
1584	AD	93		SAR	ERRL	4	6655	J	F3Z		
1585	AD	94		MCH	#T0,ERRL-012,M	8	6659	M	#T0	F2X	M
1586	AD	95		BCE	ERH,TAD2,1	8	6667	B	J7Z	#02	1
1587	AD	96		B	TRREX	4	6675	B	D8*		
1588	AD	97	ERH	H		1	6679				
1589	AD	98	TRREX	B	0000	4	6680	B	J00		
1590	AD	99	MODIF	CU	#U1,B	5	6684	J	#U1	B	
1591	AP	00		NOP	OVLP#	4	6689	V	POU		
1592	AP	01									
1593	AP	02									
1594	AP	03		MA	BK26,TRREX#003	7	6693	#	F4X	J8T	

TAPE READ ERROR ROUTINE
THIS ROUTINE IS ENTERED #WHENEVER
A TAPE READ ERROR IS
ENCOUNTERED WITHIN TEST ROUTINE

1570	AD	79	TPRDR	SBR	TRREX#003	4	6574	H	J8T		
1571	AD	80		SBR	REDAD#003	4	6578	H	J5*		
1572	AD	81		B	BRANCH,MODIF	7	6582	L	F0V	J8U	
1573	AD	82		A	*#009	4	6589	B	D0/		
1574	AD	83		A	ONE,TRDCNT	4	6593	H	D8T		
1575	AD	84		C	ONE,RDCNT	4	6597	H	J5*		
1576	AD	85		BU	RDCNT,TEN	5	6601	B	J68	J	
1577	AD	86		LCA	MODIF	7	6606	A	ADV	F4U	
1578	AD	87		BCE	ZZ,MDCNT	7	6613	A	ADV	E2Z	
1579	AD	88	REDAD	SW	TRREX,TAD0,1	5	6620	C	E2Z	F1S	
1580	AD	89			0000	7	6627	B	J8J	/	
1581	AD	90									
1582	AD	91									
1583	AD	92									
1584	AD	93		SAR	ERRL	4	6655	J	F3Z		
1585	AD	94		MCH	#T0,ERRL-012,M	8	6659	M	#T0	F2X	M
1586	AD	95		BCE	ERH,TAD2,1	8	6667	B	J7Z	#02	1
1587	AD	96		B	TRREX	4	6675	B	D8*		
1588	AD	97	ERH	H		1	6679				
1589	AD	98	TRREX	B	0000	4	6680	B	J00		
1590	AD	99	MODIF	CU	#U1,B	5	6684	J	#U1	B	
1591	AP	00		NOP	OVLP#	4	6689	V	POU		
1592	AP	01									
1593	AP	02									
1594	AP	03		MA	BK26,TRREX#003	7	6693	#	F4X	J8T	

1410/7010-1401 TOPSY COMPATIBILITY TEST

SEQ PG LIN	LABEL	OP	OPERANDS	SFX	CT	LOCN	INSTRUCTION
1595 AP 04							
1596 AP 05	OVLPM	B	TRREX	4	6700	B 08*	GO TO EXIT
1597 AP 06		LCA	NOP,MODIF&005	7	6704	L FOW 08Z	SET SW TO NOP
1598 AP 07		MA	BK50,TRREX&003	7	6711	# F5* 08T	DEC ADDR BY 43
1599 AP 08		B	TRREX	4	6718	B 08*	GO TO EXIT
1600 AP 09							
1601 AP 10							
1602 AP 11							
1603 AP 12							
1604 AP 13							
1605 AP 14							
1606 AP 15							
1607 AP 16							
1608 AP 17							
1609 AP 18							
1610 AP 19							
1611 AP 20							
1612 AP 21							
1613 AP 22							
1614 AP 23							
1615 AP 24							
1616 AP 25							
1617 AP 26							
1618 AP 27							
1619 AP 28							
1620 AP 29							
1621 AP 30							
1622 AP 31							
1623 AP 32							
1624 AP 33							
1625 AP 34							
1626 AP 35							
1627 AP 36							
1628 AP 37							
1629 AP 38							
1630 AP 39							
1631 AP 40							
1632 AP 41							
1633 AP 42							
1634 AP 43							
1635 AP 44							
1636 AP 45							
1637 AP 46							
1638 AP 47							
1639 AP 48							
1640 AP 49							
1641 AP 50							
1642 AP 51							
1643 AP 52							
1644 AP 53							
1605 AP 14	ENREEL	SBR	ENREX&003	4	6722	H P4U	SET ROUTINE EXIT
1606 AP 15		LCA	NOP,EOFSW	7	6726	L FOW P5T	SET EOF SW
1607 AP 16		MCH	%TO,REELLEN-010,W	8	6733	% %TO F5/ #	TYPE MESSAGE
1609 AP 18	ENREX	B	0000	4	6741	B 000	ROUTINE EXIT
1617 AP 26	EOF1	SBR	EOFEX&003	4	6745	H Q1X	SET ROUTINE EXIT
1618 AP 27		SBR	INDEOF&003	4	6749	H P7M	SET REDUCE INSTR
1620 AP 29	EOFSW	B	INDEOF	4	6753	B P7T	THIS WILL BE A
1627 AP 36	INDEOF	BCE	%U1,R	5	6757	J %U1 %	BR TO IND ERR IF
1628 AP 37		SW	BRANCH,EOFSW	7	6752	L FOW P5T	EDR WAS NOT
1629 AP 38		CW	0001	4	6759	B 001	ENCTRD ON WRITE
1630 AP 39		CW	EOFSW, TAD0,1	8	6773	B Q1J #00 1	REWIND TAPE
1631 AP 40		CW	0000	4	6781	% 000	RESTORE SW
1632 AP 41		CW		1	6785	%	RETRY ROUTINE
1633 AP 42		CW		1	6786	%	BYPASS ERR IND
1634 AP 43		CW		1	6787	%	REDUCE
1635 AP 44		CW		1	6788	%	ADDRESS
1636 AP 45		CW		1	6789	%	AND
1637 AP 46		CW		1	6789	%	STORE
1638 AP 47		CW		1	6793	%	IN
1639 AP 48		CW		1	6793	%	EOFIN
1640 AP 49		CW		1	6793	%	WRITE MESSAGE
1641 AP 50		CW		1	6801	%	CK FOR ERR HALT
1642 AP 51		CW		1	6809	%	GO TO EXIT
1643 AP 52		CW		1	6813	%	ERROR HALT
1644 AP 53		CW		1	6814	%	ROUTINE EXIT
1638 AP 47	EOFH	H	0000	4	6814	B 000	ERROR ROUTINE
1639 AP 48	EOFEX	B	0000	4	6814	B 000	THIS ROUTINE IS ENTERED WHEN AN ERROR
1640 AP 49							IS ENCOUNTERED WITHIN TEST ROUTINE
1641 AP 50							TEST TAD0
1642 AP 51							IF 1 BYPASS ERR IND AND CK INQUIRY
1643 AP 52							IF 0 TYPE ERROR ADDRESS AND TEST
1644 AP 53							

1410/7010-1401 TOPSY COMPATIBILITY TEST

SEQ PG LIN	LABEL	OP	OPERANDS	SFX	CT	LOCN	INSTRUCTION
1645 AP 54							
1646 AP 55							
1647 AP 56							
1648 AP 57		SBR	TPEXIT&003	4		6818	H ROW
1649 AP 58		SBR	REDD&003	4		6822	H Q3X
1650 AP 59		BCE	LPC,TAD0,1	8		6826	B Q9Y #00 1
1651 AP 60	REDD	SW	0000	4		6834	0 000
1652 AP 61		CW		1		6838	0
1653 AP 62		CW		1		6839	0
1654 AP 63		CW		1		6840	0
1655 AP 64		SAR	ERRLOC	1		6841	0 J65
1656 AP 65		B	TYPBR	4		6845	B Q7X
1657 AP 66	TYPI	SBR	TPEXIT&003	4		6849	H ROW
1658 AP 67		SBR	REDADD&003	4		6853	H 26Y
1659 AP 68		BCE	LPC,TAD0,1	8		6857	B Q9Y #00 1
1660 AP 69	REDADD	SW	0000	4		6855	0 000
1661 AP 70		CW		1		6869	0
1662 AP 71		CW		1		6870	0
1663 AP 72		CW		1		6871	0
1664 AP 73		CW		1		6872	0
1665 AP 74		SAR	ERRLOC	1		6873	0 U66
1666 AP 75	TYPBR	MCW	ZTO,ERRLOC-006,W	8		6877	M ZTO J50 W
1667 AP 76		BCE	ERHALT,TAD2,1	8		6885	B Q9X #02 1
1668 AP 77		B	LPC	4		6893	B Q9Y
1669 AP 78	ERHALT H	H	ALTER,Q	1		6897	0
1670 AP 79	LPC	BIN	0000	5		6898	B U68 Q
1671 AP 80	TPEXIT B	B		4		6903	B 000
1672 AP 81							
1673 AP 82							
1674 AP 83							
1675 AP 84							
1676 AP 85							
1677 AP 86							
1678 AP 87							
1679 AP 88							
1680 AP 89	SVRES	SBR	SVRSEX&003	4		6907	H R7M
1681 AP 90		LCA	ZZZ,0089	7		6911	L B3X 089
1682 AP 91	MVIT	MN	0000&X2,0000&X3	7		6918	D 0-0 060
1683 AP 92		MZ	0000&X2,0000&X3	7		6925	V 0-0 060
1684 AP 93		BWZ	WM,0000&X2,1	8		6932	V R7X 0-0 1
1685 AP 94	UPXR	A	ONE,0089	7		6940	A ADV 089
1686 AP 95		MA	ZZ1,0094	7		6947	# E2# 094
1687 AP 96		MA	ZZ1,0099	7		6954	# E2# 099
1688 AP 97		C	0089,ZZ5	7		6951	C 089 81W
1689 AP 98		BU	MVIT	5		6968	B R1Y /
1690 AP 99	SVRSEX B	B	0000	4		6973	B 000
1691 AQ 00	WM	SM	0000&X3	4		6977	0 060
1692 AQ 01		B	UPXR	4		6981	B R4#

ROUTINES TO SAVE AND RESTORE STORAGE AREAS TO BE REPLACED WITH UNCOND. BR INSTRS USED WITH CERTAIN TEST ROUTINES

SET EXIT
 RESET XR 1
 MOVE NUM BITS
 MOVE ZV BITS
 CK FOR WM
 UP
 CK FOR 5 MOVES
 MOVE NEXT DIGIT
 ROUTINE EXIT
 SET WM
 UP XRS

1410/7010-1401 TOPSY COMPATIBILITY TEST

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SFX CT LOCN INSTRUCTION

OPERANDS

LABEL OP

SEQ PG LIN

1410/7010-1401 TOPSY COMPATIBILITY TEST

SEQ PG LIN	LABEL	OP	OPERANDS
1693 AQ 03	TPINS1	DCW	0XU0
1694 AQ 05	TPINS2	DCW	00U0
1695 AQ 06	TPRINA	DCW	00010
1696 AQ 07	TPRINA	DCW	01010
1697 AQ 08	TPRINB	DCW	02010
1698 AQ 09	TPRINC	DCW	03010
1699 AQ 10	CC1	DSA	0WKAREA
1700 AQ 11	CC2	DSA	0WKAREA079
1701 AQ 12	TPOTA	DSA	00080
1702 AQ 13	TPOTB	DSA	00180
1703 AQ 14	TPOTC	DSA	00280
1704 AQ 15	RESTA	B	1901
1705 AQ 16		DC	0
1706 AQ 17	POST	EQU	1904
1707 AQ 18		EQU	1905
1708 AQ 18	1905	DCW	0B000 0
1709 AQ 19	ZZ1	DCW	00010
1710 AQ 20	DMS	DCW	0NBNCNDNE0
1711 AQ 21	ZER3	DCW	00000
1712 AQ 22	CHTEST	DCW	0
1713 AQ 23	TWO	DCW	020
1714 AQ 24	Z10	DCW	00100
1715 AQ 25	TSBR	DCW	0XXX0
1716 AQ 26	XXX	DCW	0XXX0
1717 AQ 27	TSBRAN	DSA	0TSBR
1718 AQ 28	PRTHAM	DCW	0DISABLE 1403 PRINT HAMMER PRESS0
1719 AQ 29		DC	0 START0
1720 AQ 30	GMHM	DCW	0
1721 AQ 31	PRBMSG	DCW	001234567890
1722 AQ 32	IMSEG	DCW	0PRTRRTEST0
1723 AQ 33	ONE	DCW	010
1724 AQ 34	FRTHOU	DCW	000*0
1725 AQ 35	SAVA	DC	0
1726 AQ 36	SVLOC	DSA	0SAVA-004
1727 AQ 37	RESCK	DCW	0SET CK CONTROL SW TO NORMAL PRE0
1728 AQ 38		DC	0SS START0
1729 AQ 39		DCW	0
1730 AQ 40	RESHAM	DCW	0RESTORE 1403 PRINT HAMMER TO NDR0
1731 AQ 41		DC	0MAL STATUS PRESS START0
1732 AQ 42		DCW	0
1733 AQ 43	ZZ5	DCW	00050
1734 AQ 44	PRBSEG	DCW	0PRBUSYTEST0
1735 AQ 45	CYCNT	DCW	000000
1736 AQ 46	ZZZZ	DCW	000000
1737 AQ 47	ZZZ	DCW	00000
1738 AQ 48	ZZ4	DCW	00040
1739 AQ 49	AA1	DCW	0110
1740 AQ 50		DCW	0/10
1741 AQ 51		DCW	0J10
1742 AQ 52		DCW	0A10

SFX	CT	LOCN	INSTRUCTION
2		6986	
2		6988	
3		6991	
3		6994	
3		6997	
3		7000	0J*
3		7003	07Z
3		7006	080
3		7009	180
3		7012	280
4		7013	0 Z01
1		7017	
		1904	
		1905	
5		1905	
3		7020	
8		7028	
3		7031	
1		7032	
1		7033	
3		7036	
3		7039	
3		7042	
3		7045	03Z
32		7077	
6		7083	
1		7084	
10		7094	
10		7104	
1		7105	
3		7108	
5		7113	
3		7116	0DZ
32		7148	
8		7156	
1		7157	
32		7189	
23		7212	
1		7213	
3		7216	
10		7226	
4		7230	
4		7234	
3		7237	
3		7240	
2		7242	
2		7244	
2		7246	
2		7248	

1410/7010-1401 TOPSY COMPATIBILITY TEST

MD14 LOCN INSTRUCTION

SEQ PG LIN LABEL OP OPERANDS

SEQ PG LIN	LABEL	OP	OPERANDS	SFX	CT	LOCN	INSTRUCTION
1743 AQ 53	ADAN	DCW	@/0@	2	7250		
1744 AQ 54		DCW	@J0@	2	7252		
1745 AQ 55		DCW	@A0@	2	7254		
1746 AQ 56		DCW	@I0@	2	7256		
1747 AQ 57	ADAREA	DCW	@	2	7258		
1748 AQ 58	NINT9	DCW	@99@	2	7260		
1749 AQ 59		DCW	@BZ01 FGHJKLMNQRSTU VWXYZ012345@	32	7292		
1750 AQ 60		DC	@6789.0 1 2 3 4 5 6 7 8 9 0 -/ * # @ & - + A @	32	7324		
1751 AQ 61	RDCOMP	DC	@BCDEFGHIJKLMN@	13	7337		
1752 AQ 62		DC	@OPQ@	3	7340		
1753 AQ 63		DCW	@	1	7341		
1754 AQ 64	PROCMS	DCW	@ALTER LOC 7800 TO NO BITS SET C@	32	7373		
1755 AQ 65		DC	@K CONTROL SW TO RESTART AND PRES@	32	7405		
1756 AQ 66		DC	@S START@	7	7412		
1757 AQ 67		DCW	@	1	7413		
1758 AQ 68	PNERMS	DCW	@READY 10 CARDS JUST PUNCHED IN P@	32	7445		
1759 AQ 69		DC	@UNCH 9 EDGE FIRST FACE DOWN FOLL@	32	7477		
1760 AQ 70		DC	@NED BY BLANK CARDS PRESS START@	32	7509		
1761 AQ 71		DCW	@	1	7510		
1762 AQ 72	ETHOU	DCW	@00-@	3	7513		
1763 AQ 73	BRBK	B	BCK	4	7514		B R11
1764 AQ 74		DC	@	1	7518		
1765 AQ 75	BRBK1	B	BCKX	4	7519		B A81
1766 AQ 76		DC	@	1	7523		
1767 AQ 77	ZZ	DCW	@00@	2	7525		
1768 AQ 78	HRCNT	DCW	@00@	2	7527		
1769 AQ 79	WKAREA	EQU	7800	2	7800		
1770 AQ 80	RDCNT	DCW	@	2	7529		
1771 AQ 81	Z26	DCW	@026@	3	7532		
1772 AQ 82	ONHUND	DCW	@100@	3	7535		
1773 AQ 83	PAS	DCW	@PASS@	4	7539		
1774 AQ 84		DCW	@	1	7540		
1775 AQ 85	MD1410	DCW	@SET COMPATIBILITY SW TO 1410/701@	32	7572		
1776 AQ 86		DC	@0 PRESS COMPUTER RESET & START@	31	7603		
1777 AQ 87		DCW	@	1	7604		
1778 AQ 88	BRANCH	DCW	@B@	1	7605		
1779 AQ 89	NOP	DCW	@N@	1	7606		
1780 AQ 90	TWRCNT	DCW	@0000@	4	7610		
1781 AQ 91	TEN	DCW	@10@	2	7612		
1782 AQ 92	ERRLO	DCW	@TP HR ERR @	13	7625		
1783 AQ 93		DCW	@	1	7626		
1784 AQ 94	ERRL	DCW	@TP RD ERR @	13	7639		
1785 AQ 95		DCW	@	1	7640		
1786 AQ 96	TRDCNT	DCW	@0000@	4	7644		
1787 AQ 97	BK26	DCW	@170@	3	7647		
1788 AQ 98	BK50	DCW	@15@	3	7650		
1789 AQ 99	REELEN	DCW	@END OF REEL@	11	7651		
1790 AR 00		DCW	@	1	7662		
1791 AR 01	EDFIN	DCW	@FALSE TP EOF @	16	7678		
1792 AR 02		DCW	@	1	7679		

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SFX CT LCN INSTRUCTION

SEQ PG LIN LABEL OP DPERANDS

SEQ	PG	LIN	LABEL	OP	DPERANDS	SFX	CT	LCN	INSTRUCTION
1793	AR	03	SETXX	LCA	ZZZ,0089	7	7680	L	83X 089
1794	AR	04		B	TTTT	4	7687	B	08Z
1795	AR	05		DCW	@ @	1	7691		
1796	AR	06	RTX	EQU	7991	1	7991		
1797	AR	06	7991	DCW	@ @	1	7991		
1798	AR	07	WTX	EQU	7992	1	7992		
1799	AR	07	7992	DCW	@ @	1	7992		

SET XR 1 TO 0
GO TO WRITE TP

1410/7010-1401 TOPSY COMPATIBILITY TEST

SFX CT LOCN INSTRUCTION

OPERANDS

SEQ PG LIN LABEL OP

8000

1410/7010-1401 TOPSY COMPATIBILITY TEST
8000

JOB ORG

1410 ROUTINE TO SET UP PJST
RESTART,TYPE PROGRAM IO AND
SET UP INSTRUCTIONS

SEQ PG LIN LABEL OP	OPERANDS	SFX CT	LOCN	INSTRUCTION
1800 AR 09				
1801 AR 11				
1802 AR 12				
1803 AR 13				
1804 AR 14				
1805 AR 15				
1806 AR 16				
1807 AR 17				
1808 AR 18				
1809 AR 19				
1810 AR 20				
1811 AR 21				
1812 AR 22				
1813 AR 23				
1814 AR 24				
1815 AR 25				
1816 AR 26				
1817 AR 27				
1818 AR 28				
1819 AR 29				
1820 AR 30				
1821 AR 31				
1822 AR 32				
1823 AR 33				
1824 AR 34				
1825 AR 35				
1826 AR 36				
1827 AR 37				
1828 AR 38				
1829 AR 39				
1830 AR 40				
1831 AR 41				
1832 AR 42				
1833 AR 43				
1834 AR 44				
1835 AR 45				
1836 AR 46				
1837 AR 47				
1838 AR 48				
1839 AR 49				
1840 AR 50				
1841 AR 51				
1842 AR 52				
1843 AR 53				
1844 AR 54				
1845 AR 55				
1846 AR 56				
1847 AR 57				
1848 AR 58				
1849 AR 59				
DCW	@D0802800004X@	12	8011	
DCW	@.00005@	6	8017	
DCW	@J08029 @	7	8024	
DCW	@8-08@	4	8028	
DCW	@M%T001250W@	10	8038	
DCW	@R08029 @	7	8045	
DCW	@M%T008167W@	10	8055	
DCW	@R08046 @	7	8052	
DCW	@M%T008185W@	10	8072	
DCW	@R08063 @	7	8079	
DCW	@M%T008208W@	10	8089	
DCW	@N08080 @	7	8096	
DCW	@M%T008138W@	10	8106	
DCW	@R08097 @	7	8113	
DCW	@M%T008228W@	10	8123	
DCW	@R08114 @	7	8130	
DCW	@J08240 @	7	8137	
DCW	@SET COMPATIBILITY SW TO 1401@	28	8155	
DCW	@ @	1	8166	
DCW	@SET SENSE SW A ON@	17	8183	
DCW	@ @	1	8184	
DCW	@SET I/O CK STOP SW OFF@	22	8206	
DCW	@ @	1	8207	
DCW	@READY ALL I/O UNITS@	19	8226	
DCW	@ @	1	8227	
DCW	@PRESS START@	11	8238	
DCW	@ @	1	8239	
DCW	@D08028000084X@	12	8251	
DCW	@.00085@	6	8257	
DCW	@J02007 @	7	8264	
DCW	@.a	1	8265	
DRG	8500			8500
DCW	@B0851901301R@	12	8511	
DCW	@J08569 @	7	8518	
DCW	@M%1909000R@	10	8528	
DCW	@R085192@	7	8535	
DCW	@R08543 @	7	8542	
DCW	@R085571@	7	8549	
DCW	@J08569 @	7	8556	
DCW	@D09002013013@	12	8568	
DCW	@B0858801303P@	12	8580	
DCW	@J08638 @	7	8587	
DCW	@M%4909000W@	10	8597	

1410/7010-1401 TOPSY COMPATIBILITY TEST

SEQ PG LIN LABEL OP OPERANDS SFX CT LOCN INSTRUCTION

SEQ PG LIN	LABEL	OP	OPERANDS	SFX	CT	LOCN	INSTRUCTION
1850	AR 60	DCW	@R085882@	7	7	8604	
1851	AR 61	DCW	@R08612 @	7	7	8611	
1852	AR 62	DCW	@R086261@	7	7	8618	
1853	AR 63	DCW	@J08638 @	7	7	8625	
1854	AR 64	DCW	@D09002013033@	12	12	8637	
1855	AR 65	DCW	@B0865701305P@	12	12	8649	
1856	AR 66	DCW	@J08699 @	7	7	8655	
1857	AR 67	DCW	@F1@	2	2	8658	
1858	AR 68	DCW	@R086572@	7	7	8655	
1859	AR 69	DCW	@R08673 @	7	7	8672	
1860	AR 70	DCW	@R086871@	7	7	8679	
1861	AR 71	DCW	@J08699 @	7	7	8686	
1862	AR 72	DCW	@D09002013053@	12	12	8698	
1863	AR 73	DCW	@N@	1	1	8699	
1864	AR 74	DCW	@D08710079911@	12	12	8711	
1865	AR 75	DCW	@B08743012911@	12	12	8723	
1866	AR 76	DCW	@N08919012921@	12	12	8735	
1867	AR 77	DCW	@J08000 @	7	7	8742	
1868	AR 78	DCW	@D07991087581@	12	12	8754	
1869	AR 79	DCW	@ZU R@	5	5	8759	
1870	AR 80	DCW	@R087552@	7	7	8756	
1871	AR 81	DCW	@R08774 @	7	7	8773	
1872	AR 82	DCW	@R087881@	7	7	8780	
1873	AR 83	DCW	@J08844 @	7	7	8787	
1874	AR 84	DCW	@A0871007991@	11	11	8798	
1875	AR 85	DCW	@J088132@	7	7	8805	
1876	AR 86	DCW	@J08743 @	7	7	8812	
1877	AR 87	DCW	@D09002012913@	12	12	8824	
1878	AR 88	DCW	@D09002079923@	12	12	8836	
1879	AR 89	DCW	@J08000 @	7	7	8843	
1880	AR 90	DCW	@D07991079921@	12	12	8855	
1881	AR 91	DCW	@A0871007992@	11	11	8866	
1882	AR 92	DCW	@J088252@	7	7	8873	
1883	AR 93	DCW	@D07992088891@	12	12	8885	
1884	AR 94	DCW	@ZU R@	5	5	8890	
1885	AR 95	DCW	@R088862@	7	7	8897	
1886	AR 96	DCW	@R08905 @	7	7	8904	
1887	AR 97	DCW	@R088561@	7	7	8911	
1888	AR 98	DCW	@J08000 @	7	7	8918	
1889	AR 99	DCW	@D07991079921@	12	12	8930	
1890	AS 00	DCW	@J08874 @	7	7	8937	
1891	AS 01	DCW	@N08844012921@	12	12	8949	
1892	AS 02	DCW	@J08000 @	7	7	8956	
1893	AS 03	DCW	@ @	1	1	8957	
1894	AS 04	ORG	9000				9000
1895	AS 05	DC	@ @	1	1	9000	
1896	AS 06	DCW	@ @	1	1	9001	
1897	AS 07	DC	@ @	1	1	9002	
1898	AS 08	END	START				/ -00 080

060
M014