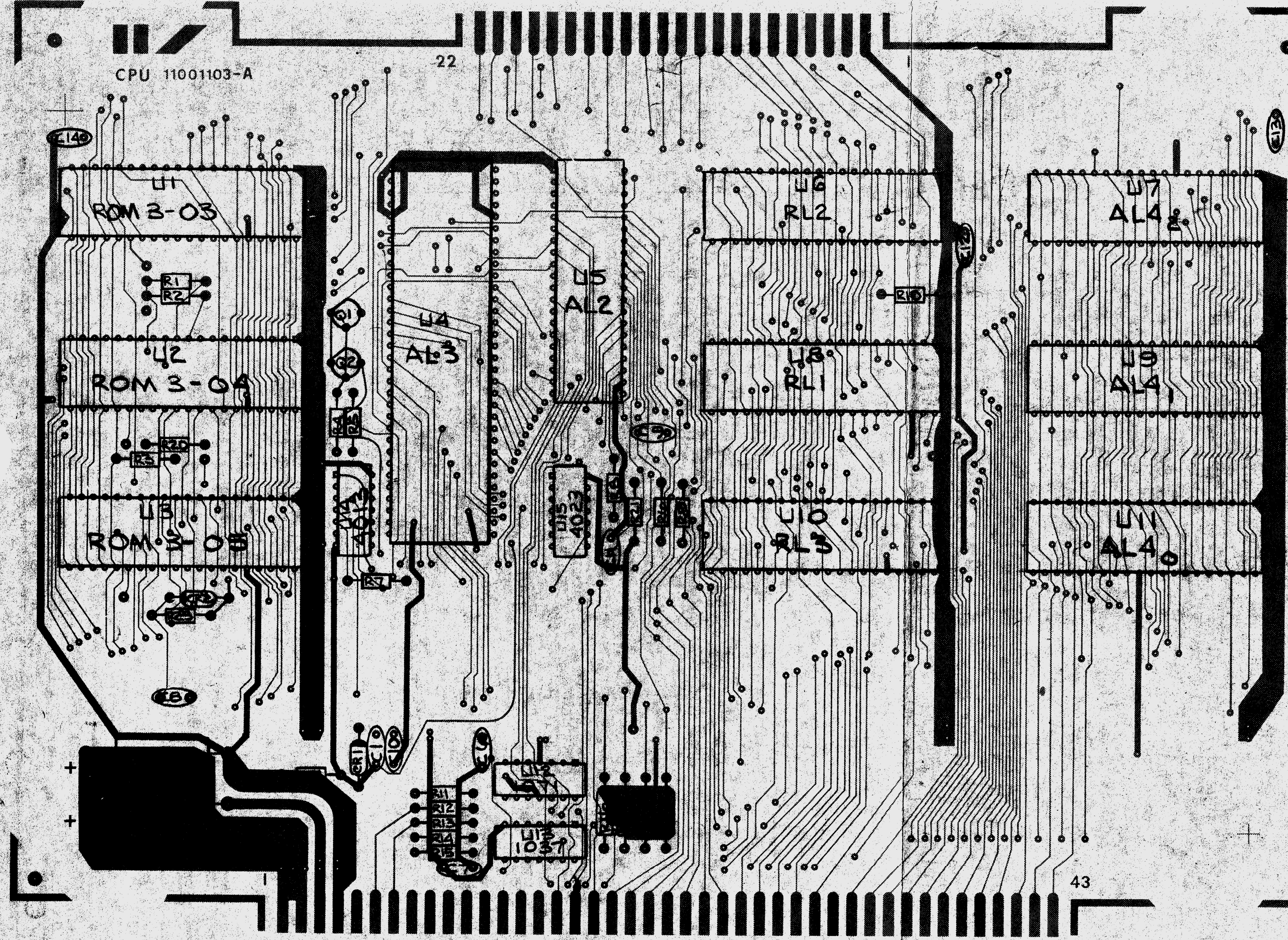


CPU 11001103-A

22


SYM	DESCRIPTION	BY & DATE
DI	ADDED C10-14, PCO#1902 ARTWORK WAS 11001102	MIKE 12-16-74 JFL



ALTERNATE TO-92
CASE FOR Q1 & Q2

ADDITIONAL
ALTERNATE T1 CASE
FOR Q2

NOTE: 1. ASSEMBLY 90012941 IS THE SAME AS 90012752 WITH AL2, AL3 CHIPS ADDED. A SEPARATE SCHEMATIC DRAWING DOES NOT EXIST BUT THE INFORMATION FOR BOTH IS INCLUDED ON DRAWING 90012752-07. (REF. 90012752-07 NOTE 4)

DRAWN M. Tolson 12-13-74	DATE 12-13-74	 FOUR-PHASE SYSTEMS, INC. CUPERTINO, CALIFORNIA
CHECKED J. LYBERG 12-16-74	DATE 12-16-74	
ENGINEER P. CHEN 12-18-74	DATE 12-18-74	CPU CARD WITH DECIMAL ARITHME
MFG		90012941 -05
RELEASED J. Van Zandt 12-19-74	DATE 12-19-74	SHEET 1 OF 1