

6.8.75

IDENTIFICATION
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PRODUCT CODE: MAINDEC-11-DZBMD-D-D
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PRODUCT NAME: BM873 UNIVERSAL RESTART LOADER
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DATE CREATED: APRIL 21, 1975
.....
MAINTAINER: DIAGNOSTIC GROUP
.....
AUTHOR: JOHN EGOLF
.....
REVISED BY: BOB MISNER 10/21/74
.....
FAY BASHAW 3/21/75

YA-version

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1. ABSTRACT

THIS MAINDEC CONSISTS OF FOUR PROGRAMS. THE TWO MAIN PROGRAMS ARE PROGRAM ONE AND PROGRAM FOUR. THESE PROGRAMS WILL BE DISCUSSED LATER. THE PURPOSE OF THIS DIAGNOSTIC IS TO VERIFY THE DATA IN THE ROM, MAKE SURE ALL ADDRESS WILL CAUSE A TIME OUT TRAP WHEN WRITTEN INTO (EXCEPT THE TRAP VECTORS: 173024,173224) ,AND ALERT THE OPERATOR AS TO WHAT THE OFFSET ADDRESS WOULD BE IF A SELECTED BUTTON IS PUSHED.

NOTE: FOR NORMAL CONFIGURATIONS; THE ONLY PROGRAMS NECESSARY FOR ACCEPTANCE OF THE BM873 ARE PROGRAMS ONE AND FOUR. PROGRAM TWO IS NECESSARY FOR "NON-STANDARD" SETUPS AND IS A MAINTAINCE TOOL. PROGRAM THREE IS ALSO JUST FOR MAINTAINCE AID.

2. REQUIRMENTS

2.1 EQUIPMENT

ANY PDP-11 FAMILY CPU
UNIVERSAL RESTART LOADER
TELETYPE OR EQUIVALENT
LINE PRINTER (OPTIONAL)
AT LEAST 4K OF MEMORY.

2.2 STORAGE

THIS PROGRAM RESERVES THE RIGHT TO USE ALL OF THE FIRST 4K EXCEPT WHERE BOOTSTRAP LOADER AND ABSOLUTE LOADER RESIDE.

3. LOADING PROCEDURE

THE PROGRAM MAY BE LOADED LIKE ANY OTHER PROGRAM SUCH AS: PAPER TAPE, DECTAPE MAGTAPE, DISK, ETC. MOST COMMON WILL BE THROUGH PAPER TAPE BY THE USE OF ABSOLUTE LOADER.

4. STARTING PROCEDURE

4.1 CONTROL SWITCH SETTINGS

SWITCH 00 CLEARED INDICATES ONLY FIRST 128 WORDS TO BE CHECKED.
SET INDICATES EXTENDED 128. WORDS ARE TO BE CHECKED IN WHICH CASE PROGRAM 2 MUST BE RUN FIRST.
WHEN RUNNING ON BM873YB, BM873YC, OR BM873YD, 256 WORDS ARE AUTOMATICALLY CHECKED.

4.2 STARTING ADDRESS

STARTING ADDRESS 000200
RESTART ADDRESS 000210

4.3 OPERATOR ACTION

4.3.1 FOR NORMAL OPERATION (WITHOUT EXTENDED 128 WORDS)

1. LOAD STARTING ADDRESS (000200)
2. SET SWITCHES AS PER 5.1.1 (NORMAL ALL SWITCHES DOWN)
3. PRESS START SWITCH AND RELEASE.
4. DEVICE VERSION.

WHEN PROGRAM IS STARTED FOR THE FIRST TIME THE FOLLOWING WILL BE PRINTED OUT:

"DEVICE VERSION 000000 THIS DEVICE IS A PDP-11 256 WORDS TO BE CHECKED.
BM873-Y "

THE OPERATOR WILL THEN SPECIFY THE VERSION BEING RUN.

BM873-Y* IS ANY NON-STANDARD VERSION,

NOTE: PROGRAM TWO MUST BE RUN FIRST.

EXCISE BY: BM873-YA REPLACES M792-YA, MR11-DB, M792-YH
DEFER BYCATT BM873-YB MASSBUS
BM873-YC DDCMP BOOTSTRAP ROM
BM873-YD KL10 (PDP-11) 256 BOOTSTRAP ROM

5. THEN TYPE IN NUMBER OF PROGRAM TO BE RUN (NORMALLY PROGRAM 1 AND 4)
6. HITTING CONTROL "G" WHILE PROGRAM IS RUNNING WILL CAUSE A RESTART.
YOU MAY THEN INPUT DIFFERENT PROGRAM NUMBER.

NOTE: RESTARTS AT ADDRESS 200 WILL GO TO STEP 5 ABOVE,
RESTARTS AT ADDRESS 210 WILL GO TO STEP 3 ABOVE.

4.3.2 IF YOU WISH TO TEST THE EXTENDED 128. WORDS THIS IS THE PROCEDURE:

(NOT NEEDED FOR NORMAL TESTING OF BM873YB, BM873YC, OR BM873YD)

1. LOAD STARTING ADD. 000200
2. SET SW00=1
3. SET HALT ENABLE SW AND SINGLE CYCLE SW UP
4. HIT START SWITCH AND RELEASE.
5. RUN PROGRAM 2 FOR ONE PASS.
6. NOW ANY PROGRAM MAY BE RUN.

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BM873D.P11 MAINDEC-11-DZBMD-D BM873 UNIVERSAL RESTART LOADER DIAGNOSTIC.

NOTE: VISUAL INSPECTION OF EXTENDED DUMP
IS YOUR RESPONSIBILITY. THAT DATA WAS
PLACED INTO SOFTWARE TABLE FOR TEST COMPARISON.

5. OPERATING PROCEDURE

5.1.1 SWITCH SETTINGS (APPLICABLE IN ALL PROGRAMS)

SW15 = 1 OR UP ... HALT ON ERROR

SW13 = 1 OR UP ... INHIBIT ERROR PRINT OUT

SW12 = 1 OR UP ... INHIBIT ALL PRINT OUT/ BELL ON ERROR.

SW11 = 1 OR UP ... INSTEAD OF EXERCISING EACH ADDRESS 10X DO IT 1X.

SW09 = 1 OR UP ... LOOP WITH CURRENT ADDRESS

SW08 = 1 OR UP ... GOTO BEGINNING OF CURRENT PROGRAM ON ERROR

6. ERRORS

6.1 ERROR PRINT OUT

ALL ERRORS WILL HAVE A PRINT OUT, IF IT WAS A COMPARISON
ERROR; THE SOFT ADDRESS, ROM ADDRESS, EXPECTED DATA
(FROM SOFTWARE MAP), AND THE FOUND DATA WILL BE PRINTED
OUT, IF IT WAS A "NO TRAP WHEN WRITTEN" ERROR; THE
ADDRESS WILL BE PRINTED OUT, IF IT WAS AN "UNEXPECTED TRAP "
WHEN READING ROM THE ADDRESS WILL BE PRINTED .

6.2 ERROR RECOVERY

1. ITS A GOOD IDEA TO LEAVE SW15=1 WHILE TEST RUNS TO PREVENT A RUN AWAY ERROR FROM GOING WILD IF YOU LEAVE THE CPU.
2. IN AN ERROR; SET SW09=1(LOOP ON THIS ADDR.) AND SET SW 13=1(DELETE ERROR PRINT OUT). IF CPU IS HALTED; HIT CONTINUE.
3. NOW THE PROGRAM IS RUNNING AND YOU MAY SCOPE IT.

7. RESTRICTIONS

7.1 STARTING RESTRICTIONS

SEE SECTION 4.

7.2 OPERATING RESTRICTIONS

- 7.2.1 IF OPERATING ON 11/45; AND JUMPER HAS BEEN CUT FOR A POWER FAIL TO GOTO ADDRESS 173200;
***** A POWER FAIL MUST NOT HAPPEN WHILE PRG IS RUNNING *****
- 7.2.2 IF YOU WISH PROGRAM TO TEST YOUR EXTENDED 128. WORDS; YOU MUST START AS PER SECTION 4 AND THEN
***** RUN PROGRAM 2 FIRST AND VISUALLY VERIFY DATA,****
(NOT APPLICABLE TO BM873YB, BM873YC, OR BM873YD)
- 7.2.3 YOU MAY NOT ALTER THE SOFTWARE MAP UNLESS--
***** YOU KNOW WHAT YOU ARE DOING *****
- 7.2.4 THE ROM ADDRESS MUST START AT 173000 AND BE AT LEAST 128 WORDS LONG. (256 FOR THE BM873YB, BM873YC, OR BM873YD)

8. MISCELLANEOUS

8.1 EXECUTION TIME

PROGRAM ONE WILL PASS AT APPROX. FIVE MINS.
PROGRAM TWO HAS NO END PASS; BUT WILL HALT AT COMPLETEION
HIT CONTINUE TO PROCEED IN THIS PROGRAM.
PROGRAM THREE (RUN) WILL PASS APPROX. FIVE MINS.
PROGRAM FOUR WILL PASS APPROX. FIVE MINS

9. PROGRAM DESCRIPTION

9.1 PROGRAM 1

PROGRAM 1 WILL VERIFY THE DATA IN THE ROM AND THE VERIFY THAT WRITING THE ROM WILL TRAP OUT (EXCEPT THE VECTORS) EACH ADDRESS IS REFERENCED FIVE TIMES IN A ROW BEFORE UPDATING TO THE NEXT ADDRESS.

IF SW00 WAS UP WHEN START WAS HIT, THE EXTENDED 128 WORDS WILL BE CHECKED. 256 WORDS WILL BE CHECKED AUTOMATICALLY IF BM873YB, BM873YC, OR BM873YD IS TESTED.

9.2 PROGRAM 2

PROGRAM 2 WILL DUMP THE CONTENTS OF THE ROM ONTO THE TTY (OR LINE PRINTER IF SW07=1). NOTE NO VERIFICATION OF ANY KIND IS PERFORMED ON THE DATA, (AN ERROR WILL OCCUR IF A TRAP IS ENCOUNTERED WHILE READING) YOU MUST INSPECT THE DATA YOUR SELF, IF SW00 WAS UP WHEN START WAS HIT THE EXTENDED 128, WORDS WILL BE PRINTED.

256 WORDS WILL BE PRINTED IF BM873YB, BM873YC, OR BM873YD IS SELECTED

NOTE: IF SW 07=1 PRINTING WILL BE DONE ON THE LINE PRINTER IF IT EXISTS.

9.3 PROGRAM 3

PROGRAM 3 IS THE SAME AS PROGRAM ONE EXCEPT THAT THE USER HAS THE ABILITY TO ALTER THE SOFTWARE MAP,

LIST OR PRINT THE SOFTWARE MAP, AND RUN THE PROGRAM,

NOTE THAT IF YOU ALTER THE MAP BE CAREFULL OF WHAT YOU CHANGE,

FOR THE COMMANDS TO BE USED SEE TOP OF PROGRAM 3 IN THIS LISTING

9.4 PROGRAM 4

PROGRAM 4 CHECKS THE OFFSET ADDRESS WHEN THE SIMULATED PUSHING OF A BUTTON IS DONE BY THE SOFTWARE, ON THE FIRST PASS

THE OFFSET IS TYPED OUT FOR YOU TO VERIFY (NOTE: THE PROGRAM HAS NO WAY OF KNOWING WHAT THE OFFSET WILL BE). AFTER THE DATA IS TYPED OUT IT IS STORED AWAY IN CORE. WHEN THE FIRST PASS IS FINISHED THE PROCESS IS REPETED ONLY NO TYPE OUT IS PERFORMED, AND THE DATA IN CORE IS COMPARED TO THE DATA FOUND AT THE ROM.

DURING THIS TEST "WRITING" THE ROM IS PERFORMED. THE VECTORS (173024,173224) ARE "WRITTEN" AND ARE **NOT** EXPECTED TO TRAP. AN ERROR MESSAGE WILL BE REPORTED IF A TRAP IS DISCOVERED.

9.5 CONTROL "G"

HITTING CONTROL "G" WILL GO TO THE BEGINING OF THE PROGRAM AND ASK FOR PROGRAM NUMBER.

9.6 THIS PROGRAM IS "XXDP AND ACT-11" COMPATIBLE; AT PRESENT TIME IF IN CHAIN MODE UNDER ACT-11 OR XXDP THE PROGRAM AUTOMATICALLY DETERMINES IF THE ROM IS BM873YA OR YB, YC, OR YD BY COMPARING THE 1ST WORD IN ROM WITH THE EXPECTED WORD. THE DIAGNOSTIC THEN RUNS PROGRAM 1 AND PROGRAM 4 BEFORE ENTERING THE MONITOR.

9.7 ELECTRICAL PREREQUISITES (HARDWARE)

9.7.1 THIS OPTION MUST BE ON THE CPU SIDE OF ANY BUS BUFFERS.

9.7.2 NPR CYCLES ARE NOT PERMITTED DURING THE POWER UP TRAP SEQUENCE.

9.7.3 ACLO AND DCLO MUST BE AVAILABLE AT BACKPLANE PINS "CV1" AND "CN1" RESPECTIVELY. THIS WIRING IS NOT PROVIDED ON THE "SPC" SLOTS OF THE 11/15, 11/20, 11/35, 11/40, AND THE DD11-A; IT IS PROVIDED ON THE 11/05, AND 11/45, ALSO ON THE DD11-B. IF FURTHER INFORMATION IS NEEDED CONSULT THE BM873 MANUAL FOR HELP.
NOTE: THE DIAGNOSTIC RUNNING WITHOUT ANY INTERFERENCE FROM THE USER HAS NO WAY OF CHECKING THE PRESENTS OF THE "ACLO" AND "DCLO" SIGNALS ON THE OPTION.


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;BM873 YX
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;STARTING PROCEDURE
;LOAD PROGRAM
;LOAD ADDRESS 000200
;PRESS START
;PROGRAM WILL TYPE "DEVICE VERSION
;          BM873-Y"
;THE OPERATOR WILL THEN SPECIFY THE VERSION.
;AT THE END OF A PASS, PROGRAM WILL TYPE PASS COMPLETE MESSAGE
;AND THEN RESUME TESTING

;SWITCH REGISTER OPTIONS
SW15=100000      ;=1, HALT ON ERROR
SW14=400000
SW13=200000      ;=1, INHIBIT ERROR PRINTOUT
SW12=100000      ;=1, DELETE TIMEOUT/BELL ON ERROR.
SW11=400000      ;=1 DO EACH ADDRESS 1 TIME INSTEAD OF 10 TIMES.
SW10=200000
SW09=100000      ;=1, LOOP WITH CURRENT DATA
SW08=400000      ;=1 RETURN TO TOP OF CURRENT PROGRAM ON ERROR.
SW06=100000
SW05=400000
SW04=200000
SW03=100000
SW02=400000
SW01=200000      ;=1 TEST THE EXPANDED 128 WORDS

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;REGISTER DEFINITIONS
R0=R0             ;GENERAL REGISTER
R1=R1             ;GENERAL REGISTER
R2=R2             ;GENERAL REGISTER
R3=R3             ;GENERAL REGISTER
R4=R4             ;GENERAL REGISTER
R5=R5             ;GENERAL REGISTER
SP=SP             ;PROCESSOR STACK POINTER
PC=PC             ;PROGRAM COUNTER

;LOCATION EQUIVALENCIES
SNR=177570        ;CONSOLE SWITCH REGISTER
LIGHTS=177570    ;PDP-11/45 DISPLAY REGISTER
PS=177776         ;PROCESSOR STATUS WORD
STACK=1200        ;START OF PROCESSOR STACK

;INSTRUCTION DEFINITIONS
PUSH1SP=5746      ;DECREMENT PROCESSOR STACK 1 WORD
POP1SP=5726       ;INCREMENT PROCESSOR STACK 1 WORD
PUSHR0=10046      ;SAVE R0 ON STACK
POPR0=12600       ;RESTORE R0 FROM STACK
PUSH2SP=24646     ;DECREMENT STACK TWICE
POP,SP=22626      ;INCREMENT STACK TWICE
,EQUIV EMT,HLT    ;BASIC DEFINITION OF ERROR CALL

BIT15=100000
BIT14=400000
BIT13=200000
BIT12=100000
BIT11=400000
BIT10=200000
BIT9=100000
BIT8=400000
BIT7=200000
BIT6=100000
BIT5=400000
BIT4=200000
BIT3=100000
BIT2=400000
BIT1=200000
BIT0=100000

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ADDR	DATA	DESCRIPTION
491	000000	TRAPCATCHER FOR ILLEGAL INTERRUPTS
492	000002	UNEXPECTED TRAP TO THIS LOCATION
493	000004	UNEXPECTED TRAP TO THIS LOCATION
494	000006	UNEXPECTED TRAP TO THIS LOCATION
495	000008	UNEXPECTED TRAP TO THIS LOCATION
496	00000A	UNEXPECTED TRAP TO THIS LOCATION
497	00000C	UNEXPECTED TRAP TO THIS LOCATION
498	00000E	UNEXPECTED TRAP TO THIS LOCATION
499	000010	UNEXPECTED TRAP TO THIS LOCATION
500	000012	UNEXPECTED TRAP TO THIS LOCATION
501	000014	UNEXPECTED TRAP TO THIS LOCATION
502	000016	UNEXPECTED TRAP TO THIS LOCATION
503	000018	UNEXPECTED TRAP TO THIS LOCATION
504	00001A	UNEXPECTED TRAP TO THIS LOCATION
505	00001C	UNEXPECTED TRAP TO THIS LOCATION
506	00001E	UNEXPECTED TRAP TO THIS LOCATION
507	000020	UNEXPECTED TRAP TO THIS LOCATION
508	000022	UNEXPECTED TRAP TO THIS LOCATION
491	000000	UNEXPECTED TRAP TO THIS LOCATION
492	000002	UNEXPECTED TRAP TO THIS LOCATION
493	000004	UNEXPECTED TRAP TO THIS LOCATION
494	000006	UNEXPECTED TRAP TO THIS LOCATION
495	000008	UNEXPECTED TRAP TO THIS LOCATION
496	00000A	UNEXPECTED TRAP TO THIS LOCATION
497	00000C	UNEXPECTED TRAP TO THIS LOCATION
498	00000E	UNEXPECTED TRAP TO THIS LOCATION
499	000010	UNEXPECTED TRAP TO THIS LOCATION
500	000012	UNEXPECTED TRAP TO THIS LOCATION
501	000014	UNEXPECTED TRAP TO THIS LOCATION
502	000016	UNEXPECTED TRAP TO THIS LOCATION
503	000018	UNEXPECTED TRAP TO THIS LOCATION
504	00001A	UNEXPECTED TRAP TO THIS LOCATION
505	00001C	UNEXPECTED TRAP TO THIS LOCATION
506	00001E	UNEXPECTED TRAP TO THIS LOCATION
507	000020	UNEXPECTED TRAP TO THIS LOCATION
508	000022	UNEXPECTED TRAP TO THIS LOCATION

ADDR	DATA	DESCRIPTION
455	000150	UNEXPECTED TRAP TO THIS LOCATION
456	000152	UNEXPECTED TRAP TO THIS LOCATION
457	000154	UNEXPECTED TRAP TO THIS LOCATION
458	000156	UNEXPECTED TRAP TO THIS LOCATION
459	000158	UNEXPECTED TRAP TO THIS LOCATION
460	00015A	UNEXPECTED TRAP TO THIS LOCATION
461	00015C	UNEXPECTED TRAP TO THIS LOCATION
462	00015E	UNEXPECTED TRAP TO THIS LOCATION
463	000160	UNEXPECTED TRAP TO THIS LOCATION
464	000162	UNEXPECTED TRAP TO THIS LOCATION
465	000164	UNEXPECTED TRAP TO THIS LOCATION
466	000166	UNEXPECTED TRAP TO THIS LOCATION
467	000168	UNEXPECTED TRAP TO THIS LOCATION
468	00016A	UNEXPECTED TRAP TO THIS LOCATION
469	00016C	UNEXPECTED TRAP TO THIS LOCATION
470	00016E	UNEXPECTED TRAP TO THIS LOCATION
471	000170	UNEXPECTED TRAP TO THIS LOCATION
472	000172	UNEXPECTED TRAP TO THIS LOCATION
473	000174	UNEXPECTED TRAP TO THIS LOCATION
474	000176	UNEXPECTED TRAP TO THIS LOCATION
475	000178	UNEXPECTED TRAP TO THIS LOCATION
476	00017A	UNEXPECTED TRAP TO THIS LOCATION
477	00017C	UNEXPECTED TRAP TO THIS LOCATION
478	00017E	UNEXPECTED TRAP TO THIS LOCATION
479	000180	UNEXPECTED TRAP TO THIS LOCATION
480	000182	UNEXPECTED TRAP TO THIS LOCATION
481	000184	UNEXPECTED TRAP TO THIS LOCATION
482	000186	UNEXPECTED TRAP TO THIS LOCATION
483	000188	UNEXPECTED TRAP TO THIS LOCATION
484	00018A	UNEXPECTED TRAP TO THIS LOCATION
485	00018C	UNEXPECTED TRAP TO THIS LOCATION
486	00018E	UNEXPECTED TRAP TO THIS LOCATION
487	000190	UNEXPECTED TRAP TO THIS LOCATION
488	000192	UNEXPECTED TRAP TO THIS LOCATION
489	000194	UNEXPECTED TRAP TO THIS LOCATION
490	000196	UNEXPECTED TRAP TO THIS LOCATION
491	000198	UNEXPECTED TRAP TO THIS LOCATION
492	00019A	UNEXPECTED TRAP TO THIS LOCATION
493	00019C	UNEXPECTED TRAP TO THIS LOCATION
494	00019E	UNEXPECTED TRAP TO THIS LOCATION
495	000200	UNEXPECTED TRAP TO THIS LOCATION
496	000202	UNEXPECTED TRAP TO THIS LOCATION
497	000204	UNEXPECTED TRAP TO THIS LOCATION
498	000206	UNEXPECTED TRAP TO THIS LOCATION
499	000208	UNEXPECTED TRAP TO THIS LOCATION
500	00020A	UNEXPECTED TRAP TO THIS LOCATION
501	00020C	UNEXPECTED TRAP TO THIS LOCATION
502	00020E	UNEXPECTED TRAP TO THIS LOCATION
503	000210	UNEXPECTED TRAP TO THIS LOCATION
504	000212	UNEXPECTED TRAP TO THIS LOCATION
505	000214	UNEXPECTED TRAP TO THIS LOCATION
506	000216	UNEXPECTED TRAP TO THIS LOCATION
507	000218	UNEXPECTED TRAP TO THIS LOCATION
508	00021A	UNEXPECTED TRAP TO THIS LOCATION

509	000334	000336	*2	UNEXPECTED TRAP TO THIS LOCATION
510	000336	000000	HALT	EXAMINE STACK TO FIND CAUSE
511	000330	000332	*2	UNEXPECTED TRAP TO THIS LOCATION
512	000332	000000	HALT	EXAMINE STACK TO FIND CAUSE
513	000334	000000	HALT	UNEXPECTED TRAP TO THIS LOCATION
514	000336	000000	*2	UNEXPECTED TRAP TO THIS LOCATION
515	000330	000332	HALT	EXAMINE STACK TO FIND CAUSE
516	000332	000000	*2	UNEXPECTED TRAP TO THIS LOCATION
517	000334	000000	HALT	UNEXPECTED TRAP TO THIS LOCATION
518	000336	000000	*2	UNEXPECTED TRAP TO THIS LOCATION
519	000330	000332	HALT	EXAMINE STACK TO FIND CAUSE
520	000332	000000	*2	UNEXPECTED TRAP TO THIS LOCATION
521	000334	000000	HALT	UNEXPECTED TRAP TO THIS LOCATION
522	000336	000000	*2	UNEXPECTED TRAP TO THIS LOCATION
523	000330	000332	HALT	EXAMINE STACK TO FIND CAUSE
524	000332	000000	*2	UNEXPECTED TRAP TO THIS LOCATION
525	000334	000000	HALT	UNEXPECTED TRAP TO THIS LOCATION
526	000336	000000	*2	UNEXPECTED TRAP TO THIS LOCATION
527	000330	000332	HALT	EXAMINE STACK TO FIND CAUSE
528	000332	000000	*2	UNEXPECTED TRAP TO THIS LOCATION
529	000334	000000	HALT	UNEXPECTED TRAP TO THIS LOCATION
530	000336	000000	*2	UNEXPECTED TRAP TO THIS LOCATION
531	000330	000402	HALT	EXAMINE STACK TO FIND CAUSE
532	000402	000000	*2	UNEXPECTED TRAP TO THIS LOCATION
533	000406	000000	HALT	EXAMINE STACK TO FIND CAUSE
534	000406	000000	*2	UNEXPECTED TRAP TO THIS LOCATION
535	000410	000412	HALT	EXAMINE STACK TO FIND CAUSE
536	000412	000000	*2	UNEXPECTED TRAP TO THIS LOCATION
537	000416	000000	HALT	UNEXPECTED TRAP TO THIS LOCATION
538	000416	000000	*2	UNEXPECTED TRAP TO THIS LOCATION
539	000420	000000	HALT	EXAMINE STACK TO FIND CAUSE
540	000422	000000	*2	UNEXPECTED TRAP TO THIS LOCATION
541	000424	000000	HALT	UNEXPECTED TRAP TO THIS LOCATION
542	000426	000000	*2	UNEXPECTED TRAP TO THIS LOCATION
543	000430	000432	HALT	EXAMINE STACK TO FIND CAUSE
544	000432	000000	*2	UNEXPECTED TRAP TO THIS LOCATION
545	000434	000436	HALT	EXAMINE STACK TO FIND CAUSE
546	000436	000000	*2	UNEXPECTED TRAP TO THIS LOCATION
547	000440	000442	HALT	EXAMINE STACK TO FIND CAUSE
548	000442	000000	*2	UNEXPECTED TRAP TO THIS LOCATION
549	000444	000446	HALT	EXAMINE STACK TO FIND CAUSE
550	000446	000000	*2	UNEXPECTED TRAP TO THIS LOCATION
551	000450	000452	HALT	EXAMINE STACK TO FIND CAUSE
552	000452	000000	*2	UNEXPECTED TRAP TO THIS LOCATION
553	000454	000456	HALT	EXAMINE STACK TO FIND CAUSE
554	000456	000000	*2	UNEXPECTED TRAP TO THIS LOCATION
555	000460	000462	HALT	EXAMINE STACK TO FIND CAUSE
556	000462	000000	*2	UNEXPECTED TRAP TO THIS LOCATION
557	000464	000466	HALT	EXAMINE STACK TO FIND CAUSE
558	000466	000000	*2	UNEXPECTED TRAP TO THIS LOCATION
559	000470	000472	HALT	EXAMINE STACK TO FIND CAUSE
560	000472	000000	*2	UNEXPECTED TRAP TO THIS LOCATION
561	000474	000476	HALT	EXAMINE STACK TO FIND CAUSE
562	000476	000000	*2	UNEXPECTED TRAP TO THIS LOCATION

563	000500	000502	*2	UNEXPECTED TRAP TO THIS LOCATION
564	000502	000000	HALT	EXAMINE STACK TO FIND CAUSE
565	000504	000506	*2	UNEXPECTED TRAP TO THIS LOCATION
566	000506	000000	HALT	EXAMINE STACK TO FIND CAUSE
567	000510	000512	*2	UNEXPECTED TRAP TO THIS LOCATION
568	000512	000000	HALT	EXAMINE STACK TO FIND CAUSE
569	000514	000516	*2	UNEXPECTED TRAP TO THIS LOCATION
570	000516	000000	HALT	EXAMINE STACK TO FIND CAUSE
571	000520	000522	*2	UNEXPECTED TRAP TO THIS LOCATION
572	000522	000000	HALT	EXAMINE STACK TO FIND CAUSE
573	000524	000526	*2	UNEXPECTED TRAP TO THIS LOCATION
574	000526	000000	HALT	EXAMINE STACK TO FIND CAUSE
575	000530	000532	*2	UNEXPECTED TRAP TO THIS LOCATION
576	000532	000000	HALT	EXAMINE STACK TO FIND CAUSE
577	000534	000536	*2	UNEXPECTED TRAP TO THIS LOCATION
578	000536	000000	HALT	EXAMINE STACK TO FIND CAUSE
579	000540	000542	*2	UNEXPECTED TRAP TO THIS LOCATION
580	000542	000000	HALT	EXAMINE STACK TO FIND CAUSE
581	000544	000546	*2	UNEXPECTED TRAP TO THIS LOCATION
582	000546	000000	HALT	EXAMINE STACK TO FIND CAUSE
583	000550	000552	*2	UNEXPECTED TRAP TO THIS LOCATION
584	000552	000000	HALT	EXAMINE STACK TO FIND CAUSE
585	000554	000556	*2	UNEXPECTED TRAP TO THIS LOCATION
586	000556	000000	HALT	EXAMINE STACK TO FIND CAUSE
587	000560	000562	*2	UNEXPECTED TRAP TO THIS LOCATION
588	000562	000000	HALT	EXAMINE STACK TO FIND CAUSE
589	000564	000566	*2	UNEXPECTED TRAP TO THIS LOCATION
590	000566	000000	HALT	EXAMINE STACK TO FIND CAUSE
591	000570	000572	*2	UNEXPECTED TRAP TO THIS LOCATION
592	000572	000000	HALT	EXAMINE STACK TO FIND CAUSE
593	000574	000576	*2	UNEXPECTED TRAP TO THIS LOCATION
594	000576	000000	HALT	EXAMINE STACK TO FIND CAUSE
595	000600	000602	*2	UNEXPECTED TRAP TO THIS LOCATION
596	000602	000000	HALT	EXAMINE STACK TO FIND CAUSE
597	000604	000606	*2	UNEXPECTED TRAP TO THIS LOCATION
598	000606	000000	HALT	EXAMINE STACK TO FIND CAUSE
599	000610	000612	*2	UNEXPECTED TRAP TO THIS LOCATION
600	000612	000000	HALT	EXAMINE STACK TO FIND CAUSE
601	000614	000616	*2	UNEXPECTED TRAP TO THIS LOCATION
602	000616	000000	HALT	EXAMINE STACK TO FIND CAUSE
603	000620	000622	*2	UNEXPECTED TRAP TO THIS LOCATION
604	000622	000000	HALT	EXAMINE STACK TO FIND CAUSE
605	000624	000626	*2	UNEXPECTED TRAP TO THIS LOCATION
606	000626	000000	HALT	EXAMINE STACK TO FIND CAUSE
607	000630	000632	*2	UNEXPECTED TRAP TO THIS LOCATION
608	000632	000000	HALT	EXAMINE STACK TO FIND CAUSE
609	000634	000636	*2	UNEXPECTED TRAP TO THIS LOCATION
610	000636	000000	HALT	EXAMINE STACK TO FIND CAUSE
611	000638	000640	*2	UNEXPECTED TRAP TO THIS LOCATION
612	000642	000644	HALT	EXAMINE STACK TO FIND CAUSE
613	000644	000646	*2	UNEXPECTED TRAP TO THIS LOCATION
614	000646	000648	HALT	EXAMINE STACK TO FIND CAUSE
615	000650	000652	*2	UNEXPECTED TRAP TO THIS LOCATION
616	000652	000000	HALT	EXAMINE STACK TO FIND CAUSE

```

617 000654 000656          *2          UNEXPECTED TRAP TO THIS LOCATION
618 000656 000660          *2          EXAMINE STACK TO FIND CAUSE
619 000660 000662          *2          UNEXPECTED TRAP TO THIS LOCATION
620 000662 000000          HALT
621 000664 000666          *2          UNEXPECTED TRAP TO FIND CAUSE
622 000666 000000          HALT
623 000670 000672          *2          UNEXPECTED TRAP TO THIS LOCATION
624 000670 000672          *2          UNEXPECTED TRAP TO THIS LOCATION
625 000674 000676          *2          UNEXPECTED TRAP TO FIND CAUSE
626 000674 000000          HALT
627 000700 000702          *2          UNEXPECTED TRAP TO THIS LOCATION
628 000700 000000          HALT
629 000704 000706          *2          UNEXPECTED TRAP TO THIS LOCATION
630 000704 000000          HALT
631 000710 000712          *2          UNEXPECTED TRAP TO THIS LOCATION
632 000712 000000          HALT
633 000714 000716          *2          UNEXPECTED TRAP TO THIS LOCATION
634 000716 000000          HALT
635 000720 000722          *2          UNEXPECTED TRAP TO THIS LOCATION
636 000720 000000          HALT
637 000724 000726          *2          UNEXPECTED TRAP TO THIS LOCATION
638 000726 000000          HALT
639 000730 000732          *2          UNEXPECTED TRAP TO THIS LOCATION
640 000730 000000          HALT
641 000734 000736          *2          UNEXPECTED TRAP TO THIS LOCATION
642 000734 000000          HALT
643 000740 000742          *2          UNEXPECTED TRAP TO THIS LOCATION
644 000742 000000          HALT
645 000744 000746          *2          UNEXPECTED TRAP TO THIS LOCATION
646 000746 000000          HALT
647 000750 000752          *2          UNEXPECTED TRAP TO THIS LOCATION
648 000750 000000          HALT
649 000754 000756          *2          UNEXPECTED TRAP TO THIS LOCATION
650 000754 000000          HALT
651 000760 000762          *2          UNEXPECTED TRAP TO FIND CAUSE
652 000762 000000          HALT
653 000764 000766          *2          UNEXPECTED TRAP TO THIS LOCATION
654 000766 000000          HALT
655 000770 000772          *2          UNEXPECTED TRAP TO THIS LOCATION
656 000772 000000          HALT
657 000774 000776          *2          UNEXPECTED TRAP TO THIS LOCATION
658 000776 000000          HALT
659
660 000824 000824          *24         *PRNLT
661 000824 014510          *30         *HLT
662 000826 000340          *30         *HLT
663 000830 015116          *30         *PRPR
664 000832 000000          *30         *PRPR
665 000834 016022          *30
666 000836 000000          *30
667 000840 000200          *200        JMP      START
668 000840 000210          *210        JMP      RESTART
669 000840 000137          *210
670 000840 000137          *210

```

```

671 001200          *1200
672
673
674
675 001200 177514          *1200        LPTCR1 177514
676 001202 177516          *1200        LPDR1 177516
677 001204 177560          *1200        NTCR1 177560
678 001206 177562          *1200        NDCR1 177562
679 001210 177564          *1200        TPCR1 177564
680 001212 177566          *1200        TDCR1 177566
681 001214 000000          *1200        TKEY1 60
682 001216 000200          *1200        TRV1 200
683 001220 000064          *1200        TPVEC1 64
684 001222 000200          *1200        TPV1 200
685
686
687
688 001224 000000          *1200
689 001226 000000          *1200
690 001230 000000          *1200
691 001232 000000          *1200
692 001234 000000          *1200
693 001236 000000          *1200
694 001240 000000          *1200
695 001242 000000          *1200
696 001244 000000          *1200
697
698
699
700 001246 000000          *1200
701 001250 000000          *1200
702 001252 000000          *1200
703 001254 000000          *1200
704 001256 000000          *1200
705 001260 000000          *1200
706 001262 000000          *1200
707 001264 000000          *1200
708 001266 000000          *1200
709 001270 000000          *1200
710 001272 000000          *1200
711 001274 000000          *1200
712 001276 000000          *1200

```

```

;INDIRECT POINTERS TO TELETYPE VECTORS AND REGISTERS
;PROGRAM CONTROL PARAMETERS
;SCOPE ADDRESS FOR LOOP ON TEST
;ADDRESS OF NEXT TEST TO BE EXECUTED
;ADDRESS FOR LOCK ON CURRENT DATA
;NUMBER OF ITERATIONS THAT CURRENT TEST WILL BE EXECUTED
;NUMBER OF ITERATIONS COMPLETED
;NUMBER OF TEST IN PROGRESS
;NUMBER OF TEST PASSED
;TOTAL NUMBER OF ERRORS
;PC OF LAST ERROR CALL

```

```

;PROGRAM VARIABLES
;TEMPORARY STORAGE
;TEMPORARY STORAGE
;TEMPORARY STORAGE
;TEMPORARY STORAGE
;TEMPORARY STORAGE
;IR1 STORAGE
;IR2 STORAGE
;IR3 STORAGE
;IR4 STORAGE
;IR5 STORAGE
;STACK POINTER STORAGE
;PROGRAM COUNTER STORAGE

```

```

713
714
715 001300 000
716 001301 000
717 001302 000
718 001303 000
719 000000
720
721
722
723
724
725
726
727
728 001304
729 104400
730 001304 014740
731 104401
732 001306 014784
733 104402
734 001310 015736
735 104403
736 001312 015770
737 104404
738 001314 015424
739 104405
740 001316 015430
741 104406
742 001320 014444
743
744
745

```

```

PROGRAM CONTROL FLAGS
IPLDGI .BYTE 0
STRTGI .BYTE 0
ERRDGI .BYTE 0
LOCKDI .BYTE 0
IEM8

```

```

DEFINITIONS FOR TRAP SUBROUTINE CALLS
POINTERS TO SUBROUTINES CAN BE FOUND
IN THE TABLE IMMEDIATELY FOLLOWING THE DEFINITIONS

```

```

*****
TRPAP4
SCOP1=TRAP4
SCOP1
TYPE=TRAP4
TYPE
SAVE5=TRAP4
SAVE5
REG65=TRAP4
REG65
CONVRT=TRAP4
CONVRT
CONVRT=TRAP4
CONVRT
KEY.TO.R2=TRAP4
KEY.TO.R2

```

```

*****

```

```

746
747
748 001400
749
750
751
752
753
754
755
756
757
758
759
760
761
762
763
764
765
766
767
768
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771
772
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777
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779
780
781
782
783
784
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786
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789
790
791
792
793
794
795
796
797
798
799
800

```

```

*1400
MAP.YA1
THE FOLLOWING IS A REPRODUCTION
OF THE ROM PROGRAM FOR BM873YA.
IT IS HERE FOR COMPARISON TO
ACTUAL ROM AND FOR REFERENCE.

```

```

*****173000
STARTING ADDRESS FOR BOOTSTRAP
THIS LOADER IS DESIGNED FOR THE RESTART MODULE M873.
IT FUNCTIONALLY REPLACES THE FOLLOWING ROMS:
M92-YA - PAPER TAPE BOOTSTRAP FOR PC11,K11
M91-DB BULK STORAGE BOOTSTRAP ROM
M92-YH TALI CASSETTE BOOTSTRAP ROM

```

```

ROM= 40
R1= 41
R2= 42
R3= 43
R4= 44
R5= 45
R6= 46
R7= 47
R8= 177376

```

```

STARTING LOCATION FOR RPL DISK
RPLI
BR OTHER
BR OTHER

```

```

*****177462
DEVICE READ INSTRUCTION

```

```

*****
THIS IS THE STARTING LOCATION FOR THE RPL CONTROLLER
RPLI
BR OTHER
BR OTHER

```

```

*****177486
DEVICE READ INSTRUCTION

```

```

*****
THIS IS A SPARE STARTING REGISTER. IT TRANSFERS TO ADDRESS
CONTAINED IN THE SWITCH REGISTER.
TRANSR NOV $SRPC
GO TO INDICATED LOCATION

```

```

*****
THIS IS THE POWER UP VECTOR REQUIRED FOR DEVICE AND
POWER.
WORD RPL1
ADDRESS OF FIRST LOCATION IN ROM
WORD 346
PROCESSOR STATUS LEVEL 7

```

```

*****
THIS IS THE STARTING ADDRESS FOR TC11 (DECTAB) CONTROLLER.
TC11
MOV PC,R2
BR TAPES
WORD 177344
DEVICE WORD COUNT ADDRESS
WORD 4003
FIND PREVIOUS BLOCK COMMAND
WORD 100000
USED AS DONE INDICATOR
WORD 24000
USED AS ERROR INDICATOR/TEST FLAG
BR OTHERX
THEN TRANSFER TO NEXT ROUTINE
WORD 5
DEVICE READ COMMAND

```

```
800 ;
801 ;THIS IS THE START LOCATION FOR TM11 MAGTAPE CONTROLLER
802 001450 010702 1173050 010702 TM11: MOV PC,R2 ;SET POINTER TO PARAMETER LIST
803 001452 000416 1173052 000416 BR TAPES ;AND TRANSFER TO FIRST ROUTINE
804 001454 172524 1173054 172524 ,WORD 172524 ;DEVICE BYTE/RECORD COUNT REGISTER
805 001456 000017 1173056 000017 ,WORD 00017 ;DEVICE REWIND COMMAND
806 001460 000200 1173060 000200 ,WORD 200 ;DEVICE DONE FLAG
807 001462 100000 1173062 100000 ,WORD 100000 ;DEVICE ERROR FLAG BIT
808 001464 000413 1173064 000413 BR TAPESX ;THEN TRANSFER TO NEXT SERVICE RTN
809 001466 000011 1173066 000011 ,WORD 00011 ;DEVICE FORWARD SPACE COMMAND
810 001470 000200 1173070 000200 ,WORD 200 ;SAME AS ABOVE
811 001472 100000 1173072 100000 ,WORD 100000 ;SAME AS ABOVE
812 001474 000431 1173074 000431 BR OTHERX ;THEN TRANSFER TO READ/TRANSFER ROUTINE
813 001476 000003 1173076 000003 ,WORD 00003 ;DEVICE READ COMMAND
814 ;
815 ;THIS IS THE START LOCATION FOR THE RP11 CONTROLLER
816 001500 010702 1173100 010702 RP11: MOV PC,R2 ;SET POINTER TO PARAMETER LIST
817 001502 000424 1173102 000424 BR OTHER ;TRANSFER TO TRANSFER ROUTINE
818 001504 176716 1173104 176716 ,WORD 176716 ;DEVICE WORD COUNT REGISTER
819 001506 000005 1173106 000005 ,WORD 5 ;DEVICE READ COMMAND
820 ;
821 ;THIS IS THE TAPE DEVICE SERVICE ROUTINE.
822 001510 010200 1173110 010200 TAPES: MOV R2,R0 ;GET ADDRESS OF PARAMETER LIST
823 001512 005720 1173112 005720 TST (R0)+ ;SKIP TWO WORDS FIRST TIME
824 001514 000005 1173114 000005 TAPESX: RESET ;RESET ALL DEVICES
825 001516 005720 1173116 005720 TST (R0)+ ;SKIP OVER BRANCH INSTRUCTION
826 001520 016201 1173120 016201 MOV 2(R2),R1 ;THEN GET DEVICE WORD/BYTE COUNT ADDRESS
827 001522 000002 1173122 000002 ;
828 001524 005311 1173124 005311 DEC @R1 ;AND SET TO -1
829 001526 012041 1173126 012041 MOV (R0)+,(R1) ;AND THEN ISSUE COMMAND TO DEVICE
830 001530 031011 1173130 031011 TAPWAT: BIT @R0,@R1 ;WAIT FOR DEVICE COMPLETION
831 001532 001776 1173132 001776 BEQ TAPWAT ;BY HANGING IN LOOP
832 001534 005720 1173134 005720 TST (R0)+ ;AND THEN SKIP DONE FLAG
833 001536 032041 1173136 032041 BIT (R0)+,(R1) ;THEN TEST FOR ERROR
834 001540 001063 1173140 001063 BNE ERROR ;THERE IS ONE
835 001542 000110 1173142 000110 RETURN: JMP @R0 ;AND TRANSFER TO FOLLOWING INSTRUCTION
836 ;
837 ;THIS IS THE STARTING ADDRESS FOR RC11 DISK CONTROLLERS
838 001544 010702 1173144 010702 RC11: MOV PC,R2 ;SET UP POINTER TO PARAMETER LIST
839 001546 000402 1173146 000402 BR OTHER ;TRANSFER TO SERVICE RTN
840 001550 177450 1173150 177450 ,WORD 177450 ;DEVICE WORD COUNT REGISTER
841 001552 000005 1173152 000005 ,WORD 5 ;DEVICE READ INSTRUCTION
842 ;
843 ;THIS ROUTINE PERFORMS THE ACTUAL READ TRANSFER TO MEMORY OF DATA
844 001554 010200 1173154 010200 OTHER: MOV R2,R0 ;SET POINTER TO LIST IN R0
845 001556 005720 1173156 005720 TST (R0)+ ;SKIP TWO WORDS FIRST TIME.
846 001560 005720 1173160 005720 OTHERX: TST (R0)+ ;SKIP PAST BR INSTRUCTION
847 001562 000005 1173162 000005 RESET ;REST THE WORLD
848 001564 016201 1173164 016201 MOV 2(R2),R1 ;OBTAIN DEVICE WORD COUNT ADDRESS
849 001566 000002 1173166 000002 ;
850 001570 012711 1173170 012711 MOV #1000,@R1 ;THEN OBTAIN LARGE WORD COUNT
851 001572 177000 1173172 177000 ;
852 001574 011041 1173174 011041 MOV @R0,(R1) ;AND PUT COMMAND TO DEVICE
853 001576 105711 1173176 105711 OTHWAT: TSTB @R1 ;WAIT FOR DONE FLAG
```

```
854 001600 100376 1173200 100376 BPL OTHWAT ;BY HANGING IN LOOP
855 001602 005711 1173202 005711 TST @R1 ;THEN TEST FOR ERROR
856 001604 100441 1173204 100441 BMI ERROR ;GOT PROBLEMS
857 001606 005007 1173206 005007 CLR PC ;AND TRANSFER TO ZERO
858 ;
859 ;THIS IS THE STARTING ADDRESS FOR THE PC11 PAPER TAPE CONTROLLER
860 001610 012704 1173210 012704 KL11: MOV #177500,R4 ;OBTAIN DEVICE ADDRESS
861 001612 177500 1173212 177500 ;
862 001614 000440 1173214 000440 BR CKDEV ;AND TRANSFER TO READER SERVICE ROUTINE
863 ;
864 ;THIS IS THE CASSETTE DEVICE COMMAND TABLE
865 ;
866 001616 017640 1173216 240 TABLE: ,BYTE 240 ;COMPARE WORD NOT A COMMAND
867 ,BYTE 37 ;ILBS+RND*00
868 001620 002415 1173220 015 ,BYTE 37 ;ILBS+RND*00
869 ,BYTE 15 ;SPACE FORWARD BLOCK+GO
870 ,BYTE 5 ;READ+GO
871 001622 112024 1173222 024 ,BYTE 5 ;READ+ILBS
872 ,BYTE 24 ;READ+ILBS+END FLAG
873 ,NOTE 773024 AND 773224 ARE DEPENDENT ON OFFSET IN DIODES FOR LINE 1
874 ;
875 ;THIS IS AN ADDITIONAL POWER VECTOR ADDRESS REQUIRED BY DEVICE
876 001624 173000 1173224 173000 POWER2: ,WORD RF11 ;ADDRESS OF BEGINNING OF BOOTSTRAP
877 001626 000340 1173226 000340 ,WORD 340 ;PRIORITY LEVEL 7
878 ;
879 ;THIS IS THE STARTING ADDRESS FOR THE CASSETTE DEVICE #0
880 001630 005004 1173230 005004 CBOOT: CLR R4 ;LOAD DEVICE NUMBER 0 IN R4
881 001632 012700 1173232 012700 RESTX: MOV #177500,R0 ;GET DEVICE ADDRESS
882 001634 177500 ;
883 ;
884 001636 000005 1173236 000005 RESTR: RESET ;ISSUE RESET INSTRUCTION
885 001640 010410 1173240 010410 MOV R4,@R0 ;LOAD DEVICE WITH UNIT NUMBER
886 001642 012701 1173242 012701 MOV #TABLE,R1 ;GET FUNNY TABLE OF INSTRUCTIONS
887 001644 173216 1173244 173216 ;
888 001646 012702 1173246 012702 MOV #375,R2 ;AND LOAD UP TRANSFER COUNTER
889 001650 000375 1173250 000375 ;
890 001652 112103 1173252 112103 LOOP1: MOVB (R1)+,R3 ;THE LOAD UP COMPARATOR
891 001654 112110 1173254 112110 MOVB (R1)+,@R0 ;LOAD DEVICE REGISTER WITH COMMAND
892 001656 100407 1173256 100407 BMI DONE ;
893 001660 130310 1173260 130310 LOOP2: BITB R3,@R0 ;HAS COMMAND COMPLETED
894 001662 001776 1173262 001776 BEQ LOOP2 ;NO, WAIT
895 001664 105202 1173264 105202 INCB R2 ;THEN INCREMENT ADDRESS CTR
896 001666 100772 1173266 100772 BMI LOOP1 ;IF NEGATIVE, GET COMMAND
897 001670 116012 1173270 116012 MOVB 2(R0),@R2 ;AND STORE DATA AWAY
898 001672 000002 1173272 000002 ;
899 001674 000771 1173274 000771 BR LOOP2 ;GO GET ANOTHER BYTE
900 001676 005710 1173276 005710 DONE: TST @R0 ;ANY DEVICE ERRORS
901 001700 100756 1173300 100756 BMI RESTR ;YES, RETRY
902 001702 005002 1173302 005002 CLR R2 ;CLEAR COMPARE ADDRESS AND TRANSFER ADDRESS
903 001704 120312 1173304 120312 CMPB R3,@R2 ;IT MUST BE 240
904 001706 001377 1173306 001377 BNE ,@0 ;NO, THERE WAS AN ERROR
905 001710 000112 1173310 000112 ERROR: JMP @R2 ;NORMAL CASSETTE AND ERROR FOR BULK STORAGE
906 ;
907 ;THIS IS THE STARTING LOCATION FOR THE PC11 CONTROLLER
908 001712 012704 1173312 012704 PC11: MOV #177500,R4 ;LOAD DEVICE ADDRESS
909 001714 177550 1173314 177550 ;
```

```

908 001716 000005 ;173316 000005 CKDEV: RESET ;KILL ALL DEVICE ACTION
909 001720 012701 ;173320 012701 MOV #160000,R1 ;THEN SET UP MEMORY TEST LIMITS
910 001722 160000 ;173322 160000
911 001724 012702 ;173324 012702 MOV #6,R2 ;AND SET UP POINTER TO TIMEOUT LOCATION
912 001726 000006 ;173326 000006
913 001730 012712 ;173330 012712 MOV #340,0R2 ;AND SET UP VECTOR TO RETURN TO NEXT
914 001732 000340 ;173332 000340
915 001734 010742 ;173334 010742 MOV PC,=(R2) ;SAVE THE PC
916 001736 012706 ;173336 012706 MOV #24,8P ;AND LOAD UP STACK POINTER
917 001740 000024 ;173340 000024
918 001742 010441 ;173342 010441 MOV R4,=(R1) ;AND LOOK FOR END OF MEMORY
919 001744 040601 ;173344 040601 BIC 8P,R1 ;THEN DROP TO XX7752
920 001746 010111 ;173346 010111 MOV R1,0R1 ;AND STORE IN ITSELF
921 001750 011102 ;173350 011102 LOOP: MOV 0R1,R2 ;THEN LOAD ADDRESS FOR DATA INSERTION
922 001752 005214 ;173352 005214 INC 0R4 ;AND START DEVICE
923 001754 105714 ;173354 105714 RDRWAT: TSTB 0R4 ;THEN WAIT FOR CHARACTER AVAILABLE
924 001756 100376 ;173356 100376 BPL RDRWAT ;HANGING THERE IF NECESSARY
925 001760 116412 ;173360 116412 MOVB 2(R4),0R2 ;STORE AWAY DATA BYTE
926 001762 000002 ;173362 000002
927 001764 005211 ;173364 005211 INC 0R1
928 001766 120227 ;173366 120227 CMPB R2,#375 ;HAS BRANCH OFFSET BEEN STORED
929 001770 000375 ;173370 000375
930 001772 001366 ;173372 001366 BNE LOOP ;NO
931 001774 105222 ;173374 105222 INCB (R2)+ ;YES, ALL DONE
932 001776 END,YA;
933 001776 000142 ;173376 000142 JMP =(R2) ;THEN TRANSFER TO RTN
  
```

```

934 ; BM873B BOOTSTRAP MACY11 27(655) 1-OCT-74 14:50 PAGE 1
935 ;
936 ; ;DATE: AUG 23, 1974
937 002000 MAP,YB;
938 ;THE FOLLOWING IS A REPRODUCTION
939 ;OF THE ROM PPROGRAM FOR BM873YB.
940 ;IT IS HERE FOR COMPARISON TO THE
941 ;ACTUAL ROM AND FOR REFERENCE
942 ;
943 ;
944 ;
945 ;
946 ; ;THIS IS THE LOADER TO REPLACE THE FOLLOW
947 ; ;M792-YA PAPER TAPE BOOTSTRAP ROM
948 ; ;MR11-DB BULK STORAGE BOOTSTRAP ROM
949 ; ;M792-YH TAII CASSETTE BOOTSTRAP ROM
950 ; ;RM873A COMBINATION OF ABOVE ROMS
951 ;
952 ; ;PREPHERIAL EXTERNAL PAGE REGISTERS ASSIGNMENTS:
953 ; 177462 RFWC= 177462 ;WORD COUNT REG. FOR RP1
954 ; 177406 RKWC= 177406 ;WORD COUNT REG. FOR RK1
955 ; 177344 TCWC= 177344 ;WORD COUNT REG. FOR TC1
956 ; 172524 TMWC= 172524 ;BYTE/RECORD COUNT FOR T
957 ; 176716 RPWC= 176716 ;WORD COUNT REG. FOR RP1
958 ; 177450 RCWC= 177450 ;WORD COUNT REG. FOR RC1
959 ; 177560 KLC8= 177560 ;CONTROL REG. FOR KL11
960 ; 177500 TACS= 177500 ;CONTROL REG. FOR TA11 C
961 ; 177550 PCCS= 177550 ;CONTROL REG. FOR PC11
962 ; 172440 TUCS= 172440 ;CONTROL STATUS REG. 1
963 ; 172442 TUNC= 172442 ;TU16 WORD COUNT REG.
964 ;
965 ; 176300 RHC8A= 176300 ;CONTROLLER REG. 1 FOR R
966 ; 176302 RHWC8A= RHC8A+2
967 ; 172040 RSC8A= 172040 ;CONTROLLER REG.1 FOR RH
968 ; 172042 RSWC8A= RSC8A+2
969 ; 176700 RPC8A= 176700 ;CONTROLLER REG. 1 FOR R
970 ; 176702 RPWC8A= RPC8A+2
971 ; ;FUNCTION VALUE FOR PREPHERALS;
972 ; 000005 RFWC= 5 ;READ FUNCTION
973 ; 004003 RNUM= 4003 ;REVERSE AND IDENTIFY BL
974 ; 000017 TRWWD= 00017 ;REWIND AND SET 800 BPI
975 ; 000011 TRFRD= 00011 ;FORWARD RECORD COMMAND
976 ; 000003 TRREAD= 00003 ;TM11 READ
977 ; 000011 DRCLR= 11 ;DRIVE CLEAR
978 ; 000071 RHREAD= 71 ;RH11 READ COMMAND
979 ; 000021 RHPRES= 21 ;READ IN PRESET
980 ; 000031 TUSPAC= 31 ;SPACE FORWARD COMMAND F
981 ; 040000 TUTAPE= 40000 ;TAPE BIT IN RH11/RHDT R
982 ; 001300 TUNODE= 1300 ;800 BPI NORMAL MODE FOR
983 ; 001000 FCE= 1000 ;FRAME COUNT ERROR BIT
984 ;
985 ; 177570 CSW= 177570
986 ;
987 ;
  
```

```

988 ; ; ONLY THE LOW BYTE OF CONSOL SWITCH REGISTER IS
989 ; ; SELECT THE UNIT NUMBER OF THE DEVICE TO BOOT FR
990 ; ;
991 ; ; 173000 . =173000
992 ; ;
993 ; ;
994 ; ; THIS IS THE STARTING ADDRESS FOR RH11/RS03/04 D
995 002000 000405 ;173000 000405 RHRSA: BR 18 ;ENTRY FOR SELECTING UNI
996 002002 010703 ;173002 010703 RHRSB: MOV PC,R3 ;ENTRY TO SELECT UNITS
997 002004 113737 ;173004 113737 MOV #RSCSA,R0;LOAD UNIT # INS
998 002006 177570 ;173006 177570
999 002010 172050 ;173010 172050
1000 002012 000401 ;173012 000401 BR 28
1001 002014 010703 ;173014 010703 18: MOV PC,R3
1002 002016 012700 ;173016 012700 28: MOV #RSCSA,R0;SET CONTROL STATUS REG
1003 002020 172040 ;173020 172040
1004 002022 000526 ;173022 000526 BR RHCOMN
1005 ; ;
1006 ; ; THIS IS THE AUTO LOAD VECTOR
1007 002024 173000 ;173024 173000 ,WORD RHRSA
1008 002026 000340 ;173026 000340 ,WORD 340
1009 ; ;
1010 ; ; THIS IS THE STARTING ADDRESS FOR RK11 CONTROLLE
1011 002030 000412 ;173030 000412 RK11A: BR 28 ;ENTRY TO SELECT UNIT 0
1012 002032 010703 ;173032 010703 RK11B: MOV PC,R3 ;ENTRY TO SELECT ALL UNI
1013 ; ; ;SAVE ERROR RETRY ADDRESS
1014 002034 113705 ;173034 113705 MOV #RSCSA,R5;SET POINTER TO PARAMETE
1015 002036 177570 ;173036 177570
1016 002040 052705 ;173040 052705 BIS #10,R5 ;SET POSITION BIT
1017 002042 000010 ;173042 000010
1018 002044 006105 ;173044 006105 18: ROL R5 ;SHIFT UNIT # TO BIT 13-
1019 002046 103376 ;173046 103376 BCC 18 ;KEEP GOING
1020 002050 010537 ;173050 010537 MOV R5,#RKNC+4;MOVE IN TO RKDA REGI
1021 002052 177412 ;173052 177412
1022 002054 000401 ;173054 000401 BR 30 ;SKIP NEXT INSTRUCTION
1023 002056 010703 ;173056 010703 28: MOV PC,R3 ;SAVE ERROR RETRY ADDRESS
1024 002060 010702 ;173060 010702 38: MOV PC,R2
1025 002062 000546 ;173062 000546 BR OTHERA
1026 002064 177406 ;173064 177406 ,WORD RKWC
1027 002066 000005 ;173066 000005 ,WORD RFPREAD
1028 ; ;
1029 ; ; THIS IS THE STARTING ADDRESS FOR TC11 (DECTAPE)
1030 002070 010703 ;173070 010703 TC11: MOV PC,R3 ;SAVE ERROR RETRY ADDRESS
1031 002072 010702 ;173072 010702 MOV PC,R2
1032 002074 000570 ;173074 000570 BR TAPES
1033 002076 177344 ;173076 177344 ,WORD TCWC
1034 002100 000005 ;173100 000005 ,WORD RFPREAD
1035 002102 004003 ;173102 004003 ,WORD RNUM
1036 002104 100000 ;173104 100000 ,WORD 100000 ;DONE MASK
1037 002106 024000 ;173106 024000 ,WORD 24000 ;ERROR MASK
1038 ; ;
1039 ; ;
1040 ; ;
1041 ; ; ;TM11 STARTING ADDRESS

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1042 002110 010703 ;173110 010703 TM11: MOV PC,R3 ;SAVE ERROR RETRY ADDRESS
1043 002112 012737 ;173112 012737 MOV #TMRWD,#TMC-2;REWIND TAPE
1044 002114 060017 ;173114 060017
1045 002116 172522 ;173116 172522
1046 002120 010702 ;173120 010702 MOV PC,R2
1047 002122 000555 ;173122 000555 BR TAPES
1048 002124 172524 ;173124 172524 ,WORD TMWC
1049 002126 060003 ;173126 060003 ,WORD TMRWD ;TM11 READ COMMAND
1050 002130 060011 ;173130 060011 ,WORD TMRWD ;TM11 FORWARD RECORD COM
1051 002132 000200 ;173132 000200 ,WORD 200 ;DONE MASK
1052 002134 100000 ;173134 100000 ,WORD 100000 ;ERROR MASK
1053 ; ;
1054 ; ; THIS IS THE STARTING ADDRESS FOR RF11 CONTROLLE
1055 002136 010703 ;173136 010703 RF11: MOV PC,R3 ;SAVE ERROR RETRY ADDRESS
1056 002140 010702 ;173140 010702 MOV PC,R2 ;SET POINTER TO PARAMETE
1057 002142 000516 ;173142 000516 BR OTHERA ;GO TO COMMON SERVICE RO
1058 ; ; ;ASSUME UNIT 0
1059 002144 177462 ;173144 177462 ,WORD RFWC ;DEVICE WORD COUNT REGIS
1060 002146 000005 ;173146 000005 ,WORD RFPREAD ;READ COMMAND
1061 ; ;
1062 ; ; THIS IS THE STARTING ADDRESS FOR RH/TU16/TM02
1063 002150 010703 ;173150 010703 TU16: MOV PC,R3 ;SAVE ERROR RETRY ADDRESS
1064 002152 012700 ;173152 012700 MOV #TUCS,R0;GET CONTROL STATUS WORD
1065 002154 172440 ;173154 172440
1066 002156 012710 ;173156 012710 TU16R: MOV #RHRPST,(R0);REWIND TAPE CLEAR E
1067 002160 000021 ;173160 000021
1068 002162 012760 ;173162 012760 MOV #TUMODE,32(R0);SET 800 BPI NORMA
1069 002164 001300 ;173164 001300
1070 002166 000032 ;173166 000032
1071 002170 012760 ;173170 012760 MOV #-1,6(R0);LOAD FRAME COUNT
1072 002172 177777 ;173172 177777
1073 002174 000006 ;173174 000006
1074 002176 012710 ;173176 012710 MOV #TUSPAC,(R0);SPACE FORWARD
1075 002200 000031 ;173200 000031
1076 002202 105760 ;173202 105760 18: TSTB 12(R0)
1077 002204 000012 ;173204 000012
1078 002206 100375 ;173206 100375 BPL 18 ;KEEP LOOPING
1079 002210 000433 ;173210 000433 BR RHCOMN
1080 ; ;
1081 ; ; THIS IS THE STARTING ADDRESS FOR RC11 CONTROLLE
1082 002212 010703 ;173212 010703 RC11: MOV PC,R3
1083 002214 010702 ;173214 010702 MOV PC,R2 ;ASSUME UNIT 0
1084 002216 000470 ;173216 000470 BR OTHERA
1085 002220 177450 ;173220 177450 ,WORD RCWC
1086 002222 000005 ;173222 000005 ,WORD RFPREAD
1087 ; ;
1088 ; ; THIS IS THE AUTO LOAD VECTOR
1089 002224 173000 ;173224 173000 ,WORD RHRSA
1090 002226 000340 ;173226 000340 ,WORD 340
1091 ; ;
1092 ; ; THIS IS THE STARTING ADDRESS FOR RH11 DEVICE CO
1093 ; ;
1094 ; ; ;NOTE! IF TM02/TU16 SHOULD BE SELECTED, THE VAL
1095 ; ; ;IN CONSOL SWITCH REGISTER IS THE POSITIO

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1096      ;                ;ON THE RH11 INSTEAD OF THE UNIT # ON TU1
1097      ;                ;THE SLAVE UNIT # (# ON TU16) SHOULD STIL
1098 002230 000405 ;173230 000405 RH11A: BR 10 ;ENTRY TO SELECT UNIT #
1099 002232 010703 ;173232 010703 RH11B: MOV PC,R3 ;ENTRY TO SELECT ALL UNI
1100 002234 113737 ;173234 113737 MOVB #0CSW,#RHCSA+10;LOAD UNIT # INB
1101 002236 177570 ;173236 177570
1102 002240 176310 ;173240 176310
1103 002242 000401 ;173242 000401 BR 20
1104 002244 010703 ;173244 010703 10: MOV PC,R3
1105 002246 012700 ;173246 012700 20: MOV #RHCSA,R0
1106 002250 176300 ;173250 176300
1107 002252 032760 ;173252 032760 RPCOMM: BIT #TUTAPE,26(R0);TAPE UNIT?
1108 002254 040000 ;173254 040000
1109 002256 000020 ;173256 000020
1110 002260 001336 ;173260 001336 BNE TU10RE ;YES, GO TO TAPE LOGIC
1111 002262 012710 ;173262 012710 MOV #RMRPST,(R0);RESET DRIVE
1112 002264 000021 ;173264 000021
1113 002266 012760 ;173266 012760 MOV #14000,32(R0);SET 16 BIT FORMAT
1114 002270 014000 ;173270 014000
1115 002272 000032 ;173272 000032
1116 002274 012710 ;173274 012710 MOV #DRCLR,(R0);CLEAR DRIVE ERROR
1117 002276 000011 ;173276 000011
1118      ;                ;
1119 002300 005720 ;173300 005720 RHCOMM: TST (R0)+ ;MOVE TO WORD COUNT ADDR
1120 002302 010037 ;173302 010037 MOV R0,#2 ;FAKE CALLING SEQUENCE
1121 002304 000002 ;173304 000002
1122 002306 012737 ;173306 012737 MOV #RHRDAD,004
1123 002310 000071 ;173310 000071
1124 002312 005004 ;173312 005004
1125 002314 005002 ;173314 005002 CLR R2 ;FOR FLAG AND POINTER TO
1126 002316 000430 ;173316 000430 BR OTHERA
1127      ;                ;
1128      ;                ;
1129      ;                ;
1130 002320 000405 ;173320 000405 RHRPA: BR 10 ;ENTRY FOR SELECT UNIT #
1131 002322 010703 ;173322 010703 RHRPB: MOV PC,R3 ;ENTRY TO SELECT ALL UNI
1132 002324 113737 ;173324 113737 MOVB #0CSW,#RHCSA+10;LOAD UNIT # INB
1133 002326 177570 ;173326 177570
1134 002330 176710 ;173330 176710
1135 002332 000401 ;173332 000401 BR 20
1136 002334 010703 ;173334 010703 10: MOV PC,R3
1137 002336 012700 ;173336 012700 20: MOV #RPCSA,R0
1138 002340 176700 ;173340 176700
1139 002342 000743 ;173342 000743 BR RPCOMM
1140      ;                ;
1141      ;                ;
1142 002344 013707 ;173344 013707 CSRGO: MOV #0CSW,PC
1143 002346 177570 ;173346 177570
1144      ;                ;
1145      ;                ;
1146      ;                ;
1147      ;                ;
1148 002350 000405 ;173350 000405 RP11A: BR 10 ;ENTRY TO SELECT UNIT #
1149 002352 010703 ;173352 010703 RP11B: MOV PC,R3 ;ENTRY TO SELECT ALL UNI

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1150 002354 113705 ;173354 113705 MOVB #0CSW,R5
1151 002356 177570 ;173356 177570
1152 002360 000305 ;173360 000305 SWAB R5 ;GET UNIT # INTO HIGH BY
1153 002362 000402 ;173362 000402 BR 30
1154 002364 010703 ;173364 010703 10: MOV PC,R3
1155 002366 005005 ;173366 005005 CLR R5
1156 002370 010702 ;173370 010702 30: MOV PC,R2
1157 002372 000403 ;173372 000403 BR OTHER
1158 002374 176716 ;173374 176716 ,WORD RPHC
1159 002376 000005 ;173376 000005 ,WORD RFPREAD
1160      ;                ;
1161 002400 005005 ;173400 005005 OTHERA: CLR R5 ;SET TO UNIT #
1162 002402 010200 ;173402 010200 OTHER: MOV R2,R0 ;R0 POINT AT WORD COUNT
1163 002404 005720 ;173404 005720 TST (R0)+ ;POINT TO PARAMETER LIST
1164 002406 012001 ;173406 012001 MOV (R0)+,R1;MOVE WORD COUNT ADDRESS
1165 002410 012711 ;173410 012711 MOV #=256,*2,(R1);LOAD WORD COUNT
1166 002412 177000 ;173412 177000
1167 002414 051005 ;173414 051005 BIS (R0),R5 ;COMBINE UNIT # WITH COM
1168 002416 010541 ;173416 010541 MOV R5,=(R1);LOAD READ COMMAND
1169 002420 032711 ;173420 032711 BIT #100200,(R1);CHECK FOR ERROR AND
1170 002422 100200 ;173422 100200
1171 002424 001775 ;173424 001775 BEQ .-4 ;WAIT UNTIL COMPLETE
1172 002426 100012 ;173426 100012 BPL 10 ;NO ERROR
1173 002430 005702 ;173430 005702 TST R2 ;WAS IT CALLED BY MASS B
1174 002432 001024 ;173432 001024 BNE AGAIN ;NO, ERROR
1175 002434 032761 ;173434 032761 BIT #TUTAPE,26(R1);IS TU16?
1176 002436 040000 ;173436 040000
1177 002440 000026 ;173440 000026
1178 002442 001420 ;173442 001420 BEQ AGAIN ;NO, ERROR
1179 002444 022761 ;173444 022761 CMP #FCE,14(R1);ARE WE READ A SHORT
1180 002446 001000 ;173446 001000
1181 002450 000014 ;173450 000014
1182 002452 001014 ;173452 001014 BNE AGAIN ;SOME OTHER ERROR
1183 002454 005007 ;173454 005007 10: CLR PC ;O.K.
1184      ;                ;
1185      ;                ;
1186 002456 010200 ;173456 010200 TAPES: MOV R2,R0 ;GET THE ADDRESS OF THE
1187 002460 005720 ;173460 005720 TST (R0)+ ;STEP TO LAST COMMAND
1188 002462 012001 ;173462 012001 MOV (R0)+,R1;GET THE WORD COUNT ADDR
1189 002464 005311 ;173464 005311 DEC (R1) ;SET UP TO ADVANCE 1 REC
1190 002466 005720 ;173466 005720 TST (R0)+ ;MOVE R0 TO FIRST COMMAN
1191 002470 012041 ;173470 012041 MOV (R0)+,=(R1);LOAD COMMAND REG.
1192 002472 031011 ;173472 031011 BIT (R0),(R1);DONE?
1193 002474 001776 ;173474 001776 BEQ .-2 ;NO, KEEP LOOPING
1194 002476 005720 ;173476 005720 TST (R0)+ ;YES, CHECK FOR ERROR
1195 002500 031041 ;173500 031041 BIT (R0),=(R1);ANY ERROR?
1196 002502 001736 ;173502 001736 BEQ OTHERA ;NO ERROR= TRY TO READ
1197 002504 000005 ;173504 000005 AGAIN: RESET
1198      ;                ;
1199 002506 000113 ;173506 000113 JMP (R3) ;ERROR RETURN
1200      ;                ;
1201      ;                ;
1202 002510 012704 ;173510 012704 KL11: MOV #KLC8,R4;OBTAIN CONTROL REG.
1203 002512 177560 ;173512 177560

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1204 002514 000443 1173514 000443 BR CKDEV ;AND TRANSFER TO READER
1205
1206
1207
1208
1209 002516 .BYTE 240 1173516 240 TABLE1 .BYTE 240 ;COMPARE WORD NOT A COMM
1210 002517 .BYTE 037 1173517 037 .BYTE 37 ;ILBS+RWD+GO
1211 002520 .BYTE 015 1173520 015 .BYTE 15 ;SPACE FORWARD BLOCK+GO
1212 002521 .BYTE 005 1173521 005 .BYTE 5 ;READ
1213 002522 .BYTE 024 1173522 024 .BYTE 24 ;READ +ILBS
1214 002523 .BYTE 224 1173523 224 .BYTE 224 ;READ+ILBS+END FLAG
1215
1216 ;
1217 002524 000404 1173524 000404 CBOOTA: BR 18 ;SELECT UNIT 0
1218 002526 113704 1173526 113704 CBOOTB: MOV 08C5W,R4;SELECT UNITS
1219 002530 177570 1173530 177570
1220 002532 000304 1173532 000304 SWAB R4
1221 002534 000401 1173534 000401 BR RESETX
1222 002536 005004 1173536 005004 131 CLR R4
1223 002540 012700 1173540 012700 RESETX: MOV #TACS,R0;GET CONTROL REG.
1224 002542 177500 1173542 177500
1225 002544 000005 1173544 000005 RESTR1: RESET
1226 002546 010410 1173546 010410 MOV R4,(R0);SELECT UNIT
1227 002550 012701 1173550 012701 MOV #TABLE,R1
1228 002552 173516 1173552 173516
1229 002554 012702 1173554 012702 MOV #375,R2;LOAD TRANSFER COUNTER
1230 002556 000375 1173556 000375
1231 002560 112103 1173560 112103 MOVB (R1)+,R3;LOAD COMPARATOR
1232 002562 112110 1173562 112110 LOOP1: MOVB (R1)+,(R0);LOAD COMMAND
1233 002564 100407 1173564 100407 BMI DONE
1234 002566 130310 1173566 130310 LOOP2: BITB R3,(R0);COMMAND COMPLETE?
1235 002570 001776 1173570 001776 BEQ LOOP2 ;NO, WAIT
1236 002572 105202 1173572 105202 INCB R2 ;INCREMENT ADDRESS CTR.
1237 002574 100772 1173574 100772 BMI LOOP1 ;IF (-), GET COMMAND
1238 002576 116012 1173576 116012 MOVB 2(R0),(R2);STORE DATA
1239 002600 000002 1173600 000002
1240 002602 000771 1173602 000771 BR LOOP2 ;GET ANOTHER BYTE
1241 002604 005710 1173604 005710 DONE: TST (R0);ANY ERROR?
1242 002606 100756 1173606 100756 BMI RESTR1 ;YES, RETRY
1243 002610 005002 1173610 005002 CLR R2 ;CLEAR COMPARE ADDRESS
1244 002612 120312 1173612 120312 CMPB R3,(R2);IT MUST BE 240
1245 002614 001377 1173614 001377 RNE
1246 002616 000112 1173616 000112 ERROR: JMP (R2)
1247
1248 ;
1249 002620 012704 1173620 012704 PC11: MOV #PCC5,R4
1250 002622 177550 1173622 177550
1251 002624 000005 1173624 000005 CKDEV: RESET
1252 002626 012701 1173626 012701 MOV #160000,R1;SET UP MEMORY TEST LI
1253 002630 160000 1173630 160000
1254 002632 012702 1173632 012702 MOV #6,R2 ;SET UP POINTER TO TIMEO
1255 002634 000006 1173634 000006
1256 002636 012712 1173636 012712 MOV #340,(R2);SET UP VECTOR TO RETUR
1257 002640 000340 1173640 000340
    
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1258 002642 010742 1173642 010742 MOV PC,-(R2);SAVE PC
1259 002644 012706 1173644 012706 MOV #24,SP ;LOAD UP STACK POINTER
1260 002646 000024 1173646 000024
1261 002650 010441 1173650 010441 MOV R4,-(R1);LOOK FOR END OF MEMORY
1262 002652 040601 1173652 040601 BIC SP,R1 ;THEN DROP TO XX752
1263 002654 010111 1173654 010111 MOV R1,(R1) ;AND STORE IN ITSELF
1264 002656 011102 1173656 011102 LOOP: MOV (R1),R2
1265 002660 005214 1173660 005214 INC (R4) ;START DEVICE
1266 002662 105714 1173662 105714 RDRWAT: TSTB (R4) ;WAIT
1267 002664 100376 1173664 100376 BPL RDRWAT
1268 002666 116412 1173666 116412 MOVB 2(R4),(R2);SAVE THE DATA
1269 002670 000002 1173670 000002
1270 002672 005211 1173672 005211 INC (R1)
1271 002674 120227 1173674 120227 CMPB R2,#375
1272 002676 000375 1173676 000375
1273 002680 001366 1173680 001366 BNE LOOP ;NO
1274 002702 105222 1173702 105222 INCB (R2) ;YES
1275 002704 000142 1173704 000142 JMP -(R2)
1276 002706 000000 1173706 000000 ;THIS AREA IS UNUSED
1277 002710 000000 1173710 000000 ;THIS AREA IS UNUSED
1278 002712 000000 1173712 000000 ;THIS AREA IS UNUSED
1279 002714 000000 1173714 000000 ;THIS AREA IS UNUSED
1280 002716 000000 1173716 000000 ;THIS AREA IS UNUSED
1281 002720 000000 1173720 000000 ;THIS AREA IS UNUSED
1282 002722 000000 1173722 000000 ;THIS AREA IS UNUSED
1283 002724 000000 1173724 000000 ;THIS AREA IS UNUSED
1284 002726 000000 1173726 000000 ;THIS AREA IS UNUSED
1285 002730 000000 1173730 000000 ;THIS AREA IS UNUSED
1286 002732 000000 1173732 000000 ;THIS AREA IS UNUSED
1287 002734 000000 1173734 000000 ;THIS AREA IS UNUSED
1288 002736 000000 1173736 000000 ;THIS AREA IS UNUSED
1289 002740 000000 1173740 000000 ;THIS AREA IS UNUSED
1290 002742 000000 1173742 000000 ;THIS AREA IS UNUSED
1291 002744 000000 1173744 000000 ;THIS AREA IS UNUSED
1292 002746 000000 1173746 000000 ;THIS AREA IS UNUSED
1293 002750 000000 1173750 000000 ;THIS AREA IS UNUSED
1294 002752 000000 1173752 000000 ;THIS AREA IS UNUSED
1295 002754 000000 1173754 000000 ;THIS AREA IS UNUSED
1296 002756 000000 1173756 000000 ;THIS AREA IS UNUSED
1297 002760 000000 1173760 000000 ;THIS AREA IS UNUSED
1298 002762 000000 1173762 000000 ;THIS AREA IS UNUSED
1299 002764 000000 1173764 000000 ;THIS AREA IS UNUSED
1300 002766 000000 1173766 000000 ;THIS AREA IS UNUSED
1301 002770 000000 1173770 000000 ;THIS AREA IS UNUSED
1302 002772 000000 1173772 000000 ;THIS AREA IS UNUSED
1303 002774 000000 1173774 000000 ;THIS AREA IS UNUSED
1304 002776 END, YR1
1305 002778 000000 1173776 000000 ;THIS AREA IS UNUSED
    
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1306 003000 MAP,YC;
1307 ;THE FOLLOWING 1000 LOCATIONS ARE
1308 ;A REPRODUCTION OF THE ROM PROGRAM
1309 ;FOR THE BM873YC, THE FIRST 400 LOCATIONS
1310 ;ARE AN EXACT COPY OF THE BM873YA, THE
1311 ;REMAINING 400 LOCATIONS ARE
1312 ;THE DDCMP BOOTSTRAP ROM PROGRAM,
1313 ;IT IS HERE FOR COMPARISON TO
1314 ;ACTUAL ROM AND FOR REFERENCE.
1315 ;173000 ,=173000 ;STARTING ADDRESS FOR BOOTSTRAP
1316 ;THIS LOADER IS DESIGNED FOR THE RESTART MODULE M873,
1317 ;IT FUNCTIONALLY REPLACES THE FOLLOWING ROMS:
1318 ;M792-YA = PAPER TAPE BOOTSTRAP FOR PC11,KL11
1319 ;MR11-DB BULK STORAGE BOOTSTRAP ROM
1320 ;M792-YH TA11 CASSETTE BOOTSTRAP ROM
1321 ;REGISTER DEFINITIONS
1322 R0= 000000
1323 R1= 000001
1324 R2= 000002
1325 R3= 000003
1326 R4= 000004
1327 R5= 000005
1328 SP= 000006
1329 PC= 000007
1330 SR= 177570 ;PROCESSOR SWITCH REGISTER
1331 ;
1332 ;STARTING LOCATION FOR RP11 DISK
1332 003000 010702 ;173000 010702 RF11: MOV PC,R2 ;SET POINTER TO PARAMETER LISTS
1333 003002 000464 ;173002 000464 BR OTHER ;TRANSFER TO SERVICE ROUTINE
1334 003004 177462 ;173004 177462 .WORD 177462 ;DEVICE WORD COUNT ADDRESS
1335 003006 000005 ;173006 000005 .WORD 5 ;DEVICE READ INSTRUCTION
1336 ;
1337 ;THIS IS THE STARTING LOCATION FOR THE RK11 CONTROLLER
1338 003010 010702 ;173010 010702 RK11: MOV PC,R2 ;SET POINTER TO PARAMETER LIST
1339 003012 000460 ;173012 000460 BR OTHER ;TRANSFER TO SERVICE ROUTINE
1340 003014 177406 ;173014 177406 .WORD 177406 ;DEVICE WORD COUNT REGISTER
1341 003016 000005 ;173016 000005 .WORD 5 ;DEVICE READ INSTRUCTION
1342 ;
1343 ;THIS IS A SPARE STARTING LOCATION, IT TRANSFERS TO ADDRESS
1344 ;CONTAINED IN THE SWITCH REGISTER.
1345 003020 013707 ;173020 013707 TRANSR: MOV #SR,PC ;GO TO INDICATED LOCATION
1346 003022 177570 ;173022 177570
1347 ;NOTE 773024 AND 773224 ARE DEPENDENT ON OFFSET IN DIODES FOR LINE 1
1348 ;
1349 ;THIS IS THE POWER UP VECTOR REQUIRED FOR DEVICE AND
1350 003024 173000 ;173024 173000 POWER: .WORD RF11 ;ADDRESS OF FIRST LOCATION IN ROM
1351 003026 000340 ;173026 000340 .WORD 340 ;PROCESSOR STATUS LEVEL 7
1352 ;
1353 ;THIS IS THE STARTING ADDRESS FOR TC11 (DECTAPE) CONTROLLER.
1354 003030 010702 ;173030 010702 TC11: MOV PC,R2 ;SET UP POINTER TO PARAMETER LIST
1355 003032 000426 ;173032 000426 BR TAPES ;AND TRANSFER TO FIRST ROUTINE
1356 003034 177344 ;173034 177344 .WORD 177344 ;DEVICE WORD COUNT ADDRESS
1357 003036 004003 ;173036 004003 .WORD 4003 ;FIND PREVIOUS BLOCK COMMAND
1358 003040 100000 ;173040 100000 .WORD 100000 ;USED AS DONE INDICATOR
1359 003042 024000 ;173042 024000 .WORD 24000 ;USED AS ERROR INDICATOR/TEST FLAG
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1360 003044 000445 ;173044 000445 BR OTHERX ;THEN TRANSFER TO NEXT ROUTINE
1361 003046 000005 ;173046 000005 .WORD 5 ;DEVICE READ COMMAND
1362 ;
1363 ;THIS IS THE START LOCATION FOR TM11 MAGTAPE CONTROLLER
1364 003050 010702 ;173050 010702 TM11: MOV PC,R2 ;SET POINTER TO PARAMETER LIST
1365 003052 000416 ;173052 000416 BR TAPES ;AND TRANSFER TO FIRST ROUTINE
1366 003054 172524 ;173054 172524 .WORD 172524 ;DEVICE BYTE/RECORD COUNT REGISTER
1367 003056 600017 ;173056 600017 .WORD 60017 ;DEVICE REMIND COMMAND
1368 003060 000200 ;173060 000200 .WORD 200 ;DEVICE DONE FLAG
1369 003062 100000 ;173062 100000 .WORD 100000 ;DEVICE ERROR FLAG BIT
1370 003064 000413 ;173064 000413 BR TAPESX ;THEN TRANSFER TO NEXT SERVICE RTN
1371 003066 600011 ;173066 600011 .WORD 60011 ;DEVICE FORWARD SPACE COMMAND
1372 003070 000200 ;173070 000200 .WORD 200 ;SAME AS ABOVE
1373 003072 100000 ;173072 100000 .WORD 100000 ;SAME AS ABOVE
1374 003074 000431 ;173074 000431 BR OTHERX ;THEN TRANSFER TO READ/TRANSFER ROUTINE
1375 003076 600003 ;173076 600003 .WORD 60003 ;DEVICE READ COMMAND
1376 ;
1377 ;THIS IS THE START LOCATION FOR THE RP11 CONTROLLER
1378 003100 010702 ;173100 010702 RP11: MOV PC,R2 ;SET POINTER TO PARAMETER LIST
1379 003102 000424 ;173102 000424 BR OTHER ;TRANSFER TO TRANSFER ROUTINE
1380 003104 176716 ;173104 176716 .WORD 176716 ;DEVICE WORD COUNT REGISTER
1381 003106 000005 ;173106 000005 .WORD 5 ;DEVICE READ COMMAND
1382 ;
1383 ;THIS IS THE TAPE DEVICE SERVICE ROUTINE,
1384 003110 010200 ;173110 010200 TAPES: MOV R2,R0 ;GET ADDRESS OF PARAMETER LIST
1385 003112 005720 ;173112 005720 TST (R0)+ ;SKIP TWO WORDS FIRST TIME
1386 003114 000005 ;173114 000005 TAPESX: RESET ;RESET ALL DEVICES
1387 003116 005720 ;173116 005720 TST (R0)+ ;SKIP OVER BRANCH INSTRUCTION
1388 003120 016201 ;173120 016201 MOV 2(R2),R1 ;THEN GET DEVICE WORD/BYTE COUNT ADDRESS
1389 003122 000002 ;173122 000002
1390 003124 005311 ;173124 005311 DEC R1 ;AND SET TO -1
1391 003126 012041 ;173126 012041 MOV (R0)+,(R1) ;AND THEN ISSUE COMMAND TO DEVICE
1392 003130 031011 ;173130 031011 TAPWAT: BIT #R0,R1 ;WAIT FOR DEVICE COMPLETION
1393 003132 001776 ;173132 001776 BEQ TAPWAT ;BY HANGING IN LOOP
1394 003134 005720 ;173134 005720 TST (R0)+ ;AND THEN SKIP DONE FLAG
1395 003136 032041 ;173136 032041 BIT (R0)+,(R1) ;THEN TEST FOR ERROR
1396 003140 001063 ;173140 001063 BNE ERROR ;THERE IS ONE
1397 003142 000110 ;173142 000110 RETURN: JMP #R0 ;AND TRANSFER TO FOLLOWING INSTRUCTION
1398 ;
1399 ;THIS IS THE STARTING ADDRESS FOR RC11 DISK CONTROLLERS
1400 003144 010702 ;173144 010702 RC11: MOV PC,R2 ;SET UP POINTER TO PARAMETER LIST
1401 003146 000402 ;173146 000402 BR OTHER ;TRANSFER TO SERVICE RTN
1402 003150 177450 ;173150 177450 .WORD 177450 ;DEVICE WORD COUNT REGISTER
1403 003152 000005 ;173152 000005 .WORD 5 ;DEVICE READ INSTRUCTION
1404 ;
1405 ;THIS ROUTINE PERFORMS THE ACTUAL TRANSFER TO MEMORY OF DATA
1406 003154 010200 ;173154 010200 OTHER: MOV R2,R0 ;SET POINTER TO LIST IN R0
1407 003156 005720 ;173156 005720 TST (R0)+ ;SKIP TWO WORDS FIRST TIME.
1408 003160 005720 ;173160 005720 OTHERX: TST (R0)+ ;SKIP PAST BR INSTRUCTION
1409 003162 000005 ;173162 000005 RESET ;REST THE WORLD
1410 003164 016201 ;173164 016201 MOV 2(R2),R1 ;OBTAIN DEVICE WORD COUNT ADDRESS
1411 003166 000002 ;173166 000002
1412 003170 012711 ;173170 012711 MOV #-1000,R1 ;THEN OBTAIN LARGE WORD COUNT
1413 003172 177000 ;173172 177000
```

DZBMD MACY11 27(657) 5-MAR-75 11:47 PAGE 31
BM873D.P11 MAINDEC-11=DZBMD=D BM873 UNIVERSAL RESTART LOADER DIAGNOSTIC.

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1414 003174 011041 1173174 011041      MOV #R0,-(R1)    ;AND PUT COMMAND TO DEVICE
1415 003176 105711 1173176 105711      OTHWAT: TSTB #R1    ;WAIT FOR DONE FLAG
1416 003200 100376 1173200 100376      BPL OTHWAT      ;BY HANGING IN LOOP
1417 003202 005711 1173202 005711      TST #R1        ;THEN TEST FOR ERROR
1418 003204 100441 1173204 100441      BMI ERROR      ;GOT PROBLEMS
1419 003206 005007 1173206 005007      CLR PC         ;AND TRANSFER TO ZERO
1420
1421
1422 003210 012704 1173210 012704      ;THIS IS THE STARTING ADDRESS FOR THE PC11 PAPER TAPE CONTROLLER
1423 003212 177560 1173212 177560      KL11: MOV #177560,R4 ;OBTAIN DEVICE ADDRESS
1424 003214 000440 1173214 000440      BR CKDEV      ;AND TRANSFER TO READER SERVICE ROUTINE
1425
1426
1427
1428 003216 017640 1173216 240          ;THIS IS THE CASSETTE DEVICE COMMAND TABLE
1429 003217 037          TABLE: .BYTE 240    ;COMPARE WORD NOT A COMMAND
1430 003220 002415 1173220 015          .BYTE 37         ;ILBS+RWD+GO
1431 003221 1173221 005          .BYTE 15        ;SPACE FORWARD BLOCK+GO
1432 003222 112024 1173222 024          .BYTE 5         ;READ+GO
1433 003223 1173223 224          .BYTE 24        ;READ+ILBS
1434 003224 1173224 224          .BYTE 224       ;READ+ILBS+END FLAG
1435 ;NOTE 773024 AND 773224 ARE DEPENDENT ON OFFSET IN DIODES FOR LINE 1
1436
1437 003224 173000 1173224 173000      ;THIS IS AN ADDITIONAL POWER VECTOR ADDRESS REQUIRED BY DEVICE
1438 003226 000340 1173226 000340      POWER2: .WORD R11  ;ADDRESS OF BEGINNING OF BOOTSTRAP
1439                                     .WORD 340        ;PRIORITY LEVEL 7
1440
1441 003230 005004 1173230 005004      ;THIS IS THE STARTING ADDRESS FOR THE CASSETTE DEVICE #0
1442 003232 012700 1173232 012700      CBOOT: CLR R4    ;LOAD DEVICE NUMBER 0 IN R4
1443 003234 177500 1173234 177500      RESTX: MOV #177500,R0 ;GET DEVICE ADDRESS
1444 003236 000005 1173236 000005      RESTR1: RESET    ;ISSUE RESET INSTRUCTION
1445 003240 010410 1173240 010410      MOV R4,#R0      ;LOAD DEVICE WITH UNIT NUMBER
1446 003242 012701 1173242 012701      MOV #TABLE,R1   ;GET FUNNY TABLE OF INSTRUCTIONS
1447 003244 173216 1173244 173216
1448 003246 012702 1173246 012702      MOV #375,R2    ;AND LOAD UP TRANSFER COUNTER
1449 003250 000375 1173250 000375
1450 003252 112103 1173252 112103      MOV# (R1)+,R3  ;THE LOAD UP COMPARATOR
1451 003254 112110 1173254 112110      LOOP1: MOV# (R1)+,#R0 ;LOAD DEVICE REGISTER WITH COMMAND
1452 003256 100407 1173256 100407      BMI DONE
1453 003260 130310 1173260 130310      LOOP2: BIT# R3,#R0  ;HAS COMMAND COMPLETED
1454 003262 001776 1173262 001776      BEQ LOOP2      ;NO, WAIT
1455 003264 105202 1173264 105202      INCB R2        ;THEN INCREMENT ADDRESS CTR
1456 003266 100772 1173266 100772      BMI LOOP1     ;IF NEGATIVE, GET COMMAND
1457 003270 116012 1173270 116012      MOV# 2,(R0),#R2 ;AND STORE DATA AWAY
1458 003272 000002 1173272 000002
1459 003274 000771 1173274 000771      BR LOOP2
1460 003276 005710 1173276 005710      DONE: TST #R0   ;GO GET ANOTHER BYTE
1461 003300 100756 1173300 100756      BMT #R0       ;ANY DEVICE ERRORS
1462 003302 005002 1173302 005002      BMI RESTR1    ;YES, RETRY
1463 003304 120312 1173304 120312      CLR R2        ;CLEAR COMPARE ADDRESS AND TRANSFER ADDRESS
1464 003306 001377 1173306 001377      CNPB R3,#R2   ;IT MUST BE 240
1465 003310 000112 1173310 000112      BNE #+0       ;NO, THERE WAS AN ERROR
1466                                     ERROR: JMP #R2  ;NORMAL CASSETTE AND ERROR FOR BULK STORAGE
1467
;THIS IS THE STARTING LOCATION FOR THE PC11 CONTROLLER

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DZBMD MACY11 27(657) 5-MAR-75 11:47 PAGE 32
BM873D.P11 MAINDEC-11=DZBMD=D BM873 UNIVERSAL RESTART LOADER DIAGNOSTIC.

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1468 003312 012704 1173312 012704      PC11: MOV #177550,R4 ;LOAD DEVICE ADDRESS
1469 003314 177550 1173314 177550
1470 003316 000005 1173316 000005      CKDEV: RESET    ;KILL ALL DEVICE ACTION
1471 003320 012701 1173320 012701      MOV #160000,R1 ;THEN SET UP MEMORY TEST LIMITS
1472 003322 160000 1173322 160000
1473 003324 012702 1173324 012702      MOV #6,R2     ;AND SET UP POINTER TO TIMEOUT LOCATION
1474 003326 000006 1173326 000006
1475 003330 012712 1173330 012712      MOV #340,#R2  ;AND SET UP VECTOR TO RETURN TO NEXT
1476 003332 000340 1173332 000340
1477 003334 010742 1173334 010742      MOV #C,-(R2)  ;SAVE THE PC
1478 003336 012706 1173336 012706      MOV #24,SP    ;AND LOAD UP STACK POINTER
1479 003340 000024 1173340 000024
1480 003342 010441 1173342 010441      MOV R4,#(R1)  ;AND LOOK FOR END OF MEMORY
1481 003344 040601 1173344 040601      BIC SP,R1     ;THEN DROP TO XX7752
1482 003346 010111 1173346 010111      MOV R1,#R1   ;AND STORE IN ITSELF
1483 003350 011102 1173350 011102      LOOP: MOV #R1,R2 ;THEN LOAD ADDRESS FOR DATA INSERTION
1484 003352 005214 1173352 005214      INC #R4      ;AND START DEVICE
1485 003354 105714 1173354 105714      RDRWAT: TSTB #R4 ;THEN WAIT FOR CHARACTER AVAILABLE
1486 003356 100376 1173356 100376      BPL RDRWAT   ;HANGING THERE IF NECESSARY
1487 003360 116412 1173360 116412      MOV# 2,(R4),#R2 ;STORE AWAY DATA BYTE
1488 003362 000002 1173362 000002
1489 003364 005211 1173364 005211      INC #R1
1490 003366 120227 1173366 120227      CMPB R2,#375  ;HAS BRANCH OFFSET BEEN STORED
1491 003370 000375 1173370 000375
1492 003372 001366 1173372 001366      BNE LOOP     ;NO
1493 003374 105222 1173374 105222      INCB (R2)+   ;YES, ALL DONE
1494 003376 000142 1173376 000142      JMP -(R2)    ;THEN TRANSFER TO RTN
1495
1496 ;THE FOLLOWING 400 LOCATIONS ARE
1497 ;A REPRODUCTION OF THE DDCMP BOOT-
1498 ;STRAP ROM. IT IS HERE FOR COM-
1499 ;PARISON TO THE ACTUAL ROM AND
1500 ;FOR REFERENCE.
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VERSION 01

STUART WECKER 01/22/75

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1522 )
1523 )
1524 ) DIGITAL EQUIPMENT CORPORATION
1525 ) COMPUTER NETWORK FACILITIES
1526 ) DOWN-LINE LOADING PROGRAM
1527 )
1528 ) THIS PROGRAM LOADS COMPUTER MEMORY FROM DATA SENT OVER
1529 ) A DATA COMMUNICATIONS LINK, IT SENDS AND RECEIVES
1530 ) MESSAGES IN DDCMP BOOT FORMAT. THE PRIMARY BOOT ONLY
1531 ) LOADS A SINGLE BLOCK, THE SECONDARY BOOT, WHICH
1532 ) THEN REQUESTS AND LOADS THE DESIRED PROGRAM.
1533 )
1534 ) CURRENT VERSION DDCMP: 3.0 - MAY 7, 1974
1535 )
1536 ) THE BOOTSTRAP MESSAGES ARE OF THE FORM:
1537 )
1538 ) SYN, SYN, DLE, CNT, F, S, FILL, FILL, ADDR, CRC1, DATA, CRC2
1539 )
1540 ) ALL ITEMS ARE 0-BITS LONG UNLESS OTHERWISE SPECIFIED
1541 )
1542 ) SYN-THE SYNC CHARACTER=SYNC-226, ASYNC-377
1543 ) DLE-THE BOOT HEADER CHARACTER=OCTAL 228
1544 ) CNT-THE 14-BIT COUNT FIELD-LENGTH OF DATA FIELD
1545 ) F-THE FINAL BIT-LINK CONTROL
1546 ) S-THE SELECT BIT-LINK CONTROL
1547 ) FILL-A FILL CHARACTER=OCTAL 000
1548 ) ADDR-THE STATION ADDR-FOR PT. TO PT.#1
1549 ) CRC1-THE 16-BIT CRC-16 COMPUTED ON DLE THROUGH ADDR
1550 ) DATA-THE BOOT DATA AS FOLLOWS:
1551 ) CODE, INFO
1552 ) ONLY THE FOLLOWING CODES ARE USED BY THE
1553 ) PRIMARY BOOT
1554 ) CODE=10 REQUEST SECONDARY PROGRAM
1555 ) INFO=DEVICE TYPE, STATION ADDRESS
1556 ) DEVICE TYPE=DP#0, DU#2, DL#4, DQ#6
1557 ) STATION ADDRESS=1
1558 ) CODE#6 PROGRAM LOAD WITH TRANSFER ADDRESS
1559 ) INFO=BLKNO, BLK LDADDR, IMAGE DATA, TRANS ADDR
1560 ) BLKNO#0
1561 ) BLOCK LDADDR#6
1562 ) TRANS ADDR#6
1563 ) HEADER COUNT > OR = TO 10.
1564 ) ADDRESSES ARE 4 BYTES=32 BITS-LOW BIT FIRST
1565 ) CRC2-THE 16-BIT CRC-16 COMPUTED ON THE DATA FIELD ONLY
1566 )
1567 ) OPTION SWITCHES:
1568 ) DEVICE=DP11, DU11, DL11
1569 ) CRC=KG11, SCRC
1570 )
1571 )
1572 )
1573 ) REGISTER DEFINITIONS
1574 )
1575 ) 000000 R0=40 ;BLOCK LOAD ADDR
  
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1576 ) 000001 R1=41 ;DEVICE CSR ADDRESS
1577 ) 000002 R2=42 ;CRC CALC TEMP
1578 ) 000003 R3=43 ;SOFTWARE CRC
1579 ) 000004 R4=44 ;BLOCK CHAR COUNT
1580 ) 000005 R5=45 ;CRC CALC TEMP
1581 ) 000006 SP=46 ;STACK ADDR
1582 ) 000007 PC=47 ;LOCATION COUNTER
1583 )
1584 ) LITERALS
1585 )
1586 ) 000001 $STADR=1 ;STATION ADDR
1587 ) 177570 $SWR=177570 ;SWITCH REGISTER ADDR
1588 ) 000226 $SYN=226 ;SYNC CHARACTER
1589 ) 000220 $DLE=220 ;DDCMP DLE CHARACTER
1590 ) 000400 $STRIP=400
1591 )
1592 ) THE STACK IS USED AS FOLLOWS:
1593 ) STACK=2:FOR JSR TO GET ROUTINE
1594 ) STACK=4:TEMP FOR CRC CALCULATION
1595 )
1596 ) START OF BOOT PROGRAM
1597 )
1598 ) START1=DEVICE UNIT 0-NORMAL CONFIGURATION
1599 ) START2=USE SWITCH REG AS DEVICE DISPLACEMENT
1600 ) I.E. #0=0, #1=10, #2=20
1601 )
1602 ) =173400
1603 003400 012700 1173400 012700 START1: MOV (PC)+,R0 ;NON ZERO VALUE TO R0
1604 003402 005000 1173402 005000 START2: CLR R0 ;CLEAR R0
1605 003404 000005 1173404 000005 RESET ;RESET SYS, MEM MGT, ETC...
1606 003406 012706 1173406 012706 MOV #17776,SP ;STACK AT 4K-2
1607 003410 017776 1173410 017776
1608 )
1609 ) FIND THE DU-11 IN THE FLOATING ADDRESS SPACE
1610 )
1611 003412 010702 1173412 010702 MOV PC,R2 ;CURRENT PC
1612 003414 062702 1173414 062702 ADD #DEVTAB-,R2 ;DEVICE TABLE ADDR
1613 003416 000360 1173416 000360
1614 003420 012703 1173420 012703 MOV #6,R3 ;TRAP PS ADDR
1615 003422 000006 1173422 000006
1616 003424 005013 1173424 005013 CLR (R3) ;CLEAR NEW PS
1617 003426 010243 1173426 010243 MOV R2,-(R3) ;TABLE ADDR TO LOC 4
1618 003430 160313 1173430 160313 SUB R3,(R3) ;SUB TO TRAP RTN
1619 003432 005303 1173432 005303 DEC R3 ;LEAVE CNT 3 FOR LOOP
1620 003434 012701 1173434 012701 MOV #160010,R1 ;START SEARCH ADDR
1621 003436 160010 1173436 160010
1622 003440 005711 1173440 005711 DEVELOP: TST (R1) ;IS DEVICE THERE
1623 003442 111204 1173442 111204 MOVB (R2),R4 ;DEVICE INCREMENT TO R3
1624 003444 060401 1173444 060401 ADD R4,R1 ;UPDATE TO NEXT DEVICE
1625 003446 005201 1173446 005201 INC R1 ;INCREMENT MODULO
1626 003450 040401 1173450 040401 BIC R4,R1 ;CLEAR EXCESS
1627 003452 005703 1173452 005703 TST R3 ;TEST FOR DONE
1628 003454 001371 1173454 001371 BNE DEVELOP ;NOT YET
1629 003456 005700 1173456 005700 TST R0 ;TEST SWITCH REG USE
  
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1630 003460 001002 1173460 001002 BNE SNDREQ ;NO SWITCH REG
1631 003462 063701 1173462 063701 ADD #063701,R1 ;ADD SWR VALUE
1632 003464 177570 1173464 177570
1633
1634 ;
1635 ; SET UP DEVICE FOR OUTPUT
1636 ;
1637 003466 012711 1173466 012711 SNDREQ: MOV #6,(R1) ;DATA TERM RDY AND REQ TO SEND
1638 003470 000006 1173470 000006 MOV #36000+8SYN,2(R1) ;SET SYNC REGISTER
1639 003472 012761 1173472 012761
1640 003474 036226 1173474 036226
1641 003476 000002 1173476 000002
1642 003500 032711 1173500 032711 L3: BIT #20000,(R1) ;TEST CLEAR TO SEND
1643 003502 020000 1173502 020000
1644 003504 001775 1173504 001775 BEQ L3 ;NOT YET
1645 003506 022121 1173506 022121 CMP (R1)+,(R1)+ ;MOVE PTR TO XMIT TSR
1646 003510 052711 1173510 052711 BIS #20,(R1) ;TURN SEND ON
1647 003512 000020 1173512 000020
1648
1649 ;
1650 ; SEND SECONDARY PGM REQUEST MESSAGE
1651 003514 010700 1173514 010700 MOV PC,R0 ;CURRENT PC
1652 003516 062700 1173516 062700 ADD #RQMSG-,R0 ;REQUEST MSG ADDR
1653 003520 000230 1173520 000230
1654 003522 012704 1173522 012704 MOV #RQMSG-RQMSG,R4 ;COUNT
1655 003524 000026 1173524 000026
1656 003526 112061 1173526 112061 L4: MOVB (R0)+,2(R1) ;CHAR TO XMIT REGISTER
1657 003530 000002 1173530 000002
1658 003532 105711 1173532 105711 L5: TSTB (R1) ;DONE YET ?
1659 003534 100376 1173534 100376 BPL L5 ;NO
1660 003536 005304 1173536 005304 DEC R4 ;DECREMENT COUNT
1661 003540 001372 1173540 001372 BNE L4 ;ONCE MORE
1662 003542 042711 1173542 042711 BIC #20,(R1) ;DROP SEND
1663 003544 000020 1173544 000020
1664 003546 024141 1173546 024141 CMP -(R1),-(R1) ;RESET PTR TO RCV CSR
1665
1666 ;
1667 ; GET SECONDARY PROGRAM
1668 ;
1669 003550 042711 1173550 042711 GETPGM: BIC #20,(R1) ;CLEAR SEARCH SYNC
1670 003552 000020 1173552 000020
1671 003554 012711 1173554 012711 MOV #422,(R1) ;SET FOR CLEAR AND STRIP SYNC
1672 003556 000422 1173556 000422
1673 003560 005003 1173560 005003 CLR R3 ;CLEAR CRC VALUE
1674
1675 ;
1676 ; GET MESSAGE HEADER AND CHECK IT
1677 003562 012700 1173562 012700 MOV #1,R0 ;LOAD HDR AT LOC. 1
1678 003564 000001 1173564 000001
1679 003566 012704 1173566 012704 MOV #8,,R4 ;BLOCK COUNT
1680 003570 000010 1173570 000010
1681 003572 004767 1173572 004767 JSR PC,GET ;GET HEADER
1682 003574 000060 1173574 000060
1683 003576 005703 1173576 005703 TST R3 ;CHECK HEADER CRC

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1684 003600 001363 1173600 001363 BNE GETPGM ;NO GOOD
1685 003602 123727 1173602 123727 CMPB #*6,8STADR ;CHECK FOR MY ADDR
1686 003604 000006 1173604 000006
1687 003606 000001 1173606 000001
1688 003610 001357 1173610 001357 BNE GETPGM ;NOT MINE
1689 003612 123727 1173612 123727 CMPB #*1,*8DLE ;IS THIS A DLE MSG
1690 003614 000001 1173614 000001
1691 003616 000220 1173616 000220
1692 003620 001322 1173620 001322 BNE SNDREQ ;NO, ASK FOR ONE
1693
1694 ;
1695 ; GET DATA BLOCK
1696 003622 013704 1173622 013704 MOV #02,R4 ;DATA FIELD LENGTH
1697 003624 000002 1173624 000002
1698 003626 042704 1173626 042704 BIC #140000,R4 ;MASK OFF S,F BITS
1699 003630 140000 1173630 140000
1700 003632 122424 1173632 122424 CMPB (R4)+,(R4)+ ;ADD 2 FOR CRC
1701 003634 005000 1173634 005000 CLR R0 ;LOAD INTO LOCATION 0
1702 003636 004767 1173636 004767 JSR PC,GET1 ;GET DATA BLOCK
1703 003640 000014 1173640 000014
1704 003642 005703 1173642 005703 TST R3 ;CHECK DATA FIELD CRC
1705 003644 001310 1173644 001310 BNE SNDREQ ;NO GOOD
1706 003646 105713 1173646 105713 TSTB (R3) ;CHECK CODE IN LOC 0
1707 003650 001306 1173650 001306 BNE SNDREQ ;NOT PROGRAM LOAD
1708 003652 000137 1173652 000137 JMP #*6 ;TRANSFER TO SECONDARY PGM
1709 003654 000006 1173654 000006
1710
1711 ;
1712 ; GET A BLOCK AND COMPUTE CRC
1713 ;
1714 ; GET1:
1715 003656 105711 1173656 105711 GET1: TSTB (R1) ;IS DEVICE DONE YET
1716 003660 100376 1173660 100376 BPL GET ;NOT YET
1717 003662 042711 1173662 042711 BIC #8STRIP,(R1) ;NO STRIP SYNC
1718 003664 000400 1173664 000400
1719 003666 116110 1173666 116110 MOVB 2(R1),(R0) ;STORE IT
1720 003670 000002 1173670 000002
1721
1722 ;
1723 ; CRC CALCULATION ROUTINE
1724 ;
1725 ; POLY=120001 ;CRC=16 POLYNOMIAL
1726 003672 012705 1173672 012705 MOV #8,,R5 ;BYTE LENGTH
1727 003674 000010 1173674 000010
1728 003676 112002 1173676 112002
1729 003700 000241 1173700 000241 CRCLOP: MOVB (R0)+,R2 ;CHARACTER TO ADD TO CRC
1730 003702 005003 1173702 005003 CLC ;CLEAR CARRY
1731 003704 103003 1173704 103003 ROR R3 ;SHIFT OLD PARTIAL
1732 003706 005002 1173706 005002 ROR R2 ;SHIFT CHECK CHAR
1733 003710 103003 1173710 103003 ROR R1 ;SHIFT CHARACTER
1734 003712 000410 1173712 000410 BR L11 ;XOR POLY
1735 003714 005002 1173714 005002 L10: ROR R2 ;NEXT BIT
1736 003716 103006 1173716 103006 BCC L12 ;SHIFT CHARACTER
1737 003720 012746 1173720 012746 L11: MOV #POLY,-(SP) ;POLY TO STACK

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1738 003722 120001 1173722 120001 BIC R3,(AB) !NOT PARTIAL AND POLY
1739 003724 040316 1173724 040316 BIC #POLY,R3 !NOT POLY AND PARTIAL
1740 003726 042703 1173726 042703 BIC (SP)+,R3 !POLY XOR PARTIAL
1741 003730 120001 1173730 120001 BIC R5 R3 !DECREMENT BIT COUNT
1742 003732 052603 1173732 052603 BIE CRCLOP !ONCE MORE
1743 003734 005305 1173734 005305 DEC R4 !DECREMENT COUNT
1744 003736 001300 1173736 001300 BIE DET !ONCE MORE
1745 003740 005304 1173740 005304 BIE PC !RETURN
1746 003742 001345 1173742 001345
1747 003744 000207 1173744 000207
1748
1749
1750
1751 003746 112226 1173746 112226 ROMSGI !SECONDARY PROGRAM RESEST MSG
1752 003750 112226 1173750 112226 .BYTE 83H,83H,83H,83H
1753 003752 022220 1173752 022220 .BYTE 0DLE,4,0,0,0,1
1754 003754 000000 1173754 000000
1755 003756 000000 1173756 000000 .BYTE 85,120
1756 003758 000000 1173758 000000 .BYTE 0
1757 00375A 000000 117375A 000000 .BYTE 2 !REQ SEC RGM CODE
1758 00375C 001010 117375C 001010 .BYTE 0 !SERVICE CODE
1759 00375E 000001 117375E 000001 .BYTE 0 !STATION ADDR
1760 003760 000001 1173760 000001 .BYTE 242,160 !FOR STRAPS1
1761 003762 030242 1173762 030242 NOTE: NODEV/ AND DEVTAB MUST BE IN THIS ORDER
1762 003764 000001 1173764 000001 DO NOT SEPARATE THEM
1763 003766 030242 1173766 030242
1764
1765
1766 003770 122243 1173770 122243 NODEV/ !
1767 003772 000002 1173772 000002 !
1768
1769 003774 007407 1173774 007407 ! ROMSGE!
1770 003776 003407 1173776 003407 ! DEVTAB!
1771
1772 003778 END, YC1 !
1773 003776 003407 !
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DZBMD MACY11 27(657) 5-MAR-75 11:47 PAGE 38  
BM873D.P11 MAINDEC-11-DZBMD-D BM873 UNIVERSAL RESTART LOADER DIAGNOSTIC.  
1777 004000 MAP.YD!  
1778  
1779 THE FOLLOWING IS A REPRODUCTION  
1780 OF THE ROM PROGRAM FOR BM873YD.  
1781 IT IS HERE FOR COMPARISON TO THE  
1782 ACTUAL ROM AND FOR REFERENCE  
1783  
1784 - K10 (PDP-11) 256 WORD BOOTSTRAP ROM VERSION 2(17) MACY11 27(657) 18-DEC-74 11:59 PAGE 1  
1785 BM873-YD.P11  
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000000 ROM=40  
000001 R1=41  
000002 R2=42  
000003 R3=43  
000004 R4=44  
000005 R5=45  
000006 P=46  
000007 PC=47  
  
! This code is to be dialed into PROMs on the BM873-YD board.  
! Written by David N. Rosenberg  
! REGISTER DEFINITIONS  
October 1974  
  
!SYMBOL DEFINITIONS  
PROCESSOR STATUS REGISTER  
FRONT PANEL SWITCH REGISTER  
PRIORITY LEVEL 0  
PRIORITY LEVEL 1  
PRIORITY LEVEL 2  
PRIORITY LEVEL 3  
PRIORITY LEVEL 4  
PRIORITY LEVEL 5  
PRIORITY LEVEL 6  
PRIORITY LEVEL 7
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1828 ;BM873-YD = KL10 (PDP-11) 256 WORD BOOTSTRAP ROM VERSION 2(17) MACY11 27(657) 18-DEC-74 11:59 PAGE 3
1829 ;BM873-YD,P11 BUTTON #1 - BOOTSTRAP USING THE PDP-11 SWITCH REGISTER
1830
1831
1832 ; 173000 ROMORG = 173000 ;Set ROM origin to 773000
1833 ; 173000 ;=ROMORG ;BM873-YD occupies 773000-773777
1834
1835 004000 033727 ;173000 033727 BUTON1: BIT 00#SW,R5 ;Is rightmost bit on?
1836 004002 177570 ;173002 177570
1837 004004 000001 ;173004 000001
1838 004006 001010 ;173006 001010 BNE LOWBIT ;If the bit is on, branch
1839 004010 013707 ;173010 013707 MOV 00#SW,R5 ;Jump to the address in the switch register
1840 004012 177570 ;173012 177570 ;without having touched any of R0 - R6
1841
1842
1843 004014 111704 ;173014 111704 BUTON3: MOVB (PC),R4 ;R4 = 1 indicates that button #3 was pressed
1844 004016 005001 ;173016 005001 CLR R1 ;Set unit number to zero
1845 004020 005005 ;173020 005005 CLR R5 ;Clear "logical switch register"
1846 004022 000424 ;173022 000424 BR TCBOOT ;Do a default boot strap from DECTape
1847
1848 004024 173000 ;173024 173000 ;WORD ROMORG,PR7
1849 004026 000340 ;173026 000340
1850
1851 004030 013701 ;173030 013701 LOWBIT: MOV 00#SW,R1 ;R1 is a copy of the switch register
1852 004032 177570 ;173032 177570
1853 004034 106301 ;173034 106301 ASLB R1 ;Left-align speed field in right byte
1854 004036 122701 ;173036 122701 CMPB #16*20,R1 ;Is the speed 16 or 17?
1855 004040 000340 ;173040 000340
1856 004042 101404 ;173042 101404 BLOS UNITNO ;If speed is 16 or 17, branch
1857 004044 122701 ;173044 122701 CMPB #3*20,R1 ;Is the speed 0, 1, or 2?
1858 004046 000000 ;173046 000000
1859 004050 101001 ;173050 101001 BHI UNITNO ;If the speed is 0, 1, or 2, branch
1860 004052 005001 ;173052 005001 CLR R1 ;Speed was 3-15; set unit number = 0
1861 004054 000301 ;173054 000301 UNITNO: SWAB R1 ;Move unit number to bits 0-2
1862
1863 ; It is possible to manually set the desired bootstrap unit number
1864 ; into the rightmost three bits of R1, set the PDP-11 front panel
1865 ; switch register, and then jump into the ROM code at this point.
1866
1867 004056 042701 ;173056 042701 BIC #*C7,R1 ;Isolate unit number in R1
1868 004060 177700 ;173060 177700
1869 004062 013705 ;173062 013705 MOV 00#SW,R5 ;R5 is now the "logical switch register"
1870 004064 177570 ;173064 177570
1871 004066 005004 ;173066 005004 CLR R4 ;R4 = 0 indicates that button #1 was pressed
1872 004070 105705 ;173070 105705 TSTB R5 ;Should we boot from DECTape or RH11/RP04?
1873 004072 100507 ;173072 100507 BMI RPBOOT ;If bit 7 was one, branch off to the RH11/RP04
1874 ;otherwise, fall through to the DECTape

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1875 ;BM873-YD = KL10 (PDP-11) 256 WORD BOOTSTRAP ROM VERSION 2(17) MACY11 27(657) 18-DEC-74 11:59 PAGE 4
1876 ;BM873-YD,P11 DECTAPE BOOTSTRAP AND DUMP ROUTINES
1877
1878
1879 ; 177344 TCWC = 177344 ;TC11 DECTape word count register
1880 ; 000001 TCGO = 1 ;TC11 "GO" bit
1881 ; 000002 TCRNUM = 1*2 ;TC11 "read block number" function
1882 ; 000004 TCREAD = 2*2 ;TC11 "read data" function
1883 ; 000014 TCWRIT = 6*2 ;TC11 "write data" function
1884 ; 004000 TCREV = 4000 ;Move DECTape in reverse direction
1885
1886 ; Bootstrap (from DECTape) Parameters
1887 ; 000400 TCBWDC = "D256 ;Word count for the secondary bootstrap
1888 ; 000000 TCBEND = 0 ;Which end of the DECTape (0 = front; 1 = back)
1889
1890 ; Dump (to DECTape) Parameters
1891 ; 070000 TCDWDC = "D20672 ;Word count for the core dump to DECTape
1892 ; 000001 TCDEND = 1 ;Which end of the DECTape (0 = front; 1 = back)
1893
1894 ; General (Bootstrap and Dump) DECTape Parameter
1895 ; 000024 TCRTRY = "D20 ;Number of retries in case of error
1896
1897 004074 012700 ;173074 012700 TCBOOT: MOV #<TCBEND*TCREV>|TCREAD|TCGO,R0 ;Set up data-transfer command
1898 004076 000005 ;173076 000005
1899 004100 012702 ;173100 012702 MOV #-TCBEND,R2 ;Set word count to 256 (512 bytes)
1900 004102 177400 ;173102 177400
1901 004104 012703 ;173104 012703 MOV #<<1-TCBEND>*TCREV>|TCRNUM|TCGO,R3 ;Set up position command
1902 004106 004003 ;173106 004003
1903 004110 000301 ;173110 000301 SWAB R1 ;Bring unit number into the left byte
1904 004112 050103 ;173112 050103 BIS R1,R3 ;Put unit number into positioning command
1905 004114 050100 ;173114 050100 BIS R1,R0 ;Put unit number into data-transfer command
1906 004116 012701 ;173116 012701 TCSTRT: MOV #TCWC,R1 ;R1 now points to TC11 word count register
1907 004120 177344 ;173120 177344
1908 004122 012706 ;173122 012706 TLOOP: MOV #TCRTRY,SP ;Initialize retry count in SP
1909 004124 000024 ;173124 000024
1910 004126 005705 ;173126 005705 TCBGIN: TST R5 ;Test "indefinite retry" bit
1911 004130 100404 ;173130 100404 BMI TCRSET ;Branch if "indefinite retry" is enabled
1912 004132 005306 ;173132 005306 DEC SP ;Decrement retry count
1913 004134 100002 ;173134 100002 BPL TCRSET ;Branch if retry count not exhausted
1914 004136 000000 ;173136 000000 TCHALT: HALT ;Retry count is exhausted so DECTape operation
1915 004140 000770 ;173140 000770 BR TLOOP ;He pressed "CONTINUE", so try again
1916 004142 000005 ;173142 000005 TCPSET: RESET ;Stop anything in progress, for next try
1917 004144 010301 ;173144 010301 MOV R3,=(R1) ;Initiate DECTape positioning operation
1918 004146 005711 ;173146 005711 TST (R1) ;Test for an "ERROR"
1919 004150 100376 ;173150 100376 BPL TCWAIT ;Loop until an "ERROR" is detected
1920 004152 005721 ;173152 005721 TST (R1)+ ;Make R1 point to the word count register
1921 004154 005761 ;173154 005761 TST -4(R1) ;Is the error "ENDZONE"?
1922 004156 177774 ;173156 177774
1923 004160 100362 ;173160 100362 BPL TCBGIN ;If not, branch back to try again
1924 004162 010211 ;173162 010211 MOV R2,=(R1) ;Set up word count for data-transfer
1925 004164 010041 ;173164 010041 MOV R0,=(R1) ;Initiate the data-transfer operation
1926 004166 105711 ;173166 105711 TCDONE: TSTB (R1) ;Test for "DONE"
1927 004170 100376 ;173170 100376 BPL TCDONE ;Loop until the "DONE" bit sets
1928 004172 005721 ;173172 005721 TST (R1)+ ;Was an "ERROR" detected?

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1929 004174 100754 1173174 100754 BMI TCBCIN ;If so, branch back and try again
1930 004176 005741 1173176 005741 TST -(R1) ;Make R1 point to the command register
1931 004200 105011 1173200 105011 CLRB (R1) ;Stop all DECTape motion
1932 004202 122700 1173202 122700 CMPB #TCREAD|TCGO,R0 ;Was this a "normal read" operation?
1933 004204 000005 1173204 000005
1934 004206 001001 1173206 001001 BNE TCSTOP ;If not, go stop
1935 004210 000137 1173210 000137 GOTO0: JMP @(PC)+ ;Jump to PDP-11 location zero
1936 004212 000000 1173212 000000 TCSTOP: HALT ;Successful completion of a "non-read" operation
1937 004214 000776 1173214 000776 BR TCSTOP ;So that pressing "CONTINUE" won't go anywhere
  
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1938          IBM873-YD      = KL10 (PDP-11) 256 WORD BOOTSTRAP ROM  VERSION 2(17)  MACY11 27(657) 10-DEC-74 11:59 PAGE 5
1939          IBM873-YD,P11  DECTAPE BOOTSTRAP AND DUMP ROUTINES
1940
1941 004216 010037 1173216 010037 TCDUMP: MOV R0,##R0TOR7 ;Save R0 in PDP-11 memory location 40
1942 004220 000040 1173220 000040 BR TCCONT ;Branch around required interrupt vector
1943 004222 000402 1173222 000402
1944
1945 004224 173000 1173224 173000 .WORD ROMORG,PR7
1946 004226 000340 1173226 000340
1947
1948 004230 010700 1173230 010700 TCCONT: MOV PC,R0 ;Use R0 for a subroutine return address
1949 004232 000410 1173232 000410 BR REGSAV ;Go to the "Register Saving" subroutine
1950 004234 012700 1173234 012700 MOV #<TCDEND*TCREV>|TCWRIT|TCGO,R0 ;Set up (write) transfer command
1951 004236 004015 1173236 004015
1952 004240 012702 1173240 012702 MOV #-TCDWDC,R2 ;Set word-count to 28K words
1953 004242 110000 1173242 110000
1954 004244 012703 1173244 012703 MOV #<<1-TCDEND>*TCREV>|TCRNUM|TCGO,R3 ;Set up position command
1955 004246 000003 1173246 000003
1956 004250 005005 1173250 005005 CLR R5 ;Clear "indefinite retry" bit
1957 004252 000721 1173252 000721 BR TCSTRT ;Branch into DECTape routine
1958
1959
1960
1961
1962 ; The following subroutine is used to save the PDP-11 general registers
1963 ; in PDP-11 memory locations 40-57,
1964
1965 ; The calling sequence is as follows: MOV R0,##R0TOR7
1966 ; MOV PC,R0
1967 ; BR REGSAV
1968 ; return here
1969
1970 004254 010137 1173254 010137 REGSAV: MOV R1,##R0TOR7+2 ;Save R1 in memory location 42
1971 004256 000042 1173256 000042
1972 004260 012701 1173260 012701 MOV #R0TOR7+4,R1 ;R1 now points to memory location 44
1973 004262 000044 1173262 000044
1974 004264 010221 1173264 010221 MOV R2,(R1)+ ;Save R2 in memory location 44
1975 004266 010321 1173266 010321 MOV R3,(R1)+ ;Save R3 in memory location 46
1976 004270 010421 1173270 010421 MOV R4,(R1)+ ;Save R4 in memory location 50
1977 004272 010521 1173272 010521 MOV R5,(R1)+ ;Save R5 in memory location 52
1978 004274 010621 1173274 010621 MOV SP,(R1)+ ;Save SP in memory location 54
1979 004276 010021 1173276 010021 MOV R0,(R1)+ ;Save PC in memory location 56
1980 004300 000160 1173300 000160 JMP 2(R0) ;Return to the calling routine
1981 004302 000002 1173302 000002
1982
  
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1983 ;BM873-YD = KL10 (PDP-11) 256 WORD BOOTSTRAP ROM VERSION 2(17) MACY11 27(657) 18-DEC-74 11:59 PAGE 6
1984 ;BM873-YD,P11 RH11/ RP04 BOOTSTRAP AND DUMP ROUTINES
1985
1986
1987 ; 176700 RPCS1 = 176700 ;Address of RH11/ RP04 Control & Status register 1
1988 ; 000002 RPNC = 2 ;Offset to RH11/ RP04 Word Count register
1989 ; 000006 RPDA = 6 ;Offset to RH11/ RP04 Track & Sector Address register
1990 ; 000010 RPCS2 = 10 ;Offset to RH11/ RP04 Control & Status register 2
1991 ; 000012 RPDS = 12 ;Offset to RH11/ RP04 Drive Status register
1992 ; 000032 RPOF = 32 ;Offset to RH11/ RP04 Offset register (CONTAINING FMT22)
1993 ; 000034 RPDC = 34 ;Offset to RH11/ RP04 Desired Cylinder register
1994
1995 ; 040000 RPTRE = BIT14 ;"Transfer Error" bit in RPCS1
1996 ; 020000 RPMCPE = BIT13 ;"Massbus Control Bus Parity Error" bit in RPCS1
1997 ; 004000 RPDVA = BIT11 ;"Drive Available" bit in RPCS1
1998 ; 100000 RPATA = BIT15 ;"Attention Active" bit in RPDS
1999 ; 040000 RPERR = BIT14 ;"Composite Error" bit in RPDS
2000 ; 010000 RPFMT = BIT12 ;"FMT22" (16-bit words) bit in RPOF
2001
2002 ; 000021 RPPRST = 21 ;Read-in Preset
2003 ; 000061 RPNRIT = 61 ;Write Data
2004 ; 000071 RPREAD = 71 ;Read Data
2005
2006 ; 000000 RPBfmt = 0 ;Bootstrap format (0 = 16-bit words; 2 = 16-bit words)
2007 ; 000400 RPBWDC = "D256" ;Word count for the secondary bootstrap from the RP04
2008 ; 000626 RPB CYL = "D406" ;Bootstrap cylinder number
2009 ; 000000 RPBTRK = 0 ;Bootstrap track number
2010 ; 000000 RPB S CT = 0 ;Bootstrap sector number
2011
2012 ; 000000 RPDFMT = 0 ;Dump format (0 = 16-bit words; 2 = 16-bit words)
2013 ; 070000 RPDWDC = "D20672" ;Word count for the core dump to the RP04
2014 ; 000631 RPD CYL = "D409" ;Dump cylinder number
2015 ; The following two assignments put the dump at the very end of the cylinder
2016 ; 000015 RPDTRK = "D10=<<RPD WDC-1>/<<"D20+RPDFMT>*"D256>>" ;Dump track number
2017 ; 000010 RPD S CT = "D19+RPDFMT=<<<RPD WDC-1>/<"D256>=<<"D10=RPDTRK>*"<"D20+RPDFMT>>"
2018
2019
2020
2021 004304 111704 173304 111704 BUTON2: MOVB (PC),R4 ;R4 = 5 indicates that button #2 was pressed
2022 004306 005005 173306 005005 CLR R5 ;Clear "logical switch register"
2023 004310 005001 173310 005001 CLR R1 ;set unit number to zero
2024
2025 004312 012700 173312 012700 RPBOOT: MOV #<RPREAD+400>|<RPB S CT*10>,R0
2026 004314 034400 173314 034400
2027 004316 012702 173316 012702 MOV #RPBWDC,R2
2028 004320 177400 173320 177400
2029 004322 012703 173322 012703 MOV #<RPBfmt+40000>|<RPBTRK*2000>|RPB CYL,R3
2030 004324 000626 173324 000626
2031 004326 000100 173326 000100 BIS R1,R0 ;Put the unit number into R0
2032 004330 012701 173330 012701 RPSTRT: MOV #RPCS1,R1 ;set R1 to the lowest address used by the RH11
2033 004332 176700 173332 176700
    
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2034 ;BM873-YD = KL10 (PDP-11) 256 WORD BOOTSTRAP ROM VERSION 2(17) MACY11 27(657) 18-DEC-74 11:59 PAGE 7
2035 ;BM873-YD,P11 RH11/ RP04 BOOTSTRAP AND DUMP ROUTINES
2036
2037 004334 000005 173334 000005 RPLOOP: RESET ;Reset in case of retry
2038 004336 010006 173336 010006 MOV R0,SP ;Get the unit number into SP
2039 004340 042706 173340 042706 BIC #<"C7,SP" ;Isolate the unit number
2040 004342 177770 173342 177770
2041 004344 010661 173344 010661 MOV SP,RPCS2(R1) ;Tell the RH11 the unit number
2042 004346 000010 173346 000010
2043 004350 032711 173350 032711 BIT #RPDVA,(R1) ;Try to seize this RP04 unit
2044 004352 004000 173352 004000
2045 004354 001767 173354 001767 BEQ RPLOOP ;Branch if we haven't seized it
2046 004356 012721 173356 012721 MOV #RPPRST,(R1)+ ;Do a "Read-In Preset" function
2047 004360 000021 173360 000021
2048 004362 010306 173362 010306 MOV R3,SP ;Get the cylinder number into SP
2049 004364 042706 173364 042706 BIC #<"C177,SP" ;Isolate the cylinder number
2050 004366 176000 173366 176000
2051 004370 010661 173370 010661 MOV SP,RPDC-2(R1) ;Tell the RP04 the cylinder number
2052 004372 000032 173372 000032
2053 004374 010306 173374 010306 MOV R3,SP ;Get the format bit and track number into SP
2054 004376 100003 173376 100003 BPL RPDONT ;Branch if 20 sector (16-bit words) format
2055 004400 012761 173400 012761 MOV #RPFMT,RPOF-2(R1) ;Establish 22 sector (16-bit words) format
2056 004402 010000 173402 010000
2057 004404 000030 173404 000030
2058 004406 006206 173406 006206 RPDONT: ASR SP ;Right align the track
2059 004410 006206 173410 006206 ASR SP ; number in the left byte
2060 004412 105006 173412 105006 CLRB SP ;Clear the right byte
2061 004414 105006 173414 105006 BIRB R0,SP ;Put the sector number into the right byte
2062 004416 106206 173416 106206 RORB SP ;Right align the
2063 004420 106206 173420 106206 ASRB SP ; sector number in
2064 004422 106206 173422 106206 ASRB SP ; the right byte
2065 004424 010661 173424 010661 MOV SP,RPDA-2(R1) ;Tell the RH11 the track and sector numbers
2066 004426 000004 173426 000004
2067 004430 010211 173430 010211 MOV R2,(R1) ;Tell the RH11 the word count
2068 004432 010006 173432 010006 MOV R0,SP ;Get the function code into SP
2069 004434 105006 173434 105006 CLRB SP ;Clear the right byte
2070 004436 000306 173436 000306 SWAB SP ;Right align the function code
2071 004440 010641 173440 010641 MOV #<(R1) ;Tell the RP04 the function code
2072 004442 105711 173442 105711 RPDONE: TSTB (R1) ;Test for RH11 "ready"
2073 004444 100376 173444 100376 BPL RPDONE ;Loop, waiting for RH11 "ready"
2074 004446 032711 173446 032711 BIT #RPTRE|RPMCPE,(R1) ;Test for RH11 error bits
2075 004450 000000 173450 000000
2076 004452 001330 173452 001330 BNE RPLOOP ;If error, branch back for retry
2077 004454 032761 173454 032761 BIT #RPATA|RPERR,RPDS(R1) ;Test for RP04 error bits
2078 004456 140000 173456 140000
2079 004460 000012 173460 000012
2080 004462 001324 173462 001324 BNE RPLOOP ;If error, branch back for retry
2081 004464 022706 173464 022706 CMP #RPREAD,SP ;Was the function a "normal read"?
2082 004466 000071 173466 000071
2083 004470 001250 173470 001250 BNE ICSTOP ;If not, branch to a HALT instruction
2084 004472 022737 173472 022737 CMP #000240,0#0 ;Was "000240" read into location zero?
2085 004474 000240 173474 000240
2086 004476 000000 173476 000000
2087 004500 001643 173500 001643 BEQ GOTO0 ;If so, branch to location zero
    
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2088 004502 000000 173502 000000 HALT          ;"000240" was not read into location zero
2089 004504 000641 173504 000641 BR          GOTO0          ;Branch to location zero
2090
2091
2092 004506 010037 173506 010037 RPDUMP1 MOV   R0,00R0TOR7 ;SAVE R0 IN PDP-11 MEMORY LOCATION "R0TOR7"
2093 004510 000040 173510 000040
2094 004512 010700 173512 010700 MOV   PC,R0          ;USE R0 FOR A SUBROUTINE RETURN ADDRESS
2095 004514 000657 173514 000657 BR          REGSAV          ;GO TO THE "REGISTER SAVING" SUBROUTINE
2096 004516 012700 173516 012700 MOV   #<R0WRIT=400>|<RPD8CT=10>,R0
2097 004520 030500 173520 030500
2098 004522 012702 173522 012702 MOV   #=RPDWC,R2
2099 004524 110000 173524 110000
2100 004526 012703 173526 012703 MOV   #<RPDMNT=40000>|<RPDTRK=2000>|RPDCYL,R3
2101 004530 032631 173530 032631
2102 004532 000676 173532 000676 BR          RPSTRT
2103
  
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2104 ;BM873-YD = KL10 (PDP-11) 256 WORD BOOTSTRAP ROM VERSION 2(17) MACY11 27(657) 10-DEC-74 11159 PAGE 8
2105 ;BM873-YD,P11 DTE20 DEVICE REGISTER AND BIT DEFINITIONS
2106
2107
2108 ; 174400 DTEBAS=174400 ;BASE OF (FIRST) DTE20 DEVICE REGISTER BLOCK
2109 ; 000040 DTEBIZ=000040 ;SPACING BETWEEN CONSECUTIVE DTE20'S
2110 ; 000004 DTEMAX=4 ;MAXIMUM NUMBER OF DTE20'S ON ONE PDP-11
2111
2112 ;OFFSETS FROM THE BASE OF THE DTE20 DEVICE REGISTER BLOCK
2113 ;TO SPECIFIC I0/I1 INTERFACE RAM LOCATIONS AND REGISTERS.
2114
2115 ; THE FIRST 12 REGISTERS ARE NOT INITIALIZED BY "INIT" (BECAUSE THEY ARE IN RAMS
2116 ; DELAY COUNT (ADDRESS XXXX00)
2117 ; DEPOSIT OR EXAMINE WORD 3 (ADDRESS XXXX02)
2118 ; DEPOSIT OR EXAMINE WORD 2 (ADDRESS XXXX04)
2119 ; DEPOSIT OR EXAMINE WORD 1 (ADDRESS XXXX06)
2120 ; 10 ADDRESS WORD 1 FOR DEX (ADDRESS XXXX10)
2121 ; 10 ADDRESS WORD 2 FOR DEX (ADDRESS XXXX12)
2122 ; T010 BYTE COUNT (ADDRESS XXXX14)
2123 ; T011 BYTE COUNT (ADDRESS XXXX16)
2124 ; T010 PDP11 MEMORY ADDRESS (ADDRESS XXXX20)
2125 ; T011 PDP11 MEMORY ADDRESS (ADDRESS XXXX22)
2126 ; T010 PDP11 DATA WORD (ADDRESS XXXX24)
2127 ; T011 PDP11 DATA WORD (ADDRESS XXXX26)
2128
2129 ; THE LAST 4 REGISTERS ARE INITIALIZED BY "INIT" (BECAUSE THEY ARE IN FLIP-FLOPS
2130 ; DIAGNOSTIC WORD 1 (ADDRESS XXXX30)
2131 ; DIAGNOSTIC WORD 2 (ADDRESS XXXX32)
2132 ; I0/I1 INTERFACE STATUS WORD (ADDRESS XXXX34)
2133 ; DIAGNOSTIC WORD 3 (ADDRESS XXXX36)
2134
2135 ; THE FOLLOWING ARE THE ADDRESSES OF THE DTE20 INTERRUPT VECTORS
2136
2137 ;
2138
2139 ; 000774 DTEIV1=774 ;INTERRUPT VECTOR FOR DTE20 #1
2140 ; 000770 DTEIV2=770 ;INTERRUPT VECTOR FOR DTE20 #2
2141 ; 000764 DTEIV3=764 ;INTERRUPT VECTOR FOR DTE20 #3
2142 ; 000760 DTEIV4=760 ;INTERRUPT VECTOR FOR DTE20 #4
2143
2144 ; Bit assignments for various DTE20 registers used by this ROM code
2145
2146 ;BIT ASSIGNMENTS FOR T010BC
2147
2148
2149
2150 ; 100000 INT11=BIT15 ;SET DONE AND INTERRUPT BOTH I0 AND I1
2151
2152 ;BIT ASSIGNMENTS FOR T011BC
2153
2154 ; 100000 INT10=BIT15 ;SET DONE AND INTERRUPT BOTH I0 AND I1
2155 ; 040000 ZSTOP=BIT14 ;STOP ON NULL (ZERO) CHARACTER
2156 ; 020000 T011BM=BIT13 ;Byte size for To-11 byte transfers
2157
  
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2158 ;BM873=YD = KL10 (PDP-11) 256 WORD BOOTSTRAP ROM VERSION 2(17) MACY11 27(657) 18-DEC-74 11:59 PAGE 9
2159 ;BM873=YD,P11 DTE20 DEVICE REGISTER AND BIT DEFINITIONS
2160
2161 ;BIT ASSIGNMENTS FOR DIAG2 (WRITE)
2162 ; PERFORM DIAGNOSTIC CLEAR
2163
2164 ;BIT ASSIGNMENTS FOR DIAG3 (READ)
2165 ;DATO UNIBUS parity error
2166 ;DATO UNIBUS receive error
2167 ;NPR UNIBUS parity error
2168
2169 ;BIT ASSIGNMENTS FOR DIAG3 (WRITE)
2170
2171 ; 000020 CDD=BIT4 ;Clear DUPE and DURE error flags
2172 ; 000002 CNUPE=BIT1 ;Clear NUPE error flag
2173 ; 000001 TO10BM=BIT0 ;Byte size for To-10 byte transfer
2174
2175 ;BIT ASSIGNMENTS FOR STATUS (WRITE)
2176
2177 ; 100000 DON10S=BIT15 ;SET TO10 DONE
2178 ; 040000 DON10C=BIT14 ;CLEAR TO10 DONE
2179 ; 020000 ERR10E=BIT13 ;SET TO10 ERROR
2180 ; 010000 ERR10C=BIT12 ;CLEAR TO10 ERROR
2181 ; 004000 INT11S=BIT11 ;Ring the PDP-11's doorbell (interrupts the -11)
2182 ; 002000 INT11C=BIT10 ;Stop Ringing the PDP-11's doorbell
2183 ; 001000 PERCLR=BIT9 ;CLEAR -11 MEMORY PARITY ERROR
2184 ; 000400 INT10S=BIT8 ;Ring the PDP-10's doorbell (interrupts the -10)
2185 ; 000200 DON11S=BIT7 ;SET TO11 DONE
2186 ; 000100 DON11C=BIT6 ;CLEAR TO11 DONE
2187 ; 000040 INTRON=BIT5 ;Enable DTE20 interrupts to the -11
2188 ; 000020 EBUSPC=BIT4 ;Clear "EBUS parity error"
2189 ; 000010 INTRPF=BIT3 ;Disable the PDP-11 interrupts
2190 ; 000004 EBUSPS=BIT2 ;SET "EBUS parity error"
2191 ; 000002 ERR11S=BIT1 ;SET TO11 ERROR
2192 ; 000001 ERR11C=BIT0 ;CLEAR TO11 ERROR
2193
2194 ;BIT ASSIGNMENTS FOR STATUS (READ)
2195
2196 ; 100000 TO10DN=BIT15 ;TO10 DONE
2197 ; 020000 TO10ER=BIT13 ;TO 10 ERROR (NPR TIMEOUT OR BUS ERROR)
2198 ; 010000 RAMIS0=BIT12 ;RAM word read is all zeros
2199 ; 004000 TO11DB=BIT11 ;11 = the PDP11's doorbell is ringing
2200 ; 002000 DXWRD1=BIT10 ;DEPOSIT OR EXAMINE WORD ONE
2201 ; 001000 MPE11=BIT9 ;Parity error within PDP-11 memory
2202 ; 000400 TO10DB=BIT8 ;10 = the PDP-10's doorbell is ringing
2203 ; 000200 TO11DN=BIT7 ;TO11 DONE
2204 ; 000100 EBSEL=BIT6 ;E BUFFER SELECT
2205 ; 000040 NULSTP=BIT5 ;NULL STOP
2206 ; 000020 BPARER=BIT4 ;EBUS parity error
2207 ; 000010 RSTRCT=BIT3 ;This PDP-11 is "RESTRICTED"
2208 ; 000004 DEXDON=BIT2 ;DEPOSIT OR EXAMINE DONE
2209 ; 000002 TO11ER=BIT1 ;TO 11 ERROR (NPR TIMEOUT OR BUS ERROR)
2210 ; 000001 INTSON=BIT0 ;DTE20 interrupts (to the -11) are enabled
  
```

```

2211 ;BM873=YD = KL10 (PDP-11) 256 WORD BOOTSTRAP ROM VERSION 2(17) MACY11 27(657) 18-DEC-74 11:59 PAGE 1
2212 ;BM873=YD,P11 PROCEDURE BY WHICH THE PDP-10 BOOTSTRAPS AND/OR DUMPS THE PDP-11
2213
2214
2215
2216
2217
2218
2219
  
```

The following is the procedure which the KL10 executes in order to dump and/or bootstrap the PDP-11 through the DTE20:

- 2222 1. Clear the DTE20 and initiate a BM873 button #4 bootstrap operation
 - CONO [SR11B|CL11PT|CLTO11|CLTO10|PILDEN]
- 2223 2. Wait to see PDP-11 power fail (AC LOW = true) - CONI [DEAD11] = 1
- 2224 3. Wait to see PDP-11 power recover (AC LOW = false) - CONI [DEAD11] = 0
- 2225 4. Wait at least another 150 milliseconds and then clear the reload -11 button
 - CONO [CR11B]
- 2226 5. Set byte counter to a special code (1365 octal) - DATA [1365]
- 2227 6. Ring PDP-11's doorbell - CONO[TO11DB]
- 2228 7. Wait until "-10 ringing -11's doorbell" is turned off by the -11
 (i.e., until CONI[TO11DB] becomes zero).
- 2229 8. Enable the DTE20 to use PI 0 interrupts
 (i.e., set CONO[PILDEN|PI0ENB]).
- 2230 9. Set up the To-10 byte pointer (in the EPT) for the first 3.5K.
- 2231 10. Set up the byte counter for the first 3.5K, indicating
 "interrupt -10 only" - DATA [1000]
- 2232 11. Wait for "To-10 done" or "To-10 error" - CONI [TO10DN|TO10ER]
- 2233 12. Note whether there was an error (CONI [TO10ER]) and then turn off
 TO10DN and TO10ER - CONO [CLTO10]. If error, go to step 17.
- 2234 13. If end of 28K, go to step 17.
- 2235 14. Set up To-10 byte pointer (in the EPT) for the next 3.5K.
- 2236 15. Set up the byte counter for the next 3.5K indicating
 "interrupt -10 only" (DATA [1000]), unless this is the
 last 3.5K (of 28K), in which case indicate "interrupt
 both processors" (DATA [TO10IB|1000]).
- 2237 16. Go to step 11.

2265 IBM873-YD - KL10 (PDP-11) 256 WORD BOOTSTRAP ROM VERSION 2(17) MACY11 27(657) 18-DEC-74 11:59 PAGE 1
 2266 IBM873-YD,P11 PROCEDURE BY WHICH THE PDP-10 BOOTSTRAPS AND/OR DUMPS THE PDP-11
 2267
 2268
 2269
 2270 17. Set up To-11 byte pointer (in the EPT) for "PDP-11 bootstrap".
 2271 Note that the first word of this "PDP-11 bootstrap" must
 2272 be the bit pattern 000240 (a PDP-11 NOP instruction).
 2273
 2274 18. Ring the PDP-11's doorbell - CONO [TO11DB]
 2275
 2276 19. Wait for either TO11DB to go off (CONI[TO11DB] = 0),
 2277 or TO10DB to come on (CONI[TO10DB] = 1).
 2278
 2279 20. If no error was noted in step 12, TO11DB should go off
 2280 (TO10DB coming on indicates a massive screwup).
 2281 If an error was noted in step 12, TO11DB going off indicates
 2282 that the error was "non-fatal" (non-ex-mem or -11 memory
 2283 parity) and the -11 is proceeding. TO10DB coming on indicates
 2284 that the error was "fatal" and the -11 is HALTED AT LOCATION 173714.
 2285 In this latter case the -10 must restart from step 1.
 2286
 2287 21. If TO11DB went off, wait for "To-11 done" or "To-11 error"
 2288 - CONI [TO11DN|TO11ER]
 2289
 2290 22. Note whether there was an error - CONI [TO11ER]
 2291
 2292 23. Turn off TO11DN and TO11ER and ring the PDP-11's doorbell
 2293 - CONO [TO11DB|CLTO11]
 2294
 2295 24. Wait for either TO11DB to go off (CONI[TO11DB] = 0),
 2296 or TO10DB to come on (CONI[TO10DB] = 1).
 2297
 2298 25. TO11DB going off indicates that the PDP-11 found no errors
 2299 and is transferring control to the code which was just
 2300 received from the -10. In this case the -10 should start
 2301 following the protocol of this code.
 2302
 2303 26. TO10DB coming on indicates that the PDP-11 has found an
 2304 error (or that the first word transmitted wasn't the
 2305 bit pattern 000240), and the PDP-11 is HALTED AT LOCATION 173766.
 2306 In this case the -10 must restart from step 1.
 2307
 2308
 2309
 2310
 2311

2312 IBM873-YD - KL10 (PDP-11) 256 WORD BOOTSTRAP ROM VERSION 2(17) MACY11 27(657) 18-DEC-74 11:59 PAGE 1
 2313 IBM873-YD,P11 BUTON #4 - BOOTSTRAP INITIATED BY THE PDP-10 (THROUGH DTE20)
 2314
 2315
 2316 J 000130 DTECOR = 130 ;Core address into which to store DTE20 regs.
 2317 J 000014 DTEREG = *D12 ;Number of DTE20 registers to store
 2318 J 000400 DTEWDC = *D256 ;Word count for secondary bootstrap from the -10
 2319 ; ENTER HERE WHEN THE DTE20 PRESSES BUTON #4 (BOOTSTRAP INITIATED
 2320 BY THE PDP-10, THROUGH THE DTE20)
 2321 004534 010037 1173534 010037 BUTON4: MOV R0,*R0TOR7 ;SAVE R0 IN PDP-11 MEMORY LOCATION "R0TOR7"
 2322 004536 000040 1173536 000040
 2323 004540 010700 1173540 010700 MOV PC,R0 ;USE R0 FOR A SUBROUTINE RETURN ADDRESS
 2324 004542 000644 1173542 000644 BR REGSAV ;GO TO THE "REGISTER SAVING" SUBROUTINE
 2325 004544 005005 1173544 005005 CLR R5 ;SET R5 = 0
 2326 004546 012501 1173546 012501 MOV (R5)+,R1 ;SAVE LOCATION 0 IN R1
 2327 004550 012503 1173550 012503 MOV (R5)+,R3 ;SAVE LOCATION 2 IN R3
 2328 004552 012504 1173552 012504 MOV (R5)+,R4 ;SAVE LOCATION 4 IN R4
 2329 004554 011500 1173554 011500 MOV (R5),R0 ;SAVE LOCATION 6 IN R0
 2330 004556 012715 1173556 012715 MOV *PR7,(R5) ;SET UP PRIORITY FOR NON-EX-MEM TRAP
 2331 004560 000340 1173560 000340
 2332 004562 005745 1173562 005745 TST =(R5) ;SET R5 = 4
 2333 004564 012702 1173564 012702 100: MOV #DTEBAS-DTESIZ,R2
 2334 004566 174340 1173566 174340
 2335 004570 010715 1173570 010715 MOV PC,(R5) ;STORE ADDRESS FOR NON-EX-MEM TRAP
 2336 004572 010506 1173572 010506 MOV R5,SP ;SET STACK POINTER = 4
 2337 004574 062702 1173574 062702 110: ADD #DTESIZ,R2 ;R2 POINTS TO THE NEXT DTE20
 2338 004576 000040 1173576 000040
 2339 004600 105702 1173600 105702 TSTB R2
 2340 004602 100770 1173602 100770 BMI 100 ;START LOOKING FROM THE BEGINNING AGAIN
 2341 004604 032762 1173604 032762 BIT #TO11DB,STATUS(R2) ;Is this -10 ringing the -11's doorbell?
 2342 004606 004000 1173606 004000
 2343 004610 000034 1173610 000034
 2344 004612 001770 1173612 001770 BEQ 110 ;If it is not, go look for another -10
 2345 004614 026217 1173614 026217 CMP TO10BC(R2),(PC) ;CHECK FOR A CODE (1365) FROM THE PDP-10
 2346 004616 000014 1173616 000014
 2347
 2348 004620 001365 1173620 001365 ;INDICATING THAT IT WANTS TO BOOTSTRAP THE -11
 2349
 2350 BNE 110
 2351 ; NOTE THAT AT THIS POINT R2 CONTAINS THE ADDRESS OF THE DEVICE REGISTER
 2352 ; BLOCK FOR THIS DTE20, THAT R5 = 4, AND THAT SP = 4
 2353 TST (R5) ;SET R5 = 6
 2354 MOV R0,(R5) ;RESTORE THE CONTENTS OF LOCATION 6
 2355 MOV R4,(R5) ;RESTORE THE CONTENTS OF LOCATION 4
 2356 MOV R3,(R5) ;RESTORE THE CONTENTS OF LOCATION 2
 2357 MOV R1,(R5) ;RESTORE THE CONTENTS OF LOCATION 0
 2358 ; Note: At this time R5 = 0. This fact will be used later.
 2359 MOV #DTECOR,R0 ;R0 = core address for storing DTE20 registers
 2360 MOV R2,R4
 2361 MOV (R4)+,(R0)+ ;Save the next DTE20 register in core
 2362 CMP #DTEREG*2>DTECOR,R0 ;Have we finished yet?
 2363 004650 101374 1173650 101374 BHI 70 ;Loop until we have finished

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2364 2364 004482 1BMR73-SD -KL10 (PDP-11) 256 WORD BOOTSTRAP ROM VERSION 2(17) MACY11 27(657) 16-DEC-74 11189 PAGE 1
2365 2365 004483 1BMR73-SD.P11 BMR73 BOOTSTRAP INITIATED BY THE PDP-10 (THROUGH DREZ0)
2366 2366 004484 000001 R2,R1 ;R1 = DREZ0 DEVICE REGISTER BLOCK
2367 2367 004485 000002 ADD #DIA02,R1 ;DO A "DIAGNOSTIC CLEAR" OF THE DREZ0,
2368 2368 004486 000003 MOV #DRESEF,R1 ;R1 = DREZ0 DEVICE REGISTER BLOCK
2369 2369 004487 000004 ;DO A "DIAGNOSTIC CLEAR" OF THE DREZ0,
2370 2370 004488 000005 ;R1 = DREZ0 DEVICE REGISTER BLOCK
2371 2371 004489 000006 ;DO A "DIAGNOSTIC CLEAR" OF THE DREZ0,
2372 2372 004490 000007 ;R1 = DREZ0 DEVICE REGISTER BLOCK
2373 2373 004491 000008 ;DO A "DIAGNOSTIC CLEAR" OF THE DREZ0,
2374 2374 004492 000009 ;R1 = DREZ0 DEVICE REGISTER BLOCK
2375 2375 004493 000010 ;DO A "DIAGNOSTIC CLEAR" OF THE DREZ0,
2376 2376 004494 000011 ;R1 = DREZ0 DEVICE REGISTER BLOCK
2377 2377 004495 000012 ;DO A "DIAGNOSTIC CLEAR" OF THE DREZ0,
2378 2378 004496 000013 ;R1 = DREZ0 DEVICE REGISTER BLOCK
2379 2379 004497 000014 ;DO A "DIAGNOSTIC CLEAR" OF THE DREZ0,
2380 2380 004498 000015 ;R1 = DREZ0 DEVICE REGISTER BLOCK
2381 2381 004499 000016 ;DO A "DIAGNOSTIC CLEAR" OF THE DREZ0,
2382 2382 004500 000017 ;R1 = DREZ0 DEVICE REGISTER BLOCK
2383 2383 004501 000018 ;DO A "DIAGNOSTIC CLEAR" OF THE DREZ0,
2384 2384 004502 000019 ;R1 = DREZ0 DEVICE REGISTER BLOCK
2385 2385 004503 000020 ;DO A "DIAGNOSTIC CLEAR" OF THE DREZ0,
2386 2386 004504 000021 ;R1 = DREZ0 DEVICE REGISTER BLOCK
2387 2387 004505 000022 ;DO A "DIAGNOSTIC CLEAR" OF THE DREZ0,
2388 2388 004506 000023 ;R1 = DREZ0 DEVICE REGISTER BLOCK
2389 2389 004507 000024 ;DO A "DIAGNOSTIC CLEAR" OF THE DREZ0,
2390 2390 004508 000025 ;R1 = DREZ0 DEVICE REGISTER BLOCK
2391 2391 004509 000026 ;DO A "DIAGNOSTIC CLEAR" OF THE DREZ0,
2392 2392 004510 000027 ;R1 = DREZ0 DEVICE REGISTER BLOCK
2393 2393 004511 000028 ;DO A "DIAGNOSTIC CLEAR" OF THE DREZ0,
2394 2394 004512 000029 ;R1 = DREZ0 DEVICE REGISTER BLOCK
2395 2395 004513 000030 ;DO A "DIAGNOSTIC CLEAR" OF THE DREZ0,
2396 2396 004514 000031 ;R1 = DREZ0 DEVICE REGISTER BLOCK
2397 2397 004515 000032 ;DO A "DIAGNOSTIC CLEAR" OF THE DREZ0,
2398 2398 004516 000033 ;R1 = DREZ0 DEVICE REGISTER BLOCK
2399 2399 004517 000034 ;DO A "DIAGNOSTIC CLEAR" OF THE DREZ0,
2400 2400 004518 000035 ;R1 = DREZ0 DEVICE REGISTER BLOCK
2401 2401 004519 000036 ;DO A "DIAGNOSTIC CLEAR" OF THE DREZ0,
2402 2402 004520 000037 ;R1 = DREZ0 DEVICE REGISTER BLOCK
2403 2403 004521 000038 ;DO A "DIAGNOSTIC CLEAR" OF THE DREZ0,
2404 2404 004522 000039 ;R1 = DREZ0 DEVICE REGISTER BLOCK
2405 2405 004523 000040 ;DO A "DIAGNOSTIC CLEAR" OF THE DREZ0,
2406 2406 004524 000041 ;R1 = DREZ0 DEVICE REGISTER BLOCK
2407 2407 004525 000042 ;DO A "DIAGNOSTIC CLEAR" OF THE DREZ0,
2408 2408 004526 000043 ;R1 = DREZ0 DEVICE REGISTER BLOCK
2409 2409 004527 000044 ;DO A "DIAGNOSTIC CLEAR" OF THE DREZ0,
2410 2410 004528 000045 ;R1 = DREZ0 DEVICE REGISTER BLOCK
2411 2411 004529 000046 ;DO A "DIAGNOSTIC CLEAR" OF THE DREZ0,
2412 2412 004530 000047 ;R1 = DREZ0 DEVICE REGISTER BLOCK
2413 2413 004531 000048 ;DO A "DIAGNOSTIC CLEAR" OF THE DREZ0,
2414 2414 004532 000049 ;R1 = DREZ0 DEVICE REGISTER BLOCK
2415 2415 004533 000050 ;DO A "DIAGNOSTIC CLEAR" OF THE DREZ0,
2416 2416 004534 000051 ;R1 = DREZ0 DEVICE REGISTER BLOCK
2417 2417 004535 000052 ;DO A "DIAGNOSTIC CLEAR" OF THE DREZ0,
2418 2418 004536 000053 ;R1 = DREZ0 DEVICE REGISTER BLOCK
2419 2419 004537 000054 ;DO A "DIAGNOSTIC CLEAR" OF THE DREZ0,
2420 2420 004538 000055 ;R1 = DREZ0 DEVICE REGISTER BLOCK
2421 2421 004539 000056 ;DO A "DIAGNOSTIC CLEAR" OF THE DREZ0,
2422 2422 004540 000057 ;R1 = DREZ0 DEVICE REGISTER BLOCK
2423 2423 004541 000058 ;DO A "DIAGNOSTIC CLEAR" OF THE DREZ0,
2424 2424 004542 000059 ;R1 = DREZ0 DEVICE REGISTER BLOCK
2425 2425 004543 000060 ;DO A "DIAGNOSTIC CLEAR" OF THE DREZ0,
2426 2426 004544 000061 ;R1 = DREZ0 DEVICE REGISTER BLOCK
    
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DBRMD MACY11 27(657) 5-MAR-75 1147 PAGE 52
 BMR73D.P11 MAINDEC-11-DBRMD-D BMR73 UNIVERSAL RESTART LOADER DIAGNOSTIC.

```

2417 2417 005000 000177 MAP,YE1,BLKX 127.
2418 2418 005001 000178 END,YE1,BLKX 127.
2419 2419 005400 000127 MAP,YF1,BLKX 127
2420 2420 005656 000101 END,YF1,BLKX 127
2421 2421 005660 000127 MAP,YG1,BLKX 127
2422 2422 006136 000001 END,YG1,BLKX 127
2423 2423 006140 000177 MAP,Y11,BLKX 127.
2424 2424 006536 000001 END,Y11,BLKX 127.
2425 2425 006540 000177 MAP,YX1,BLKX 127.
2426 2426 007136 000001 END,YX1,BLKX 127.
    
```

```
2427 *****
2428 ! INITIALIZE AND START UP OF PROGRAM *****
2429 !*****
2430
2431
2432
2433 RESTART1 CDR
2434 CDR TABLE
2435 CDR RESTRCK,SP
2436 CDR SWR,SWERN1
2437 CDR LPTFG
2438 CDR LPTFR
2439 CDR %STAT,PG,NO
2440 CDR %E,984
2441 CDR FLAG4
2442 CDR %I, RUNNING UNDER ACT-11??
2443 CDR %R IF %NOT% UNDER ACT-11!!
2444 CDR %E,984
2445 CDR %R,984
2446 CDR %R,984
2447 CDR %R,984
2448 CDR %R,984
2449 CDR %R,984
2450 CDR %R,984
2451 CDR %R,984
2452 CDR %R,984
2453 CDR %R,984
2454 CDR %R,984
2455 CDR %R,984
2456 CDR %R,984
2457 CDR %R,984
2458 CDR %R,984
2459 CDR %R,984
2460 CDR %R,984
2461 CDR %R,984
2462 CDR %R,984
2463 CDR %R,984
2464 CDR %R,984
2465 CDR %R,984
2466 CDR %R,984
2467 CDR %R,984
2468 CDR %R,984
2469 CDR %R,984
2470 CDR %R,984
2471 CDR %R,984
2472 CDR %R,984
2473 CDR %R,984
2474 CDR %R,984
2475 CDR %R,984
2476 CDR %R,984
2477 CDR %R,984
2478 CDR %R,984
2479 CDR %R,984
2480 CDR %R,984
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2481 CDR %R,984
2482 CDR %R,984
2483 CDR %R,984
2484 CDR %R,984
2485 CDR %R,984
2486 CDR %R,984
2487 CDR %R,984
2488 CDR %R,984
2489 CDR %R,984
2490 CDR %R,984
2491 CDR %R,984
2492 CDR %R,984
2493 CDR %R,984
2494 CDR %R,984
2495 CDR %R,984
2496 CDR %R,984
2497 CDR %R,984
2498 CDR %R,984
2499 CDR %R,984
2500 CDR %R,984
2501 CDR %R,984
2502 CDR %R,984
2503 CDR %R,984
2504 CDR %R,984
2505 CDR %R,984
2506 CDR %R,984
2507 CDR %R,984
2508 CDR %R,984
2509 CDR %R,984
2510 CDR %R,984
2511 CDR %R,984
2512 CDR %R,984
2513 CDR %R,984
2514 CDR %R,984
2515 CDR %R,984
2516 CDR %R,984
2517 CDR %R,984
2518 CDR %R,984
2519 CDR %R,984
2520 CDR %R,984
2521 CDR %R,984
2522 CDR %R,984
2523 CDR %R,984
2524 CDR %R,984
2525 CDR %R,984
2526 CDR %R,984
2527 CDR %R,984
2528 CDR %R,984
2529 CDR %R,984
2530 CDR %R,984
2531 CDR %R,984
2532 CDR %R,984
2533 CDR %R,984
2534 CDR %R,984
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DZBMD MACY11 27(657) 5-MAR-75 11:47 PAGE 55
 BM873D,P11 MAINDEC-11-DZBMD=D BM873 UNIVERSAL RESTART LOADER DIAGNOSTIC.

```

2535 007772 012737 004000 010156 MOV #MAP,YD,TABLE ;SET FOR START OF TABLE
2536 010000 012737 004776 010160 MOV #END,YD,ALLEND ;SET END OF TABLE
2537 010006 012737 173776 010170 MOV #173776,LASTA ;SET LAST ROM ADDR
2538 010014
2539 010014 010237 014736 688: MOV R2,VERSON ;STORE VERSION TYPE..
2540 010020 005737 010156 TST TABLE ;HAS A MAP BEEN SELECTED?
2541 010024 001003 BNE 38 ;BR IF OK...
2542 010026 104401 011014 TYPE ,BM,ERR ;TYPE ERROR
2543 010032 000666 BR 28 ;GO AND GET CORRECT MAP.
2544 010034 104401 014200 38: TYPE ,MSG3 ;TYPE MESSAGE FOR TEST NUMBER
2545 010040 012704 000002 MOV #2,R4 ;SET FOR MAX 2 INPUTS FROM TTY
2546 010044 005003 X,X,1 CLR R3 ;CLEAR CHAR STORAGE
2547 010046 105775 171132 18: TSTB #TKCSR ;WAIT FOR THE TTY KEY TO BE HIT
2548 010052 100375 BPL 18 ;KEEP WAITING.....
2549 010054 104400 KEY.TO,R2 ;GO ECHO IT ;STRIP BIT 7 FROM IT
2550 010056 022702 000015 CMP #15,R2 ;WAS IT A CR??
2551 010062 001404 BEQ 28 ;BR IF CR
2552 010064 005304 DEC R4 ;ONE MORE CHAR TO GO BEFOR ERROR
2553 010066 001426 BEQ 38 ;IF R4=0 THEN ERROR
2554 010070 010203 MOV R2,R3 ;MOV THE CHAR TO R3
2555 010072 000765 BR 18 ;GO WAIT FOR ANOTHER CHAR
2556 010074 022703 000061 28: CMP #61,R3 ;WAS 1 HIT??
2557 010100 001002 BNE ,+6 ;BR IF NO
2558 010102 000137 JMP PRG1 ;GOTO PRG 1
2559 010106 022703 000062 CMP #62,R3 ;WAS 2 HIT??
2560 010112 001002 BNE ,+6 ;BR IF NO
2561 010114 000137 JMP PRG2 ;GOTO PRG 2
2562 010120 022703 000063 CMP #63,R3 ;WAS 3 HIT??
2563 010124 001002 BNE ,+6 ;BR IF NO
2564 010126 000137 JMP PRG3 ;GOTO PRG3
2565 010132 022703 000064 CMP #64,R3 ;WAS 4 HIT??
2566 010136 001002 BNE ,+6 ;BR IF NO
2567 010140 000137 JMP PRG4 ;GOTO PRG 4
2568 010144 104401 014432 38: TYPE ,M,QM ;NEITHER 1 OR 2 OR 3 OR 4 WAS HIT
2569 010150 000137 JMP RSTRRT ;TYPE "??" GO TO THE BEGINING.
2570 010154 000000 SAVSR1: 0 ;SAVE THE SWR HERE.
2571 010156 000000 TABLE: 0
2572 010160 000000 ALLEND: 0
2573 010162 006540 EXTMAP: MAP,YX
2574 010164 007136 EXTEND: END,YX
2575 010166 000000 INITFG: 0
2576 010170 000000 LASTA: 0
2577 010172 104401 010202 NOROM: TYPE ,NOROMS ;TYPE CAN'T FIND A RESPONSE
2578 010176 000000 HALT ;NO LOADER INSTALLED?
2579 010200 000776 BR ,=2
2580 010202 005015 051124 050101 NOROMS: .ASCII <15><12>/TRAP TO 4 ON 1ST READ OF 173000/
010243 015 044412 020123 .ASCIZ <15><12>/IS LOADER INSTALLED?/
010272 005015 070503 023516 NMATCH: .ASCII <15><12>/CAN'T IDENTIFY LOADER AS YA,YB,YC, OR YD AFTER/
010352 005015 046503 020120 .ASCIZ <15><12>/CMP WITH LOC 173000/
,EVEN

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DZBMD MACY11 27(657) 5-MAR-75 11:47 PAGE 56
 BM873D,P11 MAINDEC-11-DZBMD=D BM873 UNIVERSAL RESTART LOADER DIAGNOSTIC.

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2581 ;PROGRAM 1
2582 ;THE PURPOSE OF PROGRAM 1 IS TO READ THE ROM AND
2583 ;VERIFY THAT THE DATA IS CORRECT. ALL ADDRESSES
2584 ;ARE READ, EXCEPT THE TRAP VECTOR, FIVE TIMES.
2585 ;
2586 ;THE SECOND PART OF THIS TEST VERIFIES THAT TRYING
2587 ;TO WRITE THE ROM RESULTS IN A TIME OUT TRAP.
2588 ;ALL ADDRESS ARE WRITTEN WITH A -1
2589 ;,AND ARE EXPECTED TO TRAP.
2590
2591 010400 012777 014444 170606 PRG1: MOV #,KEY.TO,R2,#TKVEC
2592 010406 012777 000100 170570 MOV #100,#TKCSR ;SET INTERRUPT ENABLE
2593 010414 012737 010400 014616 MOV #PRG1,PRG,NO ;SET FOR PWR FAIL
2594 010422 012737 000500 001232 MOV #500,ICOUNT ;DO THIS TEST 500(8) TIMES.
2595 010430 012737 014020 000004 PRG,1: MOV #NO,TRAP,#4 ;SET FOR UNEXPECTED TRAP.
2596 010436 012700 173000 MOV #173000,R0 ;SET BEGGING ADDRESS
2597 010442 012737 010466 001230 MOV #28,LOCK ;IF SW09=1; GOTO 28 WHEN SCOP1 IS HIT
2598 010450 013704 010156 MOV TABLE,R4 ;SET START OF MAP
2599 010454 013737 010170 010760 MOV LASTA,LAST ;SET LAST ADDRESS
2600 010462 012703 000005 18: MOV #5,,R3 ;DO EACH ADDRESS 5 TIMES.
2601 010466 022700 173024 28: CMP #173024,R0 ;DON'T DO THE VECTOR ADD.
2602 010472 001001 BNE ,+4 ;BR IF NOT THE VECTOR ADD.
2603 010474 022024 CMP (R0)+,(R4)+ ;UPDATE TO NEXT ADDRESS
2604 010476 022700 173224 CMP #173224,R0 ;DON'T DO THE TRAP VECTORS
2605 010502 001001 BNE ,+4 ;NO THIS ISN'T A TRAP VECTOR.
2606 010504 022024 CMP (R0)+,(R4)+ ;UPDATE THE POINTERS..
2607 010506 011005 MOV (R0),R5 ;READ THE ADDRESS
2608 010510 011401 MOV (R4),R1 ;READ THE SOFTWARE ADDRESS
2609 010512 020105 CMP R1,R5 ;DO THEY MATCH?
2610 010514 001401 BEQ ,+4 ;BR IF GOOD
2611 010516 104001 HLT 1 ;INCORRECT COMPARISON.
2612 010520 032737 004000 177570 BIT #BIT11,SWR ;QUICK PASS,?
2613 010526 001002 BNE ;BR IF YES
2614 010530 005303 DEC R3 ;HAS THAT ADD BEEN READ 5 TIMES?
2615 010532 001355 BNE 28 ;BR IF NOT 5 TIMES
2616 010534 023700 010760 CMP LAST,R0 ;WAS LAST ADDRESS CHECKED?
2617 010540 001403 BEQ ,+10 ;BR IF YES
2618 010542 104400 SCOP1: ;LOCK ON THIS ADDRESS?
2619 010544 022024 CMP (R0)+,(R4)+ ;UPDATE THE POINTERS.
2620 010546 000745 BR 18 ;CONTINUE THE TEST.
2621 010550 032737 000001 010154 BIT #BIT0,SAVSR1 ;EXTENDED WORD TO BE CHECKED?
2622 010556 001413 BEQ 38 ;BR IF NO CHECKING.
2623 010560 022737 173776 010760 CMP #173776,LAST ;IS ALL THE TEST DONE?
2624 010566 001407 BEQ 38 ;BR IF YES.
2625 010570 012737 173776 010760 MOV #173776,LAST ;SET LAST ADDRESS.
2626 010576 013704 010162 MOV EXTMAP,R4 ;SET EXTENDED MAP.
2627 010602 005720 (R0)+ ;POP POINTER
2628 010604 000726 BR 18 ;GO DO THE TEST.

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2629 ;TEST THAT WRITTING ROM RESULTS IN A TIME OUT
2630 ;TRAP.
2631
2632 010606 012737 010632 001230 301 MOV #56,LOCK ;IF SW09=1 GOTO 56 WHEN SCOP1 IS HIT
2633 010614 012700 173000 MOV #173000,R0 ;SET R0 WITH BASE ADDRESS OF ROM
2634 010620 012737 010662 000004 MOV #66,0#4 ;SET FOR TIME OUT TRAP
2635 010626 012703 000005 401 MOV #5,,R3 ;DO EACH ADD 5 TIMES
2636 010632 022700 173024 501 CMP #173024,R0 ;CHECK FOR A TRAP VECTOR
2637 010636 001001 BNE .+4 ;BR IF NOT VECTOR
2638 010640 005720 TST (R0)+ ;UPDATE THE REGISTER POINTER
2639 010642 022700 173224 CMP #173224,R0 ;CHECK FOR THE OTHER VECTOR
2640 010646 001001 BNE .+4 ;BR IF NOT THE VECTOR
2641 010650 005720 TST (R0)+ ;UPDATE THE POINTER
2642 010652 012710 177777 MOV #=-1,(R0) ;WRITE ROM WITH A -1
2643 010656 000240 NOP ;WAIT ONE INSTR. TIME
2644 010660 104002 HLT 2 ;WRITING ROM DIDN'T TIME OUT.
2645 010662 012706 001200 601 MOV #STACK,SP ;RESTORE STACK
2646 010666 032737 004000 177570 BIT #BIT11,SWR ;QUICK PASS?
2647 010674 001002 BNE .+6
2648 010676 005303 DEC R3 ;DO EACH ADD 5 TIMES
2649 010700 001354 BNE 50 ;NOT DONE WITH THIS ONE YET.
2650 010702 032737 000001 010154 BIT #BIT0,SAVSR1 ;EXTENDED 128. WORDS TO BE CHECKED?
2651 010710 001404 BEQ .+12 ;BR IF NO
2652 010712 022700 173776 CMP #173776,R0 ;HAVE ALL 286. WORDS BEEN CHECKED?
2653 010716 001407 BEQ 70 ;BR IF ALL DONE
2654 010720 000403 BR .+10 ;KEEP GOING
2655 010722 023700 010170 CMP LASTA,R0 ;ALL DONE??
2656 010726 001403 BEQ 70 ;HAVE ALL 128. WORDS DONE?
2657 010730 104400 SCOP1 ;CHECK SW09 FOR FREEZE!!
2658 010732 005720 TST (R0)+ ;UPDATE TO NEXT ADDRESS
2659 010734 000734 BR 40 ;GO DO IT AGAIN
2660 010736 005337 001232 701 DEC ICOUNT ;ITERATION COUNT DONE?
2661 010742 001004 BNE 80 ;BR IF NOT DONE.
2662 010744 004737 014660 JSR PC,EOP ;TYPE END MESSAGE
2663 010750 000137 010400 JMP PRG1 ;GO DO IT AGAIN.
2664 010754 000137 010430 801 JMP PRG.1 ;GO RESTART.
2665 010760 000000 LAST: 0
2666
2667 010762 005015 042504 044526 BM873X: .ASCII <15><12>/DEVICE VERSION/
011002 005015 046502 033470 .ASCII <15><12>/BM873=Y/
011014 005015 026052 026101 BM.ERR: .ASCII <15><12>/*,A,B,C,D ONLY./
011036 020040 053040 051105 VERS: .ASCII / VERSION: BM873=Y/
,EVEN
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2668
2669
2670 ;PROGRAM 2
2671 ;BLIND READ FROM ROM,
2672 ;THIS PROGRAM WILL DUMP THE CONTENTS OF THE ROM OUT
2673 ;PERFORMING NO CHECKING AT ALL.
2674 ;PLEASE NOTE: NO CHECKING IS DONE.
2675 ;NOTE: HITTING "CONTROL "C" RESTARTS AT TOP OF PROG.
2676 ;NOTE: IF YOU LEAVE SW07=1; THE DUMP WILL
2677 ;BE MADE ON THE LINE PRINTER
2678 ;IF IT EXISTS.
2679
2680 011062 012737 011062 014616 PRG21 MOV #PRG2,PRG.NO ;SET FOR POWER FAIL
2681 011070 012737 014620 000004 MOV #NO,TRAP,0#4 ;SET FOR UNEXPECTED TRAP TO 4
2682 011076 013737 010170 010760 MOV LASTA,LAST
2683 011104 002737 000002 010760 ADD #2,LAST
2684 011112 105737 177570 TSTB SWR ;IF SW07=1; OUTPUT TABLE ON LPT
2685 011116 100004 BPL .+12 ;IF IT EXISTS. BR IF SW07=0
2686 011120 012737 177777 015114 MOV #=-1,LPTFLG ;SET THE LPT FLAG
2687 011126 000402 BR .+6 ;BR TO TEST
2688 011130 005037 015114 CLR LPTFLG ;CLEAR THE LPT FLAG
2689 011134 012700 173000 MOV #173000,R0 ;SET R0 WITH THE STARTING ROM ADD.
2690 011140 012777 011430 170046 MOV #48,0TKVEC ;SET TTY VECTOR FOR CONTROL C
2691 011146 012777 000100 170030 MOV #100,0TKCSR ;SET TTY KYBD INTERRUPT EN.
2692 011154 013703 010156 MOV TABLE,R3 ;SET POINTER.
2693 011160 104401 011456 TYPE ,DH.2 ;TYPE MESSAGE
2694 011164 104401 011556 TYPE ,DH.2B ;TYPE THE HEADER
2695 011170 012737 000007 001256 101 MOV #7,TEMP5 ;SET COUNTER
2696 011176 011001 MOV (R0),R1 ;READ THE ROM
2697 011200 010037 001252 R0,TEMP3 ;STORE R0
2698 011204 010137 001254 R1,TEMP4 ;STORE R1
2699 011210 022737 006140 010156 CMP #MAP.Y,,TABLE ;IF BM873.Y* SELECTED; FILL TABLE
2700 011216 001001 BNE .+4 ;BR IF NOT BM873.Y*
2701 011220 011023 MOV (R0),(R3)+ ;FILL THE TABLE.
2702 011222 005720 TST (R0)+ ;POP THE POINTER
2703 011224 104404 CONVRT ;TYPE AND CONVERT NUMBERS
2704 011226 012076 DT,2 ;IN DATA TABLE 2
2705 011230 011001 701 MOV (R0),R1 ;STORE ROM DATA
2706 011232 010037 001252 R0,TEMP3 ;STORE ROM ADDRESS
2707 011236 010137 001254 R1,TEMP4 ;PREPARE DATA FOR TYPE OUT
2708 011242 022737 006140 010156 CMP #MAP.Y,,TABLE ;IS BM873.Y* SELECTED?
2709 011250 001001 BNE .+4 ;BR IF NO.
2710 011252 011023 MOV (R0),(R3)+ ;FILL THE DATA TABLE
2711 011254 005720 TST (R0)+ ;POP THE POINTER
2712 011256 104405 CONVRT ;TYPE OUT MAP
2713 011260 012110 DT,2A ;USE DATA TABLE 2A
2714 011262 023700 010760 CMP LAST,R0 ;HAS THE HIGHEST LIMIT BEEN HIT?
2715 011266 001404 BEQ 20 ;BR IF ALL DONE.
2716 011270 005337 001256 DEC TEMP5 ;DECREASE COUNTER
2717 011274 001355 BNE 70 ;BR IF NOT 0; KEEP GOING
2718 011276 000734 BR 10 ;GO TYPE ADDRESS NOW
2719 011300 032737 000001 010154 201 BIT #BIT0,SAVSR1 ;IS THE EXTENDED 128. WORDS TO BE CHECKED??
2720 011306 001442 BEQ 30 ;BR IF NO.
2721 011310 012700 173400 MOV #173400,R0 ;RESET POINTER OF ROM
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2722 011314 013703 010162 MOV EXTMAP,R3 ;SET SOFTWARE MAP POINTER
2723 011320 104401 011671 TYPE ,DH,2A ;TYPE NEW HEADER
2724 011324 104401 011556 TYPE ,DH,2B ;TYPE ADDRESS AND +XX
2725 011330 012737 000007 001256 66: MOV #7,TEMP5 ;SET TYPE OUT COUNTER
2726 011336 011001 MOV (R0),R1 ;READ THE ROM
2727 011340 010037 001252 MOV R0,TEMP3 ;STORE R0
2728 011344 010137 001254 MOV R1,TEMP4 ;STORE R1
2729 011350 012023 MOV (R0)+,(R3)+ ;STORE THE DATA IN SOFTWARE MAP
2730 011352 104404 CONVRT ;TYPE AND CONVERT NUMBERS
2731 011354 012076 DT,2 ;IN DATA TABLE 2
2732 011356 011001 66: MOV (R0),R1 ;SAVE THE ROM DATA
2733 011360 010037 001252 MOV R0,TEMP3 ;SAVE THE ROM ADDRESS
2734 011364 010137 001254 MOV R1,TEMP4 ;SET DATA FOR TYPE OUT
2735 011370 104405 CNVRT ;TYPE OUT THE TABLE,
2736 011372 012110 DT,2A ;FIND DATA THROUGH DATA TABLE 2A
2737 011374 012023 MOV (R0)+,(R3)+ ;STORE THE DATA IN SOFTWARE TABLE
2738 011376 022700 174000 CMP #174000,R0 ;HAS THE HIGHEST LIMIT BEEN HIT?
2739 011402 001404 BEQ 38 ;BR IF ALL DONE,
2740 011404 005337 001256 DEC TEMPS ;DEC TABLE COUNTER
2741 011410 001362 BNE 84 ;BR TO JUST TYPE DATA
2742 011412 000746 BR 66 ;BR TO TYPE ADDRESS
2743 011414 104401 014437 38: TYPE ,MCRLF ;DUMP LAST LINE FROM LPT.
2744 011420 005000 CLR R0 ;CLEAR DATA LIGHTS
2745 011422 000000 HALT ;HIT CONTINUE TO PROCEED,
2746 011424 000137 011062 JMP PRG2 ;GOTO PRG 2
2747 ;ENTER BY KYBD INTERRUPT.
2748 011430 104406 48: KEY.TO,R2 ;ECHO KEY
2749 011432 022702 000003 CMP #3,R2 ;WAS CONTROL C HIT
2750 011436 001006 BNE 58 ;BR IF NO
2751 011440 005037 015114 CLR LPTFLG ;STOP PRINTING ON LPT
2752 011444 104401 013326 TYPE ,MC ;TYPE "C"
2753 011450 012716 011062 MOV #PRG2,(SP) ;SET FOR RETURN
2754 011454 000002 RTI ;LEAVE THIS ROUTINE
2755
2756 011456 006414 005012 016412 DH,2: .ASCII <14><15><12><12><12><38><37><177><177><177>/BLIND READ OF ROM/
011511 015 006412 077577 .ASCII <15><12><15><177><177>/NOTE: NO CHECKING IS PERFORMED,/
011556 005015 040412 042104 DH,2B: .ASCII <15><12><12>/ADDRESS ADD+00 ADD+02 ADD+04/
011620 020040 042101 025504 .ASCII / ADD+06 ADD+10 ADD+12 ADD+14 ADD+16/
011671 015 005012 054105 DH,2A: .ASCII <15><12><12>/EXTENDED 128 WORD ROM DUMP,/
011730 005015 047503 052116 .ASCII <15><12>/CONTENTS DUMPED IS PLACED IN THE SOFTWARE/
012003 015 046412 050101 .ASCII <15><12>/MAP, VISUAL INSPECTION OF DATA IS/
012046 005015 047531 051125 .ASCII <15><12>/YOUR RESPONSIBILITY!!/

2757 012076 000002 ;EVEN
2758 012100 006 003 DT,2: 2
2759 012102 001252 006,3 .BYTE 206,3
2760 012104 006 002 .BYTE 6,2
2761 012106 001254 .TEMP4
2762
2763 012110 000001 DT,2A: 1
2764 012112 006 002 .BYTE 6,2
2765 012114 001254 .TEMP4

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2766
2767
2768 ;PROGRAM 3
2769 ;PROGRAM 3 IS THE SAME AS PROGRAM 1 ONLY YOU THE
2770 ;USER HAS THE CHANCE TO ALTER THE MAP WHICH IS
2771 ;COMPARED TO THE DATA IN THE ROM ADDRESSES
2772 ;NOTE THE FOLLOWING COMMANDS:
2773 ;
2774 ;*D DATA INSERT DATA; HIT LINE FEED TO ESCAPE.
2775 ;*R RUN RUN THE PROGRAM
2776 ;*L LIST LIST THE SOFTWARE TABLE ON TTY.
2777 ;*P PRINT PRINT LISTING OF SOFTWARE TABLE ON LINE PRINTER.
2778 ;*A ADDRESS INPUT THE ADDRESS OF THE DATA YOU WANT TO ALTER.
2779 ;LINE FEED WHEN IN THE DATA INPUT MODE HIT LINE FEED TO
2780 ; ESCAPE TO COMMAND MODE
2781 ;CR CARRAGE RETURN= WHEN IN THE DATA INPUT MODE A CARRAGE RETURN
2782 ; ZEROS NEXT ADDRESS AND WAITS FOR NEW DATA,
2783 ;
2784
2785
2786 012116 005037 015114 PRG3: CLR LPTFLG ;SET FOR TTY OUTPUT
2787 012122 012737 012116 014616 MOV #PRG3,PRG,NO ;SET FOR POWER FAIL
2788 012130 012777 012170 167056 MOV #ST,VEC,@TKVEC ;SET FOR KEY BOARD INTERRUPT
2789 012136 013701 010156 MOV TABLE,R1 ;DEFAULT STARTING ADDRESS TO MAP
2790 012142 010137 013352 MOV R1,ADDRESS ;SAVE THE SOFTWARE ADDRESS
2791 012146 104401 014341 XHOLD: TYPE ,MASTER ;TYPE AN "*"
2792 012152 012777 000100 167024 MOV #100,@TKCSR ;SET INTERRUPT ENABLE FOR TTY
2793 012160 000001 WAIT ;HIT A KEY
2794 012162 000771 BR XHOLD ;GO BACK AN TYPE AN "*"
2795 012164 000001 HOLD: WAIT ;HIT A KEY !!
2796 012166 000776 BR HOLD ;GO BACK AND HIT AGAIN !!
2797 012170 104406 ST,VEC: KEY.TO,R2 ;ECHO CHAR AND STRIP BIT 7
2798 012172 104401 014437 TYPE ,MCRLF ;TYPE A CARRAGE RETURN + LINE FEED
2799 012176 022702 000114 CMP #114,R2 ;WAS AN "L" (LIST) HIT?
2800 012202 001571 BEQ SRV,L ;WAS A "P" (PRINT) HIT?
2801 012204 022702 000120 CMP #120,R2 ;WAS A "D" (DATA) HIT?
2802 012210 001563 BEQ SRV,P ;WAS A "D" (DATA) HIT?
2803 012212 022702 000104 CMP #104,R2 ;WAS A "D" (DATA) HIT?
2804 012216 001417 BEQ SRV,D ;WAS AN "R" (RUN) HIT?
2805 012220 022702 000122 CMP #122,R2 ;WAS AN "R" (RUN) HIT?
2806 012224 001002 BNE ,+6 ;
2807 012226 000137 012754 JMP SRV,R ;WAS AN "A" (ADDRESS) HIT?
2808 012232 022702 000101 CMP #101,R2 ;WAS AN "A" (ADDRESS) HIT?
2809 012236 001501 BEQ SRV,A ;WAS AN "A" (ADDRESS) HIT?
2810 012240 022702 000015 CMP #15,R2 ;WAS CARRAGE RETURN HIT?
2811 012244 001001 BNE ,+4 ;
2812 012246 000002 RTI ;GO WAIT FOR VALID CHAR.
2813 012250 104401 014432 TYPE ,M,QM ;TYPE A "?"
2814 012254 000002 RTI ;NEITHER A "L","P","D","R","A",OR CR WAS HIT.
2815
2816 012256 012777 012310 166730 SRV,D: 28: MOV #48,@TKVEC ;SET VECTOR FOR DATA INPUT.
2817 012264 013701 013352 MOV ADDRESS,R1 ;RESET ADDRESS POINTER.
2818 012270 010137 001254 MOV R1,TEMP4 ;SET FOR TYPE OUT ROUTINE
2819 012274 104404 CONVRT ;TYPE THE SOFTWARE ADDRESS

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2820 012276 013332 DT,3A ;FIND TABLE 3A
2821 012300 005011 CLR (R1) ;ZERO THE ADDRESS AND WAIT FOR DATA
2822 012302 012716 MOV #HOLD,(SP) ;SET RETURN (NO **)
2823 012306 000002 R1 ;GO BACK WAIT FOR KEY TO BE HIT
2824 012310 104406 KEY,TO,R2 ;ECHO CHAR AND STRIP BIT 7
2825 012312 022702 CMP #15,R2 ;HAS CARRAGE RETURN HIT?
2826 012316 001016 BNE ;NO GO TO 58
2827 012320 104401 MUF ;UPDATE THE SOFTWARE ADDRESS
2828 012324 085721 TPT (R1)+ ;INPUT LIMIT EXCEEDED!! ERROR.
2829 012326 037701 CMP EXTEND,R1 ;ZERO ADDRESS WAIT FOR DATA
2830 012334 101400 BLOS 78 ;ZERO ADDRESS WAIT FOR DATA
2831 012336 005011 MOV (R1),TEMP4 ;SAVE THE ADDRESS.
2832 012342 010137 CONVNT ;TYPE THE ADDRESS.
2833 012344 104404 DT,3A ;FROM DATA TABLE 3A
2834 012346 013332 MOV #R1,ADDRESS ;SAVE THE ADDRESS FOR GOOD
2835 012350 000002 R1 ;LEAVE HERE.
2836 012352 022702 CMP #12,R2 ;HAS A LINE FEED HIT??
2837 012354 001006 BNE ;NO LINE FEED NOT HIT.
2838 012358 001006 MOV #ST,VEC,STVEC ;SET THE RESPOND VECTOR
2839 012362 012777 ;WHEN I RETURN TYPE AN "*"
2840 012366 012716 MOV #XHOLD,(SP) ;LEAVE HERE
2841 012374 000002 R1 ;LEAVE HERE
2842 012376 022702 BIT #16,R2 ;HAS IT OCTAL CHAR HIT?
2843 012402 001074 BNE ;SET INITIAL CHAR HIT?
2844 012404 042702 BIC #260,R2 ;CLEAR ALT EUI LMSI 3 BITS
2845 012410 000241 CLR CPU ;CLEAR CPU CARRY BIT
2846 012412 009311 ROL (R1) ;SHIFT AGAIN.
2847 012414 104406 BLS #8 ;ERROR ??
2848 012418 009311 ROL (R1) ;SHIFT AGAIN.
2849 012422 009311 ROL (R1) ;ERROR ??
2850 012426 009311 ROL (R1) ;SHIFT ONCE MORE
2851 012432 009002 BLS #8 ;ERROR ??
2852 012436 009002 BIT #2,(R1) ;SET NEW NUMBER IN ADDRESS
2853 012442 008002 BNE ;LEAVE HERE
2854 012444 004011 CLR CPU ;CLEAR THE ADDRESS.
2855 012446 004011 R1 ;TYPE A"?
2856 012448 000002 R1 ;TYPE A"?
2857 012450 000241 ROL (R1) ;SHIFT THE ADDRESS
2858 012452 009002 BLS #8 ;ERROR ??
2859 012456 009002 BIT #2,(R1) ;SET NEW NUMBER IN ADDRESS
2860 012462 004011 CLR CPU ;CLEAR CPU CARRY BIT
2861 012464 004011 R1 ;SHIFT THE ADDRESS
2862 012466 009002 BLS #8 ;ERROR ??
2863 012468 009002 BIT #2,(R1) ;SET NEW NUMBER IN ADDRESS
2864 012474 004011 CLR CPU ;CLEAR CPU CARRY BIT
2865 012476 004011 R1 ;SHIFT THE ADDRESS
2866 012478 009002 BLS #8 ;ERROR ??
2867 012482 009002 BIT #2,(R1) ;SET NEW NUMBER IN ADDRESS
2868 012486 004011 CLR CPU ;CLEAR CPU CARRY BIT
2869 012492 004011 R1 ;SHIFT THE ADDRESS
2870 012494 009002 BLS #8 ;ERROR ??
2871 012498 009002 BIT #2,(R1) ;SET NEW NUMBER IN ADDRESS
2872 012504 004011 CLR CPU ;CLEAR CPU CARRY BIT
2873 012506 004011 R1 ;SHIFT THE ADDRESS
    
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2874 012502 103420 PCS 38 ;ERROR 37
2875 012504 006103 ROL R3 ;SHIFT AGAIN
2876 012506 103416 BCS 38 ;ERROR ??
2877 012510 006103 ROL R3 ;ONE LAST TIME
2878 012514 103414 BCS 38 ;ERROR ??
2879 012518 042702 R3 ;CLEAR UNWANTED BITS
2880 012522 000203 BLS R3,R3 ;PLACE DATA IN R3
2881 012526 000203 R3,R3 ;LEAVE THIS ROUTINE
2882 012534 012716 #XHOLD,(SP) ;WHEN RETURNING TYPE AN "*"
2883 012536 012777 MOV #ST,VEC,STVEC ;SET INITIAL VECTOR
2884 012542 000002 R1 ;SAVE GOOD ADDRESS FOR FUTURE USE
2885 012544 005003 R1 ;LEAVE HERE
2886 012546 004003 CLR R3 ;ZERO BAD ADDRESS
2887 012548 104401 ;TYPE A CR IF
2888 012552 104401 ;TYPE "?
2889 012556 000002 R1 ;LEAVE !!
2890 012560 012737 MOV *-1,LPFLG ;*-1,LPFLG
2891 012562 177777 ;YOU ENTERED HERE BECAUSE YOU HIT "L"
2892 012564 015114 ;YOU TOLD ME YOU WANTED A LISTING OF THE SOFTWARE MAP
2893 012566 180 HERE IT IS.
2894 012568 012566 SRV,L1 ;SRV,L1
2895 012570 013700 MOV TABLE,R0 ;GET SOFTWARE MAP
2896 012572 013752 MOV #AEND,DEAD ;SET DEAD END POINTER
2897 012574 014432 ;TYPE HEADER
2898 012576 104401 ;TYPE ADDRESS ADD+XX
2899 012578 011556 MOV #7,TEMP5 ;SET COUNTER FOR ACROSS PAGE
2900 012582 013757 ;GET DATA
2901 012586 011001 MOV #R0,R1 ;GET ADDRESS
2902 012590 010037 R0,TEMP3 ;SET DATA FOR TYPE OUT
2903 012594 010137 R1,TEMP4 ;UPDATE ADDRESS POINTER
2904 012598 005720 ;TYPE ADDRESS ADD+XX
2905 012602 104404 ;GET DATA TABLE 35
2906 012606 011001 MOV #R0,R1 ;GET ADDRESS
2907 012610 010037 R0,TEMP3 ;SET DATA FOR TYPE OUT
2908 012614 010137 R1,TEMP4 ;UPDATE ADDRESS POINTER
2909 012618 013300 ;TYPE ADDRESS ADD+XX
2910 012622 009311 DT,3A ;FROM DATA TABLE 3A
2911 012626 001037 MOV #R1,ADDRESS ;SAVE THE ADDRESS
2912 012630 009311 ROL (R1) ;SHIFT AGAIN.
2913 012634 009311 ROL (R1) ;ERROR ??
2914 012638 009311 ROL (R1) ;SHIFT ONCE MORE
2915 012642 009002 BLS #8 ;ERROR ??
2916 012646 009002 BIT #2,(R1) ;SET NEW NUMBER IN ADDRESS
2917 012650 004011 CLR CPU ;CLEAR THE ADDRESS.
2918 012654 004011 R1 ;TYPE A"?
2919 012658 008002 BNE ;LEAVE HERE
2920 012662 001380 ;EXTENDED SOFTWARE DUMP?
2921 012666 000744 BR 18 ;BR IF NO DUMP
2922 012700 ;EXTENDED SOFTWARE DUMP?
2923 012706 001416 BR 48 ;BR IF NO DUMP
2924 012712 005743 ;PUSH POINTER
2925 012718 023703 CMP EXTEND,R3
2926 012724 010164 BEQ 66 ;BR IF ALL DONE
2927 012730 001412 BEQ 66
    
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2928 012720 104401 014276
2929 012724 104401 011956
2930 012730 013100 010102
2931 012734 013137 010164 012792
2932 012742 000722
2933 012744 005637 019114
2934 012750 000002
2935 012752 000000
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BM877D.P11 MAINDEC-11-DB8MD-D BM873 UNIVERSAL RESTAUR LOADER DIAGNOSTIC.
    
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TEST THAT WRITING ROW RESULTS IN A TIME OUT
 /TRAP.

ADDRESS: 0

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3042 013354 012777 014444 165632 PRG4: MOV #,KEY,TO,R2,STKVEC
3043 013362 012777 000100 165614 MOV #100,STKCSR
3044 013370 012737 013354 014616 MOV #PRG4,PRG,NO
3045 013376 005037 001244 CLR LSTERR
3046 013402 012706 001200 MOV #STACK,SP
3047 013406 012737 020000 001232 MOV #20000,ICOUNT
3048 013414 005737 014042 TST FLAG4
3049 013420 001073 BNE TAG,A
3050 013422 005137 014042 COM FLAG4
3051 013426 012705 000002 MOV #2,R5
3052 013432 012704 014032 MOV #LOC1,R4
3053 013436 012737 014620 000004 MOV #NO,TRAP,004
3054 013444 012737 000001 001252 MOV #1,TEMP3
3055 013452 104401 014044 10: TYPE ,MCHAN
3056 013456 104405 CNVRT DT,4A
3057 013460 014164 DT,4A
3058 013462 104401 014060 TYPE ,MACTV
3059 013466 104401 014073 20: TYPE ,MADD1
3060 013472 012700 173024 MOV #173024,R0
3061 013476 005037 173024 CLR #173024
3062 013502 010537 173024 MOV R5,#173024
3063 013506 000240 NOP
3064 013510 012706 001200 30: MOV #STACK,SP
3065 013514 012700 173024 MOV #173024,R0
3066 013520 012737 014620 000004 MOV #NO,TRAP,004
3067 013526 013737 173024 001254 MOV #173024,TEMP4
3068 013534 104405 CNVRT DT,4B
3069 013536 014172 DT,4B
3070 013540 013724 173024 MOV #173024,(R4)+
3071 013544 104401 014127 TYPE ,MADD2
3072 013550 012700 173224 MOV #173224,R0
3073 013554 013737 173224 001254 MOV #173224,TEMP4
3074 013562 104405 CNVRT DT,4B
3075 013564 014172 DT,4B
3076 013566 005237 001252 INC TEMP3
3077 013572 000241 CLC
3078 013574 006105 ROL R5
3079 013576 022705 000040 CMP #40,R5
3080 013602 001323 BNE 10
3081 013604 000137 013354 JMP PRG4
3082

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3083 013610 012703 000002 TAG,A: MOV #2,R3
3084 013614 012704 014032 MOV #LOC1,R4
3085 013620 012737 014620 000004 10: MOV #NO,TRAP,004
3086 013626 005037 173024 CLR #173024
3087 013632 010337 173024 MOV R3,#173024
3088 013636 000240 NOP
3089 013640 012706 001200 20: MOV #STACK,SP
3090 013644 012737 014620 000004 MOV #NO,TRAP,004
3091 013652 012700 173024 MOV #173024,R0
3092 013656 011401 MOV (R4),R1
3093 013660 013705 173024 MOV #173024,R5
3094 013664 020105 CMP R1,R5
3095 013666 001401 BEQ #+4
3096 013670 104001 HLT 1
3097 013672 012700 173224 MOV #173224,R0
3098 013676 013703 173224 MOV #173224,R5
3099 013702 020105 CMP R1,R5
3100 013704 001401 BEQ #+4
3101 013706 104001 HLT 1
3102 013710 005724 TST (R4)+
3103 013712 000241 CLC
3104 013714 006103 ROL R3
3105 013716 022703 000040 CMP #40,R3
3106 013722 001336 BNE 10
3107 013724 005337 001232 DEC ICOUNT
3108 013730 001327 BNE TAG,A
3109 013732 012737 177777 173224 MOV #1,00173224
3110 013740 005037 173024 CLR #173024
3111 013744 012700 173024 MOV #173024,R0
3112 013750 013701 014032 MOV LOC1,R1
3113 013754 013705 173024 MOV #173024,R5
3114 013760 020105 CMP R1,R5
3115 013762 001401 BEQ #+4
3116 013764 104001 HLT 1
3117 013766 012737 177777 173024 MOV #1,00173024
3118 013774 005037 173224 CLR #173224
3119 014000 012700 173224 MOV #173224,R0
3120 014004 013701 014032 MOV LOC1,R1
3121 014010 013705 173224 MOV #173224,R5
3122 014014 020105 CMP R1,R5
3123 014016 001401 BEQ #+4
3124 014020 104001 HLT 1
3125 014022 004737 014660 JSR PC,EOP
3126 014026 000137 013354 JMP PRG4
3127
3128 014032 000000 LOC1: 0
3129 014034 000000 LOC2: 0
3130 014036 000000 LOC3: 0
3131 014040 000000 LOC4: 0
3132 014042 000000 FLAG4: 0

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3133 014044 005015 041412 040510 MCHAN: .ASCIZ <15><12><12>/CHANNEL /
      014060 041501 044524 040520 MACTV: .ASCIZ/ACTIVATED./
      014073 015 040412 042104 MADD1: .ASCIZ <15><12>/ADDRESS 773024 CONTAINS: /
      014127 015 040412 042104 MADD2: .ASCIZ <15><12>/ADDRESS 773224 CONTAINS: /
      014164 000001 .EVEN
3134 014164 000001 DT,4A: 1
3135 014166 002 002 .BYTE 2,2
3136 014170 001252 TEMP3
3137 014172 000001 DT,4B: 1
3138 014174 006 002 .BYTE 6,2
3139 014176 001254 TEMP4
3140
3141 014200 005015 051120 043517 MSG3: .ASCIZ <15><12>/PROGRAM NO. (1,2,3,4) /
      014232 004414 016412 077437 MSG4: .ASCIZ <14><15><12><35><37><177><177>/SOFTWARE MAP IS AS FOLLOWS:/
      014276 005015 020012 054105 MSG5: .ASCIZ <15><12><12>/ EXTENDED SOFTWARE MAP FOLLOWS./
      014341 015 025012 000 MASTER: .ASCIZ <15><12>/*/
      014345 007 006407 042412 M,END: .ASCIZ <7><7><15><12>/END PASS BM873-Y/
      014372 005015 053520 020122 M,FAIL: .ASCIZ <15><12>/PWR UP AFTER/
      014410 005015 042522 046101 .ASCIZ <15><12>/REAL PWR FAIL/
      014430 000044 M,DOL: .ASCIZ /#
      014432 005015 037477 000 M,OM: .ASCIZ <15><12>/??/
      014437 015 000012 M,REF: .ASCIZ <15><12>
      014442 000012 M,F: .ASCIZ <12>
      .EVEN
      .KEY,TO,R2:
3142 014444 NOV
3143 014444 017702 164536 NOV STKDBR,R2 ;READ THE TTY KYBD BUFFER
3144 014450 105777 164534 TSTB STPCSR ;PRINTER READY??
3145 014454 100378 BPL -4 ;BR IF NOT READY,
3146 014456 010277 164530 NOV R2,STPDBR ;ECHO INPUTED CHARACTER
3147 014462 042702 000200 BIC #BIT7,R2 ;CLEAR BIT 7 FOR A SEVEN BIT CHAR,
3148 014466 022702 000007 CMP #7,R2 ;WAS CNTR "G" HIT?
3149 014472 001008 BNE 10 ;NO!
3150 014474 000005 RESET ;CLEAR THE WORLD
3151 014476 005037 177776 CLR PS
3152 014502 000137 007150 JMP START ;GO TO TEST
3153 014506 000002 10: RTI ;LEAVE HERE
3154

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3155 014510 005037 001244 .PFAIL: CLR LSTERR
3156 014514 013746 000004 MOV #4,-(SP)
3157 014520 012737 014550 000004 MOV #10,#4
3158 014526 005737 173000 TST #173000 ;IS THIS PF REAL?
3159 014532 000240 NOP ;TRAP IS CAUSED BY LOADER
3160 014534 012737 014550 000024 MOV #PWR,UP,#24 ;ITS REAL, PREPARE FOR PWR UP
3161 014542 012637 000004 MOV (SP)+,#4
3162 014546 000000 HALT
3163 014550 022626 10: POP,SP
3164 014552 012637 000004 MOV (SP)+,#4
3165 014556 000000 HALT ;HARDWARE ERROR, BOOT DIDN'T FORCE
3166 ;HIGH ADDR LINES AND LOAD BUTTON WAS ACTIVATED
3167 014560 012737 014510 000024 PWR,UP: MOV #.PFAIL,#24
3168 014566 012706 001200 MOV #STACK,SP
3169 014572 005000 CLR R0 ;SET DELAY
3170 014574 062700 ADD #1,R0 ;WAIT FOR TTY
3171 014600 001375 BNE -4 ;
3172 014602 104401 014372 TYPE ,MFAIL ;TYPE FAILED,
3173 014606 005037 177776 CLR PS ;SET STATUS TO ZERO
3174 014612 000177 000000 JMP @PRG,NO
3175 014616 000000 PRG,NO: 0
3176 014620 NO,TRAP:
3177 014620 011637 014556 MOV (SP),XSTORE
3178 014624 032716 100000 BIT #BIT15,(SP)
3179 014630 001410 BEQ 10
3180 014632 011600 MOV (SP),R0
3181 014634 104004 HLT 4
3182 014636 012706 001200 MOV #STACK,SP
3183 014642 005037 177776 CLR PS
3184 014646 000177 177744 JMP @PRG,NO
3185 014652 104003 10: HLT 3
3186 014654 000002 RTI
3187 014656 000000 XSTORE: 0
3188
3189 014660 005037 001244 EOP: CLR LSTERR
3190 014664 005037 015114 CLR LPTFLG
3191 014670 104401 014345 TYPE ,M,END
3192 014674 104401 014736 TYPE ,VERSON
3193 014700 013701 000042 MOV #42,R1
3194 014704 001413 BEQ X1
3195 014706 022737 010400 014616 CMP #PRG1,PRG,NO
3196 014714 001002 BNE +6
3197 014716 000137 013354 JMP PRG4
3198 014722 004711 LOGIC: JSR PC,(R1)
3199 014724 000240 NOP
3200 014726 000240 NOP
3201 014730 000240 NOP
3202 014732 000240 NOP
3203 014734 000207 X1: RTS PC
3204 014736 000101 VERSON: 10: ;SEVEN BIT ASCII FOR DEFAULT "A"

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3205
3206
3207
3208 ;CHECK FOR FREEZE ON CURRENT DATA
3209 014740 032737 001000 177570 ,SCOPI: BIT #SW09,SWR
3210 014746 001402 BEQ 18
3211 014750 013716 001230 MOV LOCK,(SP)
3212 014754 000002 RTI 18:
3213
3214 ;TELETYPE OUTPUT ROUTINE
3215
3216 014756 013737 000004 001000 ,TYPE: MOV #04,001000
3217 014764 010537 001002 MOV RS,001002
3218 014770 017605 000000 MOV 0(SP),RS
3219 014774 062716 000002 ADD #2,(SP)
3220 015000 032737 010000 177570 18: BIT #SW12,SWR
3221 015006 001013 BNE 30
3222 015010 105715 TSTB (RS)
3223 015012 001411 BEQ 30
3224 015014 005737 015114 TST LPTFLG
3225 015020 001014 BNE 40
3226 015022 105777 164162 20: TSTB 0TPCSR
3227 015026 100375 BPL 20
3228 015030 112577 164156 MOV (RS)+,0TPDDBR
3229 015034 000761 BR 18
3230 015036 013737 001000 000004 30: MOV #01000,0#4
3231 015044 013705 001002 MOV #01002,RS
3232 015050 000002 RTI
3233 015052 012737 015070 000004 40: MOV #05,0#4
3234
3235 015060 005777 164114 TST 0LPTCSR
3236 015064 000240 NOP
3237 015066 000404 BR 60
3238 015070 005037 015114 50: CLR LPTFLG
3239 015074 022626 POP,SP
3240 015076 000740 BR 18
3241 015100 105777 164074 60: TSTB 0LPTCSR
3242 015104 100375 BPL 60
3243 015106 112577 164070 MOV (RS)+,0LPTDDBR
3244 015112 000732 BR 18
3245 015114 000000 LPTFLG: 0
3246
3247 ;ERROR HANDLER
3248
3249 015116 005037 015114 ,HLT: CLR LPTFLG
3250 015122 032737 010000 177570 BIT #SW12,SWR
3251 015130 001406 BEQ XB1
3252 015132 105777 164052 TSTB 0TPCSR
3253 015136 100003 BPL XB1
3254 015140 112777 164044 MOV #207,0TPDDBR
3255 015146 032737 020000 177570 XB1: BIT #SW13,SWR
3256 015154 001075 BNE HALTS
3257 015156 021637 001244 CMP (SP),LSTERR
3258 015162 001404 BEQ 18
    
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3259 015164 011637 001244 MOV (SP),LSTERR
3260 015170 105037 001302 CLRB ERRFLG
3261 015174 104402 18: SAV05
3262 015176 011605 MOV (SP),R5
3263 015200 162705 000002 SUB #1,R5
3264 015204 011504 MOV (R5),R4
3265 015206 006304 ASL R4
3266 015210 061504 ADD (R5),R4
3267 015212 006304 ASL R4
3268 015214 042704 177001 BIC #177001,R4
3269 015220 062704 016064 ADD #,ERRTAB,R4
3270 015224 012437 015320 MOV (R4)+,ERRMSG
3271 015230 012437 015332 MOV (R4)+,DATAHD
3272 015234 011437 015344 MOV (R4),DATABP
3273 015240 105737 001302 TSTB ERRFLG
3274 015244 001403 BEQ TYPMSG
3275 015246 005737 015344 TST DATABP
3276 015252 001030 BNE TYPDAT
3277 015254 104401 TYPMSG: TYPE
3278 015256 016054 MERRRPC
3279 015260 104405 CNVRT
3280 015262 015416 ERTAB0
3281 015264 104401 TYPE
3282 015266 011036 VERS
3283 015270 105777 163714 TSTB 0TPCSR
3284 015274 100375 BPL ,=4
3285 015276 113777 014736 163706 MOV (R5),0TPDDBR
3286 015304 104401 TYPE
3287 015306 014437 MCRLF
3288 015310 112737 177777 001302 MOV #=-1,ERRFLG
3289 015316 104401 TYPE
3290 015320 000000 ERRMSG: 0
3291 015322 005737 015332 TST DATAHD
3292 015326 001402 BEQ TYPDAT
3293 015330 104401 TYPE
3294 015332 000000 DATAHD: 0
3295 015334 005737 015344 TYPDAT: TST DATABP
3296 015340 001402 BEQ RESREG
3297 015342 104404 CONVRT
3298 015344 000000 DATABP: 0
3299 015346 104403 RESREG: RES05
3300 015350 005737 177570 HALTS: TST SWR
3301 015354 100005 BPL EXITER
3302 015356 010046 PUSHR0
3303 015360 016600 000002 MOV 2(SP),R0
3304 015364 000000 HALT
3305 015366 012600 POPR0
3306 015370 005237 001242 EXITER: INC ERRCNT
3307 015374 032737 000400 177570 BIT #BIT0,SWR
3308 015402 001404 BEQ 18
3309 015404 012706 001200 MOV #STACK,SP
3310 015410 000177 177202 JMP 0PRG,NO
3311 015414 000002 18: RTI
3312 015416 000001 ERTAB0: 1
    
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3313	015420	006	002	CONVRT TYPE	MCRT	8(SP),TEMP2
3314	015422	001276		CONVRT,NOV	015419	TEMP,TEMP1
3315				ADD	015418	TEMP,TEMP1
3316				MOV	015417	TEMP,TEMP1
3317				MOV	015416	TEMP,TEMP1
3318	015424	104431		MOV	015415	TEMP,TEMP1
3319	015426	014437		MOV	015414	TEMP,TEMP1
3320	015430	017437		MOV	015413	TEMP,TEMP1
3321	015444	011317		ADD	015412	TEMP,TEMP1
3322	015444	062716		MOV	015411	TEMP,TEMP1
3323	015448	016046		MOV	015410	TEMP,TEMP1
3324	015448	016146		MOV	015409	TEMP,TEMP1
3325	015448	016246		MOV	015408	TEMP,TEMP1
3326	015446	016346		MOV	015407	TEMP,TEMP1
3327	015446	016446		MOV	015406	TEMP,TEMP1
3328	015462	016546		MOV	015405	TEMP,TEMP1
3329	015464	013701		MOV	015404	TEMP,TEMP1
3330	015470	012137		MOV	015403	TEMP,TEMP1
3331	015474	112137		MOV	015402	TEMP,TEMP1
3332	015480	112137		MOV	015401	TEMP,TEMP1
3333	015504	013137		MOV	015400	TEMP,TEMP1
3334	015510	015337		MOV	015399	TEMP,TEMP1
3335	015514	100007		BPL	015398	TEMP,TEMP1
3336	015516	042337		BIC	015397	TEMP,TEMP1
3337	015524	022337		MOV	015396	TEMP,TEMP1
3338	015524	022337		MOV	015395	TEMP,TEMP1
3339	015532	006002		BR	015394	TEMP,TEMP1
3340	015534	006037		BR	015393	TEMP,TEMP1
3341	015540	013704		MOV	015392	TEMP,TEMP1
3342	015544	113705		MOV	015391	TEMP,TEMP1
3343	015550	012700		MOV	015390	TEMP,TEMP1
3344	015554	016403		MOV	015389	TEMP,TEMP1
3345	015556	042703		BIC	015388	TEMP,TEMP1
3346	015562	062703		MOV	015387	TEMP,TEMP1
3347	015566	110320		MOV	015386	TEMP,TEMP1
3348	015570	000341		CLC	015385	TEMP,TEMP1
3349	015572	006004		CLC	015384	TEMP,TEMP1
3350	015574	000341		CLC	015383	TEMP,TEMP1
3351	015576	006004		ROR	015382	TEMP,TEMP1
3352	015602	006004		ROR	015381	TEMP,TEMP1
3353	015604	005305		DEC	015380	TEMP,TEMP1
3354	015606	001362		BNE	015379	TEMP,TEMP1
3355	015610	012703		MOV	015378	TEMP,TEMP1
3356	015614	114023		MOV	015377	TEMP,TEMP1
3357	015616	105374		DEC	015376	TEMP,TEMP1
3358	015622	001374		BNE	015375	TEMP,TEMP1
3359	015624	005737		TST	015374	TEMP,TEMP1
3360	015630	001403		BEQ	015373	TEMP,TEMP1
3361	015632	052737		BIS	015372	TEMP,TEMP1
3362	015640	105737		BEQ	015371	TEMP,TEMP1
3363	015644	001405		BEQ	015370	TEMP,TEMP1
3364	015646	112723		MOV	015369	TEMP,TEMP1
3365	015652	105373		MOV	015368	TEMP,TEMP1
3366	015656	001373		BNE	015367	TEMP,TEMP1

! CONVERT OCTAL NUMBER TO ASCII AND OUTPUT TO TELEPRINTER

3367	015660	105013		CLRB	(R3)
3368	015662	104401		TYPE	
3369	015664	016572		NDATA	
3370	015666	005337		DEC	WDRCNT
3371	015672	001300		BNE	18
3372	015674	013737		MOV	TEMP1,TEMP
3373	015702	012805		MOV	(BP)+,RS
3374	015704	012804		MOV	(BP)+,RS
3375	015706	012803		MOV	(BP)+,R4
3376	015710	012802		MOV	(BP)+,R3
3377	015712	012801		MOV	(BP)+,R2
3378	015714	012800		MOV	(BP)+,R1
3379	015716	000002		RTI	(BP)+,R0
3380	015720	000000			
3381	015722	000000			
3382	015724	000000			
3383	015726	000000			
3384	015728	000000			
3385					
3386	015730	016637			
3387					
3388					
3389					
3390					
3391					
3392	015736	010537		MOV	RS,SAVRS
3393	015742	010437		MOV	R4,SAVRS
3394	015746	010337		MOV	R3,SAVRS
3395	015752	010237		MOV	R2,SAVRS
3396	015756	010137		MOV	R1,SAVRS
3397	015762	010037		MOV	R0,SAVRS
3398	015766	000002		RTI	
3399					
3400					
3401					
3402	015770	013700		MOV	SAVR0,R0
3403	015774	013701		MOV	SAVR1,R0
3404	016000	013702		MOV	SAVR2,R0
3405	016004	013703		MOV	SAVR3,R0
3406	016008	013704		MOV	SAVR4,R0
3407	016014	013705		MOV	SAVR5,R0
3408	016020	000002		RTI	
3409					
3410					
3411					
3412					
3413	016024	011646			
3414	016024	162716			
3415	016030	017616			
3416	016030	006316			
3417	016034	006316			
3418	016036	042716			
3419	016042	062716			
3420	016046	017616			

! SAVE PC OF TEST THAT FAILED AND R0-R5
 ! SAVE R0-R5
 ! RESTORE R0-R5
 ! TRAP DISPATCH SERVICE
 ! ARGUMENT OF TRAP IS EXTRACTED
 ! AND USED AS OFFSET TO OBTAIN POINTER
 ! TO SELECTED SUBROUTINE
 ! GET PC OF RETURN
 ! GEN TRP
 ! MULTIPLY TRAP ARG BY 2
 ! CLEAR UNWANTED BITS
 ! POINTER TO SUBROUTINE ADDRESS
 ! SUBROUTINE ADDRESS


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3421 016052 000136          JMP      0(SP)+          ;GO TO SUBROUTINE
3422 016054 005015 041520 020072 MERRPC: .ASCII <15><12>/PC1 /
3423 016062          000
3424 016064          016064          .EVEN
3425 016064          000000          .ERRTAB:
3426 016064          000000          0
3427 016066          000000          0
3428 016070          000000          0
3429 016072          016122          EM0
3430 016074          016330          DM0
3431 016076          016466          DT0
3432 016100          016164          EM1
3433 016102          016415          DH1
3434 016104          016510          DT1
3435 016106          016223          EM2
3436 016110          016430          DH2
3437 016112          016516          DT2
3438 016114          016270          EM3
3439 016116          016415          DH1
3440 016120          016510          DT1
3441 016122 005015 047522 020115 EM0: .ASCII <15><12>/ROM READ DATA COMPARISON ERROR./
    016164 005015 051127 052111 EM1: .ASCII <15><12>/WRITTING ROM FAILED TO TRAP./
    016223          015 052412 042516 EM2: .ASCII <15><12>/UNEXPECTED TRAP WHILE READING ROM./
    016270 005015 040506 040524 EM3: .ASCII <15><12>/FATAL TRAP. ROM PC ON STACK./
    016330 005015 051440 043117 DH0: .ASCII <15><12>/ SOFT ROM/
    016350 005015 042101 051104          .ASCII <15><12>/ADDRESS ADDRESS EXPECTED FOUND /
    016415          015 040412 042104 DH1: .ASCII <15><12>/ADDRESS /
    016430 005015 041520 047440 DH2: .ASCII <15><12>/PC OF TRAP ROM ADDRESS /
    016466          016466          .EVEN
3442 016466 000004          DT0: 4
3443 016470          006          .BYTE 6,3
3444 016472 001270          SAVR4
3445 016474          206          .BYTE 206,3
3446 016476 001260          SAVR0
3447 016500          006          .BYTE 6,4
3448 016502 001262          SAVR1
3449 016504          006          .BYTE 6,2
3450 016506 001272          SAVR5
3451
3452 016510 000001          DT1: 1
3453 016512          206          .BYTE 206,2
3454 016514 001260          SAVR0
3455
3456 016516 000002          DT2: 2
3457 016520          006          .BYTE 6,0.
3458 016522 014656          XSTORE
3459 016524          206          .BYTE 206,2
3460 016526 001260          SAVR0
3461
3462 016530 000000          TEMP: 0
3463          016572          .,.,+40
3464 016572 000000          MDATA: 0
3465          016634          .,.,+40
3466 016634          CORMAX:

```


