

IDENTIFICATION

PRODUCT CODE: MAINDEC-11-DCKBA TO DCKBE-B-D

PRODUCT NAME: PDP11/45-11/40 BASIC CP TESTS

DATE CREATED: 15 NOV 1972

MAINTAINER: DIAGNOSTIC GROUP

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MAYNARD, MASS

THIS GROUP OF TESTS CONSIST OF:

MAINDEC NO.	TEST FUNCTION	PROCESSOR
DCKRA-A	SXT INSTRUCTION	11/45,11/40
DCKRB-A	SOP INSTRUCTION	11/45,11/40
DCKBC-A	XOR INSTRUCTION	11/45,11/40
DCKRD-B	MARK INSTRUCTION	11/45,11/40
DCKBE-B	RTT INSTRUCTION	11/45,11/40



1.0 ABSTRACT

THIS IS THE FIRST 5 OF 15 TESTS THAT INCREMENTALLY TEST AND ISOLATE SIMPLE MALFUNCTIONS IN THE PDP 11/45, 11/40. THE TESTS SHOULD BE RUN IN THE INDICATED ALPHABETIC SEQUENCE. THERE ARE ADDITIONAL TESTS FOR MORE COMPLEX MALFUNCTIONS. ALL TESTS ARE EXECUTED IN KERNEL MODE ONLY EXCEPT FOR TEST BCKBE (11/45 ONLY).

2.0 REQUIREMENTS

2.1 EQUIPMENT

PDP11/45 OR PDP 11/40

2.2 STORAGE

PROGRAM STORAGE - THE PROGRAMS USE ALL OF A 4KW MEMORY WITH THE EXCEPTION OF 17500 TO 17776 (WHICH IS RESERVED FOR THE BOOT AND ABSOLUTE LOADER).

2.3 PRELIMINARY PROGRAMS

TESTS TD-T13(D00AA=00MA)

3.0 LOADING PROCEDURE

LOAD PROGRAM INTO MEMORY USING ABS LOADER.

4.0 STARTING PROCEDURE

LOAD ADDRESS 200, PRESS START, THE PROGRAM WILL LOOP, AND RING BELL ON COMPLETION. PASS COUNT MAY BE MONITORED IN THE DISPLAY REGISTER (11/45 ONLY), AND IS STORED IN ADDRESS 1000.

5.0 OPERATING PROCEDURE

5.1 OPERATIONAL SWITCH SETTINGS

SW<08>=1        LOAD PDP11/45 MICRO BREAK REGISTER WITH VALUE  
IN SW<00-07>. (AT START OF TEST ONLY).

## 5.2 SUBROUTINE ABSTRACTS

## 5.2.1 SCOPE

SCOPE IS A MOVE PC, R1(012701) AND STORES THE PC+2 IN R1 (OR R11 IF THE REGISTER SET RIT IS SET IN THE PDP11/45 PROCESSOR STATUS WORD.) THUS, R1 (R11) MAY BE USED AS A TAG TO DETERMINE THE LAST TEST SUCCESSFULLY COMPLETED.

## 5.2.2 HLT

HLT IS A HALT INSTRUCTION AND IS EXECUTED WHENEVER A HARDWARE MALFUNCTION IS DETECTED BY THE DIAGNOSTIC. THE ADDRESS LIGHTS DISPLAY THE PC-PC+2 OF THE HALT INSTRUCTION (PDP11/45-11/45).

## 5.3 PROGRAM AND/OR OPERATOR ACTION

## 5.3.1 PASS COUNT (ICNT)

THE NUMBER OF PROGRAM PASSES COMPLETED IS CONTAINED IN ADDRESS ICNT (1020). THIS ADDRESS MAY BE EXAMINED TO DETERMINE IN WHICH PASS THE ERROR OCCURED. NOTE, THE PASS COUNT IS DISPLAYED IN THE DISPLAY REGISTER IN THE PDP11/45.

## 6.0 ERRORS

## 6.1 TEST ERRORS WILL CAUSE A HALT

FALSE TRAP/INTERRUPT ERRORS - THE PROGRAM WILL HALT AT THE TRAP VECTOR ADDRESS +2.

## 6.2 ERROR RECOVERY

TEST ERRORS - PRESS CONTINUE OR LOOP TEST (SEE SEC 6.3)

TRAP ERRORS - DETERMINE WHERE ERROR OCCURRED (SEE SEC 8)

## 6.3 ERROR LOOPING

TO LOOP ON AN ERROR REPLACE THE HLT INSTRUCTION WITH A BRANCH BACK TO THE PREVIOUS SCOPE INSTRUCTION. NOTE THAT IF THE ERROR IS INTERMITTENT THAT THE TEST WILL DROP THROUGH THE HLT AND PROCEED TO THE NEXT TEST. THEREFORE TO LOOP THE TEST CONTINUOUSLY REPLACE THE BEQ .+4 INSTRUCTION IMMEDIATELY

PRECEDING THE HLT WITH THE BRANCH BACK TO THE PREVIOUS SCOPE.

TO LOOP TRAP FAILURES PATCH IN THE FOLLOWING ROUTINE AT THE ADDRESS OF THE TRAP VECTOR:

```

TRAPVEC:      TRAPVEC+4
TRAPVEC+2:    0
TRAPVEC+4:    012716  ;MOVE 'SCOPE'
                ;ADDRESS TO STACK
TRAPVEC+6:    ADDRESS ;ADDRESS OF PREVIOUS
                ;SCOPE INST,
TRAPVEC+10:   000006  ;RETURN TO TEST AT 'SCOPE'

```

RESTORE ALL LOCATIONS BEFORE PROCEEDING TO NEXT TEST(S).

## 7.2 RESTRICTIONS

THESE PROGRAMS MUST BE LOADED IN THE LOWER 4K OF MEMORY.

### 7.1 STARTING RESTRICTION

ALL PROGRAMS MUST BE INITIALLY STARTED AT 200 AND MAY BE STARTED AT A SCOPE INSTRUCTION THEREAFTER.

### 7.2 OPERATIONAL RESTRICTION

NONE

## 8.2 MISCELLANEOUS

IF A HALT OCCURS IN THE TRAP OR INTERRUPT VECTOR AREA, EXAMINE REGISTER 6 (THE STACK POINTER). THE CONTENTS OF R6 CONTAINS THE ADDRESS WHERE THE PC OF THE INSTRUCTION THAT CAUSED THE TRAP IS STORED.

### 8.1 EXECUTION TIME

ALL TESTS TAKE APPROXIMATELY 1 MINUTE EACH ON AN 11/45 WITH CORE MEMORY.

### 8.2 STACK POINTER

ALL PROGRAMS INITIALLY SET THE STACK POINTER AT 500

## 9.0 PROGRAM DESCRIPTION

- DCKBA THIS IS A TEST OF THE SXT INSTRUCTION AND INSURES CORRECT RESULTS AND CONDITION CODE OPERATION, THE SXT INSTRUCTION IS TESTED IN ALL ADDRESS MODES IN A GENERAL REGISTER AND THE PC.
- DCKBR THIS IS A TEST OF THE SOB INSTRUCTION AND INSURES CORRECT BRANCHING AND CONDITION CODE OPERATION.
- DCKBC THIS IS A TEST OF THE XOR INSTRUCTION AND INSURES CORRECT RESULTS AND CONDITION CODE OPERATION, THE XOR INSTRUCTION IS EXECUTED USING VARIOUS OPERANDS AND IS EXECUTED USING ALL ADDRESS MODES USING A GENERAL REGISTER AND THE PC.
- DCKBR THIS IS A TEST OF THE MARK INSTRUCTION, THE TEST EXECUTES THE MARK INSTRUCTION USING ALL VALUES OF 'N' AND CHECKS RESULTS, CORRECT CONDITION CODE OPERATION IS ALSO TESTED.
- DCKBE THIS IS A TEST OF THE RTT AND RTI INSTRUCTIONS AND USES 'I' BIT TRAPS IN THE TEST, PROPER STACK OPERATION IS TESTED AND ALSO PROPER STATUS CHANGES ARE TESTED.

000242	000000	HALT
000244	000046	,+2
000246	000000	HALT
000250	000052	,+2
000252	000000	HALT
000254	000056	,+2
000256	000000	HALT
000260	000062	,+2
000262	000000	HALT
000264	000066	,+2
000266	000000	HALT
000270	000072	,+2
000272	000000	HALT
000274	000076	,+2
000276	000000	HALT
000280	000102	,+2
000282	000000	HALT
000284	000106	,+2
000286	000000	HALT
000290	000112	,+2
000292	000000	HALT
000294	000116	,+2
000296	000000	HALT
000300	000122	,+2
000302	000000	HALT
000304	000126	,+2
000306	000000	HALT
000310	000132	,+2
000312	000000	HALT
000314	000136	,+2
000316	000000	HALT
000320	000142	,+2
000322	000000	HALT
000324	000146	,+2
000326	000000	HALT
000330	000152	,+2
000332	000000	HALT
000334	000156	,+2
000336	000000	HALT
000340	000162	,+2
000342	000000	HALT
000344	000166	,+2
000346	000000	HALT
000350	000172	,+2
000352	000000	HALT
000354	000176	,+2
000356	000000	HALT
000360	000202	,+2
000362	000000	HALT
000364	000206	,+2
000366	000000	HALT
000370	000212	,+2
000372	000000	HALT
000374	000216	,+2

000216	000000	HALT
000220	002222	,+2
000222	000000	HALT
000224	000226	,+2
000226	000000	HALT
000230	000232	,+2
000232	000000	HALT
000234	000236	,+2
000236	000000	HALT
000240	000242	,+2
000242	000000	HALT
000244	000246	,+2
000246	000000	HALT
000250	000252	,+2
000252	000000	HALT
000254	000256	,+2
000256	000000	HALT
000260	000262	,+2
000262	000000	HALT
000264	000266	,+2
000266	000000	HALT
000270	000272	,+2
000272	000000	HALT
000274	000276	,+2
000276	000000	HALT
000280	000302	,+2
000282	000000	HALT
000284	000306	,+2
000286	000000	HALT
000290	000312	,+2
000292	000000	HALT
000294	000316	,+2
000296	000000	HALT
000300	000322	,+2
000302	000000	HALT
000304	000326	,+2
000306	000000	HALT
000310	000332	,+2
000312	000000	HALT
000314	000336	,+2
000316	000000	HALT
000320	000342	,+2
000322	000000	HALT
000324	000346	,+2
000326	000000	HALT
000330	000352	,+2
000332	000000	HALT
000334	000356	,+2
000336	000000	HALT
000340	000362	,+2
000342	000000	HALT
000344	000366	,+2
000346	000000	HALT
000350	000372	,+2
000352	000000	HALT
000354	000376	,+2
000356	000000	HALT

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001372 000000          HALT
001374 000376          .+2
001376 000000          HALT

000000          .#0
000000 000167 001034          JMP      SXT07

000000          DEST=#0
000200          .#200
000200 000167 000376          JMP      START
001000          .#1000
001000 000000          ICNT: 0          IPASS COUNT
001002 005067 177772          START: CLR          ICNT          ICLEAR PASS COUNT
001006 012706 000500          BEGIN: MOV          #STKPTR,%6    ISFT STACK POINTER
001012 016737 177762 177570          MOV          ICNT,%#DISPLAY    IDISPLAY PASS COUNT
001020 032737 000400 177570          BIT          #400,%#SWR        ILOAD MICRO BREAK REGISTER
001026 001473          RLO          .+10             IBRANCH IF NOT
001030 113737 177570 177770          MOVB        ##SWR,%#UBREAK    ILOAD MICRO BREAK WITH SR0=7

ITEST THAT WHEN N IS CLEAR SXT CLEARS THE DESTINATION
001036 010701          SCOPE
001040 012700 177777          MOV          #=-1,DEST
001044 000277          SCC          .          ISFT ALL CONDITION CODES
001046 000250          CLN          .          ICLEAR N
001050 006700          SXT          DEST          IEXTEND N BIT (0) INTO DESTINATION
001052 005700          TST          DEST          IDESTINATION=0?
001054 001401          RLO          .+4
001056 000000          HLT          IDESTINATION OTHER THAN 0

ITEST THAT WHEN N IS SET THAT SXT SETS ALL BITS IN THE DESTINATION
001060 010701          SCOPE
001062 005070          CLR          DEST
001064 000257          CCC          .          ICLEAR ALL CONDITON CODES
001066 000270          SEN          .          ISET N
001070 006700          SXT          DEST          IEXTEND N BIT (1) INTO DESTINATION
001072 000277 177777          CMP          DEST,%#-1        IDID ALL BITS SET IN DESTINATION
001076 001401          RLO          .+4
001100 000000          HLT          IDESTINATION OTHER THAN 177777

ITEST THAT N IS CLEAR AFTER SXT EXTENDS 0/5 INTO THE DESTINATION
001102 010701          SCOPE
001104 012700 177777          MOV          #=-1,DEST
001108 000250          CLN          .
001112 006700          SXT          DEST
001114 100001          RPL          .+4
001116 000000          HLT

ITEST THAT N IS SET AFTER SXT EXTENDS 1/5 INTO THE DESTINATION
001120 010701          SCOPE
001122 005000          CLR          DEST
001124 000270          SEN          .
001126 006700          SXT          DEST
001130 100401          BMI          .+4
    
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001132 000000          HALT

ITEST THAT SIGN EXTENDS PROPERLY AFTER A MOVE INSTRUCTION (N SET)
001134 010701          SCOPE
001136 005070          CLR          DEST
001140 012747 177777 177774          MOV          #=-1,%2
001144 006700          SXT          DEST
001148 000277 177777          CMP          DEST,%#-1
001152 001401          RLO          .+4
001156 000000          HLT
    
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001160 010701          TEST THAT SIGN EXTENDS PROPERLY AFTER A MOVE INSTRUCTION (N CLEAR)
001162 012700 177777          SCOPE
001166 012767 000000 177774          MOV     #=1,DEST
001174 006700          MOV     #0,+2
001176 020027 000000          SXT   DEST
001202 001401          CMP    DEST,#0
001204 000000          BEQ   ,+4
          HLT

001206 005000          TEST THAT SIGN EXTENDS PROPERLY AFTER VARIOUS BYTE INSTRUCTIONS
001210 005007 000226          (NOTE) THESE TESTS MUST BE RUN SEQUENTIALLY OR TEMP & TEMP+2 MUST
001214 105167 000223          BE MANUALLY LOADED BEFORE STARTING THE TEST, THE COMMENT AT THE BEG-
001220 006700          INNING OF THE TEST SHOWS CONTENTS OF TEMP & TEMP+2.
001222 022700 177777          ((TEMP+1),(TEMP)/(TEMP+3),(TEMP+2)
001226 001401          CLR   DEST      17,7/7,7
001230 000000          CLR   TEMP     10,0/7,7
001232 010701          COMB  TEMP+1   1=1,0/7,7
001234 005000          SXT   DEST
001236 105367 000200          CMP   #=1,DEST
001242 006700 177777          BEQ   ,+4          (ERROR) SIGN EXTEND FAILED (N=1)
001244 022700          HLT
001246 001401          SCOPE
001250 000000          CLR   DEST      1=1,0/7,7
001252 000000          DECB  TEMP     1=1,-1/7,7
001254 010701          SXT   DEST
001256 005000          CMP   #=1,DEST
001260 006700 177777          BEQ   ,+4          (ERROR) SIGN EXTEND FAILED (N=1)
001262 022700          HLT
001264 001401          SCOPE
001266 005000          CLR   DEST      1=1,-1/7,1
001268 156767 000157 000150          BJSB  TEMP+1,TEMP+2  1=1,-1/7,0.1
001270 006700 177777          SXT   DEST
001272 022700          CMP   #=1,DEST
001274 001401          BEQ   ,+4
001276 000000          HLT          (ERROR) SIGN EXTEND FAILED (N=1)
001278 010701          SCOPE
001280 005000          CLR   DEST      1=1,-1/7,0.1
001282 116767 000134 000133          MOVB  TEMP+2,TEMP+3  1=1,-1/-1,0.1
001284 006700          SXT   DEST
001286 022700 177777          CMP   #=1,DEST
001288 001401          BEQ   ,+4
001290 000000          HLT          (ERROR) SIGN EXTEND FAILED (N=1)
001292 010701          SCOPE
001294 005000          MOV   #=1,DEST  1=1,-1/7,0.1
001296 146767 000105 000100          BICB  TEMP+1,TEMP+3  1=1,-1/0,0.1
001298 006700          SXT   DEST
001300 005700          TST  DEST
001302 001401          BEQ   ,+4
001304 000000          HLT          (ERROR) SIGN EXTEND FAILED (N=0)
001306 010701          SCOPE
    
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001308 010701          SCOPE
001312 012700 177777          MOV   #=1,DEST  1=1,-1/0,0.1
001316 000241 000000          SLC   15FT CARRY
001320 105567 000097          AUCB  TEMP+1   10,-1/2,-1
001324 006700          SXT   DEST
001326 005700          TST  DEST
001330 001401          BEQ   ,+4
001332 000000          HLT          (ERROR) SIGN EXTEND FAILED (N=0)
001334 010701          SCOPE
001336 012700 177777          MOV   #=1,DEST  10,-1/0,-1
001340 105267 000034          INCB  TEMP     10,0/0,-1
001342 006700          SXT   DEST
001344 005700          TST  DEST
001346 001401          BEQ   ,+4
001348 000000          HLT          (ERROR) SIGN EXTEND FAILED (N=0)
001350 010701          SCOPE
001352 012700 177777          MOV   #=1,DEST  10,0/0,-1
001354 105167 000014          COMB  TEMP+2   10,0/0,0
001356 006700          SXT   DEST
001358 005700          TST  DEST
001360 001401          BEQ   ,+4
001362 000000          HLT          (ERROR) SIGN EXTEN FAILED (N=0)
001364 010701          SCOPE
001366 000000          TEMP+0
001368 001446          ,+2

001446 010701          TEST THAT 2 BIT IS SET AFTER SXT EXTENDS 0'S
001450 012700 177777          (2) SCOPE
001454 000240          MOV   #=1,DEST
001456 006700          CLN
001458 001401          SXT   DEST      (CLEAR N
001460 000000          BEQ   ,+4          (EXTEND N BIT (0) INTO DESTINATION
          HLT          (SPACE IF 2 IS SET
          (2 NOT SET AFTER 0'S WERE EXTENDED)

001464 012701          TEST THAT 2 BIT IS CLEAR AFTER SXT EXTENDS 1'S
001466 005000          SCOPE
001470 000270          CLR   DEST
001472 006700          SEN
001474 001071          SXT   DEST
001476 000000          BNE  ,+4          HLT

001500 010701          TEST THAT THE CARRY BIT (P) IS UNCHANGED BY SXT (C=0,N=0)
001502 000277          SCOPE
001504 006700          CLC
001506 101071          SXT   DEST      (CLEAR ALL CONDITION CODES
          BEQ   ,+4          (EXTEND SIGN
          HLT          (UNLESS IF CARRY IS CLEAR
          (UNLESS CARRY SET)

001510 000000          TEST THAT THE CARRY BIT IS UNCHANGED BY SXT (C=0,N=1)
    
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001512 010701          SCOPE
001514 000277          CCC
001516 000270          SEN
001518 004700          SXT DEST          IC=0,N=1
001520 103401          BCS          EXTEND SIGN
001522 000000          HLT          BRANCH IF CARRY IS CLEAR
001524 000000          HLT          ERROR! CARRY SET

ITEST THAT THE CARRY BIT IS UNCHANGED BY SXT (C=1,N=0)
001526 010701          SCOPE
001530 000257          CCC
001532 000261          SEC
001534 006700          SXT DEST          IC=1,N=0
001536 103401          BCS          EXTEND SIGN
001540 000000          HLT          BRANCH IF CARRY IS SET
001542 000000          HLT          ERROR! CARRY CLEARED

ITEST THAT CARRY IS UNCHANGED BY SXT (C=1,N=1)
001542 010701          SCOPE
001544 000277          SCC
001546 006700          SXT DEST          IC=1,N=1
001550 103401          BCS          EXTEND SIGN
001552 000000          HLT          BRANCH IF CARRY SET
001554 000000          HLT          ERROR! CARRY CLEARED

ITEST THAT THE V BIT IS CLEARED BY SXT (V=0,N=0)
001554 010701          SCOPE
001556 000257          CCC
001560 006700          SXT DEST          IV=0,N=0
001562 102001          BVC          EXTEND SIGN
001564 000000          HLT          BRANCH IF V IS CLEAR
001566 000000          HLT          ERROR! V SET

ITEST THAT V IS CLEARED BY SXT (V=0,N=1)
001566 010701          SCOPE
001570 000257          CCC
001572 000270          SEN
001574 004700          SXT DEST          IV=0,N=1
001576 102001          BVC          EXTEND SIGN
001580 000000          HLT          BRANCH IF V IS CLEAR
001582 000000          HLT          ERROR! V SET

ITEST THAT V IS CLEARED BY SXT (V=1,N=0)
001602 010701          SCOPE
001604 000277          SCC
001606 003250          CLN
001610 006700          SXT DEST          IC=1,N=0
001612 102001          BVC          EXTEND SIGN
001614 000000          HLT          BRANCH IF V IS CLEAR
001616 000000          HLT          ERROR! V SET
    
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ITEST THAT V IS CLEARED BY SXT (C=1,N=1)
001616 010701          SCOPE
001620 000277          SCC
001622 006700          SXT DEST          IV=1,N=1
001624 102001          BVC          EXTEND SIGN
001626 000000          HLT          BRANCH IF V IS CLEAR
001628 000000          HLT          ERROR! V REMAINED SET

DEST=07
ITEST THAT SIGN EXTENDS INTO R7 (N=0)
001630 010701          SCOPE
001632 000257          CCC
001634 006700          SXT DEST          IN=0
001636 000000          HLT          EXTEND 0/S INTO THE PC
001640 000402          SXT07I          MODE67          ERROR! PC SHOULD'VE GONE TO 0

001642          DEST=,
001644          ,*+2
001646          #
ITEST DESTINATION MODE 67
001646 010701          SCOPE
001650 005067          CLR          DEST
001652 000270          SEN          ISFT N
001654 006700          SXT DEST          EXTEND 1/S INTO DEST
001656 004767          SXT DEST          IDID 1/S EXTEND
001660 026727          CMP          DEST,*-1
001662 177754          BEQ          ,*4
001664 177777          HLT          1/S FAILED TO EXTEND

ITEST DESTINATION MODE 27
001674 010701          SCOPE
001676 005067          CLR          MODE27
001678 000270          SEN          ISFT N
001680 006727          SXT          (7)+          EXTEND 1/S INTO NEXT LOCATION
001682 000000          HLT
001684 026727          CMP          MODE27I          #
001686 177772          BEQ          MODE27,*-1
001688 177777          HLT          ,*4
001690 000000          HLT          IDID 1/S EXTEND

ITEST DESTINATION MODE 37
001702 010701          SCOPE
001704 012757          MOV          #=1,#DEST
001706 000250          CLN          ICLEAR N
001708 006737          SXT          #DEST          EXTEND 0/S
001710 023727          CMP          #DEST,#0          IDID 0/S EXTEND
001712 001401          BEQ          ,*4
001714 000000          HLT

ITEST DESTINATION MODE 77
001754 010701          SCOPE
001756 012767          MOV          #DEST,DEST
001758 012777          MOV          #=1,#DEST
001760 000250          CLN
001762 006777          SXT          #DEST
001764 027727          CMP          #DEST,#0
001766 177640          BEQ          ,*4
001768 001401          HLT
    
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002026 000000          HLT
                                ITEST DESTINATION MODE 1
                                SCOPE
002010 010701          MOV      #MODE1,X0
002012 012700 002320  CLR      X2
002016 005002          SEC
002020 000261          SBC      X2
002022 005602          SXT      (0)
002024 006710          CMP      #=1,MODE1
002026 022767 177777 000264 BEQ      ,+4
002030 001401          HLT
                                IERROR: INCORRECT RESULT

002030 010701          HLT
                                ITEST DESTINATION MODE2
                                SCOPE
002032 005004          CLR      =(6)
002034 012700 002056  MOV      #MODE2,-(6)
002036 012702 002320  MOV      #MODE1,X2
002038 000002          RTI
002040 006722          MODE21 SXT      (2)+
002042 005767 000234  TST      MODE1
002044 001401          BEQ      ,+4
002046 000000          HLT
                                IERROR: INCORRECT RESULT
002048 022702 002322  ICHECK AUTO-INCREMENT
002050 001401          BEQ      ,+4
002052 000000          HLT
                                IERROR: AUTO INCREMENT FAILED

002100 010701          HLT
                                ITEST DESTINATION MODE 3
                                SCOPE
002102 012767 002322 000210 MOV      #MODE3,MODE1
002110 012702 002320  MOV      #MODE1,X2
002114 000270          SEN
002116 006732          SXT      0(2)+
002120 022767 177777 000174 CMP      #=1,MODE3
002122 001401          BEQ      ,+4
002130 000000          HLT
                                IERROR: INCORRECT RESULT

002132 010701          HLT
                                ITEST DESTINATION MODE 4
                                SCOPE
002134 012703 002322  MOV      #MODE3,X3
002140 012767 177777 000152 MOV      #=1,MODE1
002146 005007 000150  CLR      MODE3
002152 006743          SXT      -(3)
002154 005767 000140  TST      MODE1
002156 001401          BEQ      ,+4
002158 000000          HLT
                                IERROR: INCORRECT RESULT
002160 022703 002320  ICHECK AUTO-DECREMENT
002162 001401          CMP      #MODE3-2,X3
002164 000000          BEQ      ,+4
002172 000000          HLT
                                IERROR: AUTO-DECREMENT FAILED

002174 010701          HLT
                                ITEST DESTINATION MODE 5
                                SCOPE
002176 012767 002320 000110 MOV      #MODE1,MODE3
002184 012704 002324  MOV      #MODE3+2,X4
    
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002210 000270          SEN
002212 006754          SXT      0=(4)
002214 022767 177777 000070 CMP      #=1,MODE1
002222 001401          BEQ      ,+4
002224 000000          HLT
                                IERROR: INCORRECT RESULT
002226 022704 002322  ICHECK AUTO-DECREMENT
002232 001401          CMP      #MODE3,X4
002234 000000          BEQ      ,+4
                                HLT
                                IERROR: AUTO-DECREMENT DEFERRED FAILED

002236 010701          HLT
                                ITEST DESTINATION MODE 6
                                SCOPE
002240 012705 002322  MOV      #MODE3,X5
002244 006705 177776  SXT      -2(5)
002246 005767 000044  TST      MODE1
002254 001401          BEQ      ,+4
002256 000000          HLT
                                IERROR: INCORRECT RESULT

002260 010701          HLT
                                ITEST DESTINATION MODE 7
                                SCOPE
002262 012767 177777 000030 MOV      #=1,MODE1
002270 012704 177770  MOV      #=10,X4
002274 012767 002320 000020 MOV      #MODE1,MODE3
002282 006774 002332  SXT      #MODE3+10(4)
002286 005767 000006  TST      MODE1
002292 001401          BEQ      ,+4
002294 000000          HLT
                                IERROR: INCORRECT RESULT
002296 000472          BR      ,+6
                                IGO TO NEXT TEST
002300 000000          MODE1: 0
002302 000000          MODE3: 0

                                ITEST COMBINATION CODES WHEN THE DESTINATION MODE IS NOT 0
                                ITEST: V=1,V=0,C=0
                                SCOPE
002304 010701          MOV      #=1,MODE1
002306 012767 177777 000004 CLC
002310 006767          SXT      MODE1
002312 012700          MOV      PSW,X0
002314 022700 000000  CMP      #4,X0
002316 001401          BEQ      ,+4
002324 000000          HLT
                                IERROR: INCORRECT CONDITION CODES

002326 010701          ITEST: V=0,V=0,C=1
                                SCOPE
002328 012767 177777 177732 MOV      #=1,MODE1
002330 012702 002320  MOV      #MODE1,X2
002332 012767 000003 175376 MOV      #3,PSW
002334 006712          SXT      (2)
002336 016700 175370  MOV      PSW,X0
002338 022700 000005  CMP      #5,X0
002340 001401          BEQ      ,+4
002342 000000          HLT
                                IERROR: INCORRECT CONDITION CODES

002410 010701          ITEST: V=0,V=0,C=0
                                SCOPE
    
```

```

002420 012702 002520      MOV      #MODE1,X2
002424 0050A7 177670      CLR      MODL1
002430 012767 000210 175340  MOV      #10,PSW
002436 006722            SXT      (2)+
002440 016770 175332      MOV      PSW,X0
002444 022700 000010      CMP      #10,X0
002450 001401            BEQ      ,+4
002452 000000            HLT

```

!ERROR! INCORRECT CONDITION CODES

IN=1,Z=0,V=0,C=1

```

002454 010701            SCOPE
002456 012772 002322      MOV      #MODE1+2,X2
002462 005067 177632      CLR      MODE1
002466 0127A7 000017 175302  MOV      #17,PSW
002474 006742            EXT      -(2)
002476 016770 175274      MOV      PSW,X0
002482 022700 000011      CMP      #11,X0
002490 001401            BEQ      ,+4
002492 000000            HLT

```

!ERROR! INCORRECT CONDITION CODES

!TEST THAT SXT EXTENDS B'S INTO THE PSW

```

002512 010701            SCOPE
002514 012767 000357 175254  MOV      #357,PSW
002522 000250            CLN
002524 006767 175246      SXT      PSW
002530 016770 175242      MOV      PSW,X0
002534 001401            BEQ      ,+4
002536 000000            HLT

```

!GET & TEST PSW CONTENTS

```

002540 005267 176234            INC      ICNT
002544 026727 176230 177777  CMP      ICNT,#-1
002550 001402            BEQ      DONE
002554 000167 176226      JMP      BEGIN
002560 0127A7 000007 175020  MOV      #7,TPBUF
002566 005767 174772      TSTB   TPC5H
002572 000370            HPL      ,+4
002574 013772 000042      MOV      #42,X2
002600 001424            BEQ      DONE1
002602 004712            JSR      7,(2)
002604 000240            NOP
002606 000240            NOP
002610 000240            NOP
002612 000167 176164  DONE11  JMP      START

```

!INCREMENT PASS COUNT  
 !GO TO DONE IF 120, PASSFS COMPLETED  
 !RING BELL  
 !WAIT FOR THE  
 !FILL TO RING  
 !GET DICTAPE MONITOR ADDRESS  
 !DO NOT RETURN IF (42)=0  
 !RETURN TO DICTAPE MONITOR  
 !ACT11  
 !OVERLAY  
 !AREA  
 !AND REPEAT TEST

000001 ,END

BEGIN	001000	DEST	001440	DIFPLA	177570	DONE	002560
DONE1	002010	HLT	000000	ICNT	001000	DEFBY	001444
MODE1	002320	MODE2	002000	MODE3	001700	MODE4	002320
MODEA7	001640	PSW	177770	SCOPE	010701	START	001000
STKPTH	000000	SWR	177570	SXT07	001440	TEMP	001442
TPBUF	177060	TPCSR	177564	TX	001440	UBREAK	177770
	002610						

ERRORS DETECTED: 0

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TITLE MAINDEC-11-DCKRB-A PDP11/25-11/45 SOB INST TEST  
NLIST MC,MD,SEQ  
LIST ME  
ARS  
TEST DCKRBA- TEST OF THE SOB INSTRUCTION;  
THE SOB INSTRUCTION SUBTRACTS ONE (1) FROM THE REGISTER SPECIFIED  
AND IF THE RESULT IS NOT ZERO (0) THEN A BRANCH IS TAKEN TO THE ADDRESS  
(OR NUMBER OF WORDS) SPECIFIED. IF THE RESULT IS 0 THEN THE NEXT SEQUENTIAL  
INSTRUCTION IS EXECUTED.  
STARTING PROCEDURE  
LOAD ADDRESS=200  
PRESS START  
STACK POINTER IS SET AT 430  
HELL WILL RING WHEN TEST IS COMPLETE

000000	000000	,#0
000002	000002	,+2
000004	000004	HALT
000006	000006	,+2
000008	000008	HALT
000010	000010	,+2
000012	000012	HALT
000014	000014	,+2
000016	000016	HALT
000018	000018	,+2
000020	000020	HALT
000022	000022	,+2
000024	000024	HALT
000026	000026	,+2
000028	000028	HALT
000030	000030	,+2
000032	000032	HALT
000034	000034	,+2
000036	000036	HALT
000038	000038	,+2
000040	000040	HALT
000042	000042	,+2
000044	000044	HALT
000046	000046	,+2
000048	000048	HALT
000050	000050	,+2
000052	000052	HALT
000054	000054	,+2
000056	000056	HALT
000058	000058	,+2
000060	000060	HALT
000062	000062	,+2
000064	000064	HALT
000066	000066	,+2
000068	000068	HALT
000070	000070	,+2
000072	000072	HALT
000074	000074	,+2
000076	000076	HALT
000078	000078	,+2
000080	000080	HALT
000082	000082	,+2
000084	000084	HALT
000086	000086	,+2
000088	000088	HALT
000090	000090	,+2
000092	000092	HALT
000094	000094	,+2
000096	000096	HALT
000098	000098	,+2
000100	000100	HALT
000102	000102	,+2
000104	000104	HALT
000106	000106	,+2
000108	000108	HALT
000110	000110	,+2

000112	000000	HALT
000114	000116	,+2
000116	000000	HALT
000120	000122	,+2
000122	000000	HALT
000124	000126	,+2
000126	000000	HALT
000130	000132	,+2
000132	000000	HALT
000134	000136	,+2
000136	000000	HALT
000140	000142	,+2
000142	000000	HALT
000144	000146	,+2
000146	000000	HALT
000150	000152	,+2
000152	000000	HALT
000154	000156	,+2
000156	000000	HALT
000160	000162	,+2
000162	000000	HALT
000164	000166	,+2
000166	000000	HALT
000170	000172	,+2
000172	000000	HALT
000174	000176	,+2
000176	000000	HALT
000180	000182	,+2
000182	000000	HALT
000184	000186	,+2
000186	000000	HALT
000190	000192	,+2
000192	000000	HALT
000194	000196	,+2
000196	000000	HALT
000198	000200	,+2
000200	000000	HALT
000202	000204	,+2
000204	000000	HALT
000206	000208	,+2
000208	000000	HALT
000210	000212	,+2
000212	000000	HALT
000214	000216	,+2
000216	000000	HALT
000220	000222	,+2
000222	000000	HALT
000224	000226	,+2
000226	000000	HALT
000230	000232	,+2
000232	000000	HALT
000234	000236	,+2
000236	000000	HALT
000240	000242	,+2
000242	000000	HALT
000244	000246	,+2
000246	000000	HALT
000250	000252	,+2
000252	000000	HALT
000254	000256	,+2
000256	000000	HALT
000260	000262	,+2
000262	000000	HALT
000264	000266	,+2

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```

000266 000000 HALT
000270 000272 ,+2
000272 000000 HALT
000274 000276 ,+2
000276 000000 HALT
000300 000302 ,+2
000302 000000 HALT
000304 000306 ,+2
000306 000000 HALT
000310 000312 ,+2
000312 000000 HALT
000314 000316 ,+2
000316 000000 HALT
000320 000322 ,+2
000322 000000 HALT
000324 000326 ,+2
000326 000000 HALT
000330 000332 ,+2
000332 000000 HALT
000334 000336 ,+2
000336 000000 HALT
000340 000342 ,+2
000342 000000 HALT
000344 000346 ,+2
000346 000000 HALT
000350 000352 ,+2
000352 000000 HALT
000354 000356 ,+2
000356 000000 HALT
000360 000362 ,+2
000362 000000 HALT
000364 000366 ,+2
000366 000000 HALT
000370 000372 ,+2
000372 000000 HALT
000374 000376 ,+2
000376 000000 HALT
    
```

```

010701          SCOPE=010701          JMOV PC,R1
000000          HLT=HALT
177776          PSW=177776          JADDRESS OF PROCESSOR STATUS WORD
177770          UBHEAK=177770        JADDRESS OF MICRO BREAK REGISTER
177564          TPCSW=177564        JADDRESS OF TELEPRINTER CSR
177566          TPCSW=177566        JADDRESS OF TELEPRINTER BUFFER
177570          TSW=177570         JADDRESS OF CONSOLE SWITCH REGISTER
177570          DISPLAY=177570     JADDRESS OF CONSOLE DISPLAY REGISTER
*****INITIAL STACK POINTER*****
STKPTR=0000          INITIAL STACK SETTING
          ,00
          ,200
000200 000167 000576          JMP          START
    
```

```

001000 000000          ,+1000
001002 000000          ICNTI          R          ICNTAINS PASS COUNT
001004 000000          STARTI          CLR          ICNT          INITIALI27 PASS COUNT
001006 000000          R0,INI          MOV          #STKPTR,X0          ISET STACK POINTER
001010 000000          177570          MOV          ICNT,#DISPLAY          IDISPLAY PASS COUNT
001012 000000          000402 177570          BIT          #400,#SWR          ILOAD MICRO BREAK REGISTER?
001014 000000          001403          BLO          +10          ILOAD MICRO BREAK REG WITH SP0=7
001016 113737 177570 177770          MOVB          #SWR,#UBBREAK

I TEST THAT SOB DOES NOT BRANCH WHEN THE SPECIFIED REGISTER (R0) DECREMENTS TO 0.
001036 010701          SCOPE
001040 012700 000001          T0A:          MOV          #1,X0          ILOAD R0=1
001044 000401          RM          T0B          IGO TO SOB INSTRUCTION
001046 000402          BR          T0C
001050 000000          T0B:          SOB          X0,-2          ISOB SHOULD NOT BRANCH
001052 000401          BR          T1A          IGO TO NEXT TEST
001054 000000          T0C:          HLT          IERROR SOB BRANCHED

I TEST THAT SOB BRANCHES WHEN R0 BECOMES NEGATIVE.
001056 010701          SCOPE
001060 000000          CLR          X0
001062 000405          RM          T1B          IGO TO SOB INSTRUCTION
001064 020077 177777          T1A:          CMP          X0,#-1          IAS R0 DECREMENTED?
001066 001404          BLO          T2A          IERROR SOB FAILED TO DECREMENT R0
001070 000000          HLT
001072 000402          BR          T2A
001074 000000          T2A:          SOB          X0,T1A          ISOB SHOULD BRANCH
001076 000000          HLT          IERROR SOB FAILED TO BRANCH

I TEST THAT SOB BRANCHES WHEN R0 DOES NOT DECREMENT TO 0
001102 010701          SCOPE
001104 012700 000002          MOV          #2,X0          IRR=2
001106 000401          RM          T2B          IGO TO SOB INSTRUCTION
001108 000402          RM          T3A          IGO TO NEXT TEST
001110 000000          SOB          X0,-2          ISOB SHOULD BRANCH
001112 000000          HLT          IERROR SOB FAILED TO BRANCH

I TEST THAT SOB DECREMENTS R0 TO 0 PROPERLY.
001120 010701          SCOPE
001122 012700 000001          T3A:          MOV          #1,X0          ILOAD R0 = 1
001124 000401          SOB          X0,T3A          ISOB SHOULD NOT BRANCH
001126 000402          CMP          X0,#0          IGO SOB DECREMENT R0 TO 0?
001128 020077 000000          REQ          T4A
001130 000000          HLT          ISOB DID NOT DECREMENT R0 TO 0

I TEST THAT SOB BRANCHES WHEN R0 IS DECREMENTED AND BECOMES POSITIVE.
001140 010701          SCOPE
001142 012700 100000          MOV          #100000,X0          ILOAD R0 SUCH THAT WHEN DECREMENTED
001144 000401          RM          T4B          IT BECOMES POSITIVE (277777)
001146 000402          RM          T0A          IGO TO SOB INSTRUCTION
001148 000000          SOB          X0,-2          IGO TO NEXT TEST
001150 000000          HLT          ISOB SHOULD BRANCH
001152 000000          HLT          IERROR SOB FAILED TO BRANCH
    
```

TEST THAT SOB DECREMENTS R2 PROPERLY WHEN R2=102000  
 T5A1 SCOPE  
 MOV #100000,X0 ILOAD PC=102000  
 BR T5B IGT DO SOB INSTRUCTION  
 T5AA1 CMP #0,X0 DID SOB DECREMENT R2?  
 BLS T6A IGT TO NEXT TEST  
 HLT IERRR SOB DID NOT DECREMENT R2  
 BR T6A IGT TO NEXT TEST  
 T5B1 SOB #0,T5AA ISOB SHOULD BRANCH  
 HLT SOB FAILED TO BRANCH

TEST THAT SOB INSTRUCTION DECREMENTS R2 PROPERLY  
 T6A1 SCOPE  
 MOV #77777,X0 ILOAD R2 & R2 WITH THE  
 MOV #0,X2 LARGEST POSITIVE NUMBER  
 BR T6C IGT DO SOB INSTRUCTION  
 T6B1 DEC X2 DECREMENT R2  
 CMP #0,X2 IRT=P2?  
 BEQ T6C IGT TO SOB INSTRUCTION  
 HLT IERRR SOB DID NOT DECREMENT PROPERLY  
 BR T7A IGT TO NEXT TEST  
 T6C1 SOB #0,T6B ISOB

TEST THAT SOB DOES NOT BRANCH WHEN R2 DECREMENTS TO 0 (R2 WILL  
 INITIALLY BE LOADED TO THE LARGEST POSITIVE NUMBER).  
 T7A1 SCOPE  
 MOV #77777,X0 ILOAD R2 & R2 WITH THE  
 MOV #0,X2 LARGEST POSITIVE NUMBER  
 DEC X2  
 T7B1 SOB #0,-2 IRT=P2=0?  
 ADD #0,X2  
 BEQ #,4 IERRR  
 HLT

C=N  
 N=N

TEST THAT SOB DOES NOT AFFECT THE CONDITION CODES WHEN SOB DOES NOT BRANCH

T10A1 SCOPE  
 MOV #1,X2  
 MOV #0,PSW ILOAD PSW  
 BR T10B IGT DO SOB INSTRUCTION  
 T10AA1 HLT IERRR SOB BRANCHED  
 BR T10D  
 T10B1 SOB #2,T10AA ISOB SHOULD NOT BRANCH  
 MOV PSW,X0 IGT STATUS CONTENTS  
 CMP #0,X0 ISTATUS = 0  
 BLS #,4  
 T10D1 HLT IERRR STATUS WORD CHANGED  
 NOP  
 C=C+1  
 N=N+1

T11A1 SCOPE  
 MOV #1,X2  
 MOV #1,PSW ILOAD PSW  
 BR T11B IGT DO SOB INSTRUCTION  
 T11AA1 HLT IERRR SOB BRANCHED  
 BR T11D  
 T11B1 SOB #2,T11AA ISOB SHOULD NOT BRANCH  
 MOV PSW,X0 IGT STATUS CONTENTS  
 CMP #1,X0 ISTATUS = 1  
 BLS #,4  
 T11D1 HLT IERRR STATUS WORD CHANGED  
 NOP  
 C=C+1  
 N=N+1

T12A1 SCOPE  
 MOV #1,X2  
 MOV #2,PSW ILOAD PSW  
 BR T12B IGT DO SOB INSTRUCTION  
 T12AA1 HLT IERRR SOB BRANCHED  
 BR T12D  
 T12B1 SOB #2,T12AA ISOB SHOULD NOT BRANCH  
 MOV PSW,X0 IGT STATUS CONTENTS  
 CMP #2,X0 ISTATUS = 2  
 BLS #,4  
 T12D1 HLT IERRR STATUS WORD CHANGED  
 NOP  
 C=C+1  
 N=N+1

T13A1 SCOPE  
 MOV #1,X2  
 MOV #3,PSW ILOAD PSW  
 BR T13B IGT DO SOB INSTRUCTION  
 T13AA1 HLT IERRR SOB BRANCHED  
 BR T13D  
 T13B1 SOB #2,T13AA ISOB SHOULD NOT BRANCH  
 MOV PSW,X0 IGT STATUS CONTENTS  
 CMP #3,X0 ISTATUS = 3  
 BLS #,4  
 T13D1 HLT IERRR STATUS WORD CHANGED  
 NOP  
 C=C+1  
 N=N+1

T14A1 SCOPE  
 MOV #1,X2  
 MOV #4,PSW ILOAD PSW  
 BR T14B IGT DO SOB INSTRUCTION  
 T14AA1 HLT IERRR SOB BRANCHED  
 BR T14D  
 T14B1 SOB #2,T14AA ISOB SHOULD NOT BRANCH  
 MOV PSW,X0 IGT STATUS CONTENTS  
 CMP #4,X0 ISTATUS = 4  
 BLS #,4

001520	001401		REQ	,+4	
001522	000070		HLT		IERRR STATUS WORD CHANGED
001524	000240	T1401	NOP		
	000015		C=C+1		
	000070		N=N+1		
001526	010701	T15A1	SCOPE		
001530	012702	000001	MOV	#1,X2	
001534	012767	000005 176234	MOV	#5,PSW	ILOAD PSW
001542	000402		BH	T15B	IGO DO SOB INSTRUCTION
001544	000000	T15AA1	HLT		IERRR SOB BRANCHED
001546	000407		BH	T15D	
001550	077203	T15B1	SOB	X2,T15AA	ISOB SHOULD NOT BRANCH
001552	016700	176220	MOV	PSW,X0	IGET STATUS CONTENTS
001556	022700	000005	CHP	#5,X2	ISTATUS = 5
001562	001401		REQ	,+4	
001564	000000		HLT		IERRR STATUS WORD CHANGED
001566	000240	T15D1	NOP		
	000010		C=C+1		
	000000		N=N+1		
001570	010701	T16A1	SCOPE		
001572	012702	000001	MOV	#1,X2	
001576	012767	000006 176172	MOV	#6,PSW	ILOAD PSW
001604	000402		BH	T16B	IGO DO SOB INSTRUCTION
001606	000000	T16AA1	HLT		IERRR SOB BRANCHED
001610	000407		BH	T16D	
001612	077203	T16B1	SOB	X2,T16AA	ISOB SHOULD NOT BRANCH
001614	016700	176156	MOV	PSW,X2	IGET STATUS CONTENTS
001620	022700	000006	CHP	#6,X2	ISTATUS = 6
001624	001401		REQ	,+4	
001626	000000		HLT		IERRR STATUS WORD CHANGED
001630	000240	T16D1	NOP		
	000017		C=C+1		
	000007		N=N+1		
001632	010701	T17A1	SCOPE		
001634	012702	000001	MOV	#1,X2	
001642	012767	000007 176130	MOV	#7,PSW	ILOAD PSW
001646	000402		BH	T17B	IGO DO SOB INSTRUCTION
001650	000000	T17AA1	HLT		IERRR SOB BRANCHED
001652	000407		BH	T17D	
001654	077203	T17B1	SOB	X2,T17AA	ISOB SHOULD NOT BRANCH
001658	016700	176114	MOV	PSW,X0	IGET STATUS CONTENTS
001662	022700	000007	CHP	#7,X0	ISTATUS = 7
001666	001401		REQ	,+4	
001670	000000		HLT		IERRR STATUS WORD CHANGED
001672	000240	T17D1	NOP		
	000020		C=C+1		
	000010		N=N+1		
001674	010701	T20A1	SCOPE		
001676	012702	000001	MOV	#1,X2	
001702	012767	000010 176060	MOV	#10,PSW	ILOAD PSW

001710	000402		RR	T20B	IGO DO SOB INSTRUCTION
001712	000070	T20AA1	HLT		IERRR SOB BRANCHED
001714	000407		RR	T20D	
001716	077203	T20B1	SOB	X2,T20AA	ISOB SHOULD NOT BRANCH
001720	016700	176052	MOV	PSW,X0	IGET STATUS CONTENTS
001724	022700	000010	CHP	#10,X0	ISTATUS = 10
001730	001401		REQ	,+4	
001732	000000		HLT		IERRR STATUS WORD CHANGED
001734	000240	T20D1	NOP		
	000021		C=C+1		
	000011		N=N+1		
001736	010701	T21A1	SCOPE		
001740	012702	000001	MOV	#1,X2	
001744	012767	000011 176024	MOV	#11,PSW	ILOAD PSW
001752	000402		BH	T21B	IGO DO SOB INSTRUCTION
001754	000000	T21AA1	HLT		IERRR SOB BRANCHED
001756	000407		BH	T21D	
001760	077203	T21B1	SOB	X2,T21AA	ISOB SHOULD NOT BRANCH
001762	016700	176010	MOV	PSW,X0	IGET STATUS CONTENTS
001766	022700	000011	CHP	#11,X0	ISTATUS = 11
001772	001401		REQ	,+4	
001774	000000		HLT		IERRR STATUS WORD CHANGED
001776	000240	T21D1	NOP		
	000022		C=C+1		
	000012		N=N+1		
002000	010701	T22A1	SCOPE		
002002	012702	000001	MOV	#1,X2	
002006	012767	000012 175762	MOV	#12,PSW	ILOAD PSW
002014	000402		BH	T22B	IGO DO SOB INSTRUCTION
002016	000000	T22AA1	HLT		IERRR SOB BRANCHED
002020	000407		BH	T22D	
002022	077203	T22B1	SOB	X2,T22AA	ISOB SHOULD NOT BRANCH
002024	016700	175740	MOV	PSW,X0	IGET STATUS CONTENTS
002030	022700	000012	CHP	#12,X0	ISTATUS = 12
002034	001401		REQ	,+4	
002036	000000		HLT		IERRR STATUS WORD CHANGED
002040	000240	T22D1	NOP		
	000023		C=C+1		
	000013		N=N+1		
002042	010701	T23A1	SCOPE		
002044	012702	000001	MOV	#1,X2	
002050	012767	000013 175720	MOV	#13,PSW	ILOAD PSW
002056	000402		RR	T23B	IGO DO SOB INSTRUCTION
002060	000000	T23AA1	HLT		IERRR SOB BRANCHED
002062	000407		RR	T23D	
002064	077203	T23B1	SOB	X2,T3AA	ISOB SHOULD NOT BRANCH
002066	016700	175704	MOV	PSW,X0	IGET STATUS CONTENTS
002070	022700	000013	CHP	#13,X2	ISTATUS = 13
002074	001401		REQ	,+4	
002076	000000		HLT		IERRR STATUS WORD CHANGED
002080	000240	T23D1	NOP		



000024			C=C+1		
000014			N=N+1		
002104	010701		T24A1	SCOPE	
002106	012702	000001		MOV	#1,X2
002112	012707	000014	175656	MOV	#14,PSW
002120	000402			BR	T24B
002122	000000		T24AA1	HLT	
002124	000407			BR	T24D
002126	077203		T24B1	SOB	X2,T24AA
002130	016700	175642		MOV	PSW,X0
002134	022700	000014		CHP	#14,X0
002140	001401			REQ	,+4
002142	000000			HLT	
002144	000240		T24D1	NOP	
	000025			C=C+1	
	000019			N=N+1	
002146	010701		T25A1	SCOPE	
002150	012702	000001		MOV	#1,X2
002154	012707	000019	175614	MOV	#15,PSW
002162	000402			BR	T25B
002164	000000		T25AA1	HLT	
002166	000407			BR	T25D
002170	077203		T25B1	SOB	X2,T25AA
002172	016700	175600		MOV	PSW,X0
002176	022700	000015		CHP	#15,X0
002202	001401			REQ	,+4
002204	000000			HLT	
002206	000240		T25D1	NOP	
	000026			C=C+1	
	000016			N=N+1	
002210	010701		T26A1	SCOPE	
002212	012702	000001		MOV	#1,X2
002216	012707	000016	175552	MOV	#16,PSW
002224	000402			BR	T26B
002226	000000		T26AA1	HLT	
002230	000407			BR	T26D
002232	077203		T26B1	SOB	X2,T26AA
002234	016700	175536		MOV	PSW,X0
002240	022700	000016		CHP	#16,X0
002244	001401			REQ	,+4
002246	000000			HLT	
002250	000240		T26D1	NOP	
	000027			C=C+1	
	000017			N=N+1	
002252	010701		T27A1	SCOPE	
002254	012702	000001		MOV	#1,X2
002258	012707	000017	175510	MOV	#17,PSW
002266	000402			BR	T27B
002270	000000		T27AA1	HLT	
002272	000407			BR	T27D

002274	077203		T27B1	SOB	X2,T27AA
002276	016700	175474		MOV	PSW,X0
002282	022700	000017		CHP	#17,X0
002286	001401			REQ	,+4
002290	000000			HLT	
002292	000240		T27D1	NOP	
	000030			C=C+1	
	000020			N=N+1	
000000					
			N=0		
			*TEST THAT SOB DOES NOT AFFECT THE CONDITION CODES WHEN SOB BRANCHES;		
002314	010701		T30A1	SCOPE	
002316	012702	000002		MOV	#2,X2
002322	012707	000000	175446	MOV	#0,PSW
002330	000407			BR	T30B
002332	016700	175440	T30AA1	MOV	PSW,X0
002336	022700	000000		CHP	#0,X0
002342	001404			REQ	T30D
002346	000000			HLT	
002348	000402		T30B1	SOB	X2,T30AA
002350	077203			SOB	
002352	000000		T30D1	NOP	
002354	000240			C=C+1	
	000031			N=N+1	
	000001				
002356	010701		T31A1	SCOPE	
002360	012702	000002		MOV	#2,X2
002364	012707	000001	175404	MOV	#1,PSW
002372	000407			BR	T31B
002374	016700	175376	T31AA1	MOV	PSW,X0
002380	022700	000001		CHP	#1,X0
002404	001404			REQ	T31D
002406	000000			HLT	
002410	000402			BR	T31D
002412	077203		T31B1	SOB	X2,T31AA
002414	000000			HLT	
002416	000240		T31D1	NOP	
	000032			C=C+1	
	000022			N=N+1	
002420	010701		T32A1	SCOPE	
002422	012702	000002		MOV	#2,X2
002426	012707	000002	175342	MOV	#2,PSW
002434	000407			BR	T32B
002436	016700	175334	T32AA1	MOV	PSW,X0
002442	022700	000002		CHP	#2,X0
002446	001404			REQ	T32D
002450	000000			HLT	
002452	000402			BR	T32D
002454	077203		T32B1	SOB	X2,T32AA
002456	000000			HLT	
002460	000240		T32D1	NOP	

000033			C=C+1		
000003			N=N+1		
002462	010701		T33A1	SCOPE	
002464	012702	000004		MOV	#2,X7
002470	012767	000003	175100	MOV	#1,PSW
002476	000407			RM	T33B
002500	016700	175272	T33AA1	MOV	PSW,X0
002504	022700	000003		CMF	#3,X0
002510	001404			REQ	T33D
002512	000000			HLT	
002514	000402			RM	T33D
002516	077210		T33B1	SOB	X2,T33AA
002520	000000			HLT	
002522	000240		T33D1	NOP	
	000034			C=C+1	
	000004			N=N+1	
002524	010701		T34A1	SCOPE	
002526	012702	000002		MOV	#2,X2
002532	012767	000204	175230	MOV	#4,PSW
002540	000407			RM	T34B
002542	016700	175230	T34AA1	MOV	PSW,X0
002546	022700	000004		CMF	#4,X0
002552	001404			REQ	T34D
002554	000000			HLT	
002556	000402			RM	T34D
002560	077210		T34B1	SOB	X2,T34AA
002562	000000			HLT	
002564	000240		T34D1	NOP	
	000035			C=C+1	
	000005			N=N+1	
002566	010701		T35A1	SCOPE	
002570	012702	000002		MOV	#2,X2
002574	012767	000005	175174	MOV	#5,PSW
002602	000407			RM	T35B
002604	016700	175166	T35AA1	MOV	PSW,X0
002610	022700	000005		CMF	#5,X0
002614	001404			REQ	T35D
002616	000000			HLT	
002620	000402			RM	T35D
002622	077210		T35B1	SOB	X2,T35AA
002624	000000			HLT	
002626	000240		T35D1	NOP	
	000036			C=C+1	
	000006			N=N+1	
002630	010701		T36A1	SCOPE	
002632	012702	000002		MOV	#2,X2
002636	012767	000006	175132	MOV	#6,PSW
002644	000407			RM	T36B
002646	016700	175124	T36AA1	MOV	PSW,X0
002652	022700	000006		CMF	#6,X0

002656	001404			REQ	T36D
002660	000000			HLT	
002662	000402			RM	T36D
002664	077210		T36B1	SOB	X2,T36AA
002666	000000			HLT	
002670	000240		T36D1	NOP	
	000037			C=C+1	
	000007			N=N+1	
002672	010701		T37A1	SCOPE	
002674	012702	000002		MOV	#2,X2
002700	012767	000007	175070	MOV	#7,PSW
002706	000407			RM	T37B
002710	016700	175062	T37AA1	MOV	PSW,X0
002714	022700	000007		CMF	#7,X2
002720	001404			REQ	T37D
002722	000000			HLT	
002724	000402			RM	T37D
002726	077210		T37B1	SOB	X2,T37AA
002730	000000			HLT	
002732	000240		T37D1	NOP	
	000040			C=C+1	
	000010			N=N+1	
002734	010701		T40A1	SCOPE	
002736	012702	000002		MOV	#2,X2
002742	012767	000010	175026	MOV	#10,PSW
002750	000407			RM	T40B
002752	016700	175020	T40AA1	MOV	PSW,X0
002756	022700	000010		CMF	#10,X0
002762	001404			REQ	T40D
002764	000000			HLT	
002766	000402			RM	T40D
002770	077210		T40B1	SOB	X2,T40AA
002772	000000			HLT	
002774	000240		T40D1	NOP	
	000041			C=C+1	
	000011			N=N+1	
002776	010701		T41A1	SCOPE	
002780	012702	000002		MOV	#2,X2
002804	012767	000011	174764	MOV	#11,PSW
002812	000407			RM	T41B
002814	016700	174766	T41AA1	MOV	PSW,X0
002822	022700	000011		CMF	#11,X0
002824	001404			REQ	T41D
002826	000000			HLT	
002828	000402			RM	T41D
002832	077210		T41B1	SOB	X2,T41AA
002834	000000			HLT	
002836	000240		T41D1	NOP	
	000042			C=C+1	
	000012			N=N+1	

003040	010701		T42A1	SCOPE		
003042	012702	000002		MOV	#2,X2	ISET UP SOB TO BRANCH
003046	012767	000012	174722	MOV	#12,PSW	ILOAD 12 INTO STATUS WORD
003054	000407			RH	T42B	IGD DO SOB INSTRUCTION
003056	016700	174714	T42AA1	MOV	PSW,X0	IGET STATUS WORD CONTENTS
003062	022700	000012		CMP	#12,X0	ISTATUS = 127
003066	001404			BEQ	T42D	
003070	000000			HLT		ERROR STATUS WORD CHANGED
003072	000402			BH	T42D	
003074	077210		T42B1	SOB	X2,T42AA	ISOB SHOULD BRANCH
003076	000000			HLT		ISOB FAILED TO BRANCH
003100	000240		T42D1	NOP		
000043				C=C+1		
000013				N=N+1		
003102	010701		T43A1	SCOPE		
003104	012702	000002		MOV	#2,X2	ISET UP SOB TO BRANCH
003110	012767	000013	174660	MOV	#13,PSW	ILOAD 13 INTO STATUS WORD
003116	000407			RH	T43B	IGD DO SOB INSTRUCTION
003120	016700	174652	T43AA1	MOV	PSW,X0	IGET STATUS WORD CONTENTS
003124	022700	000013		CMP	#13,X0	ISTATUS = 137
003130	001404			BEQ	T43D	
003132	000000			HLT		ERROR STATUS WORD CHANGED
003134	000402			BH	T43D	
003136	077210		T43B1	SOB	X2,T43AA	ISOB SHOULD BRANCH
003140	000000			HLT		ISOB FAILED TO BRANCH
003142	000240		T43D1	NOP		
000044				C=C+1		
000014				N=N+1		
003144	010701		T44A1	SCOPE		
003146	012702	000002		MOV	#2,X2	ISET UP SOB TO BRANCH
003152	012767	000014	174610	MOV	#14,PSW	ILOAD 14 INTO STATUS WORD
003160	000407			RH	T44B	IGD DO SOB INSTRUCTION
003162	016700	174610	T44AA1	MOV	PSW,X0	IGET STATUS WORD CONTENTS
003166	022700	000014		CMP	#14,X0	ISTATUS = 147
003172	001404			BEQ	T44D	
003174	000000			HLT		ERROR STATUS WORD CHANGED
003176	000402			BH	T44D	
003180	077210		T44B1	SOB	X2,T44AA	ISOB SHOULD BRANCH
003182	000000			HLT		ISOB FAILED TO BRANCH
003184	000240		T44D1	NOP		
000045				C=C+1		
000015				N=N+1		
003200	010701		T45A1	SCOPE		
003202	012702	000002		MOV	#2,X2	ISET UP SOB TO BRANCH
003204	012767	000015	174554	MOV	#15,PSW	ILOAD 15 INTO STATUS WORD
003206	000407			RH	T45B	IGD DO SOB INSTRUCTION
003208	016700	174546	T45AA1	MOV	PSW,X0	IGET STATUS WORD CONTENTS
003210	022700	000015		CMP	#15,X0	ISTATUS = 157
003212	001404			BEQ	T45D	
003214	000000			HLT		ERROR STATUS WORD CHANGED
003216	000402			BH	T45D	

003242	077210		T45B1	SOB	X2,T45AA	ISOB SHOULD BRANCH
003244	000000			HLT		ISOB FAILED TO BRANCH
003246	000240		T45D1	NOP		
000046				C=C+1		
000016				N=N+1		
003250	010701		T46A1	SCOPE		
003252	012702	000002		MOV	#2,X2	ISET UP SOB TO BRANCH
003254	012767	000016	174512	MOV	#16,PSW	ILOAD 16 INTO STATUS WORD
003256	000407			RH	T46B	IGD DO SOB INSTRUCTION
003258	016700	174504	T46AA1	MOV	PSW,X0	IGET STATUS WORD CONTENTS
003260	022700	000016		CMP	#16,X0	ISTATUS = 167
003262	001404			BEQ	T46D	
003264	000000			HLT		ERROR STATUS WORD CHANGED
003266	000402			BH	T46D	
003268	077210		T46B1	SOB	X2,T46AA	ISOB SHOULD BRANCH
003270	000000			HLT		ISOB FAILED TO BRANCH
003272	000240		T46D1	NOP		
000047				C=C+1		
000017				N=N+1		
003312	010701		T47A1	SCOPE		
003314	012702	000002		MOV	#2,X2	ISET UP SOB TO BRANCH
003316	012767	000017	174450	MOV	#17,PSW	ILOAD 17 INTO STATUS WORD
003318	000407			RH	T47B	IGD DO SOB INSTRUCTION
003320	016700	174442	T47AA1	MOV	PSW,X0	IGET STATUS WORD CONTENTS
003322	022700	000017		CMP	#17,X0	ISTATUS = 177
003324	001404			BEQ	T47D	
003326	000000			HLT		ERROR STATUS WORD CHANGED
003328	000402			BH	T47D	
003330	077210		T47B1	SOB	X2,T47AA	ISOB SHOULD BRANCH
003332	000000			HLT		ISOB FAILED TO BRANCH
003334	000240		T47D1	NOP		
000048				C=C+1		
000018				N=N+1		
003336	012702	000002		MOV	#2,X2	ISET THAT SOB CAN BRANCH BACK 64 WORDS
003338	000407		SOB END	RH	SOB	IGD TO SOB INSTRUCTION
003340	000000			END		
003342	000000			P		WITH HALTS
003344	000000			P		WITH HALTS
003346	000000			P		WITH HALTS
003348	000000			P		WITH HALTS
003350	000000			P		WITH HALTS
003352	000000			P		WITH HALTS
003354	000000			P		WITH HALTS
003356	000000			P		WITH HALTS
003358	000000			P		WITH HALTS
003360	000000			P		WITH HALTS
003362	000000			P		WITH HALTS
003364	000000			P		WITH HALTS
003366	000000			P		WITH HALTS
003368	000000			P		WITH HALTS
003370	000000			P		WITH HALTS
003372	000000			P		WITH HALTS
003374	000000			P		WITH HALTS
003376	000000			P		WITH HALTS
003378	000000			P		WITH HALTS
003380	000000			P		WITH HALTS
003382	000000			P		WITH HALTS
003384	000000			P		WITH HALTS
003386	000000			P		WITH HALTS
003388	000000			P		WITH HALTS
003390	000000			P		WITH HALTS
003392	000000			P		WITH HALTS
003394	000000			P		WITH HALTS
003396	000000			P		WITH HALTS
003398	000000			P		WITH HALTS
003400	000000			P		WITH HALTS

003422	000000	0				WITH HALTS
003422	000000	0				WITH HALTS
003424	000000	0				WITH HALTS
003426	000000	0				WITH HALTS
003430	000000	0				WITH HALTS
003432	000000	0				WITH HALTS
003434	000000	0				WITH HALTS
003436	000000	0				WITH HALTS
003440	000000	0				WITH HALTS
003442	000000	0				WITH HALTS
003444	000000	0				WITH HALTS
003446	000000	0				WITH HALTS
003450	000000	0				WITH HALTS
003452	000000	0				WITH HALTS
003454	000000	0				WITH HALTS
003456	000000	0				WITH HALTS
003460	000000	0				WITH HALTS
003462	000000	0				WITH HALTS
003464	000000	0				WITH HALTS
003466	000000	0				WITH HALTS
003470	000000	0				WITH HALTS
003472	000000	0				WITH HALTS
003474	000000	0				WITH HALTS
003476	000000	0				WITH HALTS
003520	000000	0				WITH HALTS
003522	000000	0				WITH HALTS
003524	000000	0				WITH HALTS
003526	000000	0				WITH HALTS
003530	000000	0				WITH HALTS
003532	000000	0				WITH HALTS
003534	000000	0				WITH HALTS
003536	000000	0				WITH HALTS
003540	000000	0				WITH HALTS
003542	000000	0				WITH HALTS
003544	000000	0				WITH HALTS
003546	000000	0				WITH HALTS
003550	000000	0				WITH HALTS
003552	000000	0				WITH HALTS
003554	000000	0				WITH HALTS
003556	000000	0				WITH HALTS
003562	000000	0				WITH HALTS
003562	005267	175212	SOBSI	SOB	Y2,SOBLND	IBRANCH BACK 64, WORDS
003566	026727	175206	000370	HLT		ERROR DID NOT BRANCH
003574	041422					
003576	000167	175204	ENDI	JNC	ICNT	INCREMENT PASS COUNT
				CMQ	ICNT,#370	
				BEQ	DONE	JGO TO DONE IF 1200 PASSES COMPLETED
				JMP	BEGIN	RESTART TEST

003602	012707	000007	173750	DONE1	MOV	#7,TPBR	IRING BELL
003610	105767	173750			TSTB	TPCR	
003614	105375				RPL	,#4	
003616	013702	000042			MOV	#42,X2	ICNT DECTAPE MONITOR RETURN ADDRESS
003622	004404				HLQ	DONE1	JC NOT RETURN IF (42)*0
003624	004712				JBR	7,(2)	RETURN TO DECTAPE MONITOR
003626	000240				NOP		FACT11
003630	000240				NOP		LOVPLAY
003632	000240				NOP		IARL4
003634	000167	173142	DONE11	JMP	START		
	000001				,END		

REGIN	001000	C	000000	DISPLA	177570	PCNE	003002
ONE1	003634	END	003962	HLT	000700	ICNT	001700
2	003020	PSW	177776	SCOPE	210701	SCOPE'D	003362
TOBS	003550	START	001202	STKPTR	000500	SWR	177570
TPRUF	177560	TPCSP	177564	T0A	001036	T0B	001750
T7C	001454	T1A	001056	T1AA	001064	T1B	001076
T1C	001100	T10A	001254	T10AA	001272	T10B	001276
T10D	001414	T11A	001316	T11AA	001334	T11B	001340
T11D	001450	T12A	001360	T12AA	001376	T12B	001402
T12D	001420	T13A	001422	T13AA	001440	T13B	001444
T13D	001464	T14A	001464	T14AA	001502	T14B	001506
T14D	001524	T15A	001526	T15AA	001544	T15B	001550
T15D	001560	T16A	001570	T16AA	001600	T16B	001612
T16D	001630	T17A	001632	T17AA	001650	T17B	001654
T17D	001672	T2A	001102	T2H	001114	T20A	001674
T20AA	001712	T20H	001716	T20D	001734	T21A	001736
T21AA	001754	T21H	001760	T21D	001776	T22A	002000
T22AA	002016	T22H	002022	T22D	002040	T23A	002042
T23AA	002060	T23H	002064	T23D	002102	T24A	002104
T24AA	002122	T24H	002126	T24D	002144	T25A	002146
T25AA	002164	T25H	002170	T25D	002200	T26A	002210
T26AA	002226	T26H	002232	T26D	002250	T27A	002252
T27AA	002270	T27H	002274	T27D	002312	T3A	001120
T3B	001126	T30A	002314	T30AA	002332	T30B	002350
T30D	002354	T31A	002356	T31AA	002374	T31B	002412
T31D	002416	T32A	002420	T32AA	002436	T32B	002454
T32D	002460	T33A	002462	T33AA	002500	T33B	002516
T33D	002522	T34A	002524	T34AA	002542	T34B	002560
T34D	002564	T35A	002566	T35AA	002604	T35B	002622
T35D	002626	T36A	002630	T36AA	002646	T36B	002664
T36D	002670	T37A	002672	T37AA	002710	T37B	002726
T37D	002732	T4A	001140	T4B	001152	T40A	002734
T40AA	002752	T40B	002770	T40D	002774	T41A	002776
T41AA	003014	T41B	003032	T41D	003036	T42A	003040
T42AA	003050	T42B	003074	T42D	003100	T43A	003102
T43AA	003120	T43B	003136	T43D	003142	T44A	003144
T44AA	003162	T44B	003200	T44D	003204	T45A	003206
T45AA	003224	T45B	003242	T45D	003246	T46A	003250
T46AA	003266	T46B	003304	T46D	003310	T47A	003312
T47AA	003330	T47B	003346	T47D	003352	T5A	001156
T5AA	001160	T5B	001200	T6A	001204	T6B	001216
T6C	001230	T7A	001232	T7B	001244	UBPEAK	177770
	003640						

ERRORS DETECTED: 0

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,TITLE MAINDEC-11-DCK8C-A POP11/25-11/45 XOR INST TEST  
 ,NLIST MD,PC,CND,SLD  
 ,LIST ME  
 ,ARS

IFTEST DCK8CA- TEST OF THE XOR INSTRUCTION AND INCLUDES DATA CHECKS AND  
 CONDITION CODES CHECKS  
 IF THE XOR INSTRUCTION IS ESSENTIALLY A /HALF ADD/ INSTRUCTION, IE, IF  
 THE CORRESPONDING BIT IN THE SOURCE AND DESTINATION ADDRESS ARE THE  
 SAME THE RESULT IN DESTINATION ADDRESS IS A '0' IN THAT BIT POSITION  
 AND, IF THE CORRESPONDING BITS ARE DIFFERENT THEN THE RESULT IS A '1'  
 IN THAT BIT POSITION, THE CONDITION CODES ARE AFFECTED AS FOLLOWS:  
 IFZ IS SET IF THE RESULT IS = TO 0, OTHERWISE IT IS CLEARED  
 IFN IS SET IF THE RESULT IS NEGATIVE, CLEARED IF POSITIVE  
 IFV IS UNCHANGED  
 IFV IS CLEARED

IFFUNCTIONS NOT TESTED BY THIS PROGRAM  
 IF THAT XOR WILL EXPLICITLY CHANGE THE USER/EXEC BIT IN THE STATUS WORD,  
 STARTING PROCEDURE  
 LOAD ADDRESS=200  
 PRESS START  
 STACK POINTER IS SET AT 500  
 BELL WILL RING WHEN TEST IS COMPLETE

010701	SCOPE=010701	IMCV	PC,R1
000000	HLT=HALT		
177776	PSW=177776		ADDRESS OF PROCESSOR STATUS WORD
177570	UBPEAK=177570		ADDRESS OF MICRO BREAK REGISTER
177564	TPCSP=177564		ADDRESS OF TELEPRINTER CSR
177566	TPRUF=177566		ADDRESS OF TELEPRINTER BUFFER
177570	SWR=177570		ADDRESS OF CONSOLE SWITCH REGISTER
177570	DISPLAY=177570		ADDRESS OF CONSOLE LIGHT REGISTER

\*\*\*\*\*INITIAL STACK POINTER=2500\*\*\*\*\*  
 STACK=0500 INITIAL STACK SETTING

000000	000000	,#	
000000	000000	,+2	
000000	000000	HALT	
000000	000000	,+2	
000000	000000	HALT	
000010	000012	,+2	
000012	000000	HALT	
000014	000016	,+2	
000016	000030	HALT	
000020	000022	,+2	
000022	000000	HALT	

000024	000026	,+2
000026	000028	HALT
000028	000030	,+2
000030	000032	HALT
000032	000034	,+2
000034	000036	HALT
000036	000038	,+2
000038	000040	HALT
000040	000042	,+2
000042	000044	HALT
000044	000046	,+2
000046	000048	HALT
000048	000050	,+2
000050	000052	HALT
000052	000054	,+2
000054	000056	HALT
000056	000058	,+2
000058	000060	HALT
000060	000062	,+2
000062	000064	HALT
000064	000066	,+2
000066	000068	HALT
000068	000070	,+2
000070	000072	HALT
000072	000074	,+2
000074	000076	HALT
000076	000078	,+2
000078	000080	HALT
000080	000082	,+2
000082	000084	HALT
000084	000086	,+2
000086	000088	HALT
000088	000090	,+2
000090	000092	HALT
000092	000094	,+2
000094	000096	HALT
000096	000098	,+2
000098	000100	HALT
000100	000102	,+2
000102	000104	HALT
000104	000106	,+2
000106	000108	HALT
000108	000110	,+2
000110	000112	HALT
000112	000114	,+2
000114	000116	HALT
000116	000118	,+2
000118	000120	HALT
000120	000122	,+2
000122	000124	HALT
000124	000126	,+2
000126	000128	HALT
000128	000130	,+2
000130	000132	HALT
000132	000134	,+2
000134	000136	HALT
000136	000138	,+2
000138	000140	HALT
000140	000142	,+2
000142	000144	HALT
000144	000146	,+2
000146	000148	HALT
000148	000150	,+2
000150	000152	HALT
000152	000154	,+2
000154	000156	HALT
000156	000158	,+2
000158	000160	HALT
000160	000162	,+2
000162	000164	HALT
000164	000166	,+2
000166	000168	HALT
000168	000170	,+2
000170	000172	HALT
000172	000174	,+2
000174	000176	HALT
000176	000000	HALT

000200	000202	,+2
000202	000204	HALT
000204	000206	,+2
000206	000208	HALT
000208	000210	,+2
000210	000212	HALT
000212	000214	,+2
000214	000216	HALT
000216	000218	,+2
000218	000220	HALT
000220	000222	,+2
000222	000224	HALT
000224	000226	,+2
000226	000228	HALT
000228	000230	,+2
000230	000232	HALT
000232	000234	,+2
000234	000236	HALT
000236	000238	,+2
000238	000240	HALT
000240	000242	,+2
000242	000244	HALT
000244	000246	,+2
000246	000248	HALT
000248	000250	,+2
000250	000252	HALT
000252	000254	,+2
000254	000256	HALT
000256	000258	,+2
000258	000260	HALT
000260	000262	,+2
000262	000264	HALT
000264	000266	,+2
000266	000268	HALT
000268	000270	,+2
000270	000272	HALT
000272	000274	,+2
000274	000276	HALT
000276	000278	,+2
000278	000280	HALT
000280	000282	,+2
000282	000284	HALT
000284	000286	,+2
000286	000288	HALT
000288	000290	,+2
000290	000292	HALT
000292	000294	,+2
000294	000296	HALT
000296	000298	,+2
000298	000300	HALT
000300	000302	,+2
000302	000304	HALT
000304	000306	,+2
000306	000308	HALT
000308	000310	,+2
000310	000312	HALT
000312	000314	,+2
000314	000316	HALT
000316	000318	,+2
000318	000320	HALT
000320	000322	,+2
000322	000324	HALT
000324	000326	,+2
000326	000328	HALT
000328	000330	,+2
000330	000332	HALT
000332	000334	,+2
000334	000336	HALT
000336	000338	,+2
000338	000340	HALT
000340	000342	,+2
000342	000344	HALT
000344	000346	,+2
000346	000348	HALT
000348	000350	,+2
000350	000352	HALT
000352	000000	HALT

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000354 000356 ,+2
000356 000000 HALT
000360 000362 ,+2
000362 000000 HALT
000364 000366 ,+2
000366 000000 HALT
000370 000372 ,+2
000372 000000 HALT
000374 000376 ,+2
000376 000000 HALT

000200 000200 ,#200
000200 000167 000576 JMP START
000200 001000 ,#1000
001000 000000 ICNTI 0 ICNTAINS PASS COUNT
001002 005067 177772 STARTI CLR ICNT ICLEAR PASS COUNT
001006 012776 000500 REGINI MOV #STKPTH,46 INITIALIZE STACK POINTER
001012 016737 177762 177570 MOV ICNT,0#DISPLAY DISPLAY PASS COUNT
001020 032737 000400 177570 BIT #400,0#SWR LOAD MICRO BREAK REGISTER
001026 021403 RLO ,+10
001030 113737 177570 177770 MOVB #SWR,0#UBREAK LOAD MICRO BREAK REG WITH SP0=7

SOURCE=X2
DEST=X0
Y=0

001036 010701 SCOPE
001040 012702 000000 MOV #0,SOURCE MOVE SOURCE OPERAND TO SOURCE
001044 012700 000000 MOV #0,DEST MOVE DESTINATION OPERAND TO DESTINATION
001050 000257 CCC
001052 074200 XOR SOURCE,DEST XOR SOURCE AND DESTINATION
001054 103001 HCC ,+4
001056 000000 HLT ERROR: CARRY CHANGED
001060 102001 RVC ,+4
001062 000000 HLT ERROR: V FAILED TO CLEAR
001064 020027 000000 CMP DEST,#0 IDEST=P?
001070 001401 RLO ,+4
001072 000000 HLT 10 XOR 0 FAILED

000001 Y=Y+1
001074 010701 SCOPE
001076 012702 125252 MOV #125252,SOURCE MOVE SOURCE OPERAND TO SOURCE
001102 012700 125252 MOV #125252,DEST MOVE DESTINATION OPERAND TO DESTINATION
001106 000277 CCC
001110 074200 XOR SOURCE,DEST XOR SOURCE AND DESTINATION
001112 103401 BCS ,+4
001114 000000 HLT ERROR: CARRY CHANGED
001116 102001 RVC ,+4
001120 000000 HLT ERROR: V FAILED TO CLEAR
001122 020027 000000 CMP DEST,#0 IDEST=P?
001126 001401 RLO ,+4
001130 000000 HLT 10 XOR 125252 FAILED

000002 Y=Y+1
001132 010701 SCOPE
001134 012702 052925 MOV #052925,SOURCE MOVE SOURCE OPERAND TO SOURCE
    
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001140 012700 052925 MOV #052925,DEST MOVE DESTINATION OPERAND TO DESTINATION
001144 000257 CCC
001146 074200 XOR SOURCE,DEST XOR SOURCE AND DESTINATION
001150 103001 BCC ,+4
001152 000000 HLT ERROR: CARRY CHANGED
001154 102001 RVC ,+4
001156 000000 HLT ERROR: V FAILED TO CLEAR
001160 020027 000000 CMP DEST,#0 IDEST=P?
001164 001401 RLO ,+4
001166 000000 HLT 10 XOR 052925 FAILED

000003 Y=Y+1
001170 010701 SCOPE
001172 012702 177777 MOV #-1,SOURCE MOVE SOURCE OPERAND TO SOURCE
001176 012700 177777 MOV #-1,DEST MOVE DESTINATION OPERAND TO DESTINATION
001202 000277 CCC
001204 074200 XOR SOURCE,DEST XOR SOURCE AND DESTINATION
001206 103401 BCS ,+4
001210 000000 HLT ERROR: CARRY CHANGED
001212 102001 RVC ,+4
001214 000000 HLT ERROR: V FAILED TO CLEAR
001216 020027 000000 CMP DEST,#-1 IDEST=-1?
001222 001401 RLO ,+4
001224 000000 HLT 10 XOR -1 FAILED

000004 Y=Y+1
001226 010701 SCOPE
001230 012702 125252 MOV #125252,SOURCE MOVE SOURCE OPERAND TO SOURCE
001234 012700 052925 MOV #052925,DEST MOVE DESTINATION OPERAND TO DESTINATION
001240 000257 CCC
001242 074200 XOR SOURCE,DEST XOR SOURCE AND DESTINATION
001244 103001 BCC ,+4
001246 000000 HLT ERROR: CARRY CHANGED
001250 102001 RVC ,+4
001252 000000 HLT ERROR: V FAILED TO CLEAR
001254 020027 177777 CMP DEST,#-1 IDEST=-1?
001260 001401 RLO ,+4
001262 000000 HLT 10 XOR 052925 FAILED

000005 Y=Y+1
001264 010701 SCOPE
001266 012702 052925 MOV #052925,SOURCE MOVE SOURCE OPERAND TO SOURCE
001270 012700 125252 MOV #125252,DEST MOVE DESTINATION OPERAND TO DESTINATION
001276 000277 CCC
001280 074200 XOR SOURCE,DEST XOR SOURCE AND DESTINATION
001282 103401 BCS ,+4
001284 000000 HLT ERROR: CARRY CHANGED
001286 102001 RVC ,+4
001290 000000 HLT ERROR: V FAILED TO CLEAR
001292 020027 177777 CMP DEST,#-1 IDEST=-1?
001296 001401 RLO ,+4
001298 000000 HLT 10 XOR 125252 FAILED

000006 Y=Y+1
    
```

001322	010701		SCOPE	
001324	012702	100000	MOV	#100000,SOURCE
001332	012700	100001	MOV	#100001,DEST
001334	000257		CCC	
001336	074200		XOR	SOURCE,DEST
001342	103001		RCC	,+4
001342	000000		HLT	
001344	102001		RVC	,+4
001346	000000		HLT	
001300	020027	000001	CHP	DEST,#1 IDEST#17
001304	001401		REQ	,+4
001306	000000		HLT	
	000007		Y#Y+1	
001360	010701		SCOPE	
001362	012702	077777	MOV	#77777,SOURCE
001366	012700	100000	MOV	#100000,DEST
001372	000277		CCC	
001374	074200		XOR	SOURCE,DEST
001376	103401		RCC	,+4
001400	000000		HLT	
001402	102001		RVC	,+4
001404	000000		HLT	
001406	020027	177777	CHP	DEST,#-1 IDEST#-17
001412	001401		REQ	,+4
001414	000000		HLT	
	000010		Y#Y+1	
	000001		N#N	
001416	010701		SCOPE	
001420	012702	000001	MOV	#1,SOURCE
001424	012700	000001	MOV	#1,DEST
001432	000257		CCC	
001432	074200		XOR	SOURCE,DEST
001434	103001		RCC	,+4
001436	000000		HLT	
001440	102001		RVC	,+4
001442	000000		HLT	
001444	020027	000000	CHP	DEST,#0 IDEST#07
001450	001401		REQ	,+4
001452	000000		HLT	
	000011		Y#Y+1	
	000002		N#N#N	
001454	010701		SCOPE	
001456	012702	000002	MOV	#2,SOURCE
001462	012700	000002	MOV	#2,DEST
001466	000277		CCC	
001470	074200		XOR	SOURCE,DEST
001472	103401		RCC	,+4
001474	000000		HLT	
001476	102001		RVC	,+4
001500	000000		HLT	

001502	020027	000000	CHP	DEST,#0 IDEST#07
001506	001401		REQ	,+4
001510	000000		HLT	
	000012		Y#Y+1	
	000004		N#N#N	
001512	010701		SCOPE	
001514	012702	000004	MOV	#4,SOURCE
001520	012700	000004	MOV	#4,DEST
001524	000257		CCC	
001526	074200		XOR	SOURCE,DEST
001530	103001		RCC	,+4
001532	000000		HLT	
001534	102001		RVC	,+4
001536	000000		HLT	
001540	020027	000000	CHP	DEST,#0 IDEST#07
001544	001401		REQ	,+4
001546	000000		HLT	
	000013		Y#Y+1	
	000010		N#N#N	
001550	010701		SCOPE	
001552	012702	000010	MOV	#10,SOURCE
001556	012700	000010	MOV	#10,DEST
001562	000277		CCC	
001564	074200		XOR	SOURCE,DEST
001566	103001		RCC	,+4
001570	000000		HLT	
001572	102001		RVC	,+4
001574	000000		HLT	
001576	020027	000000	CHP	DEST,#0 IDEST#07
001602	001401		REQ	,+4
001604	000000		HLT	
	000014		Y#Y+1	
	000000		N#N#N	
001606	010701		SCOPE	
001610	012702	000020	MOV	#20,SOURCE
001614	012700	000020	MOV	#20,DEST
001622	000257		CCC	
001624	074200		XOR	SOURCE,DEST
001626	103001		RCC	,+4
001630	000000		HLT	
001632	102001		RVC	,+4
001634	000000		HLT	
001636	020027	000000	CHP	DEST,#0 IDEST#07
001642	001401		REQ	,+4
001644	000000		HLT	
	000015		Y#Y+1	
	000010		N#N#N	
001644	010701		SCOPE	
001646	012702	000040	MOV	#40,SOURCE
001652	012700	000040	MOV	#40,DEST



001636	000277		BCC		
001640	074200		XOR	SOURCE,DEST	1XOR SOURCE AND DESTINATION
001642	103401		MCL	,+4	
001644	000000		HLT		1ERROR! CARRY CHANGED
001666	102001		BVC	,+4	
001670	000000		HLT		1ERROR! V FAILED TO CLEAR
001672	020027	000000	CMP	DEST,#0 IDEST#0?	
001676	001401		REQ	,+4	
001700	000000		HLT		140 XOR 40 FAILED
	000010		Y=Y+1		
	000100		N=N+N		
	010701		SCOPE		
001722	012702	000100	MOV	#100,SOURCE	1MOVE SOURCE OPERAND TO SOURCE
001710	012700	000100	MOV	#100,DEST	1MOVE DESTINATION OPERAND TO DESTINATION
001714	000257		CCC		
001716	074200		XOR	SOURCE,DEST	1XOR SOURCE AND DESTINATION
001720	103001		BCC	,+4	
001722	000000		HLT		1ERROR! CARRY CHANGED
001724	102001		BVC	,+4	
001726	000000		HLT		1ERROR! V FAILED TO CLEAR
001730	020027	000000	CMP	DEST,#0 IDEST#0?	
001734	001401		REQ	,+4	
001736	000000		HLT		1100 XOR 100 FAILED
	000017		Y=Y+1		
	000200		N=N+N		
	010701		SCOPE		
001740	012702	000200	MOV	#200,SOURCE	1MOVE SOURCE OPERAND TO SOURCE
001746	012700	000200	MOV	#200,DEST	1MOVE DESTINATION OPERAND TO DESTINATION
001752	000277		CCC		
001754	074200		XOR	SOURCE,DEST	1XOR SOURCE AND DESTINATION
001756	103401		BCC	,+4	
001760	000000		HLT		1ERROR! CARRY CHANGED
001762	102001		BVC	,+4	
001764	000000		HLT		1ERROR! V FAILED TO CLEAR
001766	020027	000000	CMP	DEST,#0 IDEST#0?	
001772	001401		REQ	,+4	
001774	000000		HLT		120 XOR 200 FAILED
	000020		Y=Y+1		
	000400		N=N+N		
	010701		SCOPE		
001776	012702	000400	MOV	#400,SOURCE	1MOVE SOURCE OPERAND TO SOURCE
001780	012700	000400	MOV	#400,DEST	1MOVE DESTINATION OPERAND TO DESTINATION
001784	000257		CCC		
001786	074200		XOR	SOURCE,DEST	1XOR SOURCE AND DESTINATION
001790	103001		BCC	,+4	
001792	000000		HLT		1ERROR! CARRY CHANGED
001794	102001		BVC	,+4	
001796	000000		HLT		1ERROR! V FAILED TO CLEAR
001800	020027	000000	CMP	DEST,#0 IDEST#0?	
001804	001401		REQ	,+4	
001806	000000		HLT		1400 XOR 400 FAILED

	000021		Y=Y+1		
	001000		N=N+N		
	010701		SCOPE		
002036	012702	001000	MOV	#1000,SOURCE	1MOVE SOURCE OPERAND TO SOURCE
002042	012700	001000	MOV	#1000,DEST	1MOVE DESTINATION OPERAND TO DESTINATION
002046	000277		CCC		
002050	074200		XOR	SOURCE,DEST	1XOR SOURCE AND DESTINATION
002052	103401		BCC	,+4	
002054	000000		HLT		1ERROR! CARRY CHANGED
002056	102001		BVC	,+4	
002060	000000		HLT		1ERROR! V FAILED TO CLEAR
002062	020027	000000	CMP	DEST,#0 IDEST#0?	
002066	001401		REQ	,+4	
002070	000000		HLT		11200 XOR 1000 FAILED
	000022		Y=Y+1		
	002000		N=N+N		
	010701		SCOPE		
002072	012702	002000	MOV	#2000,SOURCE	1MOVE SOURCE OPERAND TO SOURCE
002100	012700	002000	MOV	#2000,DEST	1MOVE DESTINATION OPERAND TO DESTINATION
002104	000257		CCC		
002106	074200		XOR	SOURCE,DEST	1XOR SOURCE AND DESTINATION
002110	103001		BCC	,+4	
002112	000000		HLT		1ERROR! CARRY CHANGED
002114	102001		BVC	,+4	
002116	000000		HLT		1ERROR! V FAILED TO CLEAR
002120	020027	000000	CMP	DEST,#0 IDEST#0?	
002124	001401		REQ	,+4	
002126	000000		HLT		12000 XOR 2000 FAILED
	000023		Y=Y+1		
	004000		N=N+N		
	010701		SCOPE		
002130	012702	004000	MOV	#4000,SOURCE	1MOVE SOURCE OPERAND TO SOURCE
002136	012700	004000	MOV	#4000,DEST	1MOVE DESTINATION OPERAND TO DESTINATION
002142	000277		CCC		
002144	074200		XOR	SOURCE,DEST	1XOR SOURCE AND DESTINATION
002146	103401		BCC	,+4	
002150	000000		HLT		1ERROR! CARRY CHANGED
002152	102001		BVC	,+4	
002154	000000		HLT		1ERROR! V FAILED TO CLEAR
002156	020027	000000	CMP	DEST,#0 IDEST#0?	
002160	001401		REQ	,+4	
002164	000000		HLT		14000 XOR 4000 FAILED
	000024		Y=Y+1		
	010000		N=N+N		
	010701		SCOPE		
002170	012702	010000	MOV	#10000,SOURCE	1MOVE SOURCE OPERAND TO SOURCE
002174	012700	010000	MOV	#10000,DEST	1MOVE DESTINATION OPERAND TO DESTINATION
002200	000257		CCC		
002202	074200		XOR	SOURCE,DEST	1XOR SOURCE AND DESTINATION
002204	103001		BCC	,+4	

002236	000000		HLT		ERROR! CARRY CHANGED
002237	000000		BVC	,+4	
002238	000000		HLT		ERROR! V FAILED TO CLEAR
002239	000000	000000	CMP	DEST,#0 IDEST=0?	
002240	000000		REQ	,+4	
002241	000000		HLT		10000 XOR 10000 FAILED
	000025		Y=Y+1		
	020000		N=N+N		
	SCOPE				
002224	010701		MOV	#20000,SOURCE	IMOVE SOURCE OPERAND TO SOURCE
002225	012702	020000	MOV	#20000,DEST	IMOVE DESTINATION OPERAND TO DESTINATION
002232	000277	020000	SCC		
002240	074200		XOR	SOURCE,DEST	IXOR SOURCE AND DESTINATION
002242	103401		BCC	,+4	
002244	000000		HLT		ERROR! CARRY CHANGED
002246	102001		BVC	,+4	
002250	000000		HLT		ERROR! V FAILED TO CLEAR
002252	020027	000000	CMP	DEST,#0 IDEST=0?	
002256	001401		REQ	,+4	
002260	000000		HLT		12000 XOR 20000 FAILED
	000026		Y=Y+1		
	040000		N=N+N		
	SCOPE				
002262	010701		MOV	#40000,SOURCE	IMOVE SOURCE OPERAND TO SOURCE
002270	012702	040000	MOV	#40000,DEST	IMOVE DESTINATION OPERAND TO DESTINATION
002274	000257		CCC		
002276	074200		XOR	SOURCE,DEST	IXOR SOURCE AND DESTINATION
002300	103001		BCC	,+4	
002302	000000		HLT		ERROR! CARRY CHANGED
002304	107001		BVC	,+4	
002306	000000		HLT		ERROR! V FAILED TO CLEAR
002310	020027	000000	CMP	DEST,#0 IDEST=0?	
002314	001401		REQ	,+4	
002316	000000		HLT		14000 XOR 40000 FAILED
	000027		Y=Y+1		
	100000		N=N+N		
	SCOPE				
002320	010701		MOV	#100000,SOURCE	IMOVE SOURCE OPERAND TO SOURCE
002322	012702	100000	MOV	#100000,DEST	IMOVE DESTINATION OPERAND TO DESTINATION
002326	000277	100000	SCC		
002332	000277		XOR	SOURCE,DEST	IXOR SOURCE AND DESTINATION
002334	074200		BCC	,+4	
002336	103401		BVC	,+4	
002340	000000		HLT		ERROR! CARRY CHANGED
002342	102001		BVC	,+4	
002344	000000		HLT		ERROR! V FAILED TO CLEAR
002346	020027	000000	CMP	DEST,#0 IDEST=0?	
002350	001401		REQ	,+4	
002354	000000		HLT		100000 XOR 100000 FAILED
	000030		Y=Y+1		
	000000		N=N+N		

002356	010701		SCOPE		
002358	012702	177777	MOV	#1,SOURCE	IMOVE SOURCE OPERAND TO SOURCE
002364	012700	000001	MOV	#1,DEST	IMOVE DESTINATION OPERAND TO DESTINATION
002370	000257		CCC		
002372	074200		XOR	SOURCE,DEST	IXOR SOURCE AND DESTINATION
002374	103001		BCC	,+4	
002376	000000		HLT		ERROR! CARRY CHANGED
002400	102001		BVC	,+4	
002402	000000		HLT		ERROR! V FAILED TO CLEAR
002404	020027	177776	CMP	DEST,#177776	IDEST=177776?
002410	001401		REQ	,+4	
002412	000000		HLT		I=I XOR 1 FAILED
	000031		Y=Y+1		
	000002		N=N+N		
	SCOPE				
002414	010701		MOV	#1,SOURCE	IMOVE SOURCE OPERAND TO SOURCE
002416	012702	177777	MOV	#2,DEST	IMOVE DESTINATION OPERAND TO DESTINATION
002422	012700	000002	SCC		
002426	000277		XOR	SOURCE,DEST	IXOR SOURCE AND DESTINATION
002430	074200		BCC	,+4	
002432	103401		BVC	,+4	
002434	000000		HLT		ERROR! CARRY CHANGED
002436	102001		BVC	,+4	
002440	000000		HLT		ERROR! V FAILED TO CLEAR
002442	020027	177775	CMP	DEST,#177775	IDEST=177775?
002446	001401		REQ	,+4	
002450	000000		HLT		I=I XOR 2 FAILED
	000032		Y=Y+1		
	000004		N=N+N		
	SCOPE				
002452	010701		MOV	#1,SOURCE	IMOVE SOURCE OPERAND TO SOURCE
002454	012702	177777	MOV	#4,DEST	IMOVE DESTINATION OPERAND TO DESTINATION
002460	000257	000004	CCC		
002466	074200		XOR	SOURCE,DEST	IXOR SOURCE AND DESTINATION
002470	103001		BCC	,+4	
002472	000000		HLT		ERROR! CARRY CHANGED
002474	102001		BVC	,+4	
002476	000000		HLT		ERROR! V FAILED TO CLEAR
002500	020027	177773	CMP	DEST,#177773	IDEST=177773?
002504	001401		REQ	,+4	
002506	000000		HLT		I=I XOR 4 FAILED
	000033		Y=Y+1		
	000010		N=N+N		
	SCOPE				
002510	010701		MOV	#1,SOURCE	IMOVE SOURCE OPERAND TO SOURCE
002512	012702	177777	MOV	#10,DEST	IMOVE DESTINATION OPERAND TO DESTINATION
002516	012700	000010	SCC		
002522	000277		XOR	SOURCE,DEST	IXOR SOURCE AND DESTINATION
002524	074200		BCC	,+4	
002526	103401		BVC	,+4	
002530	000000		HLT		ERROR! CARRY CHANGED

022532	102001		BVC	,+4	
022534	200000		HLT		ERROR: V FAILED TO CLEAR
022536	220077	177767	CMP	DEST,#177767	IDEST=1777677
022542	201401		REC		
022544	000000		HLT		I=I XOR 10 FAILED
	200034		Y=Y+1		
	200020		N=N+N		
	210771		SCOPE		
022546	212702	177777	MOV	#=1,SOURCE	IMOVE SOURCE OPERAND TO SOURCE
022554	212700	000020	MOV	#20,DEST	IMOVE DESTINATION OPERAND TO DESTINATION
022500	200257		CCC		
022502	274200		XOR	SOURCE,DEST	IXOR SOURCE AND DESTINATION
022504	103001		BCC	,+4	
022506	200000		HLT		ERROR: CARRY CHANGED
022570	102001		BVC	,+4	
022572	200000		HLT		ERROR: V FAILED TO CLEAR
022574	200027	177757	CMP	DEST,#177757	IDEST=1777577
022600	201401		REQ		
022602	200000		HLT		I=I XOR 20 FAILED
	200039		Y=Y+1		
	200040		N=N+N		
	210701		SCOPE		
022604	212702	177777	MOV	#=1,SOURCE	IMOVE SOURCE OPERAND TO SOURCE
022612	212700	000040	MOV	#40,DEST	IMOVE DESTINATION OPERAND TO DESTINATION
022616	200277		SCC		
022620	074200		XOR	SOURCE,DEST	IXOR SOURCE AND DESTINATION
022622	103401		BCC	,+4	
022624	200000		HLT		ERROR: CARRY CHANGED
022626	102001		BVC	,+4	
022630	200000		HLT		ERROR: V FAILED TO CLEAR
022632	220027	177737	CMP	DEST,#177737	IDEST=1777377
022636	201401		REQ		
022640	200000		HLT		I=I XOR 40 FAILED
	200036		Y=Y+1		
	200100		N=N+N		
	210701		SCOPE		
022642	212702	177777	MOV	#=1,SOURCE	IMOVE SOURCE OPERAND TO SOURCE
022650	212700	000100	MOV	#100,DEST	IMOVE DESTINATION OPERAND TO DESTINATION
022654	200257		CCC		
022656	274200		XOR	SOURCE,DEST	IXOR SOURCE AND DESTINATION
022660	103001		BCC	,+4	
022662	200000		HLT		ERROR: CARRY CHANGED
022664	102001		BVC	,+4	
022666	200000		HLT		ERROR: V FAILED TO CLEAR
022670	220027	177677	CMP	DEST,#177677	IDEST=1776777
022674	201401		REQ		
022676	200000		HLT		I=I XOR 100 FAILED
	200037		Y=Y+1		
	200200		N=N+N		
022700	210701		SCOPE		

022702	212702	177777	MOV	#=1,SOURCE	IMOVE SOURCE OPERAND TO SOURCE
022706	212700	000200	MOV	#200,DEST	IMOVE DESTINATION OPERAND TO DESTINATION
022712	200277		SCC		
022714	074200		XOR	SOURCE,DEST	IXOR SOURCE AND DESTINATION
022716	103401		BCC	,+4	
022720	200000		HLT		ERROR: CARRY CHANGED
022722	102001		BVC	,+4	
022724	200000		HLT		ERROR: V FAILED TO CLEAR
022726	220027	177577	CMP	DEST,#177577	IDEST=1775777
022732	201401		REQ		
022734	200000		HLT		I=I XOR 200 FAILED
	200040		Y=Y+1		
	200400		N=N+N		
	210701		SCOPE		
022736	212702	177777	MOV	#=1,SOURCE	IMOVE SOURCE OPERAND TO SOURCE
022744	212700	000400	MOV	#400,DEST	IMOVE DESTINATION OPERAND TO DESTINATION
022750	200257		CCC		
022752	074200		XOR	SOURCE,DEST	IXOR SOURCE AND DESTINATION
022754	103001		BCC	,+4	
022756	200000		HLT		ERROR: CARRY CHANGED
022760	102001		BVC	,+4	
022762	200000		HLT		ERROR: V FAILED TO CLEAR
022764	220027	177377	CMP	DEST,#177377	IDEST=1773777
022770	201401		REQ		
022772	200000		HLT		I=I XOR 400 FAILED
	200041		Y=Y+1		
	201000		N=N+N		
	210701		SCOPE		
022774	212702	177777	MOV	#=1,SOURCE	IMOVE SOURCE OPERAND TO SOURCE
022776	212700	001000	MOV	#1000,DEST	IMOVE DESTINATION OPERAND TO DESTINATION
022780	200277		SCC		
022782	074200		XOR	SOURCE,DEST	IXOR SOURCE AND DESTINATION
022784	103401		BCC	,+4	
022786	200000		HLT		ERROR: CARRY CHANGED
022788	102001		BVC	,+4	
022790	200000		HLT		ERROR: V FAILED TO CLEAR
022792	220027	176777	CMP	DEST,#176777	IDEST=1767777
022794	201401		REQ		
022796	200000		HLT		I=I XOR 1000 FAILED
	200042		Y=Y+1		
	202000		N=N+N		
	210701		SCOPE		
022802	212702	177777	MOV	#=1,SOURCE	IMOVE SOURCE OPERAND TO SOURCE
022804	212700	002000	MOV	#2000,DEST	IMOVE DESTINATION OPERAND TO DESTINATION
022806	200257		CCC		
022808	074200		XOR	SOURCE,DEST	IXOR SOURCE AND DESTINATION
022810	103001		BCC	,+4	
022812	200000		HLT		ERROR: CARRY CHANGED
022814	102001		BVC	,+4	
022816	200000		HLT		ERROR: V FAILED TO CLEAR
022818	220027	175777	CMP	DEST,#175777	IDEST=1757777
022820	201401		REQ		
022822	200000		HLT		I=I XOR 10000 FAILED

003064	001401		REQ	,+4	
003066	000000		HLT		I=I XOR 2000 FAILED
	000043		Y=Y+1		
	004000		N=N+N		
	010701		SCOPE		
003070	012702	177777	MOV	#=1,SOURCE	IMOVE SOURCE OPERAND TO SOURCE
003072	012700	004000	MOV	#4000,DEST	IMOVE DESTINATION OPERAND TO DESTINATION
003076	004277		SCC		
003102	074200		XOR	SOURCE,DEST	IXOR SOURCE AND DESTINATION
003104	103401		RCS	,+4	
003110	000000		HLT		ERROR! CARRY CHANGED
003112	102001		BVC	,+4	
003114	000000		HLT		ERROR! V FAILED TO CLEAR
003116	020027	173777	CMF	DEST,#173777	IFST=173777
003122	001401		REQ	,+4	
003124	000000		HLT		I=I XOR 4000 FAILED
	000044		Y=Y+1		
	010000		N=N+N		
	010701		SCOPE		
003126	012702	177777	MOV	#=1,SOURCE	IMOVE SOURCE OPERAND TO SOURCE
003130	012700	010000	MOV	#10000,DEST	IMOVE DESTINATION OPERAND TO DESTINATION
003134	000257		CCC		
003140	074200		XOR	SOURCE,DEST	IXOR SOURCE AND DESTINATION
003142	103001		RCC	,+4	
003144	000000		HLT		ERROR! CARRY CHANGED
003146	102001		BVC	,+4	
003150	000000		HLT		ERROR! V FAILED TO CLEAR
003152	020027	107777	CMF	DEST,#107777	IFST=107777
003154	001401		REQ	,+4	
003160	000000		HLT		I=I XOR 10000 FAILED
	000045		Y=Y+1		
	020000		N=N+N		
	010701		SCOPE		
003164	012702	177777	MOV	#=1,SOURCE	IMOVE SOURCE OPERAND TO SOURCE
003166	012700	020000	MOV	#20000,DEST	IMOVE DESTINATION OPERAND TO DESTINATION
003172	000277		SCC		
003176	074200		XOR	SOURCE,DEST	IXOR SOURCE AND DESTINATION
003200	103401		RCS	,+4	
003202	000000		HLT		ERROR! CARRY CHANGED
003204	102001		BVC	,+4	
003206	000000		HLT		ERROR! V FAILED TO CLEAR
003210	020027	157777	CMF	DEST,#157777	IFST=157777
003212	001401		REQ	,+4	
003216	000000		HLT		I=I XOR 20000 FAILED
	000046		Y=Y+1		
	040000		N=N+N		
	010701		SCOPE		
003222	012702	177777	MOV	#=1,SOURCE	IMOVE SOURCE OPERAND TO SOURCE
003224	012700	040000	MOV	#40000,DEST	IMOVE DESTINATION OPERAND TO DESTINATION
003230	000257		CCC		

003236	074200		XOR	SOURCE,DEST	IXOR SOURCE AND DESTINATION
003240	103001		RCC	,+4	
003242	000000		HLT		ERROR! CARRY CHANGED
003244	102001		BVC	,+4	
003246	000000		HLT		ERROR! V FAILED TO CLEAR
003250	020027	137777	CMF	DEST,#137777	IFST=137777
003254	001401		REQ	,+4	
003256	000000		HLT		I=I XOR 40000 FAILED
	000047		Y=Y+1		
	100000		N=N+N		
	010701		SCOPE		
003260	012702	177777	MOV	#=1,SOURCE	IMOVE SOURCE OPERAND TO SOURCE
003262	012700	100000	MOV	#100000,DEST	IMOVE DESTINATION OPERAND TO DESTINATION
003270	000277		SCC		
003274	074200		XOR	SOURCE,DEST	IXOR SOURCE AND DESTINATION
003276	103401		RCS	,+4	
003300	000000		HLT		ERROR! CARRY CHANGED
003302	102001		BVC	,+4	
003304	000000		HLT		ERROR! V FAILED TO CLEAR
003306	020027	077777	CMF	DEST,#77777	IFST=77777
003312	001401		REQ	,+4	
003314	000000		HLT		I=I XOR 120000 FAILED
	000050		Y=Y+1		
	000000		N=N+N		

			N=1		
003316	010701			SCOPE	
003320	012772	000000		MOV	#0,SOURCE IMOVE SOURCE OPERAND TO SOURCE
003324	012700	000001		MOV	#1,DEST IMOVE DESTINATION OPERAND TO DESTINATION
003330	000257			CCC	
003332	074200			XOR	SOURCE,DEST IXOR SOURCE AND DESTINATION
003334	103001			BCC	,+4
003336	000000			HLT	ERROR: CARRY CHANGED
003340	102001			BVC	,+4
003342	000070			HLT	ERROR: V FAILED TO CLEAR
003344	020077	000001		CMP	DEST,#1 IDEST=17
003350	001401			REQ	,+4
003352	000000			HLT	IXOR 1 FAILED
	000051			Y=Y+1	
	000002			N=N+N	
	010701			SCOPE	
003356	012772	000000		MOV	#0,SOURCE IMOVE SOURCE OPERAND TO SOURCE
003360	012700	000002		MOV	#2,DEST IMOVE DESTINATION OPERAND TO DESTINATION
003366	000277			CCC	
003370	074200			XOR	SOURCE,DEST IXOR SOURCE AND DESTINATION
003372	103401			BCC	,+4
003374	000000			HLT	ERROR: CARRY CHANGED
003376	102001			BVC	,+4
003380	000070			HLT	ERROR: V FAILED TO CLEAR
003382	020077	000002		CMP	DEST,#2 IDEST=27
003384	001401			REQ	,+4
003386	000000			HLT	IXOR 2 FAILED
	000052			Y=Y+1	
	000004			N=N+N	
	010701			SCOPE	
003412	012772	000000		MOV	#0,SOURCE IMOVE SOURCE OPERAND TO SOURCE
003414	012700	000004		MOV	#4,DEST IMOVE DESTINATION OPERAND TO DESTINATION
003420	000257			CCC	
003426	074200			XOR	SOURCE,DEST IXOR SOURCE AND DESTINATION
003432	103001			BCC	,+4
003434	000000			HLT	ERROR: CARRY CHANGED
003436	102001			BVC	,+4
003438	000070			HLT	ERROR: V FAILED TO CLEAR
003440	020027	000004		CMP	DEST,#4 IDEST=47
003442	001401			REQ	,+4
003444	000000			HLT	IXOR 4 FAILED
	000053			Y=Y+1	
	000010			N=N+N	
	010701			SCOPE	
003450	012772	000000		MOV	#0,SOURCE IMOVE SOURCE OPERAND TO SOURCE
003452	012700	000010		MOV	#10,DEST IMOVE DESTINATION OPERAND TO DESTINATION
003454	000277			CCC	
003456	074200			XOR	SOURCE,DEST IXOR SOURCE AND DESTINATION
003458	103401			BCC	,+4
003460	000000			HLT	ERROR: CARRY CHANGED

003472	102001			BVC	,+4
003474	000070			HLT	ERROR: V FAILED TO CLEAR
003476	020027	000010		CMP	DEST,#10 IDEST=107
003502	001401			REQ	,+4
003504	000000			HLT	IXOR 10 FAILED
	000054			Y=Y+1	
	000020			N=N+N	
	010701			SCOPE	
003510	012772	000000		MOV	#0,SOURCE IMOVE SOURCE OPERAND TO SOURCE
003514	012700	000020		MOV	#20,DEST IMOVE DESTINATION OPERAND TO DESTINATION
003520	000257			CCC	
003522	074200			XOR	SOURCE,DEST IXOR SOURCE AND DESTINATION
003524	103001			BCC	,+4
003526	000000			HLT	ERROR: CARRY CHANGED
003530	102001			BVC	,+4
003532	000070			HLT	ERROR: V FAILED TO CLEAR
003534	020027	000020		CMP	DEST,#20 IDEST=207
003540	001401			REQ	,+4
003542	000000			HLT	IXOR 20 FAILED
	000055			Y=Y+1	
	000040			N=N+N	
	010701			SCOPE	
003544	012772	000000		MOV	#0,SOURCE IMOVE SOURCE OPERAND TO SOURCE
003546	012700	000040		MOV	#40,DEST IMOVE DESTINATION OPERAND TO DESTINATION
003552	000277			CCC	
003556	074200			XOR	SOURCE,DEST IXOR SOURCE AND DESTINATION
003560	103401			BCC	,+4
003562	000000			HLT	ERROR: CARRY CHANGED
003564	102001			BVC	,+4
003566	000070			HLT	ERROR: V FAILED TO CLEAR
003570	020027	000040		CMP	DEST,#40 IDEST=407
003572	001401			REQ	,+4
003574	000000			HLT	IXOR 40 FAILED
	000056			Y=Y+1	
	000100			N=N+N	
	010701			SCOPE	
003602	012772	000000		MOV	#0,SOURCE IMOVE SOURCE OPERAND TO SOURCE
003604	012700	000100		MOV	#100,DEST IMOVE DESTINATION OPERAND TO DESTINATION
003610	000257			CCC	
003614	074200			XOR	SOURCE,DEST IXOR SOURCE AND DESTINATION
003616	103001			BCC	,+4
003618	000000			HLT	ERROR: CARRY CHANGED
003624	102001			BVC	,+4
003626	000070			HLT	ERROR: V FAILED TO CLEAR
003630	020027	000100		CMP	DEST,#100 IDEST=1007
003634	001401			REQ	,+4
003636	000000			HLT	IXOR 100 FAILED
	000057			Y=Y+1	
	000200			N=N+N	
	010701			SCOPE	

003642	012722	000000	MOV	#0,SOURCE	IMOVE SOURCE OPERAND TO SOURCE
003646	012700	002000	MOV	#200,DEST	IMOVE DESTINATION OPERAND TO DESTINATION
003652	000277		SCC		
003654	074200		XOR	SOURCE,DEST	IXOR SOURCE AND DESTINATION
003656	103401		BCC	,+4	
003660	000000		HLT		ERROR: CARRY CHANGED
003662	102001		BVC	,+4	
003664	000000		HLT		ERROR: V FAILED TO CLEAR
003666	020027	000200	CMP	DEST,#200	DEST=200?
003672	001401		BEQ	,+4	
003674	000000		HLT		10 XOR 200 FAILED
	000000		Y=Y+1		
	000400		N=N+N		
	010701		SCOPE		
003700	012722	000000	MOV	#0,SOURCE	IMOVE SOURCE OPERAND TO SOURCE
003704	012700	000400	MOV	#400,DEST	IMOVE DESTINATION OPERAND TO DESTINATION
003710	000257		CCC		
003712	074200		XOR	SOURCE,DEST	IXOR SOURCE AND DESTINATION
003714	103001		BCC	,+4	
003716	000000		HLT		ERROR: CARRY CHANGED
003720	102001		BVC	,+4	
003722	000000		HLT		ERROR: V FAILED TO CLEAR
003724	020027	000400	CMP	DEST,#400	DEST=400?
003730	001401		BEQ	,+4	
003732	000000		HLT		10 XOR 400 FAILED
	000001		Y=Y+1		
	010701		N=N+N		
	010701		SCOPE		
003734	012722	000000	MOV	#0,SOURCE	IMOVE SOURCE OPERAND TO SOURCE
003742	012700	001000	MOV	#1000,DEST	IMOVE DESTINATION OPERAND TO DESTINATION
003746	000277		SCC		
003750	074200		XOR	SOURCE,DEST	IXOR SOURCE AND DESTINATION
003752	103401		BCC	,+4	
003754	000000		HLT		ERROR: CARRY CHANGED
003756	102001		BVC	,+4	
003760	000000		HLT		ERROR: V FAILED TO CLEAR
003762	020027	001000	CMP	DEST,#1000	DEST=1000?
003764	001401		BEQ	,+4	
003770	000000		HLT		10 XOR 1000 FAILED
	000002		Y=Y+1		
	002000		N=N+N		
	010701		SCOPE		
003772	012722	000000	MOV	#0,SOURCE	IMOVE SOURCE OPERAND TO SOURCE
003774	012700	002000	MOV	#2000,DEST	IMOVE DESTINATION OPERAND TO DESTINATION
004000	000257		CCC		
004006	074200		XOR	SOURCE,DEST	IXOR SOURCE AND DESTINATION
004010	103001		BCC	,+4	
004012	000000		HLT		ERROR: CARRY CHANGED
004014	102001		BVC	,+4	
004016	000000		HLT		ERROR: V FAILED TO CLEAR
004020	020027	002000	CMP	DEST,#2000	DEST=2000?

004024	001401		REQ	,+4	
004026	000000		HLT		10 XOR 2000 FAILED
	000003		Y=Y+1		
	004000		N=N+N		
	010701		SCOPE		
004030	012722	000000	MOV	#0,SOURCE	IMOVE SOURCE OPERAND TO SOURCE
004036	012700	004000	MOV	#4000,DEST	IMOVE DESTINATION OPERAND TO DESTINATION
004042	000277		SCC		
004044	074200		XOR	SOURCE,DEST	IXOR SOURCE AND DESTINATION
004046	103401		BCC	,+4	
004050	000000		HLT		ERROR: CARRY CHANGED
004052	102001		BVC	,+4	
004054	000000		HLT		ERROR: V FAILED TO CLEAR
004056	020027	004000	CMP	DEST,#4000	DEST=4000?
004062	001401		REQ	,+4	
004064	000000		HLT		10 XOR 4000 FAILED
	000004		Y=Y+1		
	010701		N=N+N		
	010701		SCOPE		
004066	012722	000000	MOV	#0,SOURCE	IMOVE SOURCE OPERAND TO SOURCE
004074	012700	010000	MOV	#10000,DEST	IMOVE DESTINATION OPERAND TO DESTINATION
004100	000257		CCC		
004102	074200		XOR	SOURCE,DEST	IXOR SOURCE AND DESTINATION
004104	103001		BCC	,+4	
004106	000000		HLT		ERROR: CARRY CHANGED
004110	102001		BVC	,+4	
004112	000000		HLT		ERROR: V FAILED TO CLEAR
004114	020027	010000	CMP	DEST,#10000	DEST=10000?
004120	001401		REQ	,+4	
004122	000000		HLT		10 XOR 10000 FAILED
	000005		Y=Y+1		
	020000		N=N+N		
	010701		SCOPE		
004124	012722	000000	MOV	#0,SOURCE	IMOVE SOURCE OPERAND TO SOURCE
004132	012700	020000	MOV	#20000,DEST	IMOVE DESTINATION OPERAND TO DESTINATION
004136	000277		SCC		
004140	074200		XOR	SOURCE,DEST	IXOR SOURCE AND DESTINATION
004142	103401		BCC	,+4	
004144	000000		HLT		ERROR: CARRY CHANGED
004146	102001		BVC	,+4	
004150	000000		HLT		ERROR: V FAILED TO CLEAR
004152	020027	020000	CMP	DEST,#20000	DEST=20000?
004156	001401		REQ	,+4	
004160	000000		HLT		10 XOR 20000 FAILED
	000006		Y=Y+1		
	040000		N=N+N		
	010701		SCOPE		
004162	012722	000000	MOV	#0,SOURCE	IMOVE SOURCE OPERAND TO SOURCE
004170	012700	040000	MOV	#40000,DEST	IMOVE DESTINATION OPERAND TO DESTINATION
004174	000257		CCC		

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004176	074200		XOR	SOURCE,DEST	1XOR SOURCE AND DESTINATION
004200	101001		BCC	,+4	
004202	000000		HLT		1ERROR: CARRY CHANGED
004204	102001		BVC	,+4	
004206	000000		HLT		1ERROR: V FAILED TO CLEAR
004210	020027	040000	CMF	DEST,#40000	1DEST=40000?
004214	001401		BEQ	,+4	
004216	000000		HLT		10 XOR 40000 FAILED
	000007		Y=Y+1		
	100000		N=N+N		
004220	010701		SCOPE		
004222	012702	000000	MOV	#0,SOURCE	1MOVE SOURCE OPERAND TO SOURCE
004226	012700	100000	MOV	#100000,DEST	1MOVE DESTINATION OPERAND TO DESTINATION
004232	000277		CCC		
004234	074200		XOR	SOURCE,DEST	1XOR SOURCE AND DESTINATION
004236	103401		BCC	,+4	
004240	000000		HLT		1ERROR: CARRY CHANGED
004242	102001		BVC	,+4	
004244	000000		HLT		1ERROR: V FAILED TO CLEAR
004246	020027	100000	CMF	DEST,#100000	1DEST=100000?
004252	001401		BEQ	,+4	
004254	000000		HLT		10 XOR 100000 FAILED
	000070		Y=Y+1		
	000000		N=N+N		
	000001		N=N		
	010701		SCOPE		
004256	012722	000001	MOV	#1,SOURCE	1MOVE SOURCE OPERAND TO SOURCE
004264	012720	177776	MOV	#177776,DEST	1MOVE DESTINATION OPERAND TO DESTINATION
004270	000257		CCC		
004272	074200		XOR	SOURCE,DEST	1XOR SOURCE AND DESTINATION
004274	103001		BCC	,+4	
004276	000000		HLT		1ERROR: CARRY CHANGED
004320	102001		BVC	,+4	
004322	000000		HLT		1ERROR: V FAILED TO CLEAR
004324	020027	177777	CMF	DEST,#177777	1DEST=177777?
004310	001401		BEQ	,+4	
004312	000000		HLT		11 XOR 177776 FAILED
	000071		Y=Y+1		
	000002		N=N+N		
	010701		SCOPE		
004314	012722	000002	MOV	#2,SOURCE	1MOVE SOURCE OPERAND TO SOURCE
004322	012720	177776	MOV	#177776,DEST	1MOVE DESTINATION OPERAND TO DESTINATION
004324	000277		CCC		
004326	074200		XOR	SOURCE,DEST	1XOR SOURCE AND DESTINATION
004328	103401		BCC	,+4	
004330	000000		HLT		1ERROR: CARRY CHANGED
004332	102001		BVC	,+4	
004334	000000		HLT		1ERROR: V FAILED TO CLEAR
004336	020027	177777	CMF	DEST,#177777	1DEST=177777?
004340	001401		BEQ	,+4	

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004350	000000		HLT		12 XOR 177776 FAILED
	000072		Y=Y+1		
	000004		N=N+N		
	010701		SCOPE		
004352	012722	000004	MOV	#4,SOURCE	1MOVE SOURCE OPERAND TO SOURCE
004360	012700	177773	MOV	#177773,DEST	1MOVE DESTINATION OPERAND TO DESTINATION
004364	000257		CCC		
004366	074200		XOR	SOURCE,DEST	1XOR SOURCE AND DESTINATION
004370	103001		BCC	,+4	
004372	000000		HLT		1ERROR: CARRY CHANGED
004374	102001		BVC	,+4	
004376	000000		HLT		1ERROR: V FAILED TO CLEAR
004400	020027	177777	CMF	DEST,#177777	1DEST=177777?
004404	001401		BEQ	,+4	
004406	000000		HLT		14 XOR 177773 FAILED
	000073		Y=Y+1		
	000010		N=N+N		
	010701		SCOPE		
004410	012702	000010	MOV	#10,SOURCE	1MOVE SOURCE OPERAND TO SOURCE
004416	012700	177767	MOV	#177767,DEST	1MOVE DESTINATION OPERAND TO DESTINATION
004422	000277		CCC		
004424	074200		XOR	SOURCE,DEST	1XOR SOURCE AND DESTINATION
004426	103401		BCC	,+4	
004430	000000		HLT		1ERROR: CARRY CHANGED
004432	102001		BVC	,+4	
004434	000000		HLT		1ERROR: V FAILED TO CLEAR
004436	020027	177777	CMF	DEST,#177777	1DEST=177777?
004442	001401		BEQ	,+4	
004444	000000		HLT		110 XOR 177767 FAILED
	000074		Y=Y+1		
	000020		N=N+N		
	010701		SCOPE		
004446	012702	000020	MOV	#20,SOURCE	1MOVE SOURCE OPERAND TO SOURCE
004454	012700	177757	MOV	#177757,DEST	1MOVE DESTINATION OPERAND TO DESTINATION
004460	000257		CCC		
004462	074200		XOR	SOURCE,DEST	1XOR SOURCE AND DESTINATION
004464	103001		BCC	,+4	
004466	000000		HLT		1ERROR: CARRY CHANGED
004470	102001		BVC	,+4	
004472	000000		HLT		1ERROR: V FAILED TO CLEAR
004474	020027	177777	CMF	DEST,#177777	1DEST=177777?
004478	001401		BEQ	,+4	
004480	000000		HLT		120 XOR 177757 FAILED
	000075		Y=Y+1		
	000040		N=N+N		
	010701		SCOPE		
004484	012702	000040	MOV	#40,SOURCE	1MOVE SOURCE OPERAND TO SOURCE
004512	012700	177737	MOV	#177737,DEST	1MOVE DESTINATION OPERAND TO DESTINATION
004516	000277		CCC		
004520	074200		XOR	SOURCE,DEST	1XOR SOURCE AND DESTINATION

004522	103401		RCS	,+4	
004524	000000		HLT		ERROR! CARRY CHANGED
004526	102001		RVC	,+4	
004530	000000		HLT		ERROR! V FAILED TO CLEAR
004532	020027	177777	CMP	DEST,#177777	DEST=#177777
004536	001401		BEQ	,+4	
004540	000000		HLT		140 XOR 177737 FAILED
	000076		Y=Y+1		
	000100		N=N+N		
	000100		SCOPE		
004544	010701		MOV	#100,SOURCE	IMOVE SOURCE OPERAND TO SOURCE
004550	012702	000100	MOV	#177677,DEST	IMOVE DESTINATION OPERAND TO DESTINATION
004554	000257	177677	CCC		
004556	074200		XOR	SOURCE,DEST	IXOR SOURCE AND DESTINATION
004560	103001		BCC	,+4	
004562	000000		HLT		ERROR! CARRY CHANGED
004564	102001		RVC	,+4	
004566	000000		HLT		ERROR! V FAILED TO CLEAR
004570	020027	177777	CMP	DEST,#177777	DEST=#177777
004574	001401		BEQ	,+4	
004576	000000		HLT		1120 XOR 177677 FAILED
	000077		Y=Y+1		
	000200		N=N+N		
	000200		SCOPE		
004600	010701		MOV	#200,SOURCE	IMOVE SOURCE OPERAND TO SOURCE
004602	012702	000200	MOV	#177577,DEST	IMOVE DESTINATION OPERAND TO DESTINATION
004606	000257	177577	CCC		
004612	000277		XOR	SOURCE,DEST	IXOR SOURCE AND DESTINATION
004614	074200		RCS	,+4	
004616	103401		HLT		ERROR! CARRY CHANGED
004620	000000		RVC	,+4	
004622	102001		HLT		ERROR! V FAILED TO CLEAR
004624	000000		CMP	DEST,#177777	DEST=#177777
004626	020027	177777	BEQ	,+4	
004632	001401		HLT		1200 XOR 177577 FAILED
004634	000000				
	000100		Y=Y+1		
	000400		N=N+N		
	000400		SCOPE		
004636	010701		MOV	#400,SOURCE	IMOVE SOURCE OPERAND TO SOURCE
004640	012702	000400	MOV	#177377,DEST	IMOVE DESTINATION OPERAND TO DESTINATION
004644	000257	177377	CCC		
004650	000257		XOR	SOURCE,DEST	IXOR SOURCE AND DESTINATION
004654	103001		BCC	,+4	
004656	000000		HLT		ERROR! CARRY CHANGED
004660	102001		RVC	,+4	
004662	000000		HLT		ERROR! V FAILED TO CLEAR
004664	020027	177777	CMP	DEST,#177777	DEST=#177777
004670	001401		BEQ	,+4	
004672	000000		HLT		1400 XOR 177377 FAILED
	000101		Y=Y+1		

	001000		N=N+N		
	001000		SCOPE		
004674	010701		MOV	#1000,SOURCE	IMOVE SOURCE OPERAND TO SOURCE
004676	012702	001000	MOV	#176777,DEST	IMOVE DESTINATION OPERAND TO DESTINATION
004702	012700	176777	CCC		
004706	000277		XOR	SOURCE,DEST	IXOR SOURCE AND DESTINATION
004710	074200		RCS	,+4	
004712	103401		HLT		ERROR! CARRY CHANGED
004714	000000		RVC	,+4	
004716	102001		HLT		ERROR! V FAILED TO CLEAR
004720	000000		CMP	DEST,#177777	DEST=#177777
004722	020027	177777	BEQ	,+4	
004726	001401		HLT		11000 XOR 176777 FAILED
004730	000000				
	000102		Y=Y+1		
	002000		N=N+N		
	002000		SCOPE		
004732	010701		MOV	#2000,SOURCE	IMOVE SOURCE OPERAND TO SOURCE
004734	012702	002000	MOV	#175777,DEST	IMOVE DESTINATION OPERAND TO DESTINATION
004740	000257	175777	CCC		
004744	000257		XOR	SOURCE,DEST	IXOR SOURCE AND DESTINATION
004746	074200		BCC	,+4	
004750	103001		HLT		ERROR! CARRY CHANGED
004752	000000		RVC	,+4	
004754	102001		HLT		ERROR! V FAILED TO CLEAR
004756	000000		CMP	DEST,#177777	DEST=#177777
004760	020027	177777	BEQ	,+4	
004764	001401		HLT		12000 XOR 175777 FAILED
004766	000000				
	000103		Y=Y+1		
	004000		N=N+N		
	004000		SCOPE		
004770	010701		MOV	#4000,SOURCE	IMOVE SOURCE OPERAND TO SOURCE
004772	012702	004000	MOV	#173777,DEST	IMOVE DESTINATION OPERAND TO DESTINATION
004774	000257	173777	CCC		
004776	000257		XOR	SOURCE,DEST	IXOR SOURCE AND DESTINATION
004778	074200		BCC	,+4	
004780	103401		HLT		ERROR! CARRY CHANGED
004782	000000		RVC	,+4	
004784	102001		HLT		ERROR! V FAILED TO CLEAR
004786	000000		CMP	DEST,#177777	DEST=#177777
004788	020027	177777	BEQ	,+4	
004790	001401		HLT		14000 XOR 173777 FAILED
004792	000000				
	000104		Y=Y+1		
	010000		N=N+N		
	010000		SCOPE		
005026	010701		MOV	#10000,SOURCE	IMOVE SOURCE OPERAND TO SOURCE
005030	012702	010000	MOV	#167777,DEST	IMOVE DESTINATION OPERAND TO DESTINATION
005034	000257	167777	CCC		
005036	000257		XOR	SOURCE,DEST	IXOR SOURCE AND DESTINATION
005038	074200		BCC	,+4	
005040	103001		HLT		ERROR! CARRY CHANGED
005042	102001		RVC	,+4	



000002	000000		HLT		IFPROM V FAILED TO CLEAR
000004	020027	177777	CMF	DEST,#177777	IFPST=177777
000006	001401		BEO	,+4	
000008	000000		HLT		17000 XOR 107777 FAILED
	000105		Y=Y+1		
	020000		N=N+N		
	010701		SCOPE		
000004	012702	020000	MOV	#20000,SOURCE	IMOVE SOURCE OPERAND TO SOURCE
000006	012702	020000	MOV	#157777,DEST	IMOVE DESTINATION OPERAND TO DESTINATION
000008	000277	157777	SCC		
000010	074200		XOR	SOURCE,DEST	IXOR SOURCE AND DESTINATION
000012	103401		BCC	,+4	
000014	000000		HLT		IFPROM CARRY CHANGED
000016	102001		BVC	,+4	
000018	000000		HLT		IFPROM V FAILED TO CLEAR
000020	020027	177777	CMF	DEST,#177777	IFPST=177777
000022	001401		BEO	,+4	
000024	000000		HLT		12000 XOR 157777 FAILED
	000106		Y=Y+1		
	040000		N=N+N		
	010701		SCOPE		
000122	012702	040000	MOV	#40000,SOURCE	IMOVE SOURCE OPERAND TO SOURCE
000124	012702	137777	MOV	#137777,DEST	IMOVE DESTINATION OPERAND TO DESTINATION
000126	000277		SCC		
000128	074200		XOR	SOURCE,DEST	IXOR SOURCE AND DESTINATION
000130	103401		BCC	,+4	
000132	000000		HLT		IFPROM CARRY CHANGED
000134	102001		BVC	,+4	
000136	000000		HLT		IFPROM V FAILED TO CLEAR
000138	020027	177777	CMF	DEST,#177777	IFPST=177777
000140	001401		BEO	,+4	
000142	000000		HLT		14000 XOR 137777 FAILED
	000107		Y=Y+1		
	100000		N=N+N		
	010701		SCOPE		
000160	012702	100000	MOV	#100000,SOURCE	IMOVE SOURCE OPERAND TO SOURCE
000162	012702	077777	MOV	#77777,DEST	IMOVE DESTINATION OPERAND TO DESTINATION
000164	000277		SCC		
000166	074200		XOR	SOURCE,DEST	IXOR SOURCE AND DESTINATION
000168	103401		BCC	,+4	
000170	000000		HLT		IFPROM CARRY CHANGED
000172	102001		BVC	,+4	
000174	000000		HLT		IFPROM V FAILED TO CLEAR
000176	020027	177777	CMF	DEST,#177777	IFPST=177777
000178	001401		BEO	,+4	
000180	000000		HLT		10000 XOR 77777 FAILED
	000110		Y=Y+1		
	000000		N=N+N		
	000000		SOURCE=X0		

000002			DEST=X2		
000002			RSLT=X2		
	000216	010701	SCOPE		
000220	012702	031463	MOV	#031463,SOURCE	IMOVE SOURCE OPERAND TO SOURCE
000224	012702	146314	MOV	#146314,DEST	IMOVE DESTINATION OPERAND TO DESTINATION
000228	000257		SCC		
000232	074002		XOR	SOURCE,DEST	IXOR SOURCE AND DESTINATION
000236	103001		BCC	,+4	
000240	000000		HLT		IFPROM CARRY CHANGED
000244	102001		BVC	,+4	
000248	000000		HLT		IFPROM V FAILED TO CLEAR
000252	020227	177777	CMF	DEST,#-1	IFPST=-1
000256	001401		BEO	,+4	
000260	000000		HLT		1031463 XOR 146314 FAILED
	000111		Y=Y+1		
	000002		SOURCE=X2		
	000000		DEST=X0		
	000002		RSLT=X2		
	000294	010701	SCOPE		
000298	012702	146314	MOV	#146314,SOURCE	IMOVE SOURCE OPERAND TO SOURCE
000302	012702	031463	MOV	#031463,DEST	IMOVE DESTINATION OPERAND TO DESTINATION
000306	000277		SCC		
000310	074200		XOR	SOURCE,DEST	IXOR SOURCE AND DESTINATION
000314	103401		BCC	,+4	
000318	000000		HLT		IFPROM CARRY CHANGED
000322	102001		BVC	,+4	
000326	000000		HLT		IFPROM V FAILED TO CLEAR
000330	020027	177777	CMF	DEST,#-1	IFPST=-1
000334	001401		BEO	,+4	
000338	000000		HLT		1146314 XOR 031463 FAILED
	000112		Y=Y+1		
	000003		SOURCE=X3		
	000312	010701	SCOPE		
000316	012702	016161	MOV	#016161,SOURCE	IMOVE SOURCE OPERAND TO SOURCE
000320	012702	161616	MOV	#161616,DEST	IMOVE DESTINATION OPERAND TO DESTINATION
000324	000257		SCC		
000328	074300		XOR	SOURCE,DEST	IXOR SOURCE AND DESTINATION
000332	103001		BCC	,+4	
000336	000000		HLT		IFPROM CARRY CHANGED
000340	102001		BVC	,+4	
000344	000000		HLT		IFPROM V FAILED TO CLEAR
000348	020027	177777	CMF	DEST,#-1	IFPST=-1
000352	001401		BEO	,+4	
000356	000000		HLT		1016161 XOR 161616 FAILED
	000113		Y=Y+1		
	000009		SOURCE=X5		

```

005350 210701 SCOPE
005352 212773 161616 MOV #161616,SOURCE IMOVE SOURCE OPERAND TO SOURCE
005356 212775 216161 MOV #216161,DEST IMOVE DESTINATION OPERAND TO DESTINATION
005362 200277 SCC
005364 274305 XOR SOURCE,DEST IXOR SOURCE AND DESTINATION
005366 103421 BCS ,+4
005370 200000 HLT IERRORI CARRY CHANGED
005372 102021 BVC ,+4
005374 200000 HLT IERRORI V FAILED TO CLEAR
005376 220527 177777 CMP DEST,#-1 IDEST=-17
005402 201401 BEQ ,+4
005404 200000 HLT I161616 XOR 216161 FAILED
    
```

Y=Y+1  
 DEST=X0

TEST THAT CONDITION CODES ARE OPERATED ON CORRECTLY  
 CARRY REMAINS UNCHANGED (CARRY SET)

```

005406 210701 SCOPE
005410 212772 177777 MOV #-1,X2
005414 212730 177777 MOV #-1,X0
005420 200261 SEC ISET CARRY
005422 274200 XOR X2,X0 IXOR
005424 103421 BCS ,+4 IBRANCH IF CARRY IS SET
005426 200000 HLT
    
```

TEST /N/

```

005430 210701 SCOPE
005432 212700 100000 MOV #100000,X0
005436 200002 CLR X2
005440 274200 XOR X2,X0
005442 100401 BMI ,+4
005444 200000 HLT IERRORI /N/ FAILED TO SET
    
```

SCOPE

```

005446 210701 SCOPE
005450 212772 100000 MOV #100000,X2
005454 212720 177777 MOV #-1,X0
005460 200261 CLN
005462 274200 XOR X2,X0
005464 100001 RPL ,+4
005466 200000 HLT IERRORI /N/ FAILED TO CLEAR
    
```

CARRY REMAINS UNCHANGED (CARRY CLEAR)

```

005470 210701 SCOPE
005472 212772 177777 MOV #-1,X2
005476 212700 177777 MOV #-1,X0
005502 200261 CLC ICLEAR CARRY
005504 274200 XOR X2,X0 IXOR
005506 103001 BCC ,+4 IBRANCH IF CARRY IS CLEAR
005510 200000 HLT
    
```

N=1

TEST THAT Z BIT IS CLEAR IF RESULT IS NOT ZERO

```

005512 210701 SCOPE
005514 212770 200001 MOV #1,DEST
005520 200264 SEZ ISET Z BIT
005522 274300 XOR SOURCE,DEST IXOR
005524 201001 BNE ,+4
005526 200000 HLT IB XOR 1 FAILED TO CLEAR Z

005530 210701 N=N+N SCOPE
005532 212700 200002 MOV #2,DEST
005536 200264 SEZ ISET Z BIT
005540 274300 XOR SOURCE,DEST IXOR
005542 201001 BNE ,+4
005544 200000 HLT IB XOR 2 FAILED TO CLEAR Z

005546 210701 N=N+N SCOPE
005550 212700 200004 MOV #4,DEST
005554 200264 SEZ ISET Z BIT
005556 274300 XOR SOURCE,DEST IXOR
005560 201001 BNE ,+4
005562 200000 HLT IB XOR 4 FAILED TO CLEAR Z

005564 210701 N=N+N SCOPE
005566 212700 200010 MOV #10,DEST
005572 200264 SEZ ISET Z BIT
005574 274300 XOR SOURCE,DEST IXOR
005576 201001 BNE ,+4
005580 200000 HLT IB XOR 10 FAILED TO CLEAR Z

005602 210701 N=N+N SCOPE
005604 212700 200020 MOV #20,DEST
005610 200264 SEZ ISET Z BIT
005612 274300 XOR SOURCE,DEST IXOR
005614 201001 BNE ,+4
005616 200000 HLT IB XOR 20 FAILED TO CLEAR Z

005620 210701 N=N+N SCOPE
005622 212700 200040 MOV #40,DEST
005626 200264 SEZ ISET Z BIT
005630 274300 XOR SOURCE,DEST IXOR
005632 201001 BNE ,+4
005634 200000 HLT IB XOR 40 FAILED TO CLEAR Z

005636 210701 N=N+N SCOPE
005640 212700 200100 MOV #100,DEST
005644 200264 SEZ ISET Z BIT
005646 274300 XOR SOURCE,DEST IXOR
005650 201001 BNE ,+4
    
```

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025652	000000		HLT		10 XOR 100 FAILED TO CLEAR Z
	200200		N#N+N		
025654	010701		SCOPE		
025656	012700	002200	MOV	#200,DEST	
025662	000264		SEL		ISET Z BIT
025664	074300		XOR	SOURCE,DEST	IXOR
025666	001001		BNE	,+4	
025670	000000		HLT		10 XOR 200 FAILED TO CLEAR Z
	002400		N#N+N		
025672	010701		SCOPE		
025674	012700	000400	MOV	#400,DEST	
025708	000264		SEL		ISET Z BIT
025702	074300		XOR	SOURCE,DEST	IXOR
025704	001001		BNE	,+4	
025706	000000		HLT		10 XOR 400 FAILED TO CLEAR Z
	001000		N#N+N		
025710	010701		SCOPE		
025712	012700	001000	MOV	#1000,DEST	
025716	000264		SEL		ISET Z BIT
025720	074300		XOR	SOURCE,DEST	IXOR
025722	001001		BNE	,+4	
025724	000000		HLT		10 XOR 1000 FAILED TO CLEAR Z
	002000		N#N+N		
025726	010701		SCOPE		
025730	012700	002000	MOV	#2000,DEST	
025734	000264		SEL		ISET Z BIT
025736	074300		XOR	SOURCE,DEST	IXOR
025740	001001		BNE	,+4	
025742	000000		HLT		10 XOR 2000 FAILED TO CLEAR Z
	004000		N#N+N		
025744	010701		SCOPE		
025746	012700	004000	MOV	#4000,DEST	
025752	000264		SEL		ISET Z BIT
025754	074300		XOR	SOURCE,DEST	IXOR
025756	001001		BNE	,+4	
025760	000000		HLT		10 XOR 4000 FAILED TO CLEAR Z
	010000		N#N+N		
025762	010701		SCOPE		
025764	012700	010000	MOV	#10000,DEST	
025770	000264		SEL		ISET Z BIT
025772	074300		XOR	SOURCE,DEST	IXOR
025774	001001		BNE	,+4	
025776	000000		HLT		10 XOR 10000 FAILED TO CLEAR Z
	020000		N#N+N		
026000	010701		SCOPE		
026002	012700	020000	MOV	#20000,DEST	
026006	000264		SEL		ISET Z BIT

026010	074300		XOR	SOURCE,DEST	IXOR
026012	001001		BNE	,+4	
026014	000000		HLT		10 XOR 20000 FAILED TO CLEAR Z
	040000		N#N+N		
026016	010701		SCOPE		
026020	012700	040000	MOV	#40000,DEST	
026024	000264		SEL		ISET Z BIT
026026	074300		XOR	SOURCE,DEST	IXOR
026030	001001		BNE	,+4	
026032	000000		HLT		10 XOR 40000 FAILED TO CLEAR Z
	100000		N#N+N		
026034	010701		SCOPE		
026036	012700	100000	MOV	#100000,DEST	
026042	000264		SEL		ISET Z BIT
026044	074300		XOR	SOURCE,DEST	IXOR
026046	001001		BNE	,+4	
026050	000000		HLT		10 XOR 100000 FAILED TO CLEAR Z
	000000		N#N+N		
026052	000001		HLT		
	010701		N#1		
	012702	125252	I2 BIT IS SET IF RESULT=0		
026054	012700	125252	SCOPE		
026056	000264		MOV	#125252,X2	
026060	074200		MOV	#125252,X0	
026062	001401		CLR		I CLEAR Z
026064	000000		XOR	X2,X0	IXOR
026066	000000		BLD	,+4	I BRANCH IF Z IS SET
026068	000000		HLT		
	010701		N#1		
	012702	125252	IV IS CLEARED		
026074	012700	025252	SCOPE		
026076	000262		MOV	#125252,X2	
026078	074200		MOV	#025252,X0	
026080	000000		SLV		
026082	074200		XOR	X2,X0	IXOR
026084	102001		RYC	,+4	I BRANCH IF V IS CLEARED
026086	000000		HLT		
	010701		N#1		
	012702	125252	SCOPE		
026090	012700	125252	MOV	#025252,X2	
026092	000262		MOV	#125252,X0	
026094	074200		SLV		ISET V
026096	102001		XOR	X2,X0	IXOR
026098	000000		RYC	,+4	I BRANCH IF V IS CLEARED
026100	000000		HLT		
	010701		N#1		
	005002		SCOPE		
026102	005000		CLR	X2	
026104	005000		CLR	X0	
026106	000262		SLV		
026108	074200		XOR	X2,X0	IXOR
026110	102001		RYC	,+4	
026112	000000		HLT		
	010701		N#1		
	005002		SCOPE		
026114	005000		CLR	X2	
026116	005000		CLR	X0	
026118	000262		SLV		
026120	074200		XOR	X2,X0	IXOR
026122	102001		BVC	,+4	

```

006154 000000 HLT
006156 010701 SCOPE ERROR: V FAILED TO CLEAR

006160 012702 177777 MOV #=1,X2
006164 012700 177777 MOV #=1,X0
006170 000262 SEV
006172 074200 XOR X2,X0
006174 102003 BVC ,+10
006176 000000 HLT ERROR: V FAILED TO CLEAR

000002 SOURCE=X2
006200 DEST=,
006202 ,=,+2
006202 000000 ITEST: 0
ITEST DESTINATION MODE=67
006204 010701 SCOPE
006206 012702 177777 MOV #=1,SOURCE ;MOVE SOURCE OPERAND TO SOURCE
006212 012707 177777 177740 MOV #=1,DLST ;MOVE DESTINATION OPERAND TO DESTINATION
006220 000257 CCC
006222 074267 177792 XOR SOURCE,DEST ;XOR SOURCE AND DESTINATION
006226 103001 HCC ,+4
006230 000000 HLT ERROR: CARRY CHANGED
006232 102001 BVC ,+4
006234 000000 HLT ERROR: V FAILED TO CLEAR
006236 026727 177736 000000 CMP DEST,#0 ;DEST=0?
006244 001401 BEQ ,+4
006246 000000 HLT ;=1 XOR -1 FAILED

000115 Y=Y+1

ITEST DESTINATION MODE=27
006250 010701 SCOPE
006252 012702 177777 MOV #=1,X2
006256 012767 177777 000002 MOV #=1,MODE27
006264 074227 XOR X2,(7)*
006266 000000 MODE27: 0
006270 001401 BEQ ,+4
006272 000000 HLT

ITEST DESTINATION MODE 37
006274 010701 SCOPE
006276 012702 177777 MOV #=1,X2
006322 012737 177777 006200 MOV #=1,MODE37
006310 074237 006200 XOR X2,MODE37
006314 001401 BEQ ,+4
006316 000000 HLT

ITEST DESTINATION MODE 77
006320 010701 SCOPE
006322 012767 006202 177650 MOV #IDEST,DEST
006332 012702 177777 MOV #=1,X2
006334 012777 177777 177630 MOV #=1,MODE77
006342 074277 177632 XOR X2,MODE77
006346 001401 BEQ ,+4
    
```

CKUCA

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006350 000000 HLT

ITEST DESTINATION MODE 1
006352 010701 SCOPE
006354 012700 006750 MOV #MODE1,X0
006360 012702 177777 MOV #=1,X2
006364 012710 177770 MOV #=2,(0)
006372 074210 XOR X2,(0)
006372 022767 000001 000350 CMP #1,MODE1 ;CHECK RESULT
006400 001401 BEQ ,+4
006402 000000 HLT ERROR: INCORRECT RESULT
006404 022720 006750 CMP #MODE1,X0 ;CHECK REGISTER
006410 001401 BEQ ,+4
006412 000000 HLT ERROR: R0 CHANGED
006414 022702 177777 CMP #=1,X2 ;CHECK REGISTER
006420 001401 BEQ ,+4
006422 000000 HLT ERROR: R2 CHANGED

ITEST DESTINATION MODE 2
006424 010701 SCOPE
006426 012707 006750 MOV #MODE1,X2
006432 012703 000002 MOV #2,X3
006434 000012 CLR (2)
006440 074122 XOR X3,(2)*
006442 012767 000004 000300 CMP #2,MODE1 ;CHECK RESULT
006470 001401 BEQ ,+4
006452 000000 HLT ERROR: INCORRECT RESULT
006454 022702 006752 CMP #MODE1+2,X2 ;TEST AUTO-INCREMENT
006460 001401 BEQ ,+4
006462 000000 HLT ERROR: AUTO-INCREMENT FAILED

ITEST DESTINATION MODE 3
006464 010701 SCOPE
006466 012767 006752 000254 MOV #MODE3,MODE1
006474 012774 006750 MOV #MODE1,X4
006500 012703 000003 MOV #3,X3
006504 000007 000242 CLR MODE3
006510 074334 XOR X3,(4)*
006512 022767 000003 000232 CMP #3,MODE3
006540 001401 BEQ ,+4
006522 000000 HLT ERROR: INCORRECT RESULT
006524 022774 006752 CMP #MODE1+2,X4 ;TEST AUTO-INCREMENT
006530 001401 BEQ ,+4
006532 000000 HLT ERROR: AUTO-INCREMENT DECREMENT FAILED

ITEST DESTINATION MODE 4
006534 010701 SCOPE
006536 012726 000004 MOV #4,(0)*
006542 000005 CLR X5
006544 074540 XOR X5,-(6)
006546 022716 000004 CMP #4,(6)
006552 001401 BEQ ,+4
006554 000000 HLT ;ERROR: INCORRECT RESULT
    
```

```

006556 012706 000908          MOV    #STKPTH,X6      IRFSTORE STACK PTR

      ITEST DESTINATION MODE 5
006562 010701          SCOPE
006564 012747 006750 000160      MOV    #MODE1,MODEF3
006572 012702 006754          MOV    #MODE3+2,X2
006576 012747 000009 000144      MOV    #5,MODE1
006604 009000          CLR    %0
006606 074052          XOR    X0,-(2)
006610 022747 000005 000132      CMP    #5,MODE1
006616 001401          BLO   ,+4
006620 000000          HLT
      IERROR: INCORRECT RESULT
006622 022702 006752          CMP    #MODE3,X2
      ICHECK REGISTER
006626 001401          BLO   ,+4
006630 000000          HLT
      IERROR: REGISTER CONTENTS CHANGED

      ITEST DESTINATION MODE 6
006632 010701          SCOPE
006634 005067 000110          CLR    MODE1
006640 012702 000006          MOV    #6,X2
006644 012703 006752          MOV    #MODE3,X3
006650 074263 177776          XOR    X2,-2(3)
006654 022747 000006 000066      CMP    #6,MODE1
006662 001401          BEQ   ,+4
006664 000000          HLT
      IERROR: INCORRECT RESULT
006666 022703 006752          CMP    #MODE3,X3
      ICHECK REGISTER
006672 001401          BEQ   ,+4
006674 000000          HLT
      IERROR: REGISTER CONTENTS CHANGED

      ITEST DESTINATION MODE 7
006676 010701          SCOPE
006700 012747 006752 000042      MOV    #MODE3,MODE1
006706 012705 177776          MOV    #-10,X5
006712 012747 177777 000032      MOV    #-1,MODE3
006720 074575 006760          XOR    X5,#MODE1+10(5)
006724 022747 000007 000020      CMP    #7,MODE3
006732 001401          BEQ   ,+4
006734 000000          HLT
      IERROR: INCORRECT RESULT
006736 022705 177770          CMP    #-10,X5
006742 001401          BEQ   ,+4
006744 000000          HLT
      IERROR: REGISTER CONTENTS CHANGED
006746 000422          BR    ,+6
      IGO TO NEXT TEST
006750 000000      MODE1 0
006752 000000      MODE3 0

      ITEST CONDITION CODES WHEN DESTINATION MODE IS NOT = 0
      IN=0,Z=0,V=0,C=0
006754 010701          SCOPE
006756 012700 000001          MOV    #1,X0
006762 012747 000002 177760      MOV    #2,MODE1
006770 012702 006750          MOV    #MODE1,X2
006774 000277          SCC
006776 000241          CLC
    
```

```

007000 074022          XOR    X0,(2)+
007002 016700 170770          MOV    PSW,X0
007006 022700 000000          CMP    #0,X2
007012 001401          BEQ   ,+4
007014 000000          HLT

      IN=0,Z=0,V=0,C=1
007016 010701          SCOPE
007020 012700 000002          MOV    #2,X0
007024 012747 000004 177710      MOV    #4,MODE1
007032 012703 006752          MOV    #MODE3,X3
007036 000277          SCC
007040 074043          XOR    X0,-(3)
007042 016700 170730          MOV    PSW,X0
007046 022700 000001          CMP    #1,X0
007052 001401          BEQ   ,+4
007054 000000          HLT

      IN=0,Z=1,V=0,C=0
007056 012701          SCOPE
007060 012747 177777 177662      MOV    #-1,MODE1
007066 012702 177777          MOV    #-1,X2
007072 012747 000012 170670      MOV    #12,PSW
007100 074267 177644          XOR    X2,MODE1
007104 016700 170666          MOV    PSW,X0
007110 022700 000004          CMP    #4,X0
007114 001401          BEQ   ,+4
007116 000000          HLT
      IERROR: INCORRECT CONDITION CODES

      IN=0,Z=1,V=0,C=1
007120 010701          SCOPE
007122 012747 177777 177000      MOV    #-1,MODE1
007130 012704 177777          MOV    #-1,X4
007134 012702 006750          MOV    #MODE1,X2
007142 000277          SCC
007144 000244          CLZ
007146 074412          XOR    X4,(2)
007148 016700 170624          MOV    PSW,X0
007152 022700 000005          CMP    #5,X0
007156 001401          BEQ   ,+4
007162 000000          HLT
      IERROR: INCORRECT CONDITION CODES

      IN=1,Z=0,V=0,C=0
007162 010701          SCOPE
007164 012700 177777          MOV    #1,X0
007172 012747 077777 177552      MOV    #77777,MODE1
007176 012747 000006 170572      MOV    #6,PSW
007204 074267 177540          XOR    X0,MODE1
007210 016700 170562          MOV    PSW,X0
007214 022700 000010          CMP    #10,X0
007220 001401          BEQ   ,+4
007222 000000          HLT
      IERROR: INCORRECT CONDITION CODES
    
```

```

        IN=1,2=0,V=0,C=1
007224 010701          SCOPE
007226 012700 077777  MOV      #77777,X3
007232 012767 177777  MOV      #-1,MODE1
007240 012767 000207 170530  MOV      #7,PSW
007246 074057 177476  XOR      X0,MODE1
007252 016700 170520  MOV      PSW,X0
007256 022700 000011  CMP      #11,X0
007262 001401  HLT
007264 000000  HLT
                                IERROR: INCORRECT CONDITION CODES

        N=2
        SOURCE=X2
        ITEST THAT XOR OPERATES PROPERLY WHEN THE DESTINATION IS THE PSW,
007266 010701          SCOPE
007270 012767 007322 170516  MOV      #TBITSLT,14  ISET UP TRACE TRAP VECTOR
007276 005067 170514  CLR      16           ITO TRAP TO 'TBITSET'
007302 012702 000377  MOV      #377,X2
007306 003067 170464  CLR      PSW          ICLEAR THE PSW
007312 074267 170460  XOR      X2,PSW       IXOR THE #377 INTO A CLEAR PSW
007316 000240  NOP
007320 000000  BR
007322 000000  HLT
                                I+4
007324 022767 000357 170444  TBITSET: HLT
007332 001401  CMP      #357,PSW     IT BIT SLT
007334 000000  BLO     ,+4          IODD ALL BUT THE T BIT SET?
                                IPSW IS NOT CLEAR

007336 010701          SCOPE
007340 012702 000357  MOV      #357,X2
007344 012767 000357 170424  MOV      #357,PSW     IPRE SET THE STATUS WORD
007352 074267 170420  XOR      X2,PSW
007356 016700 170414  MOV      PSW,X0
007362 001401  BEO     ,+4          IGET RESULT
007364 000000  HLT
                                IERROR XOR #357,#357 FAILED TO CLEAR PSW
007366 005267 171406  ENDI  INC  ICNT      IINCREMENT THE PASS COUNT
007372 026707 171422 100000  CMP      ICNT,#100000
007420 001402  BEO     DONE
007422 000167 171400  BEO     DONE          IGO TO DONE IF 1000 PASSES COMPLETED
007426 012767 000007 170152  DONE1: MOV      #7,TPBUF  IRESTART PROGRAM
007414 100707 170144  JMB     TPCSH        ITRING RECALL
                                I+4
007422 013702 000042  MOV      ##42,X2     IGET DECTAPE MONITOR RETURN ADDRESS
007426 001404  BEO     DONE1       IDO NOT RETURN IF (42)40
007430 004712  JMB     7,(42)     IRETURN TO DECTAPE MONITOR
007432 000240  NOP
007434 000240  NOP
007436 000240  NOP
007440 000167 171330  DONE1: JMP      START
                                I+4
                                IEND
    
```

BEGIN	001000	DEST	000200	DISPLA	177570	DONE	007400
DONE1	007440	END	007300	HLT	000200	ICNT	001000
TEST	000200	MODE1	000700	MODE27	000200	MODE17	000700
N	000000	PSW	17777A	PSLT	000200	SCDF	000700
SOURCE	001000	START	001000	SIXPTH	000500	TWO	177570
TBITSLT	007322	TPBUF	177500	TPC5H	177500	UNPLAK	177770
V	000110		007444				

LNKERR DETECTED: 0

1

,TITLE MAINDEC-11-DCKRD-B 11/45,11/48 MARK TEST  
,NLIST MC,MD,SEQ  
,LIST MC  
,ABS

TEST DCKRD- TEST OF THE MARK INSTRUCTION  
OPERATIONS  
TEMP = PC+2\*N  
PC = R5  
R6 = TEMP  
R5 = (TEMP)           ITEMP HOLDS A MEMORY ADDRESS  
R6 = TEMP\*2

STARTING PROCEDURE  
LOAD ADDRESS=200  
PRESS START  
STACK POINTER IS SET AT 500  
BELL WILL RING WHEN TEST IS COMPLETE

EQUATE STATEMENTS  
R0=X0  
R1=X1  
R2=X2  
R3=X3  
R4=X4  
R5=X5  
SP=X6            STACK POINTER  
PC=X7            PROGRAM COUNTER

000000  
000001  
000002  
000003  
000004  
000005  
000006  
000007  
  
010701           SCOPE=010701           MOVE PC TO R1  
000010           HLT=HALT           ERROR HALT  
177776           PSW=177776           PROCESSOR STATUS WORD ADDRESS  
177770           UBREAK=177770       ADDRESS OF POP11/45 MICRO BREAK REGISTER  
177564           TPCSR=177564       ADDRESS OF TELEPRINTER CSR  
177566           TPRUF=177566  
177570           SWR=177570       ADDRESS OF CONSOLE SWITCH REGISTER  
177570           DISPLAY=177570      ADDRESS OF CONSOLE DISPLAY REGISTER

000500           \*\*\*\*\*INITIAL STACK POINTER=2500\*\*\*\*\*  
STKPTR=0500           INITIAL STACK SETTING

004400           MARK EQUATE  
004401           MARK=MARK+0  
004402           MARK1=MARK+1  
                  MARK2=MARK+2

000000           ,=P  
,NLIST MC,MD,SEQ  
  
000000 000002           ,=2  
000002 000000           HALT  
000004 000006           ,=2  
000006 000000           HALT  
000010 000012           ,=2  
000012 000000           HALT  
000014 000016           ,=2  
000016 000000           HALT  
000020 000022           ,=2  
000022 000000           HALT  
000024 000026           ,=2  
000026 000000           HALT  
000030 000032           ,=2  
000032 000000           HALT  
000034 000036           ,=2  
000036 000000           HALT  
000040 000042           ,=2  
000042 000000           HALT  
000044 000046           ,=2  
000046 000000           HALT  
000050 000052           ,=2  
000052 000000           HALT  
000054 000056           ,=2  
000056 000000           HALT  
000060 000062           ,=2  
000062 000000           HALT  
000064 000066           ,=P  
000066 000000           HALT  
000070 000072           ,=2  
000072 000000           HALT  
000074 000076           ,=2  
000076 000000           HALT  
000100 000102           ,=2  
000102 000000           HALT  
000104 000106           ,=2  
000106 000000           HALT  
000110 000112           ,=2  
000112 000000           HALT  
000114 000116           ,=2  
000116 000000           HALT  
000120 000122           ,=2  
000122 000000           HALT  
000124 000126           ,=2  
000126 000000           HALT  
000130 000132           ,=2  
000132 000000           HALT  
000134 000136           ,=2  
000136 000000           HALT  
000140 000142           ,=2

000142	000000	HALT
000144	000146	,+2
000146	000000	HALT
000148	000152	,+2
000152	000000	HALT
000154	000156	,+2
000156	000000	HALT
000158	000162	,+2
000162	000000	HALT
000164	000166	,+2
000166	000000	HALT
000170	000172	,+2
000172	000000	HALT
000174	000176	,+2
000176	000000	HALT
000200	000202	,+2
000202	000000	HALT
000204	000206	,+2
000206	000000	HALT
000210	000212	,+2
000212	000000	HALT
000214	000216	,+2
000216	000000	HALT
000220	000222	,+2
000222	000000	HALT
000224	000226	,+2
000226	000000	HALT
000230	000232	,+2
000232	000000	HALT
000234	000236	,+2
000236	000000	HALT
000240	000242	,+2
000242	000000	HALT
000244	000246	,+2
000246	000000	HALT
000250	000252	,+2
000252	000000	HALT
000254	000256	,+2
000256	000000	HALT
000260	000262	,+2
000262	000000	HALT
000264	000266	,+2
000266	000000	HALT
000270	000272	,+2
000272	000000	HALT
000274	000276	,+2
000276	000000	HALT
000300	000302	,+2
000302	000000	HALT
000304	000306	,+2
000306	000000	HALT
000310	000312	,+2
000312	000000	HALT
000314	000316	,+2

000316	000000	HALT
000320	000322	,+2
000322	000000	HALT
000324	000326	,+2
000326	000000	HALT
000330	000332	,+2
000332	000000	HALT
000334	000336	,+2
000336	000000	HALT
000340	000342	,+2
000342	000000	HALT
000344	000346	,+2
000346	000000	HALT
000350	000352	,+2
000352	000000	HALT
000354	000356	,+2
000356	000000	HALT
000360	000362	,+2
000362	000000	HALT
000364	000366	,+2
000366	000000	HALT
000370	000372	,+2
000372	000000	HALT
000374	000376	,+2
000376	000000	HALT

.LIST ME



```

      #28R
00200 00167 00576      JHP      START
      #180R
00100 00000      ICNT: 0      JCONTAINS PASS COUNT
00102 00007 17772      START: CLR      ICNT      ICLEAR PASS COUNT
00103 012776 00500      REGIN: MOV      #STKPTR,SP      ISFT STACK POINTER
00104 012777 17776 177570      MOV      ICNT,#0DISPLAY      JDISPLAY PASS COUNT
00105 032737 00400 177570      RIT      #400,#5WR      JLOAD PDP11/45 MICRO BREAK REGISTER
00106 001403      REQ      .+1R
001030 11737 177570 177770      MOV8      #5WR,#0BREAK      JLOAD MICRO BREAK REG WITH SR0=7

      JTEST THAT THE OPERATION PC=R5 IS PERFORMED
001036 010701      SCOPE
001040 012776 00500      MOV      #STKPTR,SP      JINITIALIZE THE STACK POINTER
001044 012775 001054      MOV      #T0A,R5      JLOAD R5
001050 004400      MARK      0      JMARK N=0
001052 000000      HALT
001054 000240      T0A:  NOP      JPC=R5 FAILED
                                JPC=R5 OK

      JTEST THAT THE STACK POINTER (R6) CONTAINS THE PROPER VALUE
001056 010701      SCOPE
001060 012776 00500      MOV      #STKPTR,SP      JINITIALIZE THE STACK POINTER
001064 012775 001072      MOV      #T1A,R5      JLOAD R5
001070 004400      MARK      0      JMARK N=0

      JFUNCTION RESULTS N=0
      JTEMP = PC*(2*N)      TEMP = #T1A*(2*N) = #T1A
      JPC = R5      PC = #T1A
      JSP = TEMP      SP = #T1A*(2*N) = #T1A
      JR5 = (TEMP)      R5 = (#T1A*(2*N)) = (T1A)      NOT TESTED
      JSP = TEMP*2      SP = #T1A*(2*N)*2 = #T1A*2      TESTED

001072 010600      T1A:  MOV      SP,R0      JGET RESULTANT SP
001074 022776 001074      CMP      #2*2+T1A,SP      JSP = #T1A*(2*0)+2? NOTE:
001100 001401      BEQ      .+4      J#2*2+T1A = #T1A*(2*0)*2
001102 000000      HLT      JERROR INCORRECT RESULTANT SP

      JTEST THAT R5 IS PROPERLY LOADED
001104 010701      SCOPE
001106 012776 00500      MOV      #STKPTR,SP
001112 012775 001120      MOV      #T2A,R5
001116 004400      MARK      0

      JFUNCTION RESULTS N=0
      JTEMP = PC*(2*N)      TEMP = #T2A*(2*N) = #T2A
      JPC = R5      PC = #T2A
      JSP = TEMP      SP = #T2A*(2*N) = #T2A
      JR5 = (TEMP)      R5 = (#T2A*(2*N)) = (T2A) = 24R (NOP)
      JSP = TEMP*2      SP = #T2A*(2*N)*2 = #T2A*2

001120 000240      T2A:  NOP
001122 010500      MOV      R5,R0      JGET CONTENTS OF R5
001124 022775 000240      CMP      #NOP,R5      JR5 = (#T2A*(2*N)) = (T2A)?

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001130 001401      REQ      .+4
001132 000000      HLT

      C=3
      M=0
      JTEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK
      JINSTRUCTION FOR ALL VALUES OF N (0-77)
001134 010701      SCOPE
001136 012776 00500      MOV      #STKPTR,SP      JINITIALIZE THE STACK POINTER
001142 012775 001150      MOV      #T3A,R5      JLOAD R5
001146 004400      MARK      0      JMARK N=0

      JFUNCTION RESULTS N=0
      JTEMP = PC*(2*N)      TEMP = #T3A*(2*0)
      JPC = R5      PC = T3A
      JSP = TEMP      SP = #T3A*(2*0)
      JR5 = (TEMP)      R5 = (#T3A*(2*0))
      JSP = TEMP*2      SP = #T3A*(2*0)*2

001150 010600      T3A:  MOV      SP,R0      JGET RESULTANT SP
001152 022776 001152      CMP      #2*0+T3A+2,SP      JSP = #T3A*(2*0)+2?
001156 001401      BEQ      .+4      JERROR INCORRECT RESULTANT SP
001160 000000      HLT

      C=C+1
      M=M+1
      JTEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK
      JINSTRUCTION FOR ALL VALUES OF N (0-77)
001162 010701      SCOPE
001164 012776 00500      MOV      #STKPTR,SP      JINITIALIZE THE STACK POINTER
001170 012775 001176      MOV      #T4A,R5      JLOAD R5
001174 004401      MARK      1      JMARK N=1

      JFUNCTION RESULTS N=1
      JTEMP = PC*(2*N)      TEMP = #T4A*(2*1)
      JPC = R5      PC = T4A
      JSP = TEMP      SP = #T4A*(2*1)
      JR5 = (TEMP)      R5 = (#T4A*(2*1))
      JSP = TEMP*2      SP = #T4A*(2*1)*2

001176 010600      T4A:  MOV      SP,R0      JGET RESULTANT SP
001200 022776 001202      CMP      #2*1+T4A+2,SP      JSP = #T4A*(2*1)+2?
001204 001401      BEQ      .+4      JERROR INCORRECT RESULTANT SP
001206 000000      HLT

      C=C+1
      M=M+1
      JTEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK
      JINSTRUCTION FOR ALL VALUES OF N (0-77)
001210 010701      SCOPE
001212 012776 00500      MOV      #STKPTR,SP      JINITIALIZE THE STACK POINTER

```

001210 012775 001224  
001222 006402

MOV #T5A,R5 ILOAD R5  
MARK 2 IMARK N=2

.....  
IFUNCTION RESULTS N=2  
ITEMP = PC\*(2\*N)      TEMP = #T5A\*(2\*2)  
IPC = R5              PC = T5A  
ISP = TEMP            SP = #T5A\*(2\*2)  
IR5 = (TEMP)         R5 = (#T5A\*(2\*2))  
ISP = TEMP\*2         SP = #T5A\*(2\*2)\*2  
.....

001224 010600  
001226 022706 001232  
001232 001401  
001234 000000

T5A: MOV SP,R0 IGET RESULTANT SP  
CMP #2\*2\*T5A+2,SP ISP = #T5A\*(2\*2)\*2  
REQ ,\*4  
HLT IERROR INCORRECT RESULTANT SP

C=C+1  
M=M+1

ITEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK  
INSTRUCTION FOR ALL VALUES OF N (0-77)

SCOPE  
MOV #STKPTR,SP IINITIALIZE THE STACK POINTER  
MOV #T6A,R5 ILOAD R5  
MARK 3 IMARK N=3

001236 010701  
001240 012706 000500  
001244 012705 001252  
001250 006403

.....  
IFUNCTION RESULTS N=3  
ITEMP = PC\*(2\*N)      TEMP = #T6A\*(2\*3)  
IPC = R5              PC = T6A  
ISP = TEMP            SP = #T6A\*(2\*3)  
IR5 = (TEMP)         R5 = (#T6A\*(2\*3))  
ISP = TEMP\*2         SP = #T6A\*(2\*3)\*2  
.....

001252 010600  
001254 022706 001262  
001260 001401  
001262 000000

T6A: MOV SP,R0 IGET RESULTANT SP  
CMP #2\*3\*T6A+2,SP ISP = #T6A\*(2\*3)\*2  
REQ ,\*4  
HLT IERROR INCORRECT RESULTANT SP

C=C+1  
M=M+1

ITEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK  
INSTRUCTION FOR ALL VALUES OF N (0-77)

SCOPE  
MOV #STKPTR,SP IINITIALIZE THE STACK POINTER  
MOV #T7A,R5 ILOAD R5  
MARK 4 IMARK N=4

001264 010701  
001266 012706 000500  
001272 012705 001300  
001276 006404

.....  
IFUNCTION RESULTS N=4  
ITEMP = PC\*(2\*N)      TEMP = #T7A\*(2\*4)  
IPC = R5              PC = T7A  
ISP = TEMP            SP = #T7A\*(2\*4)  
.....

001300 010600  
001302 022706 001312  
001306 001401  
001310 000000

IR5 = (TEMP)         R5 = (#T7A\*(2\*4))  
ISP = TEMP\*2         SP = #T7A\*(2\*4)\*2  
.....

T7A: MOV SP,R0 IGET RESULTANT SP  
CMP #2\*4\*T7A+2,SP ISP = #T7A\*(2\*4)\*2  
REQ ,\*4  
HLT IERROR INCORRECT RESULTANT SP

C=C+1  
M=M+1

ITEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK  
INSTRUCTION FOR ALL VALUES OF N (0-77)

SCOPE  
MOV #STKPTR,SP IINITIALIZE THE STACK POINTER  
MOV #T8A,R5 ILOAD R5  
MARK 5 IMARK N=5

001312 010701  
001314 012706 000500  
001320 012705 001326  
001324 006405

.....  
IFUNCTION RESULTS N=5  
ITEMP = PC\*(2\*N)      TEMP = #T8A\*(2\*5)  
IPC = R5              PC = T8A  
ISP = TEMP            SP = #T8A\*(2\*5)  
IR5 = (TEMP)         R5 = (#T8A\*(2\*5))  
ISP = TEMP\*2         SP = #T8A\*(2\*5)\*2  
.....

001326 010600  
001330 022706 001342  
001334 001401  
001336 000000

T8A: MOV SP,R0 IGET RESULTANT SP  
CMP #2\*5\*T8A+2,SP ISP = #T8A\*(2\*5)\*2  
REQ ,\*4  
HLT IERROR INCORRECT RESULTANT SP

C=C+1  
M=M+1

ITEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK  
INSTRUCTION FOR ALL VALUES OF N (0-77)

SCOPE  
MOV #STKPTR,SP IINITIALIZE THE STACK POINTER  
MOV #T11A,R5 ILOAD R5  
MARK 6 IMARK N=6

001340 010701  
001342 012706 000500  
001346 012705 001354  
001352 006406

.....  
IFUNCTION RESULTS N=6  
ITEMP = PC\*(2\*N)      TEMP = #T11A\*(2\*6)  
IPC = R5              PC = T11A  
ISP = TEMP            SP = #T11A\*(2\*6)  
IR5 = (TEMP)         R5 = (#T11A\*(2\*6))  
ISP = TEMP\*2         SP = #T11A\*(2\*6)\*2  
.....

001354 010600  
001356 022706 001372  
001362 001401  
001364 000000

T11A: MOV SP,R0 IGET RESULTANT SP  
CMP #2\*6\*T11A+2,SP ISP = #T11A\*(2\*6)\*2  
REQ ,\*4  
HLT IERROR INCORRECT RESULTANT SP

000012  
000007  
  
001366 010701  
001370 012706 000500  
001374 012705 001402  
001400 006407

C=C+1  
M=M+1  
TEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK  
INSTRUCTION FOR ALL VALUES OF N (0-77)  
SCOPE  
MOV #STKPTR,SP INITIALIZE THE STACK POINTER  
MOV #T12A,R5 ILOAD R5  
MARK 7 IMARK N=7

.....  
IFUNCTION RESULTS N=7  
JTEMP = PC\*(2\*N) TEMP= #T12A\*(2\*7)  
JPC = R5 PC = T12A  
JSP = TEMP SP = #T12A\*(2\*7)  
JRS = (TEMP) R5 = (#T12A\*(2\*7))  
JSP = TEMP+2 SP = #T12A\*(2\*7)+2  
.....

001402 010600  
001404 022706 001422  
001410 001401  
001412 000000

T12A1 MOV SP,R0 IGET RESULTANT SP  
CMP #2\*7+T12A\*2,SP ISP = #T12A\*(2\*7)+2?  
BEQ .+4  
HLT IERROR INCORRECT RESULTANT SP

000013  
000010

C=C+1  
M=M+1  
TEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK  
INSTRUCTION FOR ALL VALUES OF N (0-77)  
SCOPE  
MOV #STKPTR,SP INITIALIZE THE STACK POINTER  
MOV #T13A,R5 ILOAD R5  
MARK 10 IMARK N=10

.....  
IFUNCTION RESULTS N=10  
JTEMP = PC\*(2\*N) TEMP= #T13A\*(2\*10)  
JPC = R5 PC = T13A  
JSP = TEMP SP = #T13A\*(2\*10)  
JRS = (TEMP) R5 = (#T13A\*(2\*10))  
JSP = TEMP+2 SP = #T13A\*(2\*10)+2  
.....

001430 010600  
001432 022706 001452  
001436 001401  
001440 000000

T13A1 MOV SP,R0 IGET RESULTANT SP  
CMP #2\*10+T13A\*2,SP ISP = #T13A\*(2\*10)+2?  
BEQ .+4  
HLT IERROR INCORRECT RESULTANT SP

000014  
000011

C=C+1  
M=M+1  
TEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK  
INSTRUCTION FOR ALL VALUES OF N (0-77)  
SCOPE  
MOV #STKPTR,SP INITIALIZE THE STACK POINTER  
MOV #T14A,R5 ILOAD R5

001442 010701  
001444 012706 000500  
001450 012705 001456

001454 006411

MARK 11 IMARK N=11

.....  
IFUNCTION RESULTS N=11  
JTEMP = PC\*(2\*N) TEMP= #T14A\*(2\*11)  
JPC = R5 PC = T14A  
JSP = TEMP SP = #T14A\*(2\*11)  
JRS = (TEMP) R5 = (#T14A\*(2\*11))  
JSP = TEMP+2 SP = #T14A\*(2\*11)+2  
.....

001456 010600  
001460 022706 001502  
001464 001401  
001466 000000

T14A1 MOV SP,R0 IGET RESULTANT SP  
CMP #2\*11+T14A\*2,SP ISP = #T14A\*(2\*11)+2?  
BEQ .+4  
HLT IERROR INCORRECT RESULTANT SP

000015  
000012

C=C+1  
M=M+1  
TEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK  
INSTRUCTION FOR ALL VALUES OF N (0-77)  
SCOPE  
MOV #STKPTR,SP INITIALIZE THE STACK POINTER  
MOV #T15A,R5 ILOAD R5  
MARK 12 IMARK N=12

.....  
IFUNCTION RESULTS N=12  
JTEMP = PC\*(2\*N) TEMP= #T15A\*(2\*12)  
JPC = R5 PC = T15A  
JSP = TEMP SP = #T15A\*(2\*12)  
JRS = (TEMP) R5 = (#T15A\*(2\*12))  
JSP = TEMP+2 SP = #T15A\*(2\*12)+2  
.....

001504 010600  
001506 022706 001532  
001512 001401  
001514 000000

T15A1 MOV SP,R0 IGET RESULTANT SP  
CMP #2\*12+T15A\*2,SP ISP = #T15A\*(2\*12)+2?  
BEQ .+4  
HLT IERROR INCORRECT RESULTANT SP

000016  
000013

C=C+1  
M=M+1  
TEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK  
INSTRUCTION FOR ALL VALUES OF N (0-77)  
SCOPE  
MOV #STKPTR,SP INITIALIZE THE STACK POINTER  
MOV #T16A,R7 ILOAD R5  
MARK 13 IMARK N=13

.....  
IFUNCTION RESULTS N=13  
JTEMP = PC\*(2\*N) TEMP= #T16A\*(2\*13)  
JPC = R5 PC = T16A  
JSP = TEMP SP = #T16A\*(2\*13)  
JRS = (TEMP) R5 = (#T16A\*(2\*13))  
.....

001516 010701  
001520 012706 000500  
001524 012705 001532  
001530 006413

```

ISP = TEMP+2          SP = #T16A*(2+13)+2
.....
T16A1  MOV    SP,R0          IGET RESULTANT SP
        CMP    #2+13*T16A+2,SP  ISP = #T16A*(2+13)+2?
        BEQ    ,+4
        HLT                    ERROR INCORRECT RESULTANT SP

        C=C+1
        M=M+1
I TEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK
I INSTRUCTION FOR ALL VALUES OF N (0-77)

        SCOPE
        MOV    #STKPTR,SP      I INITIALIZE THE STACK POINTER
        MOV    #T17A,R5        I LOAD R5
        MARK   14              I MARK N=14
.....
I FUNCTION RESULTS N=14
I TEMP = PC+(2*N)          TEMP = #T17A*(2+14)
I PC = R5                  PC = T17A
I SP = TEMP                SP = #T17A*(2+14)
I R5 = (TEMP)              R5 = (#T17A*(2+14))
I SP = TEMP+2              SP = #T17A*(2+14)+2
.....
T17A1  MOV    SP,R0          IGET RESULTANT SP
        CMP    #2+14*T17A+2,SP  ISP = #T17A*(2+14)+2?
        BEQ    ,+4
        HLT                    ERROR INCORRECT RESULTANT SP

        C=C+1
        M=M+1
I TEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK
I INSTRUCTION FOR ALL VALUES OF N (0-77)

        SCOPE
        MOV    #STKPTR,SP      I INITIALIZE THE STACK POINTER
        MOV    #T22A,R5        I LOAD R5
        MARK   15              I MARK N=15
.....
I FUNCTION RESULTS N=15
I TEMP = PC+(2*N)          TEMP = #T22A*(2+15)
I PC = R5                  PC = T22A
I SP = TEMP                SP = #T22A*(2+15)
I R5 = (TEMP)              R5 = (#T22A*(2+15))
I SP = TEMP+2              SP = #T22A*(2+15)+2
.....
T22A1  MOV    SP,R0          IGET RESULTANT SP
        CMP    #2+15*T22A+2,SP  ISP = #T22A*(2+15)+2?
        BEQ    ,+4
        HLT                    ERROR INCORRECT RESULTANT SP

```

```

.....
        C=C+1
        M=M+1
I TEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK
I INSTRUCTION FOR ALL VALUES OF N (0-77)

        SCOPE
        MOV    #STKPTR,SP      I INITIALIZE THE STACK POINTER
        MOV    #T21A,R5        I LOAD R5
        MARK   16              I MARK N=16
.....
I FUNCTION RESULTS N=16
I TEMP = PC+(2*N)          TEMP = #T21A*(2+16)
I PC = R5                  PC = T21A
I SP = TEMP                SP = #T21A*(2+16)
I R5 = (TEMP)              R5 = (#T21A*(2+16))
I SP = TEMP+2              SP = #T21A*(2+16)+2
.....
T21A1  MOV    SP,R0          IGET RESULTANT SP
        CMP    #2+16*T21A+2,SP  ISP = #T21A*(2+16)+2?
        BEQ    ,+4
        HLT                    ERROR INCORRECT RESULTANT SP

        C=C+1
        M=M+1
I TEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK
I INSTRUCTION FOR ALL VALUES OF N (0-77)

        SCOPE
        MOV    #STKPTR,SP      I INITIALIZE THE STACK POINTER
        MOV    #T22A,R5        I LOAD R5
        MARK   17              I MARK N=17
.....
I FUNCTION RESULTS N=17
I TEMP = PC+(2*N)          TEMP = #T22A*(2+17)
I PC = R5                  PC = T22A
I SP = TEMP                SP = #T22A*(2+17)
I R5 = (TEMP)              R5 = (#T22A*(2+17))
I SP = TEMP+2              SP = #T22A*(2+17)+2
.....
T22A1  MOV    SP,R0          IGET RESULTANT SP
        CMP    #2+17*T22A+2,SP  ISP = #T22A*(2+17)+2?
        BEQ    ,+4
        HLT                    ERROR INCORRECT RESULTANT SP

        C=C+1
        M=M+1
I TEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK
I INSTRUCTION FOR ALL VALUES OF N (0-77)

        SCOPE
        MOV    #STKPTR,SP      I INITIALIZE THE STACK POINTER
        MOV    #T23A,R5        I LOAD R5
        MARK   20              I MARK N=20

```

```

.....
IFUNCTION RESULTS N=20
ITEMP = PC*(2*N)      TEMP= #T23A*(2*20)
IPC = R5              PC = T23A
ISP = TEMP            SP = #T23A*(2*20)
IR5 = (TEMP)         R5 = (#T23A*(2*20))
ISP = TEMP+2         SP = #T23A*(2*20)+2
.....

```

```

001710 011600
001712 022776 001752
001716 001401
001720 000000

```

```

T23A1 MOV SP,R0      IGET RESULTANT SP
      CMP #2*20+T23A*2,SP  ISP = #T23A*(2*20)+2?
      BEQ .+4
      HLT              ERROR INCORRECT RESULTANT SP

```

```

C=C+1
M=M+1

```

```

ITEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK
IINSTRUCTION FOR ALL VALUES OF N (0-77)

```

```

001722 011701
001724 012706 000500
001730 012705 001736
001734 006421

```

```

SCOPE
MOV #STKPTR,SP      INITIALIZE THE STACK POINTER
MOV #T24A,R5        ILOAD R5
MARK 21             IMARK N=21

```

```

.....
IFUNCTION RESULTS N=21
ITEMP = PC*(2*N)      TEMP= #T24A*(2*21)
IPC = R5              PC = T24A
ISP = TEMP            SP = #T24A*(2*21)
IR5 = (TEMP)         R5 = (#T24A*(2*21))
ISP = TEMP+2         SP = #T24A*(2*21)+2
.....

```

```

001736 011600
001740 022776 002002
001744 001401
001746 000000

```

```

T24A1 MOV SP,R0      IGET RESULTANT SP
      CMP #2*21+T24A*2,SP  ISP = #T24A*(2*21)+2?
      BEQ .+4
      HLT              ERROR INCORRECT RESULTANT SP

```

```

C=C+1
M=M+1

```

```

ITEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK
IINSTRUCTION FOR ALL VALUES OF N (0-77)

```

```

001750 010701
001752 012706 000500
001756 012705 001764
001762 006422

```

```

SCOPE
MOV #STKPTR,SP      INITIALIZE THE STACK POINTER
MOV #T25A,R5        ILOAD R5
MARK 22             IMARK N=22

```

```

.....
IFUNCTION RESULTS N=22
ITEMP = PC*(2*N)      TEMP= #T25A*(2*22)
IPC = R5              PC = T25A
ISP = TEMP            SP = #T25A*(2*22)
IR5 = (TEMP)         R5 = (#T25A*(2*22))
ISP = TEMP+2         SP = #T25A*(2*22)+2
.....

```

```

.....
IFUNCTION RESULTS N=22
ITEMP = PC*(2*N)      TEMP= #T25A*(2*22)
IPC = R5              PC = T25A
ISP = TEMP            SP = #T25A*(2*22)
IR5 = (TEMP)         R5 = (#T25A*(2*22))
ISP = TEMP+2         SP = #T25A*(2*22)+2
.....

```

```

001764 011600
001766 022776 002032
001772 001401
001774 000000

```

```

T25A1 MOV SP,R0      IGET RESULTANT SP
      CMP #2*22+T25A*2,SP  ISP = #T25A*(2*22)+2?
      BEQ .+4
      HLT              ERROR INCORRECT RESULTANT SP

```

```

C=C+1
M=M+1

```

```

ITEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK
IINSTRUCTION FOR ALL VALUES OF N (0-77)

```

```

001776 010701
002000 012706 000500
002004 012705 002012
002010 006423

```

```

SCOPE
MOV #STKPTR,SP      INITIALIZE THE STACK POINTER
MOV #T26A,R5        ILOAD R5
MARK 23             IMARK N=23

```

```

.....
IFUNCTION RESULTS N=23
ITEMP = PC*(2*N)      TEMP= #T26A*(2*23)
IPC = R5              PC = T26A
ISP = TEMP            SP = #T26A*(2*23)
IR5 = (TEMP)         R5 = (#T26A*(2*23))
ISP = TEMP+2         SP = #T26A*(2*23)+2
.....

```

```

002012 010600
002014 022776 002062
002020 001401
002022 000000

```

```

T26A1 MOV SP,R0      IGET RESULTANT SP
      CMP #2*23+T26A*2,SP  ISP = #T26A*(2*23)+2?
      BEQ .+4
      HLT              ERROR INCORRECT RESULTANT SP

```

```

C=C+1
M=M+1

```

```

ITEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK
IINSTRUCTION FOR ALL VALUES OF N (0-77)

```

```

002024 010701
002026 012706 000500
002032 012705 002040
002036 006424

```

```

SCOPE
MOV #STKPTR,SP      INITIALIZE THE STACK POINTER
MOV #T27A,R5        ILOAD R5
MARK 24             IMARK N=24

```

```

.....
IFUNCTION RESULTS N=24
ITEMP = PC*(2*N)      TEMP= #T27A*(2*24)
IPC = R5              PC = T27A
ISP = TEMP            SP = #T27A*(2*24)
IR5 = (TEMP)         R5 = (#T27A*(2*24))
ISP = TEMP+2         SP = #T27A*(2*24)+2
.....

```

```

002040 011600
002042 022776 002112
002046 001401
002050 000000

```

```

T27A1 MOV SP,R0      IGET RESULTANT SP
      CMP #2*24+T27A*2,SP  ISP = #T27A*(2*24)+2?
      BEQ .+4
      HLT              ERROR INCORRECT RESULTANT SP

```

```

C=C+1

```

000025  
007052 017791  
002054 012776 000500  
002060 012795 002066  
002064 006425

M=M+1  
TEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK  
INSTRUCTION FOR ALL VALUES OF N (0-77)

SCOPE  
MOV #STKPTR,SP INITIALIZE THE STACK POINTER  
MOV #T32A,R5 ILOAD R5  
MARK 25 IMARK N=25

.....  
FUNCTION RESULTS N=25  
TEMP = PC+(2\*N) TEMP= #T32A\*(2\*25)  
IPC = R5 PC = T32A  
ISP = TEMP SP = #T32A\*(2\*25)  
IR5 = (TEMP) R5 = (#T32A\*(2\*25))  
ISP = TEMP+2 SP = #T32A\*(2\*25)+2  
.....

007064 010600  
002070 022776 002142  
002074 001401  
002076 000000

T32A1 MOV SP,R0 IGET RESULTANT SP  
CMP #2\*25+T32A\*2,SP ISP = #T32A\*(2\*25)+27  
BEQ ,\*4  
HLT ERROR INCORRECT RESULTANT SP

000031  
000070

C=C+1  
M=M+1  
TEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK  
INSTRUCTION FOR ALL VALUES OF N (0-77)

SCOPE  
MOV #STKPTR,SP INITIALIZE THE STACK POINTER  
MOV #T31A,R5 ILOAD R5  
MARK 26 IMARK N=26

.....  
FUNCTION RESULTS N=26  
TEMP = PC+(2\*N) TEMP= #T31A\*(2\*26)  
IPC = R5 PC = T31A  
ISP = TEMP SP = #T31A\*(2\*26)  
IR5 = (TEMP) R5 = (#T31A\*(2\*26))  
ISP = TEMP+2 SP = #T31A\*(2\*26)+2  
.....

002114 010600  
002110 022776 002172  
002122 001401  
002124 000000

T31A1 MOV SP,R0 IGET RESULTANT SP  
CMP #2\*26+T31A\*2,SP ISP = #T31A\*(2\*26)+27  
BEQ ,\*4  
HLT ERROR INCORRECT RESULTANT SP

000032  
000077

C=C+1  
M=M+1  
TEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK  
INSTRUCTION FOR ALL VALUES OF N (0-77)

SCOPE  
MOV #STKPTR,SP INITIALIZE THE STACK POINTER  
MOV #T32A,R5 ILOAD R5  
MARK 27 IMARK N=27

002126 017791  
002130 012776 000500  
002134 012795 002142  
002140 006427

002142 010600  
002144 022776 002222  
002150 001401  
002152 000000

.....  
FUNCTION RESULTS N=27  
TEMP = PC+(2\*N) TEMP= #T32A\*(2\*27)  
IPC = R5 PC = T32A  
ISP = TEMP SP = #T32A\*(2\*27)  
IR5 = (TEMP) R5 = (#T32A\*(2\*27))  
ISP = TEMP+2 SP = #T32A\*(2\*27)+2  
.....

T32A1 MOV SP,R0 IGET RESULTANT SP  
CMP #2\*27+T32A\*2,SP ISP = #T32A\*(2\*27)+27  
BEQ ,\*4  
HLT ERROR INCORRECT RESULTANT SP

000033  
000030

C=C+1  
M=M+1  
TEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK  
INSTRUCTION FOR ALL VALUES OF N (0-77)

SCOPE  
MOV #STKPTR,SP INITIALIZE THE STACK POINTER  
MOV #T33A,R5 ILOAD R5  
MARK 30 IMARK N=30

.....  
FUNCTION RESULTS N=30  
TEMP = PC+(2\*N) TEMP= #T33A\*(2\*30)  
IPC = R5 PC = T33A  
ISP = TEMP SP = #T33A\*(2\*30)  
IR5 = (TEMP) R5 = (#T33A\*(2\*30))  
ISP = TEMP+2 SP = #T33A\*(2\*30)+2  
.....

002170 010600  
002172 022776 002252  
002176 001401  
002200 000000

T33A1 MOV SP,R0 IGET RESULTANT SP  
CMP #2\*30+T33A\*2,SP ISP = #T33A\*(2\*30)+27  
BEQ ,\*4  
HLT ERROR INCORRECT RESULTANT SP

000034  
000071

C=C+1  
M=M+1  
TEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK  
INSTRUCTION FOR ALL VALUES OF N (0-77)

SCOPE  
MOV #STKPTR,SP INITIALIZE THE STACK POINTER  
MOV #T34A,R5 ILOAD R5  
MARK 31 IMARK N=31

.....  
FUNCTION RESULTS N=31  
TEMP = PC+(2\*N) TEMP= #T34A\*(2\*31)  
IPC = R5 PC = T34A  
ISP = TEMP SP = #T34A\*(2\*31)  
IR5 = (TEMP) R5 = (#T34A\*(2\*31))  
ISP = TEMP+2 SP = #T34A\*(2\*31)+2  
.....

002182 017791  
002184 012776 000500  
002188 012795 002210  
002214 006427

002210 010600  
002220 022776 002302  
002224 001401  
002226 000000  
  
000035  
000032

T34A1 MOV SP,R0 IGET RESULTANT SP  
CMP #2+31+T34A+2,SP ISP = #T34A+(2+31)+27  
REQ ,+4  
HLT IERROR INCCORRECT RESULTANT SP

C=C+1  
M=M+1

I TEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK  
I INSTRUCTION FOR ALL VALUES OF N (0-77)

002230 010771  
002232 012776 000500  
002236 012705 002244  
002242 006432

SCOPE  
MOV #STKPTR,SP I INITIALIZE THE STACK POINTER  
MOV #T35A,R5 I LOAD R5  
MARK 32 I MARK N=32

.....  
I FUNCTION RESULTS N=32  
I TEMP = PC+(2\*N) TEMP = #T35A+(2+32)  
I PC = R5 PC = T35A  
I SP = TEMP SP = #T35A+(2+32)  
I R5 = (TEMP) R5 = (#T35A+(2+32))  
I SP = TEMP+2 SP = #T35A+(2+32)+2  
.....

002244 010600  
002246 022776 002332  
002252 001401  
002254 000000  
  
000036  
000033

T35A1 MOV SP,R0 IGET RESULTANT SP  
CMP #2+32+T35A+2,SP ISP = #T35A+(2+32)+27  
REQ ,+4  
HLT IERROR INCCORRECT RESULTANT SP

C=C+1  
M=M+1

I TEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK  
I INSTRUCTION FOR ALL VALUES OF N (0-77)

002256 010771  
002260 012776 000500  
002264 012705 002272  
002270 006432

SCOPE  
MOV #STKPTR,SP I INITIALIZE THE STACK POINTER  
MOV #T36A,R5 I LOAD R5  
MARK 33 I MARK N=33

.....  
I FUNCTION RESULTS N=33  
I TEMP = PC+(2\*N) TEMP = #T36A+(2+33)  
I PC = R5 PC = T36A  
I SP = TEMP SP = #T36A+(2+33)  
I R5 = (TEMP) R5 = (#T36A+(2+33))  
I SP = TEMP+2 SP = #T36A+(2+33)+2  
.....

002272 010600  
002274 022776 002362  
002320 001401  
002322 000000  
  
000037  
000034

T36A1 MOV SP,R0 IGET RESULTANT SP  
CMP #2+33+T36A+2,SP ISP = #T36A+(2+33)+27  
REQ ,+4  
HLT IERROR INCCORRECT RESULTANT SP

C=C+1  
M=M+1

002304 010771  
002306 012776 000500  
002312 012705 002320  
002316 006432  
  
000040  
000035

I TEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK  
I INSTRUCTION FOR ALL VALUES OF N (0-77)

SCOPE  
MOV #STKPTR,SP I INITIALIZE THE STACK POINTER  
MOV #T37A,R5 I LOAD R5  
MARK 34 I MARK N=34

.....  
I FUNCTION RESULTS N=34  
I TEMP = PC+(2\*N) TEMP = #T37A+(2+34)  
I PC = R5 PC = T37A  
I SP = TEMP SP = #T37A+(2+34)  
I R5 = (TEMP) R5 = (#T37A+(2+34))  
I SP = TEMP+2 SP = #T37A+(2+34)+2  
.....

002320 010600  
002322 022776 002412  
002326 001401  
002330 000000  
  
000048  
000035

T37A1 MOV SP,R0 IGET RESULTANT SP  
CMP #2+34+T37A+2,SP ISP = #T37A+(2+34)+27  
REQ ,+4  
HLT IERROR INCCORRECT RESULTANT SP

C=C+1  
M=M+1

I TEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK  
I INSTRUCTION FOR ALL VALUES OF N (0-77)

002332 010771  
002334 012776 000500  
002340 012705 002346  
002344 006432

SCOPE  
MOV #STKPTR,SP I INITIALIZE THE STACK POINTER  
MOV #T40A,R5 I LOAD R5  
MARK 35 I MARK N=35

.....  
I FUNCTION RESULTS N=35  
I TEMP = PC+(2\*N) TEMP = #T40A+(2+35)  
I PC = R5 PC = T40A  
I SP = TEMP SP = #T40A+(2+35)  
I R5 = (TEMP) R5 = (#T40A+(2+35))  
I SP = TEMP+2 SP = #T40A+(2+35)+2  
.....

002346 010600  
002348 022776 002442  
002354 001401  
002356 000000  
  
000041  
000036

T40A1 MOV SP,R0 IGET RESULTANT SP  
CMP #2+35+T40A+2,SP ISP = #T40A+(2+35)+27  
REQ ,+4  
HLT IERROR INCCORRECT RESULTANT SP

C=C+1  
M=M+1

I TEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK  
I INSTRUCTION FOR ALL VALUES OF N (0-77)

002360 010771  
002362 012776 000500  
002366 012705 002374  
002372 006432

SCOPE  
MOV #STKPTR,SP I INITIALIZE THE STACK POINTER  
MOV #T41A,R5 I LOAD R5  
MARK 36 I MARK N=36

```

FUNCTION RESULTS N=36
ITEMP = PC+(2*N)      TEMP = #T41A+(2*36)
IPC = R5              PC = T41A
ISP = TEMP            SP = #T41A+(2*36)
IR5 = (TEMP)         R5 = (#T41A+(2*36))
ISP = TEMP+2         SP = #T41A+(2*36)+2
.....
P02374 010670      T41A1  MOV    SP,R0      IGET RESULTANT SP
P02376 022706      CMP    #2*36+T41A+2,SP  ISP = #T41A+(2*36)+2?
P02402 001401      BEQ    ,*4
P02404 000000      HLT                    ERROR INCORRECT RESULTANT SP

      C=C+1
      M=M+1
TEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK
INSTRUCTION FOR ALL VALUES OF N (0-77)
      SCOPE
P02406 010701      MOV    #STKPTR,SP     INITIALIZE THE STACK POINTER
P02410 012706      MOV    #T42A,R5      ILOAD R5
P02414 012705      MARK   37           IMARK N=37
P02420 006437
.....

```

```

FUNCTION RESULTS N=37
ITEMP = PC+(2*N)      TEMP = #T42A+(2*37)
IPC = R5              PC = T42A
ISP = TEMP            SP = #T42A+(2*37)
IR5 = (TEMP)         R5 = (#T42A+(2*37))
ISP = TEMP+2         SP = #T42A+(2*37)+2
.....
P02422 010600      T42A1  MOV    SP,R0      IGET RESULTANT SP
P02424 022706      CMP    #2*37+T42A+2,SP  ISP = #T42A+(2*37)+2?
P02430 001401      BEQ    ,*4
P02432 000000      HLT                    ERROR INCORRECT RESULTANT SP

      C=C+1
      M=M+1
TEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK
INSTRUCTION FOR ALL VALUES OF N (0-77)
      SCOPE
P02434 010701      MOV    #STKPTR,SP     INITIALIZE THE STACK POINTER
P02436 012706      MOV    #T43A,R5      ILOAD R5
P02442 012705      MARK   40           IMARK N=40
P02446 006440
.....

```

```

FUNCTION RESULTS N=40
ITEMP = PC+(2*N)      TEMP = #T43A+(2*40)
IPC = R5              PC = T43A
ISP = TEMP            SP = #T43A+(2*40)
IR5 = (TEMP)         R5 = (#T43A+(2*40))
ISP = TEMP+2         SP = #T43A+(2*40)+2
.....

```

```

P02450 010670      T43A1  MOV    SP,R0      IGET RESULTANT SP
P02452 022706      CMP    #2*40+T43A+2,SP  ISP = #T43A+(2*40)+2?
P02456 001401      BEQ    ,*4
P02460 000000      HLT                    ERROR INCORRECT RESULTANT SP

      C=C+1
      M=M+1
TEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK
INSTRUCTION FOR ALL VALUES OF N (0-77)
      SCOPE
P02462 010701      MOV    #STKPTR,SP     INITIALIZE THE STACK POINTER
P02464 012706      MOV    #T44A,R5      ILOAD R5
P02470 012705      MARK   41           IMARK N=41
P02474 006441
.....
FUNCTION RESULTS N=41
ITEMP = PC+(2*N)      TEMP = #T44A+(2*41)
IPC = R5              PC = T44A
ISP = TEMP            SP = #T44A+(2*41)
IR5 = (TEMP)         R5 = (#T44A+(2*41))
ISP = TEMP+2         SP = #T44A+(2*41)+2
.....

```

```

P02476 010600      T44A1  MOV    SP,R0      IGET RESULTANT SP
P02500 022706      CMP    #2*41+T44A+2,SP  ISP = #T44A+(2*41)+2?
P02504 001401      BEQ    ,*4
P02506 000000      HLT                    ERROR INCORRECT RESULTANT SP

      C=C+1
      M=M+1
TEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK
INSTRUCTION FOR ALL VALUES OF N (0-77)
      SCOPE
P02510 010701      MOV    #STKPTR,SP     INITIALIZE THE STACK POINTER
P02512 012706      MOV    #T45A,R5      ILOAD R5
P02516 012705      MARK   42           IMARK N=42
P02522 006442
.....
FUNCTION RESULTS N=42
ITEMP = PC+(2*N)      TEMP = #T45A+(2*42)
IPC = R5              PC = T45A
ISP = TEMP            SP = #T45A+(2*42)
IR5 = (TEMP)         R5 = (#T45A+(2*42))
ISP = TEMP+2         SP = #T45A+(2*42)+2
.....

```

```

P02524 010600      T45A1  MOV    SP,R0      IGET RESULTANT SP
P02526 022706      CMP    #2*42+T45A+2,SP  ISP = #T45A+(2*42)+2?
P02530 001401      BEQ    ,*4
P02534 000000      HLT                    ERROR INCORRECT RESULTANT SP

      C=C+1
      M=M+1
TEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK

```



002536 010701  
002540 012706 000500  
002544 012705 002552  
002550 006443

INSTRUCTION FOR ALL VALUES OF N (0-77)  
SCOPE  
MOV #STKPTR,SP INITIALIZE THE STACK POINTER  
MOV #T46A,R5 ILOAD R5  
MARK 43 IMARK N=43

IFUNCTION RESULTS N=43  
ITEMP = PC\*(2\*N) TEMP= #T46A\*(2\*43)  
IPC = R5 PC = T46A  
ISP = TEMP SP = #T46A\*(2\*43)  
IR5 = (TEMP) R5 = (#T46A\*(2\*43))  
ISP = TEMP\*2 SP = #T46A\*(2\*43)+2

002552 010620  
002554 022706 002662  
002560 001401  
002562 000000

T46A1 MOV SP,R0 IGET RESULTANT SP  
CMP #2\*43+T46A\*2,SP ISP = #T46A\*(2\*43)+27  
BEQ .+4  
HLT ERROR INCORRECT RESULTANT SP

000047  
000044

C=C+1  
M=M+1  
IFTEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK  
INSTRUCTION FOR ALL VALUES OF N (0-77)

002564 010701  
002566 012706 000500  
002572 012705 002600  
002576 006444

SCOPE  
MOV #STKPTR,SP INITIALIZE THE STACK POINTER  
MOV #T47A,R5 ILOAD R5  
MARK 44 IMARK N=44

IFUNCTION RESULTS N=44  
ITEMP = PC\*(2\*N) TEMP= #T47A\*(2\*44)  
IPC = R5 PC = T47A  
ISP = TEMP SP = #T47A\*(2\*44)  
IR5 = (TEMP) R5 = (#T47A\*(2\*44))  
ISP = TEMP\*2 SP = #T47A\*(2\*44)+2

002600 010600  
002602 022706 002712  
002606 001401  
002610 000000

T47A1 MOV SP,R0 IGET RESULTANT SP  
CMP #2\*44+T47A\*2,SP ISP = #T47A\*(2\*44)+27  
BEQ .+4  
HLT ERROR INCORRECT RESULTANT SP

000050  
000045

C=C+1  
M=M+1  
IFTEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK  
INSTRUCTION FOR ALL VALUES OF N (0-77)

002612 010701  
002614 012706 000500  
002620 012705 002626  
002624 006445

SCOPE  
MOV #STKPTR,SP INITIALIZE THE STACK POINTER  
MOV #T50A,R5 ILOAD R5  
MARK 45 IMARK N=45

IFUNCTION RESULTS N=45

ITEMP = PC\*(2\*N) TEMP= #T52A\*(2\*45)  
IPC = R5 PC = T52A  
ISP = TEMP SP = #T52A\*(2\*45)  
IR5 = (TEMP) R5 = (#T52A\*(2\*45))  
ISP = TEMP\*2 SP = #T52A\*(2\*45)+2

002626 010600  
002630 022706 002742  
002634 001401  
002636 000000

T50A1 MOV SP,R0 IGET RESULTANT SP  
CMP #2\*45+T50A\*2,SP ISP = #T52A\*(2\*45)+27  
BEQ .+4  
HLT ERROR INCORRECT RESULTANT SP

000051  
000046

C=C+1  
M=M+1  
IFTEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK  
INSTRUCTION FOR ALL VALUES OF N (0-77)

002640 010701  
002642 012706 000500  
002646 012705 002654  
002652 006446

SCOPE  
MOV #STKPTR,SP INITIALIZE THE STACK POINTER  
MOV #T51A,R5 ILOAD R5  
MARK 46 IMARK N=46

IFUNCTION RESULTS N=46  
ITEMP = PC\*(2\*N) TEMP= #T51A\*(2\*46)  
IPC = R5 PC = T51A  
ISP = TEMP SP = #T51A\*(2\*46)  
IR5 = (TEMP) R5 = (#T51A\*(2\*46))  
ISP = TEMP\*2 SP = #T51A\*(2\*46)+2

002654 010600  
002656 022706 002772  
002662 001401  
002664 000000

T51A1 MOV SP,R0 IGET RESULTANT SP  
CMP #2\*46+T51A\*2,SP ISP = #T51A\*(2\*46)+27  
BEQ .+4  
HLT ERROR INCORRECT RESULTANT SP

000052  
000047

C=C+1  
M=M+1  
IFTEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK  
INSTRUCTION FOR ALL VALUES OF N (0-77)

002666 010701  
002670 012706 000500  
002674 012705 002702  
002700 006447

SCOPE  
MOV #STKPTR,SP INITIALIZE THE STACK POINTER  
MOV #T52A,R5 ILOAD R5  
MARK 47 IMARK N=47

IFUNCTION RESULTS N=47  
ITEMP = PC\*(2\*N) TEMP= #T52A\*(2\*47)  
IPC = R5 PC = T52A  
ISP = TEMP SP = #T52A\*(2\*47)  
IR5 = (TEMP) R5 = (#T52A\*(2\*47))  
ISP = TEMP\*2 SP = #T52A\*(2\*47)+2

002702 010600

T52A1 MOV SP,R0 IGET RESULTANT SP

002704 022706 003022  
002710 001401  
002712 000000

CMP #2\*47+T52A+2,SP ISP = #T52A+(2\*47)+27  
REQ ,+4  
HLT IERROR INCORRECT RESULTANT SP

C=C+1  
M=M+1

TEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK  
INSTRUCTION FOR ALL VALUES OF N (0-77)

002714 010701  
002716 012706 000500  
002722 012705 002730  
002726 006450

SCOPE  
MOV #STKPTR,SP INITIALIZE THE STACK POINTER  
MOV #T53A,R5 ILOAD R5  
MARK 50 IMARK N=50

\*\*\*\*\*  
IFUNCTION RESULTS N=50  
ITEMP = PC+(2\*N) TEMP = #T53A+(2\*50)  
IPC = R5 PC = T53A  
ISP = TEMP SP = #T53A+(2\*50)  
IRS = (TEMP) R5 = (#T53A+(2\*50))  
ISP = TEMP+2 SP = #T53A+(2\*50)+2  
\*\*\*\*\*

002730 010600  
002732 022706 003052  
002736 001401  
002740 000000

T53A1 MOV SP,R0 IGET RESULTANT SP  
CMP #2\*50+T53A+2,SP ISP = #T53A+(2\*50)+27  
REQ ,+4  
HLT IERROR INCORRECT RESULTANT SP

C=C+1  
M=M+1

TEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK  
INSTRUCTION FOR ALL VALUES OF N (0-77)

002742 010701  
002744 012706 000500  
002750 012705 002756  
002754 006451

SCOPE  
MOV #STKPTR,SP INITIALIZE THE STACK POINTER  
MOV #T54A,R5 ILOAD R5  
MARK 51 IMARK N=51

\*\*\*\*\*  
IFUNCTION RESULTS N=51  
ITEMP = PC+(2\*N) TEMP = #T54A+(2\*51)  
IPC = R5 PC = T54A  
ISP = TEMP SP = #T54A+(2\*51)  
IRS = (TEMP) R5 = (#T54A+(2\*51))  
ISP = TEMP+2 SP = #T54A+(2\*51)+2  
\*\*\*\*\*

002756 010600  
002760 022706 003102  
002764 001401  
002766 000000

T54A1 MOV SP,R0 IGET RESULTANT SP  
CMP #2\*51+T54A+2,SP ISP = #T54A+(2\*51)+27  
REQ ,+4  
HLT IERROR INCORRECT RESULTANT SP

C=C+1  
M=M+1

TEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK  
INSTRUCTION FOR ALL VALUES OF N (0-77)

002770 010701  
002772 012706 000500  
002776 012705 003004  
003002 006452

SCOPE  
MOV #STKPTR,SP INITIALIZE THE STACK POINTER  
MOV #T55A,R5 ILOAD R5  
MARK 52 IMARK N=52

\*\*\*\*\*  
IFUNCTION RESULTS N=52  
ITEMP = PC+(2\*N) TEMP = #T55A+(2\*52)  
IPC = R5 PC = T55A  
ISP = TEMP SP = #T55A+(2\*52)  
IRS = (TEMP) R5 = (#T55A+(2\*52))  
ISP = TEMP+2 SP = #T55A+(2\*52)+2  
\*\*\*\*\*

003004 010600  
003006 022706 003132  
003012 001401  
003014 000000

T55A1 MOV SP,R0 IGET RESULTANT SP  
CMP #2\*52+T55A+2,SP ISP = #T55A+(2\*52)+27  
REQ ,+4  
HLT IERROR INCORRECT RESULTANT SP

C=C+1  
M=M+1

TEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK  
INSTRUCTION FOR ALL VALUES OF N (0-77)

003016 010701  
003020 012706 000500  
003024 012705 003032  
003030 006453

SCOPE  
MOV #STKPTR,SP INITIALIZE THE STACK POINTER  
MOV #T56A,R5 ILOAD R5  
MARK 53 IMARK N=53

\*\*\*\*\*  
IFUNCTION RESULTS N=53  
ITEMP = PC+(2\*N) TEMP = #T56A+(2\*53)  
IPC = R5 PC = T56A  
ISP = TEMP SP = #T56A+(2\*53)  
IRS = (TEMP) R5 = (#T56A+(2\*53))  
ISP = TEMP+2 SP = #T56A+(2\*53)+2  
\*\*\*\*\*

003032 010600  
003034 022706 003162  
003040 001401  
003042 000000

T56A1 MOV SP,R0 IGET RESULTANT SP  
CMP #2\*53+T56A+2,SP ISP = #T56A+(2\*53)+27  
REQ ,+4  
HLT IERROR INCORRECT RESULTANT SP

C=C+1  
M=M+1

TEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK  
INSTRUCTION FOR ALL VALUES OF N (0-77)

003044 010701  
003046 012706 000500  
003052 012705 003060  
003056 006454

SCOPE  
MOV #STKPTR,SP INITIALIZE THE STACK POINTER  
MOV #T57A,R5 ILOAD R5  
MARK 54 IMARK N=54

\*\*\*\*\*  
IFUNCTION RESULTS N=54  
ITEMP = PC+(2\*N) TEMP = #T57A+(2\*54)  
\*\*\*\*\*

```

IPC = R5          PC = T57A
ISP = TEMP        SP = #T57A+(2*54)
IR5 = (TEMP)      R5 = (#T57A+(2*54))
ISP = TEMP*2      SP = #T57A+(2*54)+2
.....
T57A1  MOV     SP,R0          IGET RESULTANT SP
        CMP     #2*54+T57A*2,SP  ISP = #T57A+(2*54)+2?
        BEQ     ,+4
        HLT
        IERROR INCORRECT RESULTANT SP

        C=C+1
        M=M+1
I TEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK
I INSTRUCTION FOR ALL VALUES OF N (0-77)
        SCOPE
        MOV     #STKPTR,SP      I INITIALIZE THE STACK POINTER
        MOV     #T62A,R5       I LOAD R5
        MARK    55             I MARK N=55

```

```

.....
IFUNCTION RESULTS N=55
ITEMP = PC*(2*N)      TEMP = #T60A+(2*55)
IPC = R5              PC = T60A
ISP = TEMP            SP = #T60A+(2*55)
IR5 = (TEMP)         R5 = (#T60A+(2*55))
ISP = TEMP*2         SP = #T60A+(2*55)+2
.....
T60A1  MOV     SP,R0          IGET RESULTANT SP
        CMP     #2*55+T60A*2,SP  ISP = #T60A+(2*55)+2?
        BEQ     ,+4
        HLT
        IERROR INCORRECT RESULTANT SP

        C=C+1
        M=M+1
I TEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK
I INSTRUCTION FOR ALL VALUES OF N (0-77)
        SCOPE
        MOV     #STKPTR,SP      I INITIALIZE THE STACK POINTER
        MOV     #T61A,R5       I LOAD R5
        MARK    56             I MARK N=56

```

```

.....
IFUNCTION RESULTS N=56
ITEMP = PC*(2*N)      TEMP = #T61A+(2*56)
IPC = R5              PC = T61A
ISP = TEMP            SP = #T61A+(2*56)
IR5 = (TEMP)         R5 = (#T61A+(2*56))
ISP = TEMP*2         SP = #T61A+(2*56)+2
.....
T61A1  MOV     SP,R0          IGET RESULTANT SP
        CMP     #2*56+T61A*2,SP  ISP = #T61A+(2*56)+2?

```

```

.....
REQ     ,+4
HLT
        IERROR INCORRECT RESULTANT SP

        C=C+1
        M=M+1
I TEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK
I INSTRUCTION FOR ALL VALUES OF N (0-77)
        SCOPE
        MOV     #STKPTR,SP      I INITIALIZE THE STACK POINTER
        MOV     #T62A,R5       I LOAD R5
        MARK    57             I MARK N=57

```

```

.....
IFUNCTION RESULTS N=57
ITEMP = PC*(2*N)      TEMP = #T62A+(2*57)
IPC = R5              PC = T62A
ISP = TEMP            SP = #T62A+(2*57)
IR5 = (TEMP)         R5 = (#T62A+(2*57))
ISP = TEMP*2         SP = #T62A+(2*57)+2
.....
T62A1  MOV     SP,R0          IGET RESULTANT SP
        CMP     #2*57+T62A*2,SP  ISP = #T62A+(2*57)+2?
        BEQ     ,+4
        HLT
        IERROR INCORRECT RESULTANT SP

        C=C+1
        M=M+1
I TEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK
I INSTRUCTION FOR ALL VALUES OF N (0-77)
        SCOPE
        MOV     #STKPTR,SP      I INITIALIZE THE STACK POINTER
        MOV     #T63A,R5       I LOAD R5
        MARK    60             I MARK N=60

```

```

.....
IFUNCTION RESULTS N=60
ITEMP = PC*(2*N)      TEMP = #T63A+(2*60)
IPC = R5              PC = T63A
ISP = TEMP            SP = #T63A+(2*60)
IR5 = (TEMP)         R5 = (#T63A+(2*60))
ISP = TEMP*2         SP = #T63A+(2*60)+2
.....
T63A1  MOV     SP,R0          IGET RESULTANT SP
        CMP     #2*60+T63A*2,SP  ISP = #T63A+(2*60)+2?
        BEQ     ,+4
        HLT
        IERROR INCORRECT RESULTANT SP

        C=C+1
        M=M+1
I TEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK
I INSTRUCTION FOR ALL VALUES OF N (0-77)
        SCOPE

```

001224 012776 000508  
001230 012775 003236  
001234 006461

MOV #STKPTR,SP INITIALIZE THE STACK POINTER  
MOV #T64A,R5 ILOAD R5  
MARK 61 IMARK N=61

.....  
IFUNCTION RESULTS N=61  
ITEMP = PC+(2\*N) TEMP = #T64A\*(2+61)  
IPC = R5 PC = T64A  
ISP = TEMP SP = #T64A\*(2+61)  
IR5 = (TEMP) R5 = (#T64A\*(2+61))  
ISP = TEMP+2 SP = #T64A\*(2+61)+2  
.....

001236 010690  
001240 022776 003402  
001244 001401  
001246 000000

T64A1 MOV SP,R0 IGET RESULTANT SP  
CMP #2+61+T64A\*2,SP ISP = #T64A\*(2+61)+27  
BEQ ,+4  
HLT IERROR INCORRECT RESULTANT SP

000065  
000062

C=C+1  
M=M+1

ITEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK  
IINSTRUCTION FOR ALL VALUES OF N (0-77)

001248 010701  
001252 012776 000508  
001256 012775 003236  
001262 006462

SCOPE  
MOV #STKPTR,SP INITIALIZE THE STACK POINTER  
MOV #T65A,R5 ILOAD R5  
MARK 62 IMARK N=62

.....  
IFUNCTION RESULTS N=62  
ITEMP = PC+(2\*N) TEMP = #T65A\*(2+62)  
IPC = R5 PC = T65A  
ISP = TEMP SP = #T65A\*(2+62)  
IR5 = (TEMP) R5 = (#T65A\*(2+62))  
ISP = TEMP+2 SP = #T65A\*(2+62)+2  
.....

001264 010690  
001266 022776 003432  
001272 001401  
001274 000000

T65A1 MOV SP,R0 IGET RESULTANT SP  
CMP #2+62+T65A\*2,SP ISP = #T65A\*(2+62)+27  
BEQ ,+4  
HLT IERROR INCORRECT RESULTANT SP

000066  
000063

C=C+1  
M=M+1

ITEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK  
IINSTRUCTION FOR ALL VALUES OF N (0-77)

001276 010701  
001300 012776 000508  
001304 012775 003312  
001310 006463

SCOPE  
MOV #STKPTR,SP INITIALIZE THE STACK POINTER  
MOV #T66A,R5 ILOAD R5  
MARK 63 IMARK N=63

.....  
IFUNCTION RESULTS N=63  
ITEMP = PC+(2\*N) TEMP = #T66A\*(2+63)  
IPC = R5 PC = T66A  
.....

ISP = TEMP SP = #T66A\*(2+63)  
IR5 = (TEMP) R5 = (#T66A\*(2+63))  
ISP = TEMP+2 SP = #T66A\*(2+63)+2  
.....

001312 010690  
001314 022776 003462  
001320 001401  
001322 000000

T66A1 MOV SP,R0 IGET RESULTANT SP  
CMP #2+63+T66A\*2,SP ISP = #T66A\*(2+63)+27  
BEQ ,+4  
HLT IERROR INCORRECT RESULTANT SP

000067  
000064

C=C+1  
M=M+1

ITEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK  
IINSTRUCTION FOR ALL VALUES OF N (0-77)

001324 010701  
001326 012776 000508  
001332 012775 003340  
001336 006464

SCOPE  
MOV #STKPTR,SP INITIALIZE THE STACK POINTER  
MOV #T67A,R5 ILOAD R5  
MARK 64 IMARK N=64

.....  
IFUNCTION RESULTS N=64  
ITEMP = PC+(2\*N) TEMP = #T67A\*(2+64)  
IPC = R5 PC = T67A  
ISP = TEMP SP = #T67A\*(2+64)  
IR5 = (TEMP) R5 = (#T67A\*(2+64))  
ISP = TEMP+2 SP = #T67A\*(2+64)+2  
.....

001340 010690  
001342 022776 003512  
001346 001401  
001350 000000

T67A1 MOV SP,R0 IGET RESULTANT SP  
CMP #2+64+T67A\*2,SP ISP = #T67A\*(2+64)+27  
BEQ ,+4  
HLT IERROR INCORRECT RESULTANT SP

000070  
000065

C=C+1  
M=M+1

ITEST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK  
IINSTRUCTION FOR ALL VALUES OF N (0-77)

001352 010701  
001354 012776 000508  
001360 012775 003360  
001364 006465

SCOPE  
MOV #STKPTR,SP INITIALIZE THE STACK POINTER  
MOV #T70A,R5 ILOAD R5  
MARK 65 IMARK N=65

.....  
IFUNCTION RESULTS N=65  
ITEMP = PC+(2\*N) TEMP = #T70A\*(2+65)  
IPC = R5 PC = T70A  
ISP = TEMP SP = #T70A\*(2+65)  
IR5 = (TEMP) R5 = (#T70A\*(2+65))  
ISP = TEMP+2 SP = #T70A\*(2+65)+2  
.....

001366 010690  
001370 022776 003440  
001374 001401

T70A1 MOV SP,R0 IGET RESULTANT SP  
CMP #2+65+T70A\*2,SP ISP = #T70A\*(2+65)+27  
BEQ ,+4  
.....

56

003376 000000

HLT ERROR INCORRECT RESULTANT SP

000071  
000066

C=C+1  
M=M+1

IFST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK  
INSTRUCTION FOR ALL VALUES OF N (0-77)

001400 010701  
001402 012706 000500  
001406 012705 003414  
003412 006466

SCOPE  
MOV #STKPTR,SP INITIALIZE THE STACK POINTER  
MOV #T71A,R5 ILOAD R5  
MARK 66 IMARK N=66

.....  
IFUNCTION RESULTS N=66  
JTEMP = PC+(2\*N) TEMP = #T71A\*(2+66)  
JPC = R5 PC = T71A  
JSP = TEMP SP = #T71A\*(2+66)  
JRS = (TEMP) RS = (#T71A\*(2+66))  
JSP = TEMP+2 SP = #T71A\*(2+66)+2  
.....

003414 010600  
003416 022706 003572  
003422 001401  
003424 000000

T71A1 MOV SP,R0 IGET RESULTANT SP  
CMP #2\*66+T71A+2,SP ISP = #T71A\*(2+66)+2?  
BEQ .+4  
HLT ERROR INCORRECT RESULTANT SP

000072  
000067

C=C+1  
M=M+1

IFST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK  
INSTRUCTION FOR ALL VALUES OF N (0-77)

003426 010701  
003430 012706 000500  
003434 012705 003442  
003440 006467

SCOPE  
MOV #STKPTR,SP INITIALIZE THE STACK POINTER  
MOV #T72A,R5 ILOAD R5  
MARK 67 IMARK N=67

.....  
IFUNCTION RESULTS N=67  
JTEMP = PC+(2\*N) TEMP = #T72A\*(2+67)  
JPC = R5 PC = T72A  
JSP = TEMP SP = #T72A\*(2+67)  
JRS = (TEMP) RS = (#T72A\*(2+67))  
JSP = TEMP+2 SP = #T72A\*(2+67)+2  
.....

003442 010600  
003444 022706 003622  
003450 001401  
003452 000000

T72A1 MOV SP,R0 IGET RESULTANT SP  
CMP #2\*67+T72A+2,SP ISP = #T72A\*(2+67)+2?  
BEQ .+4  
HLT ERROR INCORRECT RESULTANT SP

000073  
000070

C=C+1  
M=M+1

IFST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK  
INSTRUCTION FOR ALL VALUES OF N (0-77)

003454 010701  
003456 012706 000500

SCOPE  
MOV #STKPTR,SP INITIALIZE THE STACK POINTER

003462 012705 003470  
003466 006470

MOV #T73A,R5 ILOAD R5  
MARK 70 IMARK N=70

.....  
IFUNCTION RESULTS N=70  
JTEMP = PC+(2\*N) TEMP = #T73A\*(2+70)  
JPC = R5 PC = T73A  
JSP = TEMP SP = #T73A\*(2+70)  
JRS = (TEMP) RS = (#T73A\*(2+70))  
JSP = TEMP+2 SP = #T73A\*(2+70)+2  
.....

003470 010600  
003472 022706 003602  
003476 001401  
003500 000000

T73A1 MOV SP,R0 IGET RESULTANT SP  
CMP #2\*70+T73A+2,SP ISP = #T73A\*(2+70)+2?  
BEQ .+4  
HLT ERROR INCORRECT RESULTANT SP

000074  
000071

C=C+1  
M=M+1

IFST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK  
INSTRUCTION FOR ALL VALUES OF N (0-77)

003502 010701  
003504 012706 000500  
003510 012705 003516  
003514 006471

SCOPE  
MOV #STKPTR,SP INITIALIZE THE STACK POINTER  
MOV #T74A,R5 ILOAD R5  
MARK 71 IMARK N=71

.....  
IFUNCTION RESULTS N=71  
JTEMP = PC+(2\*N) TEMP = #T74A\*(2+71)  
JPC = R5 PC = T74A  
JSP = TEMP SP = #T74A\*(2+71)  
JRS = (TEMP) RS = (#T74A\*(2+71))  
JSP = TEMP+2 SP = #T74A\*(2+71)+2  
.....

003516 010600  
003520 022706 003702  
003524 001401  
003526 000000

T74A1 MOV SP,R0 IGET RESULTANT SP  
CMP #2\*71+T74A+2,SP ISP = #T74A\*(2+71)+2?  
BEQ .+4  
HLT ERROR INCORRECT RESULTANT SP

000075  
000072

C=C+1  
M=M+1

IFST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK  
INSTRUCTION FOR ALL VALUES OF N (0-77)

003530 010701  
003532 012706 000500  
003536 012705 003544  
003542 006472

SCOPE  
MOV #STKPTR,SP INITIALIZE THE STACK POINTER  
MOV #T75A,R5 ILOAD R5  
MARK 72 IMARK N=72

.....  
IFUNCTION RESULTS N=72  
JTEMP = PC+(2\*N) TEMP = #T75A\*(2+72)  
JPC = R5 PC = T75A  
JSP = TEMP SP = #T75A\*(2+72)  
.....

```

IR5 = (TEMP)          R5 = (#T75A+(2*72))
ISP = TEMP+2          SP = #T75A+(2*72)+2
.....
T74A1  MOV    SP,R0          IGET RESULTANT SP
        CMP    #2*72+T75A+2,SP  ISP = #T75A+(2*72)+2?
        BEQ    ,+4
        HLT
        ERROR INCORRECT RESULTANT SP

C=C+1
M=M+1

```

```

IFST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK
INSTRUCTION FOR ALL VALUES OF N (0-77)
SCOPE
MOV    #STKPTR,SP          INITIALIZE THE STACK POINTER
MOV    #T76A,R5           ILOAD R5
MARK   73                 IMARK N=73

```

```

.....
IFUNCTION RESULTS N=73
TEMP = PC+(2*N)          TEMP = #T76A+(2*73)
IPC = R5                PC = T76A
ISP = TEMP              SP = #T76A+(2*73)
IR5 = (TEMP)           R5 = (#T76A+(2*73))
ISP = TEMP+2          SP = #T76A+(2*73)+2
.....

```

```

T76A1  MOV    SP,R0          IGET RESULTANT SP
        CMP    #2*73+T76A+2,SP  ISP = #T76A+(2*73)+2?
        BEQ    ,+4
        HLT
        ERROR INCORRECT RESULTANT SP

C=C+1
M=M+1

```

```

IFST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK
INSTRUCTION FOR ALL VALUES OF N (0-77)
SCOPE
MOV    #STKPTR,SP          INITIALIZE THE STACK POINTER
MOV    #T77A,R5           ILOAD R5
MARK   74                 IMARK N=74

```

```

.....
IFUNCTION RESULTS N=74
TEMP = PC+(2*N)          TEMP = #T77A+(2*74)
IPC = R5                PC = T77A
ISP = TEMP              SP = #T77A+(2*74)
IR5 = (TEMP)           R5 = (#T77A+(2*74))
ISP = TEMP+2          SP = #T77A+(2*74)+2
.....

```

```

T77A1  MOV    SP,R0          IGET RESULTANT SP
        CMP    #2*74+T77A+2,SP  ISP = #T77A+(2*74)+2?
        BEQ    ,+4
        HLT
        ERROR INCORRECT RESULTANT SP

```

```

C=C+1
M=M+1
IFST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK
INSTRUCTION FOR ALL VALUES OF N (0-77)
SCOPE
MOV    #STKPTR,SP          INITIALIZE THE STACK POINTER
MOV    #T100A,R5          ILOAD R5
MARK   75                 IMARK N=75

```

```

.....
IFUNCTION RESULTS N=75
TEMP = PC+(2*N)          TEMP = #T100A+(2*75)
IPC = R5                PC = T100A
ISP = TEMP              SP = #T100A+(2*75)
IR5 = (TEMP)           R5 = (#T100A+(2*75))
ISP = TEMP+2          SP = #T100A+(2*75)+2
.....

```

```

T100A1 MOV    SP,R0          IGET RESULTANT SP
        CMP    #2*75+T100A+2,SP  ISP = #T100A+(2*75)+2?
        BEQ    ,+4
        HLT
        ERROR INCORRECT RESULTANT SP

C=C+1
M=M+1

```

```

IFST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK
INSTRUCTION FOR ALL VALUES OF N (0-77)
SCOPE
MOV    #STKPTR,SP          INITIALIZE THE STACK POINTER
MOV    #T101A,R5          ILOAD R5
MARK   76                 IMARK N=76

```

```

.....
IFUNCTION RESULTS N=76
TEMP = PC+(2*N)          TEMP = #T101A+(2*76)
IPC = R5                PC = T101A
ISP = TEMP              SP = #T101A+(2*76)
IR5 = (TEMP)           R5 = (#T101A+(2*76))
ISP = TEMP+2          SP = #T101A+(2*76)+2
.....

```

```

T101A1 MOV    SP,R0          IGET RESULTANT SP
        CMP    #2*76+T101A+2,SP  ISP = #T101A+(2*76)+2?
        BEQ    ,+4
        HLT
        ERROR INCORRECT RESULTANT SP

```

```

C=C+1
M=M+1
IFST THAT THE STACK POINTER CONTAINS THE PROPER VALUE AFTER THE MARK
INSTRUCTION FOR ALL VALUES OF N (0-77)
SCOPE
MOV    #STKPTR,SP          INITIALIZE THE STACK POINTER
MOV    #T102A,R5          ILOAD R5

```

77

003720 006477

MARK 77 I MARK N=77

```

.....
JFUNCTION RESULTS N=77
JTEMP = PC+(2*77)      TEMP = #T102A+(2*77)
JPC = R5                PC = T102A
JSP = TEMP              SP = #T102A+(2*77)
JRS = (TEMP)           RS = (#T102A+(2*77))
JSP = TEMP+2           SP = #T102A+(2*77)+2
.....

```

003722 010600  
003724 022706 004122  
003730 001401  
003732 000000

```

T102A:  MOV  SP,R0          JGET RESULTANT SP
        CMP  #2*77-T102A+2,SP  JSP = #T102A+(2*77)+2
        BEQ  ,*4
        HLT                    ERROR INCORRECT RESULTANT SP

```

000103  
000100  
000000

C=C+1  
M=M+1

M=M

ITEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES  
JOF N (0-77)

003734 014701  
003736 012706 000500  
003742 012705 003750  
003746 006400

```

SCOPE
MOV  #STKPTR,SP      INITIALIZE THE STACK POINTER
MOV  #T103A,R5       JLOAD R5
MARK 0               JMARK N=0

```

```

.....
JFUNCTION RESULTS N=0
JTEMP = PC+(2*0)      TEMP = #T103A+(2*0)
JPC = R5                PC = #T103A
JSP = TEMP              SP = #T103A+(2*0)
JRS = (TEMP)           RS = (#T103A+(2*0))
JSP = TEMP+2           SP = #T103A+(2*0)+2
.....

```

003750 000240  
003752 010500  
003754 021705 003750  
003760 001401  
003762 000000

```

T103A:  NOP
        MOV  R5,R0          JGET CONTENTS OF R5
        CMP  #02*0-T103A,R5  JRS = (#T103A+(2*0))
        BEQ  ,*4
        HLT                    R5 IMPROPERLY LOADED

```

000104  
000001

C=C+1  
M=M+1

ITEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES  
JOF N (0-77)

003764 010701  
003766 012706 000500  
003772 012705 004000  
003776 006401

```

SCOPE
MOV  #STKPTR,SP      INITIALIZE THE STACK POINTER
MOV  #T104A,R5       JLOAD R5
MARK 1               JMARK N=1

```

```

.....
JFUNCTION RESULTS N=1
JTEMP = PC+(2*1)      TEMP = #T104A+(2*1)
JPC = R5                PC = #T104A
.....

```

```

.....
JSP = TEMP              SP = #T104A+(2*1)
JRS = (TEMP)           RS = (#T104A+(2*1))
JSP = TEMP+2           SP = #T104A+(2*1)+2
.....

```

004000 000240  
004002 010500  
004004 021705 004002  
004010 001401  
004012 000000

```

T104A:  NOP
        MOV  R5,R0          JGET CONTENTS OF R5
        CMP  #02*1-T104A,R5  JRS = (#T104A+(2*1))
        BEQ  ,*4
        HLT                    R5 IMPROPERLY LOADED

```

000105  
000002

C=C+1  
M=M+1

ITEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES  
JOF N (0-77)

004014 010701  
004016 012706 000500  
004022 012705 004030  
004026 006402

```

SCOPE
MOV  #STKPTR,SP      INITIALIZE THE STACK POINTER
MOV  #T105A,R5       JLOAD R5
MARK 2               JMARK N=2

```

```

.....
JFUNCTION RESULTS N=2
JTEMP = PC+(2*2)      TEMP = #T105A+(2*2)
JPC = R5                PC = #T105A
JSP = TEMP              SP = #T105A+(2*2)
JRS = (TEMP)           RS = (#T105A+(2*2))
JSP = TEMP+2           SP = #T105A+(2*2)+2
.....

```

004030 000240  
004032 010500  
004034 021705 004034  
004040 001401  
004042 000000

```

T105A:  NOP
        MOV  R5,R0          JGET CONTENTS OF R5
        CMP  #02*2-T105A,R5  JRS = (#T105A+(2*2))
        BEQ  ,*4
        HLT                    R5 IMPROPERLY LOADED

```

000106  
000003

C=C+1  
M=M+1

ITEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES  
JOF N (0-77)

004044 010701  
004046 012706 000500  
004052 012705 004060  
004056 006403

```

SCOPE
MOV  #STKPTR,SP      INITIALIZE THE STACK POINTER
MOV  #T106A,R5       JLOAD R5
MARK 3               JMARK N=3

```

```

.....
JFUNCTION RESULTS N=3
JTEMP = PC+(2*3)      TEMP = #T106A+(2*3)
JPC = R5                PC = #T106A
JSP = TEMP              SP = #T106A+(2*3)
JRS = (TEMP)           RS = (#T106A+(2*3))
JSP = TEMP+2           SP = #T106A+(2*3)+2
.....

```

004060 000240

T106A: NOP

004062 01P578  
004064 023775 004066  
004070 001471  
004072 000000  
  
000107  
000074  
  
004074 010701  
004076 012706 000500  
004102 012705 004110  
004108 006404

MOV R5,R0 IGET CONTENTS OF R5  
CMP #02\*3+T106A,R5 IR5 = (#T106A\*(2+3))  
REQ ,+4  
HLT IR5 IMPROPERLY LOADED

C=C+1  
M=M+1  
ITEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES  
IOF N (0-77)

SCOPE  
MOV #STKPTR,SP INITIALIZE THE STACK POINTER  
MOV #T107A,R5 ILOAD R5  
MARK 4 IMARK N=4

.....  
IFUNCTION RESULTS N=4  
IFEMP = PC\*(2+4) TEMP = #T107A\*(2+4)  
IPC = R5 PC = #T107A  
ISP = TEMP SP = #T107A\*(2+4)  
IR5 = (TEMP) R5 = (#T107A\*(2+4))  
ISP = TEMP\*2 SP = #T107A\*(2+4)+2  
.....

004110 000240  
004112 010500  
004114 023775 004120  
004120 001401  
004122 000000

T107A: NOP  
MOV R5,R0 IGET CONTENTS OF R5  
CMP #02\*4+T107A,R5 IR5 = (#T107A\*(2+4))  
REQ ,+4  
HLT IR5 IMPROPERLY LOADED

C=C+1  
M=M+1  
ITEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES  
IOF N (0-77)

SCOPE  
MOV #STKPTR,SP INITIALIZE THE STACK POINTER  
MOV #T110A,R5 ILOAD R5  
MARK 5 IMARK N=5

.....  
IFUNCTION RESULTS N=5  
IFEMP = PC\*(2+5) TEMP = #T110A\*(2+5)  
IPC = R5 PC = #T110A  
ISP = TEMP SP = #T110A\*(2+5)  
IR5 = (TEMP) R5 = (#T110A\*(2+5))  
ISP = TEMP\*2 SP = #T110A\*(2+5)+2  
.....

004140 000240  
004142 010500  
004144 023705 004152  
004150 001401  
004152 000000

T110A: NOP  
MOV R5,R0 IGET CONTENTS OF R5  
CMP #02\*5+T110A,R5 IR5 = (#T110A\*(2+5))  
REQ ,+4  
HLT IR5 IMPROPERLY LOADED

C=C+1

000006  
  
004154 010701  
004156 012706 000500  
004162 012705 004170  
004168 006406

M=M+1  
ITEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES  
IOF N (0-77)

SCOPE  
MOV #STKPTR,SP INITIALIZE THE STACK POINTER  
MOV #T111A,R5 ILOAD R5  
MARK 6 IMARK N=6

.....  
IFUNCTION RESULTS N=6  
IFEMP = PC\*(2+6) TEMP = #T111A\*(2+6)  
IPC = R5 PC = #T111A  
ISP = TEMP SP = #T111A\*(2+6)  
IR5 = (TEMP) R5 = (#T111A\*(2+6))  
ISP = TEMP\*2 SP = #T111A\*(2+6)+2  
.....

004170 000240  
004172 010500  
004174 023705 004204  
004200 001401  
004202 000000

T111A: NOP  
MOV R5,R0 IGET CONTENTS OF R5  
CMP #02\*6+T111A,R5 IR5 = (#T111A\*(2+6))  
REQ ,+4  
HLT IR5 IMPROPERLY LOADED

C=C+1  
M=M+1  
ITEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES  
IOF N (0-77)

SCOPE  
MOV #STKPTR,SP INITIALIZE THE STACK POINTER  
MOV #T112A,R5 ILOAD R5  
MARK 7 IMARK N=7

.....  
IFUNCTION RESULTS N=7  
IFEMP = PC\*(2+7) TEMP = #T112A\*(2+7)  
IPC = R5 PC = #T112A  
ISP = TEMP SP = #T112A\*(2+7)  
IR5 = (TEMP) R5 = (#T112A\*(2+7))  
ISP = TEMP\*2 SP = #T112A\*(2+7)+2  
.....

004220 000240  
004222 010500  
004224 023705 004236  
004230 001401  
004232 000000

T112A: NOP  
MOV R5,R0 IGET CONTENTS OF R5  
CMP #02\*7+T112A,R5 IR5 = (#T112A\*(2+7))  
REQ ,+4  
HLT IR5 IMPROPERLY LOADED

C=C+1  
M=M+1  
ITEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES  
IOF N (0-77)

SCOPE  
MOV #STKPTR,SP INITIALIZE THE STACK POINTER  
MOV #T113A,R5 ILOAD R5

004234 010701  
004236 012706 000500  
004242 012705 004250



004246 006410

MARK 10 JMARK N=10

```

.....
JFUNCTION RESULTS N=10
JTEMP = PC*(2*10)      TEMP = #T113A*(2*10)
JPC = R5                PC = #T113A
JSP = TEMP              SP = #T113A*(2*10)
JRS = (TEMP)           RS = (#T113A*(2*10))
JSP = TEMP+2           SP = #T113A*(2*10)+2
.....

```

004250 000240  
004252 010500  
004254 023705 004270  
004260 001401  
004262 000000

```

T113A1 NOP
      MOV R5,R0          JGET CONTENTS OF R5
      CMP #2*10+T113A,R5 JR5 = (#T113A*(2*10))?
      BEQ ,*4
      HLT                JR5 IMPROPERLY LOADED

```

000114  
000011

```

C=C+1
M=M+1
JTEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES
IOF N (0-77)

```

004264 010701  
004266 012706 000500  
004272 012705 004300  
004276 006411

```

SCOPE
MOV #STKPTR,SP          JINITIALIZE THE STACK POINTER
MOV #T114A,R5           JLOAD R5
MARK 11                 JMARK N=11

```

```

.....
JFUNCTION RESULTS N=11
JTEMP = PC*(2*11)      TEMP = #T114A*(2*11)
JPC = R5                PC = #T114A
JSP = TEMP              SP = #T114A*(2*11)
JRS = (TEMP)           RS = (#T114A*(2*11))
JSP = TEMP+2           SP = #T114A*(2*11)+2
.....

```

004300 000240  
004302 010500  
004304 023705 004322  
004310 001401  
004312 000000

```

T114A1 NOP
      MOV R5,R0          JGET CONTENTS OF R5
      CMP #2*11+T114A,R5 JR5 = (#T114A*(2*11))?
      BEQ ,*4
      HLT                JR5 IMPROPERLY LOADED

```

000115  
000012

```

C=C+1
M=M+1
JTEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES
IOF N (0-77)

```

004314 010701  
004316 012706 000500  
004322 012705 004330  
004326 006412

```

SCOPE
MOV #STKPTR,SP          JINITIALIZE THE STACK POINTER
MOV #T115A,R5           JLOAD R5
MARK 12                 JMARK N=12

```

```

.....
JFUNCTION RESULTS N=12
JTEMP = PC*(2*12)      TEMP = #T115A*(2*12)
JPC = R5                PC = #T115A
.....

```

```

.....
JSP = TEMP              SP = #T115A*(2*12)
JRS = (TEMP)           RS = (#T115A*(2*12))
JSP = TEMP+2           SP = #T115A*(2*12)+2
.....

```

004330 000240  
004332 010500  
004334 023705 004354  
004340 001401  
004342 000000

```

T115A1 NOP
      MOV R5,R0          JGET CONTENTS OF R5
      CMP #2*12+T115A,R5 JR5 = (#T115A*(2*12))?
      BEQ ,*4
      HLT                JR5 IMPROPERLY LOADED

```

000116  
000013

```

C=C+1
M=M+1
JTEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES
IOF N (0-77)

```

004344 010701  
004346 012706 000500  
004352 012705 004360  
004356 006413

```

SCOPE
MOV #STKPTR,SP          JINITIALIZE THE STACK POINTER
MOV #T116A,R5           JLOAD R5
MARK 13                 JMARK N=13

```

```

.....
JFUNCTION RESULTS N=13
JTEMP = PC*(2*13)      TEMP = #T116A*(2*13)
JPC = R5                PC = #T116A
JSP = TEMP              SP = #T116A*(2*13)
JRS = (TEMP)           RS = (#T116A*(2*13))
JSP = TEMP+2           SP = #T116A*(2*13)+2
.....

```

004360 000240  
004362 010500  
004364 023705 004406  
004370 001401  
004372 000000

```

T116A1 NOP
      MOV R5,R0          JGET CONTENTS OF R5
      CMP #2*13+T116A,R5 JR5 = (#T116A*(2*13))?
      BEQ ,*4
      HLT                JR5 IMPROPERLY LOADED

```

000117  
000014

```

C=C+1
M=M+1
JTEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES
IOF N (0-77)

```

004374 010701  
004376 012706 000500  
004402 012705 004410  
004406 006414

```

SCOPE
MOV #STKPTR,SP          JINITIALIZE THE STACK POINTER
MOV #T117A,R5           JLOAD R5
MARK 14                 JMARK N=14

```

```

.....
JFUNCTION RESULTS N=14
JTEMP = PC*(2*14)      TEMP = #T117A*(2*14)
JPC = R5                PC = #T117A
JSP = TEMP              SP = #T117A*(2*14)
JRS = (TEMP)           RS = (#T117A*(2*14))
JSP = TEMP+2           SP = #T117A*(2*14)+2
.....

```

004410 000240

T117A1 NOP

004412 010500  
004414 023705 004440  
004420 001401  
004422 000000

MOV R5,R0 IGET CONTENTS OF R5  
CMP #2=14+T117A,R5 IR5 = (T117A+(2\*14))  
REQ ,+4  
HLT IR5 IMPROPERLY LOADED

000120  
000015

C=C+1  
M=M+1

ITEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES  
JOF N (0=77)

004424 010701  
004426 012706 000500  
004432 012705 004440  
004436 006415

SCOPE  
MOV #STKPTR,SP INITIALIZE THE STACK POINTER  
MOV #T120A,R5 ILOAD R5  
MARK 15 IMARK N=15

IFUNCTION RESULTS N=15  
JTEMP = PC+(2\*15) TEMP = #T120A+(2\*15)  
JPC = R5 PC = #T120A  
JSP = TEMP SP = #T120A+(2\*15)  
JRS = (TEMP) R5 = (#T120A+(2\*15))  
JSP = TEMP+2 SP = #T120A+(2\*15)+2

004440 000240  
004442 010500  
004444 023705 004472  
004450 001401  
004452 000000

T120A1 NOP  
MOV R5,R0 IGET CONTENTS OF R5  
CMP #2=15+T120A,R5 IR5 = (T120A+(2\*15))  
REQ ,+4  
HLT IR5 IMPROPERLY LOADED

000121  
000016

C=C+1  
M=M+1

ITEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES  
JOF N (0=77)

004454 010701  
004456 012706 000500  
004462 012705 004470  
004466 006415

SCOPE  
MOV #STKPTR,SP INITIALIZE THE STACK POINTER  
MOV #T121A,R5 ILOAD R5  
MARK 16 IMARK N=16

IFUNCTION RESULTS N=16  
JTEMP = PC+(2\*16) TEMP = #T121A+(2\*16)  
JPC = R5 PC = #T121A  
JSP = TEMP SP = #T121A+(2\*16)  
JRS = (TEMP) R5 = (#T121A+(2\*16))  
JSP = TEMP+2 SP = #T121A+(2\*16)+2

004470 000240  
004472 010500  
004474 023705 004524  
004500 001401  
004502 000000

T121A1 NOP  
MOV R5,R0 IGET CONTENTS OF R5  
CMP #2=16+T121A,R5 IR5 = (T121A+(2\*16))  
REQ ,+4  
HLT IR5 IMPROPERLY LOADED

000122

C=C+1

000017

M=M+1  
ITEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES  
JOF N (0=77)

004504 010701  
004506 012706 000500  
004512 012705 004520  
004516 006415

SCOPE  
MOV #STKPTR,SP INITIALIZE THE STACK POINTER  
MOV #T122A,R5 ILOAD R5  
MARK 17 IMARK N=17

IFUNCTION RESULTS N=17  
JTEMP = PC+(2\*17) TEMP = #T122A+(2\*17)  
JPC = R5 PC = #T122A  
JSP = TEMP SP = #T122A+(2\*17)  
JRS = (TEMP) R5 = (#T122A+(2\*17))  
JSP = TEMP+2 SP = #T122A+(2\*17)+2

004520 000240  
004522 010500  
004524 023705 004556  
004530 001401  
004532 000000

T122A1 NOP  
MOV R5,R0 IGET CONTENTS OF R5  
CMP #2=17+T122A,R5 IR5 = (T122A+(2\*17))  
REQ ,+4  
HLT IR5 IMPROPERLY LOADED

000123  
000020

C=C+1  
M=M+1

ITEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES  
JOF N (0=77)

004534 010701  
004536 012706 000500  
004542 012705 004550  
004546 006420

SCOPE  
MOV #STKPTR,SP INITIALIZE THE STACK POINTER  
MOV #T123A,R5 ILOAD R5  
MARK 20 IMARK N=20

IFUNCTION RESULTS N=20  
JTEMP = PC+(2\*20) TEMP = #T123A+(2\*20)  
JPC = R5 PC = #T123A  
JSP = TEMP SP = #T123A+(2\*20)  
JRS = (TEMP) R5 = (#T123A+(2\*20))  
JSP = TEMP+2 SP = #T123A+(2\*20)+2

004550 000240  
004552 010500  
004554 023705 004610  
004560 001401  
004562 000000

T123A1 NOP  
MOV R5,R0 IGET CONTENTS OF R5  
CMP #2=20+T123A,R5 IR5 = (T123A+(2\*20))  
REQ ,+4  
HLT IR5 IMPROPERLY LOADED

000124  
000021

C=C+1  
M=M+1

ITEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES  
JOF N (0=77)

004564 010701  
004566 012706 000500  
004572 012705 004600

SCOPE  
MOV #STKPTR,SP INITIALIZE THE STACK POINTER  
MOV #T124A,R5 ILOAD R5

004576 006421

MARK 21 JMARK N=21

```

.....
IFUNCTION RESULTS N=21
ITEMP = PC+(2*21) TEMP = #T124A+(2*21)
IPC = R5 PC = #T124A
ISP = TEMP SP = #T124A+(2*21)
JMS = (TEMP) R5 = (#T124A+(2*21))
JSP = TEMP+2 SP = #T124A+(2*21)+2
.....

```

004600 000240  
004602 010500  
004604 023705 004642  
004610 001401  
004612 000000

```

T124A1 NOP
MOV R5,R0 IGET CONTENTS OF R5
CMP ##2*21+T124A,R5 IR5 = (#T124A+(2*21))
REQ ,+4
HLT IR5 IMPROPERLY LOADED

```

000125  
000022

```

C=C+1
M=M+1
ITEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES
IOF N (0-77)

```

004614 010701  
004616 012706 000500  
004622 012705 004630  
004626 006422

```

SCOPE
MOV #STKPTR,SP INITIALIZE THE STACK POINTER
MOV #T125A,R5 ILOAD R5
MARK 22 JMARK N=22

```

```

.....
IFUNCTION RESULTS N=22
ITEMP = PC+(2*22) TEMP = #T125A+(2*22)
IPC = R5 PC = #T125A
ISP = TEMP SP = #T125A+(2*22)
JMS = (TEMP) R5 = (#T125A+(2*22))
JSP = TEMP+2 SP = #T125A+(2*22)+2
.....

```

004630 000240  
004632 010500  
004634 023705 004674  
004640 001401  
004642 000000

```

T125A1 NOP
MOV R5,R0 IGET CONTENTS OF R5
CMP ##2*22+T125A,R5 IR5 = (#T125A+(2*22))
REQ ,+4
HLT IR5 IMPROPERLY LOADED

```

000126  
000023

```

C=C+1
M=M+1
ITEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES
IOF N (0-77)

```

004644 010701  
004646 012706 000500  
004652 012705 004660  
004656 006423

```

SCOPE
MOV #STKPTR,SP INITIALIZE THE STACK POINTER
MOV #T126A,R5 ILOAD R5
MARK 23 JMARK N=23

```

```

.....
IFUNCTION RESULTS N=23
ITEMP = PC+(2*23) TEMP = #T126A+(2*23)
IPC = R5 PC = #T126A

```

```

ISP = TEMP SP = #T126A+(2*23)
JMS = (TEMP) R5 = (#T126A+(2*23))
JSP = TEMP+2 SP = #T126A+(2*23)+2
.....

```

004660 000240  
004662 010500  
004664 023705 004726  
004670 001401  
004672 000000

```

T126A1 NOP
MOV R5,R0 IGET CONTENTS OF R5
CMP ##2*23+T126A,R5 IR5 = (#T126A+(2*23))
REQ ,+4
HLT IR5 IMPROPERLY LOADED

```

000127  
000024

```

C=C+1
M=M+1
ITEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES
IOF N (0-77)

```

004674 010701  
004676 012706 000500  
004702 012705 004710  
004706 006424

```

SCOPE
MOV #STKPTR,SP INITIALIZE THE STACK POINTER
MOV #T127A,R5 ILOAD R5
MARK 24 JMARK N=24

```

```

.....
IFUNCTION RESULTS N=24
ITEMP = PC+(2*24) TEMP = #T127A+(2*24)
IPC = R5 PC = #T127A
ISP = TEMP SP = #T127A+(2*24)
JMS = (TEMP) R5 = (#T127A+(2*24))
JSP = TEMP+2 SP = #T127A+(2*24)+2
.....

```

004710 000240  
004712 010500  
004714 023705 004760  
004720 001401  
004722 000000

```

T127A1 NOP
MOV R5,R0 IGET CONTENTS OF R5
CMP ##2*24+T127A,R5 IR5 = (#T127A+(2*24))
REQ ,+4
HLT IR5 IMPROPERLY LOADED

```

000128  
000025

```

C=C+1
M=M+1
ITEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES
IOF N (0-77)

```

004724 010701  
004726 012706 000500  
004732 012705 004740  
004736 006425

```

SCOPE
MOV #STKPTR,SP INITIALIZE THE STACK POINTER
MOV #T130A,R5 ILOAD R5
MARK 25 JMARK N=25

```

```

.....
IFUNCTION RESULTS N=25
ITEMP = PC+(2*25) TEMP = #T130A+(2*25)
IPC = R5 PC = #T130A
ISP = TEMP SP = #T130A+(2*25)
JMS = (TEMP) R5 = (#T130A+(2*25))
JSP = TEMP+2 SP = #T130A+(2*25)+2
.....

```

004740 000240

T130A1 NOP

004742 010000  
004744 021715 005012  
004750 001401  
004752 000000

MOV R5,R0 IGET CONTENTS OF R5  
CMP #2\*25+T130A,R5 IR5 = (#T130A+(2\*25))?  
REQ .+4  
HLT IR5 IMPROPERLY LOADED

000131  
000026

C=C+1  
M=M+1

IFTEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES  
JOF N (0-77)

004754 010701  
004756 012706 000500  
004762 012705 004770  
004766 006426

SCOPE  
MOV #STKPTR,SP INITIALIZE THE STACK POINTER  
MOV #T131A,R5 ILOAD R5  
MARK 26 IMARK N=26

IFUNCTION RESULTS N=26  
IFEMP = PC+(2\*26) TEMP = #T131A+(2\*26)  
IPC = R5 PC = #T131A  
ISP = TEMP SP = #T131A+(2\*26)  
IR5 = (TEMP) R5 = (#T131A+(2\*26))  
ISP = TEMP+2 SP = #T131A+(2\*26)+2

004770 000240  
004772 010500  
004774 021705 005044  
005000 001401  
005002 000000

T131A: NOP  
MOV R5,R0 IGET CONTENTS OF R5  
CMP #2\*26+T131A,R5 IR5 = (#T131A+(2\*26))?  
REQ .+4  
HLT IR5 IMPROPERLY LOADED

000132  
000027

C=C+1  
M=M+1

IFTEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES  
JOF N (0-77)

005004 010701  
005006 012706 000500  
005012 012705 005020  
005016 006427

SCOPE  
MOV #STKPTR,SP INITIALIZE THE STACK POINTER  
MOV #T132A,R5 ILOAD R5  
MARK 27 IMARK N=27

IFUNCTION RESULTS N=27  
IFEMP = PC+(2\*27) TEMP = #T132A+(2\*27)  
IPC = R5 PC = #T132A  
ISP = TEMP SP = #T132A+(2\*27)  
IR5 = (TEMP) R5 = (#T132A+(2\*27))  
ISP = TEMP+2 SP = #T132A+(2\*27)+2

005020 000240  
005022 010500  
005024 021715 005076  
005030 001401  
005032 000000

T132A: NOP  
MOV R5,R0 IGET CONTENTS OF R5  
CMP #2\*27+T132A,R5 IR5 = (#T132A+(2\*27))?  
REQ .+4  
HLT IR5 IMPROPERLY LOADED

000133

C=C+1

000030

M=M+1

IFTEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES  
JOF N (0-77)

005034 010701  
005036 012706 000500  
005042 012705 005050  
005046 006430

SCOPE  
MOV #STKPTR,SP INITIALIZE THE STACK POINTER  
MOV #T133A,R5 ILOAD R5  
MARK 30 IMARK N=30

IFUNCTION RESULTS N=30  
IFEMP = PC+(2\*30) TEMP = #T133A+(2\*30)  
IPC = R5 PC = #T133A  
ISP = TEMP SP = #T133A+(2\*30)  
IR5 = (TEMP) R5 = (#T133A+(2\*30))  
ISP = TEMP+2 SP = #T133A+(2\*30)+2

005050 000240  
005052 010500  
005054 021705 005130  
005060 001401  
005062 000000

T133A: NOP  
MOV R5,R0 IGET CONTENTS OF R5  
CMP #2\*30+T133A,R5 IR5 = (#T133A+(2\*30))?  
REQ .+4  
HLT IR5 IMPROPERLY LOADED

000134  
000031

C=C+1  
M=M+1

IFTEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES  
JOF N (0-77)

005064 010701  
005066 012706 000500  
005072 012705 005100  
005076 006431

SCOPE  
MOV #STKPTR,SP INITIALIZE THE STACK POINTER  
MOV #T134A,R5 ILOAD R5  
MARK 31 IMARK N=31

IFUNCTION RESULTS N=31  
IFEMP = PC+(2\*31) TEMP = #T134A+(2\*31)  
IPC = R5 PC = #T134A  
ISP = TEMP SP = #T134A+(2\*31)  
IR5 = (TEMP) R5 = (#T134A+(2\*31))  
ISP = TEMP+2 SP = #T134A+(2\*31)+2

005100 000240  
005102 010500  
005104 021705 005162  
005110 001401  
005112 000000

T134A: NOP  
MOV R5,R0 IGET CONTENTS OF R5  
CMP #2\*31+T134A,R5 IR5 = (#T134A+(2\*31))?  
REQ .+4  
HLT IR5 IMPROPERLY LOADED

000135  
000032

C=C+1  
M=M+1

IFTEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES  
JOF N (0-77)

005114 010701  
005116 012706 000500  
005122 012705 005130

SCOPE  
MOV #STKPTR,SP INITIALIZE THE STACK POINTER  
MOV #T135A,R5 ILOAD R5

005126 006432

MARK 32 JMARK N=32

```

.....
IFUNCTION RESULTS N=32
ITEMP = PC*(2*32)      TEMP = #T135A*(2*32)
IPC = R5                PC = #T135A
ISP = TEMP              SP = #T135A*(2*32)
IR5 = (TEMP)           R5 = (#T135A*(2*32))
ISP = TEMP*2           SP = #T135A*(2*32)+2
.....

```

005130 000240  
005132 010500  
005134 023705 005214  
005140 001401  
005142 000000

```

T135A: NOP
      MOV R5,R0          IGET CONTENTS OF R5
      CMP #2*32+T135A,R5 IR5 = (#T135A*(2*32))
      BEQ ,+4
      HLT                IR5 IMPROPERLY LOADED

```

000136  
000033

```

C=C+1
M=M+1
ITEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES
IOF N (0-77)

```

005144 010701  
005146 012706 000500  
005152 012705 005160  
005156 006433

```

SCOPE
MOV #STKPTR,SP          INITIALIZE THE STACK POINTER
MOV #T136A,R5          ILOAD R5
MARK 33                JMARK N=33

```

```

.....
IFUNCTION RESULTS N=33
ITEMP = PC*(2*33)      TEMP = #T136A*(2*33)
IPC = R5                PC = #T136A
ISP = TEMP              SP = #T136A*(2*33)
IR5 = (TEMP)           R5 = (#T136A*(2*33))
ISP = TEMP*2           SP = #T136A*(2*33)+2
.....

```

005160 000240  
005162 010500  
005164 023705 005246  
005170 001401  
005172 000000

```

T136A: NOP
      MOV R5,R0          IGET CONTENTS OF R5
      CMP #2*33+T136A,R5 IR5 = (#T136A*(2*33))
      BEQ ,+4
      HLT                IR5 IMPROPERLY LOADED

```

000137  
000034

```

C=C+1
M=M+1
ITEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES
IOF N (0-77)

```

005174 010701  
005176 012706 000500  
005202 012705 005210  
005206 006434

```

SCOPE
MOV #STKPTR,SP          INITIALIZE THE STACK POINTER
MOV #T137A,R5          ILOAD R5
MARK 34                JMARK N=34

```

```

.....
IFUNCTION RESULTS N=34
ITEMP = PC*(2*34)      TEMP = #T137A*(2*34)
IPC = R5                PC = #T137A
.....

```

```

ISP = TEMP              SP = #T137A*(2*34)
IR5 = (TEMP)           R5 = (#T137A*(2*34))
ISP = TEMP*2           SP = #T137A*(2*34)+2
.....

```

005210 000240  
005212 010500  
005214 023705 005300  
005220 001401  
005222 000000

```

T137A: NOP
      MOV R5,R0          IGET CONTENTS OF R5
      CMP #2*34+T137A,R5 IR5 = (#T137A*(2*34))
      BEQ ,+4
      HLT                IR5 IMPROPERLY LOADED

```

000140  
000035

```

C=C+1
M=M+1
ITEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES
IOF N (0-77)

```

005224 010701  
005226 012706 000500  
005232 012705 005240  
005236 006435

```

SCOPE
MOV #STKPTR,SP          INITIALIZE THE STACK POINTER
MOV #T140A,R5          ILOAD R5
MARK 35                JMARK N=35

```

```

.....
IFUNCTION RESULTS N=35
ITEMP = PC*(2*35)      TEMP = #T140A*(2*35)
IPC = R5                PC = #T140A
ISP = TEMP              SP = #T140A*(2*35)
IR5 = (TEMP)           R5 = (#T140A*(2*35))
ISP = TEMP*2           SP = #T140A*(2*35)+2
.....

```

005240 000240  
005242 010500  
005244 023705 005332  
005250 001401  
005252 000000

```

T140A: NOP
      MOV R5,R0          IGET CONTENTS OF R5
      CMP #2*35+T140A,R5 IR5 = (#T140A*(2*35))
      BEQ ,+4
      HLT                IR5 IMPROPERLY LOADED

```

000141  
000036

```

C=C+1
M=M+1
ITEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES
IOF N (0-77)

```

005254 010701  
005256 012706 000500  
005262 012705 005270  
005266 006436

```

SCOPE
MOV #STKPTR,SP          INITIALIZE THE STACK POINTER
MOV #T141A,R5          ILOAD R5
MARK 36                JMARK N=36

```

```

.....
IFUNCTION RESULTS N=36
ITEMP = PC*(2*36)      TEMP = #T141A*(2*36)
IPC = R5                PC = #T141A
ISP = TEMP              SP = #T141A*(2*36)
IR5 = (TEMP)           R5 = (#T141A*(2*36))
ISP = TEMP*2           SP = #T141A*(2*36)+2
.....

```

005270 000240

T141A: NOP

005272 010500  
005274 021705 005364  
005300 001401  
005302 000000  
  
000142  
000037  
  
005304 010701  
005306 012700 000500  
005312 012705 005320  
005316 006437

MOV R5,R0 IGET CONTENTS OF R5  
CMP #2\*36+T141A,R5 IR5 = (#T141A\*(2\*36))?  
REQ ,\*4  
HLT IR5 IMPROPERLY LOADED

C=C+1  
M=M+1  
ITEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES  
IOF N (0-77)  
SCOPE  
MOV #STKPTR,SP INITIALIZE THE STACK POINTER  
MOV #T142A,R5 ILOAD R5  
MARK 37 IMARK N=37

\*\*\*\*\*  
IFUNCTION RESULTS N=37  
ITEMP = PC\*(2\*37) TEMP = #T142A\*(2\*37)  
IPC = R5 PC = #T142A  
ISP = TEMP SP = #T142A\*(2\*37)  
IR5 = (TEMP) R5 = (#T142A\*(2\*37))  
ISP = TEMP+2 SP = #T142A\*(2\*37)+2  
\*\*\*\*\*

005320 000240  
005322 010500  
005324 021705 005416  
005330 001401  
005332 000000  
  
000143  
000040  
  
005334 010701  
005336 012700 000500  
005342 012705 005350  
005346 006440

T142A: NOP  
MOV R5,R0 IGET CONTENTS OF R5  
CMP #2\*37+T142A,R5 IR5 = (#T142A\*(2\*37))?  
REQ ,\*4  
HLT IR5 IMPROPERLY LOADED

C=C+1  
M=M+1  
ITEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES  
IOF N (0-77)  
SCOPE  
MOV #STKPTR,SP INITIALIZE THE STACK POINTER  
MOV #T143A,R5 ILOAD R5  
MARK 40 IMARK N=40

\*\*\*\*\*  
IFUNCTION RESULTS N=40  
ITEMP = PC\*(2\*40) TEMP = #T143A\*(2\*40)  
IPC = R5 PC = #T143A  
ISP = TEMP SP = #T143A\*(2\*40)  
IR5 = (TEMP) R5 = (#T143A\*(2\*40))  
ISP = TEMP+2 SP = #T143A\*(2\*40)+2  
\*\*\*\*\*

005350 000240  
005352 010500  
005354 021705 005450  
005360 001401  
005362 000000  
  
000144

T143A: NOP  
MOV R5,R0 IGET CONTENTS OF R5  
CMP #2\*40+T143A,R5 IR5 = (#T143A\*(2\*40))?  
REQ ,\*4  
HLT IR5 IMPROPERLY LOADED

C=C+1

000041  
  
005364 010701  
005366 012700 000500  
005372 012705 005400  
005376 006441  
  
000145  
000042  
  
005414 010701  
005416 012700 000500  
005422 012705 005430  
005426 006442  
  
005430 000240  
005432 010500  
005434 021705 005534  
005440 001401  
005442 000000  
  
000146  
000043  
  
005444 010701  
005446 012700 000500  
005452 012705 005460

M=M+1  
ITEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES  
IOF N (0-77)  
SCOPE  
MOV #STKPTR,SP INITIALIZE THE STACK POINTER  
MOV #T144A,R5 ILOAD R5  
MARK 41 IMARK N=41

\*\*\*\*\*  
IFUNCTION RESULTS N=41  
ITEMP = PC\*(2\*41) TEMP = #T144A\*(2\*41)  
IPC = R5 PC = #T144A  
ISP = TEMP SP = #T144A\*(2\*41)  
IR5 = (TEMP) R5 = (#T144A\*(2\*41))  
ISP = TEMP+2 SP = #T144A\*(2\*41)+2  
\*\*\*\*\*

T144A: NOP  
MOV R5,R0 IGET CONTENTS OF R5  
CMP #2\*41+T144A,R5 IR5 = (#T144A\*(2\*41))?  
REQ ,\*4  
HLT IR5 IMPROPERLY LOADED

C=C+1  
M=M+1  
ITEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES  
IOF N (0-77)  
SCOPE  
MOV #STKPTR,SP INITIALIZE THE STACK POINTER  
MOV #T145A,R5 ILOAD R5  
MARK 42 IMARK N=42

\*\*\*\*\*  
IFUNCTION RESULTS N=42  
ITEMP = PC\*(2\*42) TEMP = #T145A\*(2\*42)  
IPC = R5 PC = #T145A  
ISP = TEMP SP = #T145A\*(2\*42)  
IR5 = (TEMP) R5 = (#T145A\*(2\*42))  
ISP = TEMP+2 SP = #T145A\*(2\*42)+2  
\*\*\*\*\*

T145A: NOP  
MOV R5,R0 IGET CONTENTS OF R5  
CMP #2\*42+T145A,R5 IR5 = (#T145A\*(2\*42))?  
REQ ,\*4  
HLT IR5 IMPROPERLY LOADED

C=C+1  
M=M+1  
ITEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES  
IOF N (0-77)  
SCOPE  
MOV #STKPTR,SP INITIALIZE THE STACK POINTER  
MOV #T146A,R5 ILOAD R5

005456 006443

MARK 43 JMARK N=43

```

.....
IFUNCTION RESULTS N=43
JTEMP = PC*(2+43)      TEMP = #T146A*(2+43)
JPC = R5                PC = #T146A
JSP = TEMP              SP = #T146A*(2+43)
JRS = (TEMP)           R5 = (#T146A*(2+43))
JSP = TEMP*2           SP = #T146A*(2+43)+2
.....

```

005460 000240  
005462 010500  
005464 023705 005566  
005470 001401  
005472 000000

```

T146A1 NOP
      MOV R5,R0          JGET CONTENTS OF R5
      CMP #2+43=T146A,R5 JR5 = (#T146A*(2+43))?
      BEQ ,+4
      HLT                JR5 IMPROPERLY LOADED

```

000147  
000044

```

C=C+1
M=M+1
JTEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES
IOF N (0-77)

```

005474 010701  
005476 012706 000500  
005542 012705 005510  
005546 006444

```

SCOPE
MOV #STKPTR,SP          JINITIALIZE THE STACK POINTER
MOV #T147A,R5           JLOAD R5
MARK 44                 JMARK N=44

```

```

.....
IFUNCTION RESULTS N=44
JTEMP = PC*(2+44)      TEMP = #T147A*(2+44)
JPC = R5                PC = #T147A
JSP = TEMP              SP = #T147A*(2+44)
JRS = (TEMP)           R5 = (#T147A*(2+44))
JSP = TEMP*2           SP = #T147A*(2+44)+2
.....

```

005510 000240  
005512 010500  
005514 023705 005620  
005520 001401  
005522 000000

```

T147A1 NOP
      MOV R5,R0          JGET CONTENTS OF R5
      CMP #2+44=T147A,R5 JR5 = (#T147A*(2+44))?
      BEQ ,+4
      HLT                JR5 IMPROPERLY LOADED

```

000150  
000045

```

C=C+1
M=M+1
JTEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES
IOF N (0-77)

```

005524 010701  
005526 012706 000500  
005542 012705 005540  
005546 006444

```

SCOPE
MOV #STKPTR,SP          JINITIALIZE THE STACK POINTER
MOV #T150A,R5           JLOAD R5
MARK 45                 JMARK N=45

```

```

.....
IFUNCTION RESULTS N=45
JTEMP = PC*(2+45)      TEMP = #T150A*(2+45)
JPC = R5                PC = #T150A

```

```

JSP = TEMP              SP = #T150A*(2+45)
JRS = (TEMP)           R5 = (#T150A*(2+45))
JSP = TEMP*2           SP = #T150A*(2+45)+2
.....

```

005540 000240  
005542 010500  
005544 023705 005652  
005550 001401  
005552 000000

```

T150A1 NOP
      MOV R5,R0          JGET CONTENTS OF R5
      CMP #2+45=T150A,R5 JR5 = (#T150A*(2+45))?
      BEQ ,+4
      HLT                JR5 IMPROPERLY LOADED

```

000151  
000046

```

C=C+1
M=M+1
JTEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES
IOF N (0-77)

```

005544 010701  
005546 012706 000500  
005562 012705 005570  
005566 006444

```

SCOPE
MOV #STKPTR,SP          JINITIALIZE THE STACK POINTER
MOV #T151A,R5           JLOAD R5
MARK 46                 JMARK N=46

```

```

.....
IFUNCTION RESULTS N=46
JTEMP = PC*(2+46)      TEMP = #T151A*(2+46)
JPC = R5                PC = #T151A
JSP = TEMP              SP = #T151A*(2+46)
JRS = (TEMP)           R5 = (#T151A*(2+46))
JSP = TEMP*2           SP = #T151A*(2+46)+2
.....

```

005570 000240  
005572 010500  
005574 023705 005704  
005640 001401  
005642 000000

```

T151A1 NOP
      MOV R5,R0          JGET CONTENTS OF R5
      CMP #2+46=T151A,R5 JR5 = (#T151A*(2+46))?
      BEQ ,+4
      HLT                JR5 IMPROPERLY LOADED

```

000152  
000047

```

C=C+1
M=M+1
JTEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES
IOF N (0-77)

```

005644 010701  
005646 012706 000500  
005612 012705 005620  
005616 006444

```

SCOPE
MOV #STKPTR,SP          JINITIALIZE THE STACK POINTER
MOV #T152A,R5           JLOAD R5
MARK 47                 JMARK N=47

```

```

.....
IFUNCTION RESULTS N=47
JTEMP = PC*(2+47)      TEMP = #T152A*(2+47)
JPC = R5                PC = #T152A
JSP = TEMP              SP = #T152A*(2+47)
JRS = (TEMP)           R5 = (#T152A*(2+47))
JSP = TEMP*2           SP = #T152A*(2+47)+2
.....

```

005620 000240

T152A1 NOP

57

005602 010500  
005604 023705 005716  
005610 001401  
005612 000000

MOV R5,R0 IGET CONTENTS OF R5  
CMP #2+47+T152A,R5 IR5 = (#T152A+(2+47))  
REQ ,+4  
HLT IR5 IMPROPERLY LOADED

000153  
000050

C=C+1  
M=M+1  
ITEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES  
IOF N (0-77)

005634 010701  
005636 012706 005650  
005642 012705 005650  
005646 006450

SCOPE  
MOV #STKPTR,SP INITIALIZE THE STACK POINTER  
MOV #T153A,R5 ILOAD R5  
MARK 50 IMARK N=50

IFUNCTION RESULTS N=50  
ITEMP = PC+(2\*50) TEMP = #T153A+(2\*50)  
IPC = R5 PC = #T153A  
ISP = TEMP SP = #T153A+(2\*50)  
IR5 = (TEMP) R5 = (#T153A+(2\*50))  
ISP = TEMP+2 SP = #T153A+(2\*50)+2

005650 000240  
005652 010500  
005654 023705 005770  
005660 001401  
005662 000000

T153A| NOP  
MOV R5,R0 IGET CONTENTS OF R5  
CMP #2+50+T153A,R5 IR5 = (#T153A+(2\*50))  
REQ ,+4  
HLT IR5 IMPROPERLY LOADED

000154  
000051

C=C+1  
M=M+1  
ITEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES  
IOF N (0-77)

005664 010701  
005666 012706 005650  
005672 012705 005700  
005676 006451

SCOPE  
MOV #STKPTR,SP INITIALIZE THE STACK POINTER  
MOV #T154A,R5 ILOAD R5  
MARK 51 IMARK N=51

IFUNCTION RESULTS N=51  
ITEMP = PC+(2\*51) TEMP = #T154A+(2\*51)  
IPC = R5 PC = #T154A  
ISP = TEMP SP = #T154A+(2\*51)  
IR5 = (TEMP) R5 = (#T154A+(2\*51))  
ISP = TEMP+2 SP = #T154A+(2\*51)+2

005700 000240  
005702 010500  
005704 023705 006022  
005710 001401  
005712 000000

T154A| NOP  
MOV R5,R0 IGET CONTENTS OF R5  
CMP #2+51+T154A,R5 IR5 = (#T154A+(2\*51))  
REQ ,+4  
HLT IR5 IMPROPERLY LOADED

000155

C=C+1

000052

M=M+1  
ITEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES  
IOF N (0-77)

005714 010701  
005716 012706 005900  
005722 012705 005730  
005726 006452

SCOPE  
MOV #STKPTR,SP INITIALIZE THE STACK POINTER  
MOV #T155A,R5 ILOAD R5  
MARK 52 IMARK N=52

IFUNCTION RESULTS N=52  
ITEMP = PC+(2\*52) TEMP = #T155A+(2\*52)  
IPC = R5 PC = #T155A  
ISP = TEMP SP = #T155A+(2\*52)  
IR5 = (TEMP) R5 = (#T155A+(2\*52))  
ISP = TEMP+2 SP = #T155A+(2\*52)+2

005730 000240  
005732 010500  
005734 023705 006054  
005740 001401  
005742 000000

T155A| NOP  
MOV R5,R0 IGET CONTENTS OF R5  
CMP #2+52+T155A,R5 IR5 = (#T155A+(2\*52))  
REQ ,+4  
HLT IR5 IMPROPERLY LOADED

000156  
000053

C=C+1  
M=M+1  
ITEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES  
IOF N (0-77)

005744 010701  
005746 012706 005900  
005752 012705 005760  
005756 006453

SCOPE  
MOV #STKPTR,SP INITIALIZE THE STACK POINTER  
MOV #T156A,R5 ILOAD R5  
MARK 53 IMARK N=53

IFUNCTION RESULTS N=53  
ITEMP = PC+(2\*53) TEMP = #T156A+(2\*53)  
IPC = R5 PC = #T156A  
ISP = TEMP SP = #T156A+(2\*53)  
IR5 = (TEMP) R5 = (#T156A+(2\*53))  
ISP = TEMP+2 SP = #T156A+(2\*53)+2

005760 000240  
005762 010500  
005764 023705 006106  
005770 001401  
005772 000000

T156A| NOP  
MOV R5,R0 IGET CONTENTS OF R5  
CMP #2+53+T156A,R5 IR5 = (#T156A+(2\*53))  
REQ ,+4  
HLT IR5 IMPROPERLY LOADED

000157  
000054

C=C+1  
M=M+1  
ITEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES  
IOF N (0-77)

005774 010701  
005776 012706 005900  
005782 012705 005910

SCOPE  
MOV #STKPTR,SP INITIALIZE THE STACK POINTER  
MOV #T157A,R5 ILOAD R5

58



006006 006454

MARK 54 JMARK N=54

```

.....
IFUNCTION RESULTS N=54
ITEMP = PC*(2*54)      TEMP = #T157A*(2*54)
IPC = R5                PC = #T157A
ISP = TEMP              SP = #T157A*(2*54)
IR5 = (TEMP)           R5 = (#T157A*(2*54))
ISP = TEMP+2           SP = #T157A*(2*54)+2
.....

```

006010 000240  
006012 010500  
006014 023705 006140  
006020 001401  
006022 000000

```

T157A) NOP
      MOV R5,R0          IGET CONTENTS OF R5
      CMP #2*54+T157A,R5 IR5 = (#T157A*(2*54))
      BEQ ,*4
      HLT                IR5 IMPROPERLY LOADED

```

000160  
000055

```

C=C+1
M=M+1
ITEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES
IOF N (0-77)

```

006024 010701  
006026 012706 000500  
006032 012705 006040  
006036 006455

```

SCOPE
MOV #STKPTR,SP          IINITIALIZE THE STACK POINTER
MOV #T160A,R5           ILOAD R5
MARK 55                 IMARK N=55

```

```

.....
IFUNCTION RESULTS N=55
ITEMP = PC*(2*55)      TEMP = #T160A*(2*55)
IPC = R5                PC = #T160A
ISP = TEMP              SP = #T160A*(2*55)
IR5 = (TEMP)           R5 = (#T160A*(2*55))
ISP = TEMP+2           SP = #T160A*(2*55)+2
.....

```

006040 000240  
006042 010500  
006044 023705 006172  
006050 001401  
006052 000000

```

T160A) NOP
      MOV R5,R0          IGET CONTENTS OF R5
      CMP #2*55+T160A,R5 IR5 = (#T160A*(2*55))
      BEQ ,*4
      HLT                IR5 IMPROPERLY LOADED

```

000161  
000056

```

C=C+1
M=M+1
ITEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES
IOF N (0-77)

```

006054 010701  
006056 012706 000500  
006062 012705 006070  
006066 006456

```

SCOPE
MOV #STKPTR,SP          IINITIALIZE THE STACK POINTER
MOV #T161A,R5           ILOAD R5
MARK 56                 IMARK N=56

```

```

.....
IFUNCTION RESULTS N=56
ITEMP = PC*(2*56)      TEMP = #T161A*(2*56)
IPC = R5                PC = #T161A

```

```

.....
IR5 = (TEMP)           R5 = (#T161A*(2*56))
ISP = TEMP+2           SP = #T161A*(2*56)+2
.....

```

006070 000240  
006072 010500  
006074 023705 006224  
006100 001401  
006102 000000

```

T161A) NOP
      MOV R5,R0          IGET CONTENTS OF R5
      CMP #2*56+T161A,R5 IR5 = (#T161A*(2*56))
      BEQ ,*4
      HLT                IR5 IMPROPERLY LOADED

```

000162  
000057

```

C=C+1
M=M+1
ITEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES
IOF N (0-77)

```

006104 010701  
006106 012706 000500  
006112 012705 006120  
006116 006457

```

SCOPE
MOV #STKPTR,SP          IINITIALIZE THE STACK POINTER
MOV #T162A,R5           ILOAD R5
MARK 57                 IMARK N=57

```

```

.....
IFUNCTION RESULTS N=57
ITEMP = PC*(2*57)      TEMP = #T162A*(2*57)
IPC = R5                PC = #T162A
ISP = TEMP              SP = #T162A*(2*57)
IR5 = (TEMP)           R5 = (#T162A*(2*57))
ISP = TEMP+2           SP = #T162A*(2*57)+2
.....

```

006120 000240  
006122 010500  
006124 023705 006256  
006150 001401  
006152 000000

```

T162A) NOP
      MOV R5,R0          IGET CONTENTS OF R5
      CMP #2*57+T162A,R5 IR5 = (#T162A*(2*57))
      BEQ ,*4
      HLT                IR5 IMPROPERLY LOADED

```

000163  
000060

```

C=C+1
M=M+1
ITEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES
IOF N (0-77)

```

006134 010701  
006136 012706 000500  
006142 012705 006150  
006146 006460

```

SCOPE
MOV #STKPTR,SP          IINITIALIZE THE STACK POINTER
MOV #T163A,R5           ILOAD R5
MARK 60                 IMARK N=60

```

```

.....
IFUNCTION RESULTS N=60
ITEMP = PC*(2*60)      TEMP = #T163A*(2*60)
IPC = R5                PC = #T163A
ISP = TEMP              SP = #T163A*(2*60)
IR5 = (TEMP)           R5 = (#T163A*(2*60))
ISP = TEMP+2           SP = #T163A*(2*60)+2
.....

```

006150 000240

T163A) NOP

59

000152 019500  
000154 023705 000310  
000160 001401  
000162 000000

MOV R5,R0 IGET CONTENTS OF R5  
CMP #2\*60+T163A,R5 IR5 = (#T163A\*(2\*60))?  
REQ ,+4  
HLT IR5 IMPROPERLY LOADED

000164  
000061

C=C+1  
M=M+1

ITEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES  
IOF N (0-77)

000164 010701  
000166 012706 000500  
000172 012705 000200  
000176 000461

SCOPE  
MOV #STKPTR,SP INITIALIZE THE STACK POINTER  
MOV #T164A,R5 ILOAD R5  
MARK 61 IMARK N=61

.....  
IFUNCTION RESULTS N=61  
ITEMP = PC\*(2\*61) TEMP = #T164A\*(2\*61)  
IPC = R5 PC = #T164A  
ISP = TEMP SP = #T164A\*(2\*61)  
IR5 = (TEMP) R5 = (#T164A\*(2\*61))  
ISP = TEMP+2 SP = #T164A\*(2\*61)+2  
.....

000200 000240  
000202 010500  
000204 023705 000342  
000210 001401  
000212 000000

T164A1 NOP  
MOV R5,R0 IGET CONTENTS OF R5  
CMP #2\*61+T164A,R5 IR5 = (#T164A\*(2\*61))?  
REQ ,+4  
HLT IR5 IMPROPERLY LOADED

000165  
000062

C=C+1  
M=M+1

ITEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES  
IOF N (0-77)

000214 010701  
000216 012706 000500  
000222 012705 000200  
000226 000462

SCOPE  
MOV #STKPTR,SP INITIALIZE THE STACK POINTER  
MOV #T165A,R5 ILOAD R5  
MARK 62 IMARK N=62

.....  
IFUNCTION RESULTS N=62  
ITEMP = PC\*(2\*62) TEMP = #T165A\*(2\*62)  
IPC = R5 PC = #T165A  
ISP = TEMP SP = #T165A\*(2\*62)  
IR5 = (TEMP) R5 = (#T165A\*(2\*62))  
ISP = TEMP+2 SP = #T165A\*(2\*62)+2  
.....

000230 000240  
000232 010500  
000234 023705 000374  
000240 001401  
000242 000000

T165A1 NOP  
MOV R5,R0 IGET CONTENTS OF R5  
CMP #2\*62+T165A,R5 IR5 = (#T165A\*(2\*62))?  
REQ ,+4  
HLT IR5 IMPROPERLY LOADED

000166

C=C+1

000063

M=M+1  
ITEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES  
IOF N (0-77)

000244 010701  
000246 012706 000500  
000252 012705 000200  
000256 000463

SCOPE  
MOV #STKPTR,SP INITIALIZE THE STACK POINTER  
MOV #T166A,R5 ILOAD R5  
MARK 63 IMARK N=63

.....  
IFUNCTION RESULTS N=63  
ITEMP = PC\*(2\*63) TEMP = #T166A\*(2\*63)  
IPC = R5 PC = #T166A  
ISP = TEMP SP = #T166A\*(2\*63)  
IR5 = (TEMP) R5 = (#T166A\*(2\*63))  
ISP = TEMP+2 SP = #T166A\*(2\*63)+2  
.....

000260 000240  
000262 010500  
000264 023705 000426  
000270 001401  
000272 000000

T166A1 NOP  
MOV R5,R0 IGET CONTENTS OF R5  
CMP #2\*63+T166A,R5 IR5 = (#T166A\*(2\*63))?  
REQ ,+4  
HLT IR5 IMPROPERLY LOADED

000167  
000064

C=C+1  
M=M+1

ITEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES  
IOF N (0-77)

000274 010701  
000276 012706 000500  
000282 012705 000310  
000286 000464

SCOPE  
MOV #STKPTR,SP INITIALIZE THE STACK POINTER  
MOV #T167A,R5 ILOAD R5  
MARK 64 IMARK N=64

.....  
IFUNCTION RESULTS N=64  
ITEMP = PC\*(2\*64) TEMP = #T167A\*(2\*64)  
IPC = R5 PC = #T167A  
ISP = TEMP SP = #T167A\*(2\*64)  
IR5 = (TEMP) R5 = (#T167A\*(2\*64))  
ISP = TEMP+2 SP = #T167A\*(2\*64)+2  
.....

000310 000240  
000312 010500  
000314 023705 000460  
000320 001401  
000322 000000

T167A1 NOP  
MOV R5,R0 IGET CONTENTS OF R5  
CMP #2\*64+T167A,R5 IR5 = (#T167A\*(2\*64))?  
REQ ,+4  
HLT IR5 IMPROPERLY LOADED

000170  
000065

C=C+1  
M=M+1

ITEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES  
IOF N (0-77)

000324 010701  
000326 012706 000500  
000332 012705 000340

SCOPE  
MOV #STKPTR,SP INITIALIZE THE STACK POINTER  
MOV #T170A,R5 ILOAD R5

206336 006465

MARK 65 JMARK N=65

```

.....
IFUNCTION RESULTS N=65
JTEMP = PC*(2*65)      TEMP = #T170A*(2*65)
IPC = R5                PC = #T170A
JSP = TEMP              SP = #T170A*(2*65)
JRS = (TEMP)           R5 = (#T170A*(2*65))
JSP = TEMP*2           SP = #T170A*(2*65)+2
.....

```

206340 000240  
 206342 010500  
 206344 021705 006512  
 206350 001401  
 206352 000000

```

T170A| NOP
      MOV R5,R0          IGET CONTENTS OF R5
      CMP #2*65+T170A,R5 IR5 = (#T170A*(2*65))
      BEQ ,+4
      MLT                IR5 IMPROPERLY LOADED

```

000171  
000066

```

C=C+1
M=M+1
JFST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES
JOF N (0-77)

```

206354 010701  
 206356 012706 000500  
 206362 012705 006378  
 206366 006466

```

SCOPE
MOV #STKPTR,SP          INITIALIZE THE STACK POINTER
MOV #T171A,R5          ILOAD R5
MARK 66                JMARK N=66

```

```

.....
IFUNCTION RESULTS N=66
JTEMP = PC*(2*66)      TEMP = #T171A*(2*66)
IPC = R5                PC = #T171A
JSP = TEMP              SP = #T171A*(2*66)
JRS = (TEMP)           R5 = (#T171A*(2*66))
JSP = TEMP*2           SP = #T171A*(2*66)+2
.....

```

206370 000240  
 206372 010500  
 206374 021705 006544  
 206410 001401  
 206412 000000

```

T171A| NOP
      MOV R5,R0          IGET CONTENTS OF R5
      CMP #2*66+T171A,R5 IR5 = (#T171A*(2*66))
      BEQ ,+4
      MLT                IR5 IMPROPERLY LOADED

```

000172  
000067

```

C=C+1
M=M+1
JFST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES
JOF N (0-77)

```

206404 010701  
 206406 012706 000500  
 206412 012705 006420  
 206416 006467

```

SCOPE
MOV #STKPTR,SP          INITIALIZE THE STACK POINTER
MOV #T172A,R5          ILOAD R5
MARK 67                JMARK N=67

```

```

.....
IFUNCTION RESULTS N=67
JTEMP = PC*(2*67)      TEMP = #T172A*(2*67)
IPC = R5                PC = #T172A

```

```

.....
JSP = TEMP              SP = #T172A*(2*67)
JRS = (TEMP)           R5 = (#T172A*(2*67))
JSP = TEMP*2           SP = #T172A*(2*67)+2
.....

```

206420 000240  
 206422 010500  
 206424 021705 006576  
 206430 001401  
 206432 000000

```

T172A| NOP
      MOV R5,R0          IGET CONTENTS OF R5
      CMP #2*67+T172A,R5 IR5 = (#T172A*(2*67))
      BEQ ,+4
      MLT                IR5 IMPROPERLY LOADED

```

000173  
000070

```

C=C+1
M=M+1
JFST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES
JOF N (0-77)

```

206434 010701  
 206436 012706 000500  
 206442 012705 006458  
 206446 006470

```

SCOPE
MOV #STKPTR,SP          INITIALIZE THE STACK POINTER
MOV #T173A,R5          ILOAD R5
MARK 70                JMARK N=70

```

```

.....
IFUNCTION RESULTS N=70
JTEMP = PC*(2*70)      TEMP = #T173A*(2*70)
IPC = R5                PC = #T173A
JSP = TEMP              SP = #T173A*(2*70)
JRS = (TEMP)           R5 = (#T173A*(2*70))
JSP = TEMP*2           SP = #T173A*(2*70)+2
.....

```

206450 000240  
 206452 010500  
 206454 021705 006630  
 206460 001401  
 206462 000000

```

T173A| NOP
      MOV R5,R0          IGET CONTENTS OF R5
      CMP #2*70+T173A,R5 IR5 = (#T173A*(2*70))
      BEQ ,+4
      MLT                IR5 IMPROPERLY LOADED

```

000174  
000071

```

C=C+1
M=M+1
JFST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES
JOF N (0-77)

```

206464 010701  
 206466 012706 000500  
 206472 012705 006500  
 206476 006471

```

SCOPE
MOV #STKPTR,SP          INITIALIZE THE STACK POINTER
MOV #T174A,R5          ILOAD R5
MARK 71                JMARK N=71

```

```

.....
IFUNCTION RESULTS N=71
JTEMP = PC*(2*71)      TEMP = #T174A*(2*71)
IPC = R5                PC = #T174A
JSP = TEMP              SP = #T174A*(2*71)
JRS = (TEMP)           R5 = (#T174A*(2*71))
JSP = TEMP*2           SP = #T174A*(2*71)+2
.....

```

206500 000240

T174A| NOP

61

006502 010500  
006504 023705 006602  
006510 001401  
006512 000000

MOV R5,R0 IGFT CONTENTS OF R5  
CMP #2\*71+T174A,R5 IR5 = (#T174A\*(2\*71))?  
REQ ,\*4  
HLT IR5 IMPROPERLY LOADED

000175  
000072

C=C+1  
M=M+1

ITEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES  
IOF N (0-77)

006514 110701  
006516 012706 000500  
006522 012705 006530  
006526 006472

SCOPE  
MOV #STKPTR,SP INITIALIZE THE STACK POINTER  
MOV #T175A,R5 ILOAD R5  
MARK 72 IMARK N=72

.....  
IFUNCTION RESULTS N=72  
IFEMP = PC\*(2\*72) TEMP = #T175A\*(2\*72)  
IPC = R5 PC = #T175A  
ISP = TEMP SP = #T175A\*(2\*72)  
IR5 = (TEMP) R5 = (#T175A\*(2\*72))  
ISP = TEMP+2 SP = #T175A\*(2\*72)+2  
.....

006530 000240  
006532 010500  
006534 023705 006714  
006540 001401  
006542 000000

T175A) NOP  
MOV R5,R0 IGFT CONTENTS OF R5  
CMP #2\*72+T175A,R5 IR5 = (#T175A\*(2\*72))?  
REQ ,\*4  
HLT IR5 IMPROPERLY LOADED

000176  
000073

C=C+1  
M=M+1

ITEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES  
IOF N (0-77)

006544 010701  
006546 012706 000500  
006552 012705 006560  
006556 006473

SCOPE  
MOV #STKPTR,SP INITIALIZE THE STACK POINTER  
MOV #T176A,R5 ILOAD R5  
MARK 73 IMARK N=73

.....  
IFUNCTION RESULTS N=73  
IFEMP = PC\*(2\*73) TEMP = #T176A\*(2\*73)  
IPC = R5 PC = #T176A  
ISP = TEMP SP = #T176A\*(2\*73)  
IR5 = (TEMP) R5 = (#T176A\*(2\*73))  
ISP = TEMP+2 SP = #T176A\*(2\*73)+2  
.....

006560 000240  
006562 010500  
006564 023705 006746  
006570 001401  
006572 000000

T176A) NOP  
MOV R5,R0 IGFT CONTENTS OF R5  
CMP #2\*73+T176A,R5 IR5 = (#T176A\*(2\*73))?  
REQ ,\*4  
HLT IR5 IMPROPERLY LOADED

000177

C=C+1

000074

006574 010701  
006576 012706 000500  
006582 012705 006610  
006600 006474

M=M+1  
ITEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES  
IOF N (0-77)

SCOPE  
MOV #STKPTR,SP INITIALIZE THE STACK POINTER  
MOV #T177A,R5 ILOAD R5  
MARK 74 IMARK N=74

.....  
IFUNCTION RESULTS N=74  
IFEMP = PC\*(2\*74) TEMP = #T177A\*(2\*74)  
IPC = R5 PC = #T177A  
ISP = TEMP SP = #T177A\*(2\*74)  
IR5 = (TEMP) R5 = (#T177A\*(2\*74))  
ISP = TEMP+2 SP = #T177A\*(2\*74)+2  
.....

006610 000240  
006612 010500  
006614 023705 007000  
006620 001401  
006622 000000

T177A) NOP  
MOV R5,R0 IGFT CONTENTS OF R5  
CMP #2\*74+T177A,R5 IR5 = (#T177A\*(2\*74))?  
REQ ,\*4  
HLT IR5 IMPROPERLY LOADED

000200  
000075

C=C+1  
M=M+1

ITEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES  
IOF N (0-77)

006624 010701  
006626 012706 000500  
006632 012705 006640  
006636 006475

SCOPE  
MOV #STKPTR,SP INITIALIZE THE STACK POINTER  
MOV #T200A,R5 ILOAD R5  
MARK 75 IMARK N=75

.....  
IFUNCTION RESULTS N=75  
IFEMP = PC\*(2\*75) TEMP = #T200A\*(2\*75)  
IPC = R5 PC = #T200A  
ISP = TEMP SP = #T200A\*(2\*75)  
IR5 = (TEMP) R5 = (#T200A\*(2\*75))  
ISP = TEMP+2 SP = #T200A\*(2\*75)+2  
.....

006640 000240  
006642 010500  
006644 023705 007032  
006650 001401  
006652 000000

T200A) NOP  
MOV R5,R0 IGFT CONTENTS OF R5  
CMP #2\*75+T200A,R5 IR5 = (#T200A\*(2\*75))?  
REQ ,\*4  
HLT IR5 IMPROPERLY LOADED

000201  
000076

C=C+1  
M=M+1

ITEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES  
IOF N (0-77)

006654 010701  
006656 012706 000500  
006662 012705 006670

SCOPE  
MOV #STKPTR,SP INITIALIZE THE STACK POINTER  
MOV #T221A,R5 ILOAD R5

006666 006476

MARK 76 JMARK N=76

```

.....
IFUNCTION RESULTS N=76
ITEMP = PC+(2*76)      TEMP = #T201A+(2*76)
IPC = R5                PC = #T201A
ISP = TEMP              SP = #T201A+(2*76)
IR5 = (TEMP)           R5 = (#T201A+(2*76))
ISP = TEMP+2           SP = #T201A+(2*76)+2
.....
    
```

006670 000240  
006672 010500  
006674 023705 007064  
006678 001401  
006702 000000

```

T201A| NOP
      MOV R5,R0      IGET CONTENTS OF R5
      CMP #2*76+T201A,R5 IR5 = (#T201A+(2*76))
      BEQ ,+4
      HLT           IR5 IMPROPERLY LOADED
    
```

000202  
000077

```

      C=C+1
      M=M+1
ITEST THAT R5 IS PROPERLY LOADED BY THE MARK INSTRUCTION FOR ALL VALUES
OF N (0-77)
    
```

006704 010701  
006706 012706 000500  
006712 012705 006720  
006716 006477

```

      SCOPE
      MOV #STKPTR,SP  IINITIALIZE THE STACK POINTER
      MOV #T202A,R5   ILOAD R5
      MARK 77         IMARK N=77
    
```

```

.....
IFUNCTION RESULTS N=77
ITEMP = PC+(2*77)      TEMP = #T202A+(2*77)
IPC = R5                PC = #T202A
ISP = TEMP              SP = #T202A+(2*77)
IR5 = (TEMP)           R5 = (#T202A+(2*77))
ISP = TEMP+2           SP = #T202A+(2*77)+2
.....
    
```

006720 000240  
006722 010500  
006724 023705 007116  
006728 001401  
006732 000000

```

T202A| NOP
      MOV R5,R0      IGET CONTENTS OF R5
      CMP #2*77+T202A,R5 IR5 = (#T202A+(2*77))
      BEQ ,+4
      HLT           IR5 IMPROPERLY LOADED
    
```

000203  
000100

```

      C=C+1
      M=M+1
ITEST THAT MARK RETURNS FROM SUBROUTINE PROPERLY
    
```

006734 010701  
006736 012706 000500  
006742 012705 000007  
006746 010546  
006750 012746 006400  
006754 010605  
006756 004767 000002  
006762 004001  
006764 000205

```

T203I| SCOPE
      MOV #STKPTR,SP  IINITIALIZE THE STACK POINTER
      MOV #7,R5       ILOAD R5
      MOV R5,-(SP)    ISAVE CURRENT R5
      MOV #MARK0,-(SP) IPUSH MARK ON THE STACK (N=0)
      MOV SP,R5       ISET UP PARAMETER POINTER
      JSR 7,T203A     IGO TO SUBROUTINE
      BR T204
T203A| RTS 5         IEXIT SUBROUTINE
    
```

006766 010701  
006770 012706 000500  
006774 012705 000007  
006780 010546  
006782 012746 006400  
006786 010605  
006790 004767 000002  
006794 004001  
006796 000205  
006800 010600 000500  
006804 004767 000002  
006808 004001  
006812 000205

```

ITEST THAT SUBROUTINE LEAVES STACK POINTER POSITIONED PROPERLY
T204I| SCOPE
      MOV #STKPTR,SP  IINITIALIZE THE STACK POINTER
      MOV #7,R5       ILOAD R5
      MOV R5,-(SP)    ISAVE CURRENT R5
      MOV #MARK0,-(SP) IPUSH MARK ON THE STACK (N=0)
      MOV SP,R5       ISET UP PARAMETER POINTER
      JSR 7,T204A     IGO TO SUBROUTINE
      BR T204R
T204A| RTS 5         IEXIT SUBROUTINE
T204B| MOV SP,R0      IGET STACK POINTER
      CMP #STKPTR,R0  ISTACK POINTER AT PROPER POSITION?
      BEQ T205
      HLT           IERROR INCORRECT STACK POINTER
    
```

006802 010701  
006804 012706 000500  
006808 012705 000007  
006812 010546  
006816 012746 006400  
006820 010605  
006824 004767 000002  
006828 004001  
006832 000205  
006836 010500 000007  
006840 022700  
006844 001401  
006848 000000

```

ITEST THAT SUBROUTINE LEAVES R5 UNDISTURBED
T205I| SCOPE
      MOV #STKPTR,SP  IINITIALIZE THE STACK POINTER
      MOV #7,R5       ILOAD R5
      MOV R5,-(SP)    ISAVE CURRENT R5
      MOV #MARK1,-(SP) IPUSH MARK ON THE STACK (N=1)
      MOV SP,R5       ISET UP PARAMETER POINTER
      JSR 7,T205A     IGO TO SUBROUTINE
      BR T205B
T205A| RTS 5         IEXIT SUBROUTINE
T205B| MOV R5,R0      IGET R5
      CMP #7,R0       Iwas R5 = TO #7?
      BEQ T206
      HLT           IERROR CONTENT ARE INCORRECT
    
```

006850 010701  
006852 000000  
006854 012705 000007  
006858 010546  
006862 012746 006401  
006866 010605  
006870 004767 000002  
006874 004001 000007  
006878 022700  
006882 001401  
006886 000000

```

ITEST THAT SUBROUTINE CAN 'PICK' A PARAMETER
T206I| SCOPE
      CLR R0
      MOV #7,R5       ILOAD R5
      MOV R5,-(SP)    ISAVE CURRENT R5
      MOV #1,-(SP)    IPUSH A PARAMETER ON THE STACK
      MOV #MARK1,-(SP) IPUSH MARK ON THE STACK (N=1)
      MOV SP,R5       ISET UP PARAMETER POINTER
      JSR 7,T206A     IGO TO SUBROUTINE
      BR T206B
T206A| MOV 2(0),R0    IGET THE PARAMETER
      RTS 5         IEXIT SUBROUTINE
T206B| CMP #1,R0     IODI SUBROUTINE GET THE PARAMETER
      BEQ T207
      HLT
    
```

006890 010701  
006892 012706 000500  
006894 012705 007166  
006898 005007 170612  
006902 004767  
006906 014700 170604

```

ITEST THAT MARK DOES NOT CHANGE THE PSW
T207I| SCOPE
      MOV #STKPTR,SP  IINITIALIZE THE STACK POINTER
      MOV #T207A,R5
      CLR PSW
      MARK 0
T207A| MOV PSW,R0    IGET PSW
    
```

```

#07172 #01401          REG    T210
#07174 #00000          HLT

IFST THAT MARK DOFS NOT CHANGE THE PSW
T210I SCOPE
MOV #STKPTR,SP      INITIALIZE TH ESTACK POINTER
MOV #T210A,R5
MOV #357,PSW
MARK #
T210AI MOV #PSW,R0      IGET PSW RESULTS
MOV #STKPTR,SP     IRFSET STACK PTR
CMP #357,R0        IDID PSW CHANGE
BEQ .,04
HLT

#07240 #05267 171534   ENDI   INC    ICNT      IINCREMENT PASS COUNT
#07244 #26727 171530   #70000 CMP    ICNT,#70000
#07252 #01402          REG    DONE      IGO TO DONE IF 1000 PASSES COMPLETED
#07254 #00167 171526   JMP    BEGIN
#07260 112767 #00007 170300 DONEI MOVB  #7,TPBUF     IRING BELL
#07266 105767 170272   TSTB  TPCSR
#07272 100375          BPL    .-4
#07274 #13742 #00042   MOV    #42,X2      IGET DECTAPE MONITOR RETURN ADDRESS
#07300 #01404          REG    DONE1     IDO NOT RETURN IF (42)=0
#07302 #04712          JSR    7,(2)       IRETURN TO DECTAPE MONITOR
#07306 #00240          NOP
#07310 #00240          NOP
#07312 #00167 171444   DONEII JHF  #TARY
#00001          .END
    
```

BEGIN	#0100A	C	#000207	DISPLA	# 177570	DONE	#07260
DONE1	#07312	FND	#07240	HLT	#00000	ICNT	#01000
H	#00100	MARKP	#006400	MARK1	#006401	MARK2	#006402
PC	#000007	PSW	# 177776	R0	#000000	R1	#000001
R2	#000002	R3	#000003	R4	#000004	R5	#000005
SCOPE	# 210701	SP	#000006	START	#01002	STKPTR	#000500
SWP	# 177577	TPHUF	# 177566	TPCSP	# 177564	T0A	#01054
T1A	#01072	T10A	#01326	T100A	#03646	T101A	#03674
T102A	#03722	T103A	#005750	T104A	#04000	T105A	#04030
T106A	#04060	T107A	#004110	T11A	#01354	T110A	#04140
T111A	#04177	T112A	#04220	T113A	#04250	T114A	#04300
T115A	#04330	T116A	#04360	T117A	#04410	T12A	#01402
T120A	#04440	T121A	#04470	T122A	#04520	T123A	#04550
T124A	#04600	T125A	#04630	T126A	#04660	T127A	#04710
T13A	#01430	T130A	#04740	T131A	#04770	T132A	#05020
T133A	#05050	T134A	#05100	T135A	#05130	T136A	#05160
T137A	#05210	T14A	#05456	T140A	#05240	T141A	#05270
T142A	#05320	T143A	#05350	T144A	#05400	T145A	#05430
T146A	#05460	T147A	#05510	T15A	#01504	T150A	#05540
T151A	#05570	T152A	#05620	T153A	#05650	T154A	#05700
T155A	#05730	T156A	#05760	T157A	#00010	T16A	#01532
T160A	#06040	T161A	#06070	T162A	#06120	T163A	#06150
T164A	#06200	T165A	#06230	T166A	#06260	T167A	#06310
T17A	#01560	T170A	#06340	T171A	#06370	T172A	#06420
T173A	#06450	T174A	#06500	T175A	#06530	T176A	#06560
T177A	#06610	T2A	#02120	T20A	#01606	T200A	#06640
T201A	#06670	T202A	#06720	T203	#06734	T203A	#06764
T204	#06766	T204A	#07016	T204H	#07020	T205	#07032
T205A	#07062	T205H	#07064	T206	#07076	T206A	#07130
T2000	#07136	T207	#07146	T207A	#07166	T21A	#01634
T210	#07176	T210A	#07220	T22A	#01662	T23A	#01710
T24A	#01736	T25A	#01764	T26A	#02012	T27A	#02040
T3A	#01150	T30A	#02066	T31A	#02114	T32A	#02142
T33A	#02170	T34A	#02216	T35A	#02244	T36A	#02272
T37A	#02320	T4A	#01176	T40A	#02344	T41A	#02374
T42A	#02420	T43A	#02450	T44A	#02476	T45A	#02524
T46A	#02552	T47A	#02600	T5A	#01224	T50A	#02626
T51A	#02654	T52A	#02700	T53A	#02730	T54A	#02756
T55A	#03004	T56A	#03030	T57A	#03060	T6A	#01252
T60A	#03100	T61A	#03134	T62A	#03162	T63A	#03210
T64A	#03236	T65A	#03264	T66A	#03312	T67A	#03340
T7A	#01300	T70A	#03366	T71A	#03414	T72A	#03442
T73A	#03472	T74A	#03516	T75A	#03544	T76A	#03572
T77A	#03620	UBREAK	# 177770		#07316		

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,TITLE MAINDEC-11-DCKBE-B PDP11/45 RTI/T TEST  
,LIST ME  
,NLIN? MC,MO,SEQ  
,AHS

ITEST DCKBE. THIS IS A TEST OF THE RTI AND RTI INSTRUCTIONS.  
ITHE RTI INSTRUCTION ALLOWS A 'T' BIT TRAP IMMEDIATELY FOLLOWING THE  
INSTR, WHEREAS THE RTI WILL EXECUTE THE INSTRUCTION FOLLOWING THE RTI  
IMMEDIATELY IF THE 'T' BIT TRAP IS ALLOWED. (LIKE 11/20 RTI)  
INTELL THIS TEST EXECUTES 'T' BIT TRAPS.  
ITHE TEST TESTS RTI/PTI IN ALL THREE STATES (KERNEL,SUPERVISOR,AND USER).

ISTARTING PROCEDURE  
I LOAD ADDRESS=200  
I PRESS START  
I STACK POINTER IS AT 500  
I BELL WILL RING WHEN TEST IS COMPLETE

000000 HLT=HALT  
010701 SCDF=010701 MOVE PC TO R1  
177570 SHR=177570 ADDRESS OF CONSOL SWITCH REGISTER  
177570 DISPLAY=177570 ADDRESS OF CONSOL DISPLAY REGISTER  
177570 PSH=177570 ADDRESS OF PROCESSOR STATUS WORD  
177544 UBR1AK=177544 ADDRESS OF PDP11/45 MICRO HPEAK REGISTER  
177546 TRHU=177546

\*\*\*\*\*INITIAL STACK POINTER=000\*\*\*\*\*  
STKPTR=0500 INITIAL STACK SETTING

GENERAL REGISTERS

000000 R0=X0  
000001 R1=X1  
000002 R2=X2  
000003 R3=X3  
000004 R4=X4  
000005 R5=X5  
000006 SP=X6  
000006 KSP=X6 KERNEL'S STACK POINTER  
000006 SSP=X6 SUPERVISORY STACK POINTER  
000006 USP=X6 USER'S STACK POINTER  
000007 PC=X7  
000020 TBIT=20  
000004 ERRVEC=4 ADDRESS OF ERROR TRAP VECTOR  
000010 RESVEC=10 ADDRESS OF RESERVED INST. TRAP VECTOR  
000014 TRITVEC=14 ADDRESS OF 'T' BIT TRAP VECTOR

PROCESSOR STATUS BITS

000000 KM=0 KERNEL MODE  
040000 SM=4000 SUPERVISORY MODE  
140000 UM=140000 USER MODE  
000000 PKM=0 PREVIOUS KERNEL MODE  
010000 PSM=10000 PREVIOUS SUPERVISORY MODE  
030000 PUM=30000 PREVIOUS USER MODE  
004000 REC=4000 REGISTER SFT BIT  
000200 PRTY=200 PRIORITY LEVEL 4

000340 PRTY=340 PRIORITY LEVEL 7  
000000 PRTY=0 PRIORITY LEVEL 0

STACK POINTERS

000770 KPTR=770 KERNEL'S INITIAL STACK POINTER  
000700 SPTR=700 SUPERVISOR'S INITIAL STACK POINTER  
000730 UPTR=730 USER INITIAL STACK SETTING

000000 ,R0  
000000 ,NLIN? MC,MO,SEQ  
000000 ,+2  
000000 HALT  
000004 ,+2  
000006 HALT  
000010 ,+2  
000012 HALT  
000014 ,+2  
000016 HALT  
000020 ,+2  
000022 HALT  
000024 ,+2  
000026 HALT  
000030 ,+2  
000032 HALT  
000034 ,+2  
000036 HALT  
000040 ,+2  
000042 HALT  
000044 ,+2  
000046 HALT  
000048 ,+2  
000050 HALT  
000054 ,+2  
000056 HALT  
000060 ,+2  
000062 HALT  
000064 ,+2  
000066 HALT  
000070 ,+2  
000072 HALT  
000074 ,+2  
000076 HALT  
001000 ,+2  
001002 HALT  
001004 ,+2  
001006 HALT  
001010 ,+2  
001012 HALT  
001014 ,+2  
001016 HALT  
001020 ,+2  
001022 HALT  
001024 ,+2  
001026 HALT  
001030 ,+2  
001032 HALT

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000132 000000 HALT
000134 000136 ,+2
000136 000000 HALT
000140 000142 ,+2
000142 000000 HALT
000144 000146 ,+2
000146 000000 HALT
000150 000152 ,+2
000152 000000 HALT
000154 000156 ,+2
000156 000000 HALT
000160 000162 ,+2
000162 000000 HALT
000164 000166 ,+2
000166 000000 HALT
000170 000172 ,+2
000172 000000 HALT
000174 000176 ,+2
000176 000000 HALT
000200 000202 ,+2
000202 000000 HALT
000204 000206 ,+2
000206 000000 HALT
000210 000212 ,+2
000212 000000 HALT
000214 000216 ,+2
000216 000000 HALT
000220 000222 ,+2
000222 000000 HALT
000224 000226 ,+2
000226 000000 HALT
000230 000232 ,+2
000232 000000 HALT
000234 000236 ,+2
000236 000000 HALT
000240 000242 ,+2
000242 000000 HALT
000244 000246 ,+2
000246 000000 HALT
000250 000252 ,+2
000252 000000 HALT
000254 000256 ,+2
000256 000000 HALT
000260 000262 ,+2
000262 000000 HALT
000264 000266 ,+2
000266 000000 HALT
000270 000272 ,+2
000272 000000 HALT
000274 000276 ,+2
000276 000000 HALT
000300 000302 ,+2
000302 000000 HALT
000304 000306 ,+2
    
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000306 000000 HALT
000310 000312 ,+2
000312 000000 HALT
000314 000316 ,+2
000316 000000 HALT
000320 000322 ,+2
000322 000000 HALT
000324 000326 ,+2
000326 000000 HALT
000330 000332 ,+2
000332 000000 HALT
000334 000336 ,+2
000336 000000 HALT
000340 000342 ,+2
000342 000000 HALT
000344 000346 ,+2
000346 000000 HALT
000350 000352 ,+2
000352 000000 HALT
000354 000356 ,+2
000356 000000 HALT
000360 000362 ,+2
000362 000000 HALT
000364 000366 ,+2
000366 000000 HALT
000370 000372 ,+2
000372 000000 HALT
000374 000376 ,+2
000376 000000 HALT
000200 000200 JUMP ME
000200 000107 000000 JUMP RTART IGD START TEST
000200 000100 000000 JUMP #1000
000200 000000 ICNTI 0 ICNTAINS PASS COUNT
000200 000000 TEMPI 0
000200 000007 177770 STANTI CLR ICNT ICLEAR PASS COUNT
000200 000000 177700 REGINI MOV #STKPTH,SP INITIALIZE THE STACK POINTER
000200 000000 177700 MOV ICNT,#DISPLAY IDISPLAY PASS COUNT
000200 000000 177700 RIT #4RC,#SWR ILOAD POP11/45 MICRO BREAK REGISTER
000200 000000 177700 REO #10
000200 113737 177700 177700 MOVB #SWR,#MBREAK ILOAD MICRO BREAK REG WITH SWR-7

;TEST THAT RTI POPS THE TOP WORD OFF THE STACK INTO THE PC.
000400 000001 TBI SCOPE
000402 000046 CLR -(SP) IPUSH A PSW ONTO THE STACK
000404 000046 000056 MOV #TRA,-(SP) IPUSH A PC ONTO THE STACK
000406 000000 RTT
000408 000000 HLT ;RTI DID NOT POP TOP WORD OFF THE
;STACK INTO THE PC
000404 000755 RR BEGIN ;RESTART TEST IF ERROR
000406 000400 TBI RR T1 IGD TO THE NEXT TEST

;TEST THAT RTI POPS TWO WORDS OFF THE STACK
000406 000701 TBI SCOPE
    
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%01076 010600 T1A1 MOV SP,RR IGET THE STACK POINTER
%01140 022700 #00500 CMP #STKPTR,RR IDID RTT POP TWO WORDS
%01174 001401 REQ T2 ERROR! RTT DID NOT POP TWO WORDS
%01100 000000 HLT

ITEST THAT RTT POPS THE TWO WORDS OFF THE STACK AND INTO THE
IPC AND PSW RESPECTIVELY.
T21 SCOPE
MOV #STKPTR,SP ISET STACK PTR
CLR -(SP) IPUSH STATUS ON THE STACK
MOV #T2A,-(SP) IPUSH PC ON THE STACK
MOV #357,PSW IPRESET STATUS
RTT

T2A1 MOV PSW,RR IGET STATUS AFTER RTT
TST RR ICHECK THAT RTT LOADED
REQ T3 INEW STATUS FROM STACK
HLT ERROR! RTT FAILED TO LOAD STATUS

%01146 010701 T31 SCOPE
%01150 012700 #00500 MOV #STKPTR,SP ISET STACK PTR
%01154 012746 #00357 MOV #357,-(SP) IPUSH STATUS ON THE STACK
%01160 012746 #00172 MOV #T3A,-(SP) IPUSH PC ON THE STACK
%01164 005067 176606 CLR PSW IPRESET STATUS
%01170 000000 RTT
%01172 010700 176600 T3A1 MOV PSW,RR IGET STATUS AFTER RTT
%01176 022700 #00357 CMP #357,RR ICHECK STATUS AFTER RTT
%01200 001411 REQ T4 ERROR! INCOMPLETE STATUS AFTER RTT
%01204 000000 HLT

ICHECK THAT CP CAN TIME OUT TRAP
%01206 012737 #01224 000000 MOV #T0RET,#ERRVEC ILOAD TIMEOUT TRAP VECTOR
%01214 005037 173000 CLR #0173000 IADDRESS 173000 ALWAYS TIMES OUT ON
IDATIP/DATC BUS CYCLE
%01220 000000 HLT ERROR! FAILED TO TIME OUT TRAP
%01222 000672 BEGIN ILOOP TEST
%01224 022626 T0RET1 CMP (6), (6) IRESTORE THE STACK

ITEST THAT THE CP DOES THE INSTRUCTION FOLLOWING THE RTT IF THE TBIT
IGETS SET (BY THE RTT)
T41 SCOPE
MOV #STKPTR,SP ISET STACK PTR
MOV #T4B,TBITVEC ILOAD IT' BIT TRAP VECTOR AND
CLR TBITVEC*2 IITS STATUS
MOV #TBIT,-(SP) ISET UP STACK SUCH THAT RTT CAUSES
IT' BIT TO SET

%01226 010701 T4A1 MOV #T4A,-(SP)
%01230 012700 #00500 RTT
%01234 012746 #01266 176552 T4A1 NOP ICP SHOULD DO THIS INSTRUCTION AND
%01242 005067 176550 ITHEN TRAP
%01246 012746 #00020 T4AA1 HLT IPROCESSOR DID NOT ACKNOWLEDGE IT'
IBIT TRAP

%01252 012746 #01260 BR T5
%01256 000000
%01260 000240
%01262 000000
%01264 000405
    
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%01266 011600 T4B1 MOV (SP),RR IGET PC WHEN IT' TRAP OCCURRED
%01270 022700 #01262 CMP #T4AA,RR IDID CP DO THE NOP
%01274 001401 REQ T5 ERROR! IT' BIT DID NOT TRAP AFTER
%01276 000000 HLT ITHE NOP

ITHE TEST IS THE SAME AS T4 EXCEPT THAT THE INSTRUCTION FOLLOWING
ITHE RTT (WHICH SETS THE IT' BIT) IS A THREE WORD (MOV PSW,2) INSTRUCTION.
T51 SCOPE
MOV #STKPTR,SP ISET STACK PTR
MOV #T5B,TBITVEC ILOAD IT' BIT TRAP VECTOR
MOV #TBIT,-(SP) ISETUP TO SET IT' BIT
MOV #T5A,-(SP)
CLR TEMP
RTT IRTT SETS THE IT' BIT
MOV PSW,TEMP IMOVE STATUS TO MEM; LOCATION 2
HLT IIT' BIT DID NOT CAUSE A TRAP
BR T6
T5R1 MOV TEMP,RR IGET STATUS THAT RTT LOADED
CMP #TBIT,RR IDID RTT LOAD IT' BIT INTO STATUS
REQ T6
HLT ERROR! EITHER RTT DID NOT LOAD
IT' BIT INTO STATUS OR IT' BIT TRAPPED
BEFORE PSW WAS SAVED.

ITEST THAT THE CP ACKNOWLEDGES THE IT' BIT BEING SET BEFORE EXECUTING
ITHE INSTRUCTION FOLLOWING AN RTI
T61 SCOPE
MOV #STKPTR,SP ISET STACK PTR
MOV #T6B,TBITVEC ILOAD IT' BIT TRAP VECTOR
MOV #TBIT,-(SP) ISET UP TO SET IT' BIT
MOV #T6A,-(SP)
RTT IRTT SETS THE IT' BIT
HLT IERROR! CP SHOULD HAVE TRAPPED
BEFORE THIS INSTRUCTION WAS EXECUTED.

%01362 010701 T6R1 NOP
%01364 012700 #00500 BR T7 IGO TO NEXT TEST
%01370 012746 #01412 176410
%01376 012746 #00020
%01402 012746 #01410
%01406 000000
%01410 000000
%01412 000240
%01414 000000

ITEST THAT THE CP DOES NOT ACKNOWLEDGE THE IT' BIT IF THE INSTRUCTION
FOLLOWING THE RTT CLEARS THE IT' BIT.
    
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001416 012701 T7I SCOPE
001420 012706 000500 MOV #STKPTR,SP ITHIS IS WHAT THE STACK LOCKS
001424 012707 001462 176362 MOV #T7C,TRITVEC ILIKE BEFORE THE RTT
001432 000046 CLR -(SP) 1670 * T7A * SP=670
001434 012746 001456 MOV #T7B,-(SP) 1672 * 20 *
001440 012746 000020 MOV #TRIT,-(SP) 1674 * T7B *
001444 012746 001454 MOV #T7A,-(SP) 1676 * 0 *
001450 000240 NOP 1700 * ? * UNKNOWN
001452 0000P6 RTT ISTACK AFTER THE RTT
1674 * T7B * SP=674
1676 * 0 * AND ITI BIT
1700 * ? * IS SET
001454 0000P2 T7A1 RTI ISTACK AFTER THE RTI
1700 * ? * SP=700
I AND ITI BIT
I IS CLEAR

001456 000240 T7B1 NOP
001460 0000P1 BR EL45
001462 0000P0 T7C1 HLT ICP ACKNOWLEDGED THE ITI BIT

001464 012737 002164 000010 ITEST IF THE PROCESSOR IS AN 11/45
001472 000230 EL451 MOV #END,0#10 ISET RESERVED INST TRAP VECTOR
SPL I WILL TRAP TO END IF AN 11/40

001474 010701 ITEST THAT AN RTI LOADS THE STATUS WORD PROPERLY IN KERNEL MODE,
001476 005007 176274 T101 SCOPE
001502 012706 000770 CLR PSW ISET KERNEL STACK POINTER
001506 012710 054340 MOV #KATR,KSP #SM+PSM+REG+PRTY7,(KSP)
001512 012746 001526 MOV #SM+PSM+REG+PRTY7,(KSP)
001516 005007 177200 CLR #T10A,-(KSP)
001522 0000P2 RTI TEMP IPOP 2 TOP WORDS OF KERNEL STACK INTO
ISTATUS AND PC
IERROR! RTI FAILED
IGET STATUS WORD

001524 000000 HLT
001528 010701 176244 177240 T10A1 MOV #SM,TEMP #SM+PSM+REG+PRTY7,TEMP
001534 000000 176210 177210 T10A1 CLR PSW
001540 000000 054340 177214 T10A1 CMP #SM+PSM+REG+PRTY7,TEMP
001550 000000 BEQ #04
HLT IERROR! INCORRECT STATUS

001552 010701 ITHIS TEST SAME AS ABOVE EXCEPT THAT RTT IS FROM KERNEL TO USER MODE,
001554 012707 000340 176214 T111 SCOPE
001562 012706 000770 MOV #PRTY7,PSW
001566 012710 174000 MOV #KPTR,KSP
001572 012746 001606 MOV #UM+PUM+REG,(KSP)
001576 005007 177200 CLR #T11A,-(KSP)
001602 0000P6 RTT TEMP
001604 000000 HLT IERROR! RTT FAILED

001606 010701 176164 177166 T11A1 MOV PSW,TEMP IGET STATUS WORD
001614 005007 176156 CLR PSW
001620 022767 174000 177154 T11A1 CMP #UM+PUM+REG,TEMP
    
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001620 001401 D10 #04
001630 000000 HLT IERROR! INCORRECT STATUS WORD

001632 010701 ITEST RTT LOADS THE STATUS WORD CORRECTLY IN SUPERVISORY MODE
001634 012707 000000 176134 T121 SCOPE
001642 012706 000770 MOV #SM,PSW ISUPERVISORY MODE!!!
001646 012710 174340 MOV #SPTR,SSP ISET SUPERVISORY STACK POINTER
001652 012746 001606 MOV #UM+PUM+REG+PRTY7,(SSP)
001656 0000P6 RTT #T12A,-(SSP)
001660 000000 HLT
001666 010701 176112 177114 T12A1 MOV PSW,TEMP IGET STATUS WORD
001672 005007 176104 CLR PSW IKERNEL MODE!!!
001676 022767 174000 177102 T12A1 CMP #UM+PUM+REG,TEMP
001700 001401 BEQ #04
001702 000000 HLT IERROR! INCORRECT STATUS

001704 010701 ITEST THAT RTI DOES NOT CLEAR BITS 15-11 IN SUPERVISORY MODE,
001706 012707 074200 176062 T131 SCOPE
001714 012706 000770 MOV #SM+PUM+REG+PRTY4,PSW
001720 012710 000340 MOV #SPTR,SSP ILOAD RETURN STATUS ON SUPER STACK
001724 012746 001730 MOV #T13A,-(SSP)
001730 005007 177046 CLR TEMP
001734 0000P6 RTT
001736 010701 176034 177036 T13A1 MOV PSW,TEMP
001744 005007 176026 CLR PSW
001750 022767 074200 177024 T13A1 CMP #SM+PUM+REG+PRTY4,TEMP
001756 001401 BEQ #04
001760 000000 HLT IERROR! INCORRECT STATUS AFTER RTT

001762 010701 ITEST THAT RTI LOADS STATUS PROPERLY IN USER MODE
001764 012707 140000 176004 T141 SCOPE
001772 012706 000770 MOV #UM,PSW IUSER MODE!!!
001776 012710 174340 MOV #UPTR,USP ISET USER'S STACK POINTER
001782 012746 002014 MOV #UM+PUM+REG+PRTY7,(USP)
001786 005007 176770 MOV #T14A,-(USP)
001792 0000P2 CLR TEMP
001794 000000 RTI

001796 010701 176706 176740 T14A1 MOV PSW,TEMP IGET STATUS WORD
001802 005007 176704 CLR PSW IKERNEL MODE!!!
001806 022767 174000 176740 T14A1 CMP #UM+PUM+REG,TEMP
001814 001401 BEQ #04
001816 000000 HLT IERROR! INCORRECT STATUS AFTER RTI

001818 010701 ITEST THAT RTT DOES NOT CLEAR STATUS BITS 15-11 IN USER MODE,
001824 012707 174340 176726 T151 SCOPE
001832 012706 000770 MOV #UM+PUM+REG+PRTY7,PSW IUSER MODE!!!
001836 005007 176704 MOV #UPTR,USP
001842 012746 002070 CLR -(USP)
001846 005007 176714 MOV #T15A,-(USP)
001850 0000P2 CLR TEMP
001852 000000 RTT
    
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002070 01A7A7 175792 17A79A T15A1 MOV PSW,TEMP
002070 0094A7 175674 CLR PSW ;KERNEL MODE!!!
002102 0227A7 1743A0 176672 CMP SUM+PUM+REG+PRTY7,TEMP
002110 001401 REG ,+4
002112 000000 HLT

;TEST THAT BITS 13-11 CAN BE CLEARED BY RTT IN KERNEL MODE.
002114 01A7A1 T161 SCOPE
002116 0127A7 034000 175652 MOV #PUM+REG,PSW ;KERNEL MODE!!!
002124 0127A6 000770 MOV #KPTR,KSP ;SET KERNEL STACK
002130 005016 CLR (KSP)
002132 0127A6 002146 MOV #T16A,=(KSP)
002136 0127A7 177777 176636 MOV #-1,TEMP ;PRESET TEMP
002144 000000 RTT
002146 01A7A7 175624 176626 T16A1 MOV PSW,TEMP ;GET STATUS AFTER RTT
002154 0057A7 176622 TST TEMP ;CHECK STATUS AFTER RTT
002160 001401 BEQ ,+4
002162 000000 HLT ;ERROR! INCORRECT STATUS AFTER RTT

002164 012737 000012 000010 END1 MOV #12,#10 ;SET RESERVED INST TO HALT AT 12
002172 005267 176602 INC ICNT
002176 005767 176576 TST ICNT
002202 001402 BEQ DONE
002204 0001A7 176600 JMP REGIN
002210 0127A7 175350 DONE1 MOV #7,TPBUF
002216 104767 175342 TSTB TPCSR
002222 100375 RPL ,+4
002224 013702 000042 MOV ##42,X2 ;GET DECTAPE MONITOR RETURN ADDRESS
002230 001404 REG DONE1 ;DD NOT RETURN IF (42)=0
002232 004712 JBR 7,(X2) ;RETURN TO DECTAPE MONITOR
002234 000240 NOP ;ACT11
002236 000240 NOP ;OVERLAY
002240 000240 NOP ;AREA
002242 000167 175536 DONE11 JMP START ;RESTART TEST

000001 .END
    
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BEGIN = 00101F	DISPLA = 177570	DONE = 00221F	DONE1 = 002242
EL45 = 001464	END = 0021A4	ERRVFC = 0020C4	HLT = 000000
ICNT = 001002	KH = 00A000	KPTR = 002770	KSP = X02000
PC = X0000007	PKM = 000000	PRTY7 = 000000	PRTY4 = 000200
PRTY7 = 00034F	PSH = 01A000	PSW = 177776	PUM = 030000
REG = 004000	RESVEC = 000010	R0 = X000000	R1 = X000001
R2 = X000002	R3 = X000003	R4 = X000004	R5 = X000005
SCOPE = 010701	SH = 040000	SP = X000006	SPTR = 000750
SSP = X000006	START = 001004	STKPTR = 000500	SHR = 177570
TBIT7 = 000020	TBITVE = 00A014	TEMP = 001002	TORET = 001224
TPBUF = 177506	TPCSR = 177564	T0 = 001040	T0A = 001050
T1 = 001060	T1A = 001070	T10 = 001474	T10A = 001520
T11 = 001080	T11A = 001090	T12 = 001632	T12A = 001660
T13 = 001094	T13A = 001736	T14 = 001762	T14A = 002014
T15 = 002040	T15A = 002070	T16 = 002114	T16A = 002146
T2 = 001110	T2A = 001134	T3 = 001140	T3A = 001172
T4 = 001226	T4A = 001260	T4A = 001262	T4R = 001266
T5 = 001300	T5A = 001332	T5B = 001344	T6 = 001362
T6A = 001410	T6B = 001412	T7 = 001416	T7A = 001454
T7B = 001456	T7C = 001462	UBREAK = 177770	UM = 140000
UPTR = 000732	USP = X000006		

ERRORS DETECTED: 0