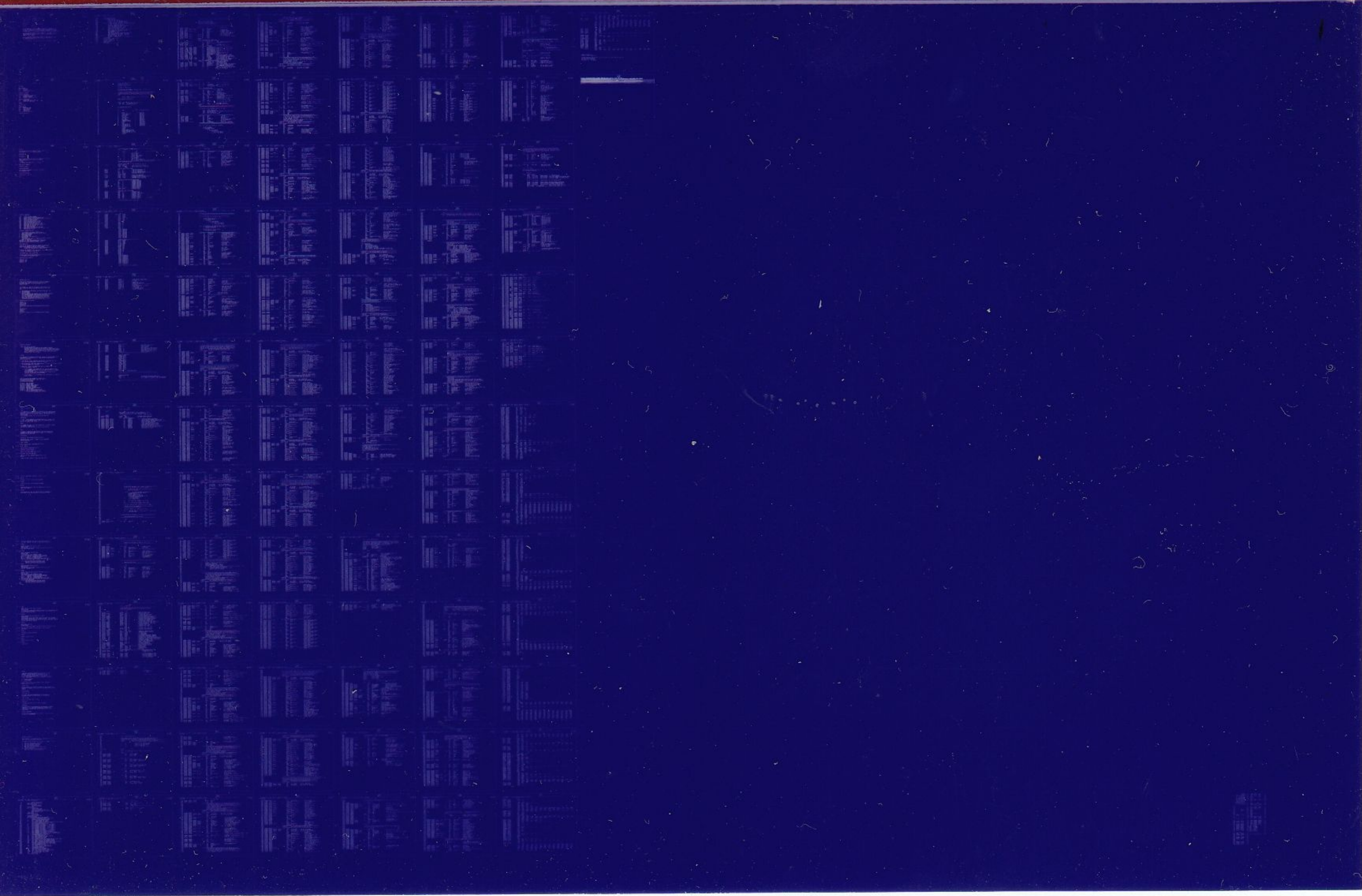


TA11

LOGIC TEST 2
MD-11-DZTAB-C

EP-DZTAB-C-DL-A NOV 1976
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B01

IDENTIFICATION

SEP 0001
SEP 0001

PRODUCT CODE: MAINDEC-11-DZTAB-C-D
PRODUCT NAME: TALL LOGIC TEST (PART 2)
DATE REVISED: 21 JUNE 76
MAINTAINER: DIAGNOSTIC ENGINEERING
AUTHOR: JIM LACEY

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9. PROGRAM DESCRIPTION

1. ABSTRACT

THIS PROGRAM CONTAINS A SERIES OF BASIC LOGIC TESTS THAT CHECK THE TAIL FOR PROPER OPERATION.

2. REQUIREMENTS

2.1 EQUIPMENT

2.2 PDP-11 COMPUTER WITH OR WITHOUT HARDWARE SWITCH REGISTER WITH CONSOLE TELETYPE, AND A TAIL CASSETTE STORAGE

THIS PROGRAM REQUIRES APPROX. 4K STORAGE.

2.3 PRELIMINARY PROGRAMS

MAINDEC-11-DZTAA

3. LOADING PROCEDURE

USE STANDARD PROCEDURE FOR LOADING .ABS TAPES OR A CASSETTE TAPE.

4. STARTING PROCEDURE

4.1 CONTROL SWITCH SETTINGS

SEE 5.1.

4.2 STARTING ADDRESSES

```

200 NORMAL STARTING ADDRESS
201 SELECT DRIVE(S) BEFORE STARTING TEST
210 SELECT DRIVE(S) AND ADDRESSES BEFORE STARTING TEST
214 SETUP FOR MANUAL LOOPING
220 WRITE FILE GAP FROM BOT TO EOT
230 WRITE CONTINUOUS BLOCKS OF DATA
240 READ CONTINUOUS BLOCKS OF DATA
244 WRITE FILE GAP AND A BLOCK OF DATA
250 READ BLOCK OF DATA AND INTO A FILE GAP
260 SPACE FWD FILE GAP FROM BOT TO EOT
270 BACK SPACE FILE GAPS
300 LOAD SWITCH REGISTER INTO THE TACS
400 WRITE SWITCH REGISTER ON TAPE FROM BOT TO EOT
700 READ FROM BOT TO EOT

```

4.3 PROGRAM & OPERATOR ACTION

1. LOAD PROGRAM INTO MEMORY (SEE SECTION 3.)
 2. LOAD A WRITE ENABLED CASSETTE IN BOTH DRIVES
 3. REWIND BOTH DRIVES
 4. LOAD ADDRESS 200.
 5. SET SWITCHES (SEE SECTION 5.1)
 6. PRESS START.
 7. THE PROGRAM WILL LOOP & TTY BELL WILL RING ONCE EVERY PASS, IF SW<10>=0.
- *** NOTE: IF USING THE SOFTWARE SWITCH REGISTER PROGRAM WILL TYPE "SWR=XXXXXX NEW=" TO ALLOW SETTING OF REGISTER BEFORE BEGINNING TEST.

4.3.1 DRIVE SELECTION

STARTING THE PROGRAM AT 200 WILL RESULT IN AUTOMATIC SELECTION OF DRIVES "A" AND "B" TO BE TESTED.
NOTE: IF LOAD MEDIUM IS CASSETTE WITH STANDARD VECTOR PROGRAM WILL RESPOND AS IF STARTED AT 210.

STARTING THE PROGRAM AT 204, 210, OR 214 ALLOWS THE OPERATOR TO SELECT THE DRIVE(S) TO BE TESTED.

THE PROGRAM WILL TYPE "DRIVE(S)?".

EITHER OR BOTH DRIVES CAN BE SELECTED BY TYPING "A" AND/OR "B" FOLLOWED BY A CARRIAGE RETURN.

4.3.1.1 DRIVE SELECTION EXAMPLES

```

DRIVE(S)? A,B
DRIVE(S)? AB
DRIVE(S)? B,A
DRIVE(S)? B

```


4.3.2 ADDRESS SELECTION

STARTING THE PROGRAM AT 210 OR 214 ALLOWS THE OPERATOR TO CHANGE THE "CONTROL AND STATUS" AND "DATA BUFFER" REGISTER ADDRESSES, THE VECTOR ADDRESS AND THE PRIORITY LEVEL.

THE PROGRAM WILL ASK FOR THE DRIVES TO BE TESTED AS PER 4.3.1. AFTER THE DRIVES HAVE BEEN SELECTED IT WILL ASK FOR:

1. BUS ADDRESS OF THE CONTROL AND STATUS REGISTER (TACS)
 2. VECTOR ADDRESS
 3. PRIORITY LEVEL
- AND THE OPERATOR MUST RESPOND WITH THE DESIRED PARAMETER OR A CARRIAGE RETURN (WHICH IMPLIES LEAVE AS IS). WHEN ALL PARAMETERS HAVE BEEN DEFINED THE PROGRAM WILL TYPE THEM BACK OUT AND ASK IF THEY ARE OK AT WHICH TIME THE OPERATOR RESPONDES WITH A "Y" OR A "CARRIAGE RETURN" FOR "YES" ANYTHING ELSE IS A "NO".

4.3.2.1 ADDRESS SELECTION EXAMPLES

```
DRIVES(S) A
TACS? 177500
VECTOR? 260
PRIORITY? 6
TACS=177500 TADB=177502 VECTOR=000260 PRIORITY=000300
OK?
```

```
DRIVES(S) A,B
TACS? 470
VECTOR?
PRIORITY?
TACS=177470 TADB=177472 VECTOR=000260 PRIORITY=000300
OK?
```


5. OPERATING PROCEDURE

5.1 OPERATIONAL SWITCH SETTINGS

IF THE DIAGNOSTIC IS RUN ON A CPU WITHOUT A SWITCH REGISTER THEN A SOFTWARE SWITCH REGISTER IS USED WHICH ALLOWS THE USER THE SAME SWITCH OPTIONS AS THE HARDWARE SWITCH REGISTER. IF THE HARDWARE SWITCH REGISTER DOES NOT EXIST OR IF ONE DOES AND IT CONTAINS ALL ONES (177777) THEN THE SOFTWARE SWITCH REGISTER (LOC. 176) IS USED.

CONTROL:

THIS PROGRAM ALSO SUPPORTS THE DYNAMIC LOADING OF THE SOFTWARE SWITCH REGISTER (LOC. 176) FROM THE TTY. THIS CAN BE ACCOMPLISHED BY DOING THE FOLLOWING:

- 1) TYPE CONTROL G (<↑G>); THIS WILL ALLOW THE TTY TO ENTER DATA INTO LOC. 176 AT SELECTED POINTS WITHIN THE PROGRAM.
- 2) THE MACHINE WILL THEN TYPE: SWR=XXXXXXNEW= (XXXXXX IS THE OCTAL CONTENTS OF THE SOFTWARE SWITCH REGISTER.)
- 3) AFTER THE '*NEW='* HAS BEEN TYPED THEN THE OPERATOR CAN DO ONE OF THE FOLLOWING AT THE TTY:
 - A) TYPE A NUMBER TO BE LOADED INTO LOC. 176 FOLLOWED BY A <CR>. (ONLY NUMBERS BETWEEN 0-7 WILL BE ACCEPTED AND ONLY 6 NUMBERS WILL BE ALLOWED)
IF A <CR> IS THE FIRST KEY DEPRESSED THE SOFTWARE SWITCH REGISTER CONTENTS WILL NOT BE CHANGED.
 - B) IF A CONTROL U (<↑U>) IS DEPRESSED THEN THE PROGRAM WILL SEND YOU BACK TO STEP 2.

WITH SW<15:08>=0 THE PROGRAM WILL PRINT OUT ON ERRORS AND CONTINUE IN TEST. BELL WILL RING AT COMPLETION OF A PASS.
THE SWITCH SETTINGS ARE:

SW<15>=1...HALT ON ERROR
 SW<14>=1...LOOP ON TEST
 SW<13>=1...INHIBIT ERROR TYPEOUTS
 SW<11>=1...INHIBIT ITERATIONS
 SW<10>=1...RING BELL ON ERROR
 SW<10>=0...RING BELL ON PASS COMPLETE
 SW<09>=1...LOOP ON ERROR
 SW<08>=1...LOOP ON TEST AS PER SW<07:00>
 SW<07>=1...LOCK ON CURRENT DRIVE (ONLY VALID FOR STARTING ADDRESSES 220 THRU 250).

5.2 SUBROUTINE ABSTRACTS

5.2.1 SCOPE

THIS SUBROUTINE CALL (VIA AN IOT INSTRUCTION) IS PLACED BETWEEN EACH TEST IN THE INSTRUCTION SECTION. IT RECORDS THE STARTING ADDRESS OF EACH TEST IN LOCATION "\$LPADR" AND "\$LPERR" AS IT IS BEING ENTERED.

;THIS ROUTINE SUPPORTS THE S/W SWITCH REG FUNCTIONS

5.2.2 TRAPCATCHER

A ".+2" - "HALT" SEQUENCE IS REPEATED FROM LOC. 0 TO LOC. 776 TO CATCH ANY UNEXPECTED TRAPS. THUS, ANY UNEXPECTED TRAPS WILL HALT AT THE DEVICE TRAP VECTOR +2.

5.2.3 ERROR

THIS SUBROUTINE CALL (VIA A EMT INSTRUCTION) IS USED TO REPORT ALL ERRORS. (REFER TO 6.)

;THIS ROUTINE SUPPORTS THE S/W SWITCH REG FUNCTIONS

5.2.4 TRAP

A NUMBER OF SUBROUTINES ARE CALLED BY THE TRAP INSTRUCTION. FOLLOWING IS THE CALLS USED AND THE LABEL OF THE STARTING ADDRESS OF THE SUBROUTINES.

5.2.4.1 TYPE (\$TYPE)

ROUTINE TO TYPE AN ASCIZ STRING ON THE TTY

THE ROUTINE WILL INSERT A NUMBER OF NULL CHARACTERS AFTER A LINE FEED.

5.2.4.2 RDCHR (\$RDCHR)

READ A SINGLE ASCII CHARACTER FROM THE TTY

5.2.4.3 RDLIN (\$RDLIN)

READ AN ASCII STRING FROM THE TTY

5.2.4.4 WAITREADY (WAIT.ON.READY)

WAIT ON THE "TALI READY" BIT TO SET

5.2.4.5 WAITXFER (WAIT.FOR.XFER.REQ)

WAIT ON THE "TALI TRANSFER REQUEST" BIT TO SET

5.2.4.6 \$TYPOC

CHANGE A BINARY NUMBER TO OCTAL ASCII AND TYPE IT.

5.2.5 THE FOLLOWING SUBROUTINES ARE CALLED BY A JSR

5.2.5.1 \$TYPEC

ROUTINE TO TYPE A SINGLE ASCII CHARACTER

5.2.5.2 TYPERR

THIS ROUTINE WILL TYPE THE ERROR MESSAGES

5.2.5.3 SELDRV

THIS ROUTINE IS USED TO ASK THE OPERATOR WHAT DRIVE(S)
ARE TO BE TESTED

5.2.5.4 SELADR

THIS ROUTINE WILL ASK THE OPERATOR FOR THE ADDRESSES OF
THE "TACS", "TADB" AND VECTOR AND THE PRIORITY TO USE.

5.2.6 THE FOLLOW ROUTINES ARE USED TO MAKE ADJUSTMENTS TO THE TUBO. BEFORE USING ANY OF THEM LOAD AND START 214.

5.2.6.1 WFGSUB

WRITE FILE GAPS FROM "BOT" TO "EOT"
START AT 220
THIS ROUTINE CAN BE USED TO ADJUST THE "WRITE GAP MONO" AND THE "WRITE DELAY MONO".

5.2.6.2 WRTSUB

WRITE CONTINUOUS BLOCKS OF DATA
START AT 224
THE PROGRAM WILL HALT THREE(3) TIMES
AFTER EACH HALT SET THE SWR AND PRESS CONTINUE
HALT 1 --- SWR<7:0> = NUMBER OF BYTES PER BLOCK
HALT 2 ---SWR<7:0> = PATTERN DESIRED
HALT 3 --- SWR<15:0> = OPERATIONAL SWITCH SETTINGS
THIS ROUTINE CAN BE USED TO ADJUST THE "GAP TIME MONO"

** IF USING SOFTWARE SWITCH REGISTER, AFTER EACH HALT OPERATOR WILL BE PROMPTED FOR THE VALUE WITH "SWR=XXXXXX NEW="

5.2.6.3 RDSUB

READ CONTINUOUS BLOCKS OF DATA
START AT 230
THIS ROUTINE CAN BE USED TO ADJUST THE "SIGNAL MONO" AND THE "THRESHOLD POT"

5.2.6.4 WGPBLK

WRITE A FILE GAP AND A BLOCK OF DATA FROM BOT TO ECT
START AT 234
THE PROGRAM WILL HALT THREE (3) TIMES
AFTER EACH HALT SET THE SWR AND PRESS CONTINUE
HALT 1 --- SWR<7:0> = NUMBER OF BYTES PER BLOCK
HALT 2 --- SWR<7:0> = PATTERN DESIRED
HALT 3 --- SWR<15:0> = OPERATIONAL SWITCH SETTINGS
THIS ROUTINE CAN BE USED TO ADJUST THE "WRITE GAP MONO" AND THE "GAP TIME MONO".

** IF USING SOFTWARE SWITCH REGISTER, AFTER EACH HALT OPERATOR WILL BE PROMPTED FOR THE VALUE WITH "SWR=XXXXXX NEW="

5.2.6.5 RDBLK

READ A BLOCK OF DATA AND A FILE GAP

START AT 240

THIS ROUTINE IS USED AFTER "WRITE A BLOCK AND A FILE GAP" ROUTINE
IT CAN BE USED TO ADJUST THE "SIGNAL MON". THE THRESHOLD POT
AND THE "TAPE BLANK MONO".

5.2.6.6 SFFGSB

SPACE FORWARD FILE GAP FROM "BOT" TO "EOT"

START AT 244

THIS ROUTINE CAN BE USED AFTER "WRITE FILE GAP" FOR LOW SPEED
SPACE FORWARD (TAPE BLANK MONO CAN BE ADJUSTED). OR AFTER READ OR
WRITE A FILE GAP AND A BLOCK OF DATA FOR HIGH SPEED SPACE FORWARD
(SIGNAL MONO CAN BE CHECKED).

5.2.6.7 BSFGSB

BACK SPACE FILE GAP

START AT 250

THIS ROUTINE CAN BE USED TO ADJUST OR CHECK THE "SIGNAL MONO".

5.2.7 THE FOLLOWING SUBROUTINES ARE USED BY THE ADJUSTMENT
ROUTINES

5.2.7.1 SETBUF

SETUP BLOCK SIZE AND PATTERN

5.2.7.2 WRTBLK

WRITES A BLOCK OF DATA

5.2.7.3 RDBLK

READS A BLOCK OF DATA

5.2.7.4 NXTDRV

CHANGE DRIVE

6. ERRORS

THERE ARE A NUMBER OF ERRORS THAT CAN OCCUR IN THIS PROGRAM. WHEN AN ERROR IS ENCOUNTERED THE CALL TO THE ERROR ROUTINE IS MADE AND IF SW<13> IS NOT SET AN ERROR MESSAGE PERTAINING TO THE ERROR WILL BE TYPED. EACH ERROR TYPE OUT WILL CONTAIN THE FOLLOWING:

1. AN ERROR MESSAGE
2. A DATA HEADER
3. A DATA STRING

REFER TO THE LISTING UNDER SEARTRB FOR THE DIFFERENT ERRORS THAT CAN OCCUR.

7. RESTRICTIONS

BEFORE STARTING THE PROGRAM THE OPERATOR MUST INSURE THAT A CASSETTE IS LOADED IN THE DRIVE(S) TO BE TESTED AND IS WRITE ENABLED.

8. MISCELLANEOUS

8.1 EXECUTION TIME

THE FIRST PASS TAKES APPROXIMATELY 100 SECONDS ALL SUBSEQUENT PASSES TAKE APPROXIMATELY 475 SECONDS.

8.2 STACK POINTER

STACK IS INITIALLY SET TO 1100.

8.3 PASS COUNT

A PROGRAM PASS THRU COUNT IS KEPT IN "\$PASS".

8.4 ITERATIONS

THE FIRST PASS OF THE PROGRAM WILL AUTOMATICALLY INHIBIT ITERATIONS. ALL SUBSEQUENT PASSES WILL PERFORM FULL, (2000 DECIMAL UNLESS OTHERWISE SPECIFIED WITHIN A TEST), ITERATIONS.

8.5 SPECIAL REGISTERS

R3, R4 AND R5 ARE RESERVED FOR "DRIVE", "TACS" AND "TADB THROUGH OUT THE PROGRAM.

9. PROGRAM DESCRIPTION

THIS PROGRAM IS A SEQUENCE OF SMALL INDEPENDENT TESTS THAT CHECK THE TAIL FOR PROPER OPERATION.

THE TESTS CAN BE GROUPED INTO THE FOLLOWING GENERAL GROUPS.

1. TEST THE TIMING ERROR CIRCUITRY
2. TEST THE INTERRUPT CIRCUITRY
3. TEST THE SPACING FUNCTIONS
4. TEST THE FILE GAP CIRCUITRY
5. TEST THAT DATA CAN BE WRITTEN AND READ
6. TEST CRC CIRCUITRY
7. INSURE THAT DATA CAN BE WRITTEN AND READ WHILE THE OTHER DRIVE IS REWINDING


```

.TITLE TA11 BASIC LOGIC TEST (PART 2) MAINDEC-11-DZTAB-C
.*COPYRIGHT (C) 1976
.*DIGITAL EQUIPMENT CORP.
.*MAYNARD, MASS. 01754
.*
.*PROGRAM BY JAMES LACEY
.*
.*THIS PROGRAM WAS ASSEMBLED USING THE PDP-11 MAINDEC SYSMAC
.*PACKAGE (MAINDEC-11-DZGAC-C1), MAR 24, 1976.
.*
*****
*****
*****
.REM!
  
```

GENERAL INFORMATION ABOUT THE TA11/TU60 CASSETTE

ADDRESS MNEMONIC DESCRIPTION

```

777500 TACS CONTROL AND STATUS REGISTER
777502 TADB DATA BUFFER REGISTER
260 TAVEC INTERRUPT VECTOR
  
```

TACS REGISTER DESCRIPTION

BIT	NAME	INIT STATE	READ AND/OR WRITE?
15	ERROR	?	READ ONLY
14	BLOCK CHECK ERROR	0	READ ONLY
13	CLEAR LEADER	?	READ ONLY
12	WRITE LOCK	?	READ ONLY
11	FILE GAP	0	READ ONLY
10	TIMING ERROR	0	READ ONLY
09	OFF LINE	?	READ ONLY
08	UNIT SELECT	0	READ/WRITE
07	TRANSFER REQUEST	0	READ ONLY
06	INTERRUPT ENABLE	0	READ/WRITE
05	READY	1	READ ONLY
04	ILBS	0	READ/WRITE
03	FUNCTION BIT 02	0	READ/WRITE
02	FUNCTION BIT 01	0	READ/WRITE
01	FUNCTION BIT 00	0	READ/WRITE
	0=WRITE-FILE-GAP		
	1=WRITE		
	2=READ		
	3=BACK SPACE FILE GAP		
	4=BACK SPACE BLOCK GAP		
	5=SPACE FORWARD FILE GAP		
	6=SPACE FORWARD BLOCK GAP		
	7=REWIND		
00	GO BIT	0	WRITE ONLY!

TA11 BASIC LOGIC TEST (PART 2) MAINDEC-11-DZTAB-C

TAB: BASIC LOGIC TEST (PART 2) MAINDEC-11-DZTAB-C
DZTAB.C.NEW BASIC DEFINITIONS

00000000
00000001
00000010
00000011
00000100
00000101
00000110
00000111
00001000
00001001
00001010
00001011
00001100
00001101
00001110
00001111
00010000
00010001
00010010
00010011
00010100
00010101
00010110
00010111
00011000
00011001
00011010
00011011
00011100
00011101
00011110
00011111
00100000
00100001
00100010
00100011
00100100
00100101
00100110
00100111
00101000
00101001
00101010
00101011
00101100
00101101
00101110
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00110000
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00110010
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00110100
00110101
00110110
00110111
00111000
00111001
00111010
00111011
00111100
00111101
00111110
00111111
01000000
01000001
01000010
01000011
01000100
01000101
01000110
01000111
01001000
01001001
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01001100
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01100001
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11110010
11110011
11110100
11110101
11110110
11110111
11111000
11111001
11111010
11111011
11111100
11111101
11111110
11111111

ERRVEC = 4
RESVEC = 10
TBITVEC = 10
TRIVEC = 14
BPTVEC = 14
IOTVEC = 20
PWRVEC = 20
EMTVEC = 30
TRAPVEC = 30
TKVEC = 60
TPVEC = 60
PIRQVEC = 240

:: TIME OUT AND OTHER ERRORS
:: RESERVED AND ILLEGAL INSTRUCTIONS
:: "T" BIT
:: TRACE TRAP
:: BREAKPOINT TRAP (BPT)
:: INPUT/OUTPUT TRAP (IOT) **SCOPE**
:: POWER FAIL
:: EMULATOR TRAP (EMT) **ERROR**
:: "TRAP" TRAP
:: TTY KEYBOARD VECTOR
:: TTY PRINTER VECTOR
:: PROGRAM INTERRUPT REQUEST VECTOR

00000000
00000001
00000002
00000003
00000004
00000005
00000006
00000007
00000008
00000009
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00000098
00000099
00000100

00000000
00000002
00000004
00000006
00000010
00000012
00000014
00000016

:TA11 FUNCTIONS

WRITE = 0
READ = 2
BSFG = 4
BSBG = 6
SFG = 8
SFBG = 10
REWIND = 16

:WRITE FILE GAP FUNCTION
:WRITE FUNCTION
:READ FUNCTION
:BACK SPACE FILE GAP FUNCTION
:BACK SPACE BLOCK GAP FUNCTION
:SPACE FWD FILE GAP FUNCTION
:SPACE FWD BLOCK GAP FUNCTION
:REWIND FUNCTION

::*****

:TA11 BIT ASSIGNMENT

ERRROR = BIT15
CRERR = BIT14
LEADER = BIT13
WRLOCK = BIT12
FGAP = BIT11
TIMERR = BIT10
OFFLINE = BIT09
UNIT = BIT08
TR. REQ = BIT07
INT. EN = BIT06
READY = BIT05
ILBS = BIT04
FUNC2 = BIT03
FUNC1 = BIT02
FUNCO = BIT01
GO = BIT00

FUNCTION = FUNC2+FUNC1+FUNCO

::
:////
:
:
:////

:SPECIAL REGISTERS

DRIVE = %3
TACS = %4
TADB = %5

:R3 CONTAINS THE DRIVE UNDER TEST
:R4 IS USED AS A POINTER TO THE TACS REGISTER
:R5 IS USED AS A POINTER TO THE TADB REGISTER.

::
:////
:
:
:////

00000003
00000004
00000005

000000
000001
000002
000003
000004
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000100

000600
000601 000005
000602 010314
000604 112714 000017
000610 032714 000040
000614 001775
000616 112714 000003
000622 105714
000624 100003
000626 017715 000306
000632 000773
000634 032714 000040
000640 001357
000642 000767

:WRITE SWITCH REGISTER ON TAPE FROM BOT TO EOT

.=600

LOOP2: RESET ;CLEAR ALL FLAGS
MOV DRIVE,@TACS ;SELECT DRIVE
MOVB #REWIND!GO,@TACS ;GO TO BOT
1\$: BIT #READY,@TACS ;WAIT TILL READY COMES UP
BEQ 1\$
MOVB #WRITE!GO,@TACS ;START A WRITE
2\$: TSTB @TACS ;CHECK FOR TRANSFER REQUEST
BPL 3\$;BR IF NOT SET
MOV @SWR,@TADB ;SEND DATA TO TA11
BR 2\$;LOOP
3\$: BIT #READY,@TACS ;DID READY SET?
BNE LOOP2 ;START OVER IF YES
BR 2\$;LOOP

:READ FROM BOT TO EOT

.=700

LOOP3: RESET ;CLEAR ALL FLAGS
MOV DRIVE,@TACS ;SELECT DRIVE
MOVB #REWIND!GO,@TACS ;START A REWIND
1\$: BIT #READY,@TACS ;WAIT ON REWIND TO FINISH
BEQ 1\$
MOVB #READ!GO,@TACS ;START A READ
2\$: TSTB @TACS ;CHECK TRANSFER REQ
BPL 3\$;BR IF NOT SET
MOV @TADB,RD ;PICKUP THE DATA
BR 2\$;LOOP
3\$: BIT #READY,@TACS ;CHECK READY
BNE LOOP3 ;START OVER
BR 2\$;LOOP

.SBTTL COMMON TAGS

 *THIS TABLE CONTAINS VARIOUS COMMON STORAGE LOCATIONS
 *USED IN THE PROGRAM.

357		001100	001100	\$.CMTAG:	.WORD	0	;; START OF COMMON TAGS
358		001100	000000	\$.PASS:	.WORD	0	;; CONTAINS PASS COUNT
359		001102	000	\$.STSNM:	.BYTE	000	;; CONTAINS THE TEST NUMBER
360		001103	000	\$.ERFLG:	.BYTE	000	;; CONTAINS ERROR FLAG
361		001104	000000	\$.SICNT:	.WORD	000000	;; CONTAINS SUBTEST ITERATION COUNT
362		001106	000000	\$.LPADR:	.WORD	000000	;; CONTAINS SCOPE LOOP ADDRESS
363		001110	000000	\$.LPERR:	.WORD	000000	;; CONTAINS SCOPE RETURN FOR ERRORS
364		001112	000000	\$.SERTTL:	.WORD	000000	;; CONTAINS TOTAL ERRORS DETECTED
365		001114	000	\$.ITEMB:	.BYTE	000	;; CONTAINS ITEM CONTROL BYTE
366		001115	001	\$.ERMAX:	.BYTE	001	;; CONTAINS MAX. ERRORS PER TEST
367		001116	000000	\$.ERRPC:	.WORD	000000	;; CONTAINS PC OF LAST ERROR INSTRUCTION
368		001120	000000	\$.GDADR:	.WORD	000000	;; CONTAINS ADDRESS OF 'GOOD' DATA
369		001122	000000	\$.BDADR:	.WORD	000000	;; CONTAINS ADDRESS OF 'BAD' DATA
370		001124	000000	\$.GDDAT:	.WORD	000000	;; CONTAINS 'GOOD' DATA
371		001126	000000	\$.BDDAT:	.WORD	000000	;; CONTAINS 'BAD' DATA
372		001130	000000		.WORD	000000	;; RESERVED--NOT TO BE USED
373		001132	000000		.WORD	000000	
374		001134	000	\$.AUTOB:	.BYTE	000	;; AUTOMATIC MODE INDICATOR
375		001135	000	\$.INTAG:	.BYTE	000	;; INTERRUPT MODE INDICATOR
376		001136	000000		.WORD	0	
377		001140	177570	\$.SWR:	.WORD	DSWR	;; ADDRESS OF SWITCH REGISTER
378		001142	177570	\$.DISPLAY:	.WORD	DDISP	;; ADDRESS OF DISPLAY REGISTER
379		001144	177560	\$.TKS:	177560		;; TTY KBD STATUS
380		001146	177562	\$.TKB:	177562		;; TTY KBD BUFFER
381		001150	177564	\$.TPS:	177564		;; TTY PRINTER STATUS REG. ADDRESS
382		001152	177566	\$.TPB:	177566		;; TTY PRINTER BUFFER REG. ADDRESS
383		001154	000	\$.NULL:	.BYTE	0	;; CONTAINS NULL CHARACTER FOR FILLS
384		001155	002	\$.FILLS:	.BYTE	2	;; CONTAINS # OF FILLER CHARACTERS REQUIRED
385		001156	012	\$.FILLC:	.BYTE	12	;; INSERT FILL CHARS. AFTER A "LINE FEED"
386		001157	000	\$.TPFLG:	.BYTE	0	;; "TERMINAL AVAILABLE" FLAG (BIT<07>=0=YES)
387		001160	000000	\$.REGAD:	.WORD	0	;; CONTAINS THE ADDRESS FROM WHICH (\$REGO) WAS OBTAINED
388		001162	000000	\$.REGO:	.WORD	0	;; CONTAINS ((\$REGAD)+0)
389		001164	000000	\$.REG1:	.WORD	0	;; CONTAINS ((\$REGAD)+2)
390		001166	000000	\$.TIMES:	0		;; MAX. NUMBER OF ITERATIONS
391		001170	000000	\$.ESCAPE:	0		;; ESCAPE ON ERROR ADDRESS
392		001172	177607	\$.BELL:	.ASCIZ	<207><377><377>	;; CODE FOR BELL
393		001176	077	\$.QUES:	.ASCII	/?/	;; QUESTION MARK
394		001177	015	\$.CRLF:	.ASCII	<15>	;; CARRIAGE RETURN
395		001200	000012	\$.LF:	.ASCIZ	<12>	;; LINE FEED
396		001202	000000	\$.SAVPC:	.WORD	0	;; STORAGE FOR THE PC
397		001204	000000	\$.SAVPS:	.WORD	0	;; STORAGE FOR THE PS
398		001206	177500	\$.TACSL:	177500		;; LOW BYTE ADDRESS OF TACS
399		001210	177501	\$.TACSH:	177501		;; HIGH BYTE ADDRESS OF TACS
400		001212	177502	\$.TADBL:	177502		;; LOW BYTE ADDRESS OF TADB
401		001214	177503	\$.TADBH:	177503		;; HIGH BYTE ADDRESS OF TADB
402		001216	000260	\$.TAVEC:	260,262		;; TAIL VECTOR ADDRESS

L02

TA11 BASIC LOGIC TEST (PART 2) MAINDEC-11-DZTAB-C
DZTABC.NEW COMMON TAGS

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SEC 0024
SEC 0024

33	001222	000300	
34	001224	000000	000000
35	001230	001224	
36	001232	000000	
37	001234	000000	

TAPRIO: 300
DRVKEY: 0.0
DRVPT: DRVKEY
ASKKEY: 0
CURDRV: 0

:TA11 BR LEVEL 6
:DRIVE SELECT KEY:

:CURRENT DRIVE BEING TESTED

.SBTTL ERROR POINTER TABLE

;*THIS TABLE CONTAINS THE INFORMATION FOR EACH ERROR THAT CAN OCCUR.
 ;*THE INFORMATION IS OBTAINED BY USING THE INDEX NUMBER FOUND IN
 ;*LOCATION \$ITEMB. THIS NUMBER INDICATES WHICH ITEM IN THE TABLE IS PERTINENT.
 ;*NOTE1: IF \$ITEMB IS 0 THE ONLY PERTINENT DATA IS (\$ERRPC).
 ;*NOTE2: EACH ITEM IN THE TABLE CONTAINS 4 POINTERS EXPLAINED AS FOLLOWS:

;* EM ;:POINTS TO THE ERROR MESSAGE
 ;* DH ;:POINTS TO THE DATA HEADER
 ;* DT ;:POINTS TO THE DATA
 ;* DF ;:POINTS TO THE DATA FORMAT

001236

\$ERRTB:

;NOTE: ALL NUMBERS ARE TYPED AS 6-DIGIT OCTAL NUMBERS

;ITEM 1
 EM1 ;STATUS PROBLEM
 DH1 ;PC TACS
 DT1 ;\$ERRPC \$REGO
 0

 ;ITEM 2
 EM2 ;READY FAILED TO SET
 DH2 ;PC TACS WAIT ADDRESS
 DT2 ;\$ERRPC \$REGO SAVPC
 0

 ;ITEM 3
 EM3 ;TRANSFER REQUEST FAILED TO SET
 DH2 ;PC TACS WAIT ADDRESS
 DT2 ;\$ERRPC \$REGO SAVPC
 0

 ;ITEM 4
 EM4 ;THE WRONG FLAG SET
 DH2 ;PC TACS WAIT ADDRESS
 DT2 ;\$ERRPC \$REGO SAVPC
 0

 ;ITEM 5
 EM5 ;DATA PROBLEM
 DH5 ;PC TACS EXPECT RCV'D
 DT5 ;\$ERRPC \$REGO \$GDDAT \$BDDAT
 0

 ;ITEM 6
 EM6 ;INTERRUPT PROBLEM
 DH6 ;PC TACS BR LEVEL
 DT6 ;\$ERRPC \$REGO \$GDDAT
 0

439
 440
 441
 442
 443
 444
 445
 446
 447
 448
 449
 450
 451
 452

 417 001236 017066
 418 001240 017256
 419 001242 017420
 420 001244 000000

 421
 422 001246 017105
 423 001250 017273
 424 001252 017426
 425 001254 000000

 426
 427
 428
 429 001256 017133
 430 001260 017273
 431 001262 017426
 432 001264 000000

 433
 434
 435 001266 017174
 436 001270 017273
 437 001272 017426
 438 001274 000000

 439
 440
 441 001276 017217
 442 001300 017330
 443 001302 017436
 444 001304 000000

 445
 446
 447 001306 017234
 448 001310 017366
 449 001312 017450
 450 001314 000000

TA11 BASIC LOGIC TEST (PART 2) MAINDEC-11-DZTAB-C
DZTABC.NEW ERROR POINTER TABLE

453	001316		ITEMS2:	;ITEMS 201-202
454				
455	001316	017472	EM201	:TA11 FAILED TO RESPOND
456	001320	017544	DH201	:PC TACS
457	001322	017460	DT201	:SERRPC TACS
458	001324	000000	0	:BOTH NUMBERS ARE TYPED AS OCTAL NUMBERS
459				
460	001326	017521	EM202	:NO DRIVES AVAILABLE
461	001330	017561	DH202	:PC
462	001332	017466	DT202	:SERRPC
463	001334	000000	0	:
464				

////////////////////////////////////
////////////////////////////////////
:*****

:BEGIN1 IS FOR NORMAL START
:BEGIN2 IS FOR DRIVE SELECTION
:BEGIN3 IS FOR DRIVE & ADDRESS SELECTION
:BEGIN4 IS FOR MANUAL OPERATION

:*****

0001335 005005
0001340 012737 041101 001224
0001346 122737 000005 000041
0001354 001015
0001356 022737 000260 001216
0001364 001011
0001366 000403
0001370 012705 000001
0001374 000405
0001376 012705 000002
0001402 000402
0001404 012705 000003
0001410

0001410 012706 001100
0001414 005026
0001416 022706 001140
0001422 001374
0001424 012706 001100

0001430 012737 012264 000020
0001436 012737 000340 000022
0001444 012737 012536 000030
0001452 012737 000340 000032
0001460 012737 016476 000034
0001466 012737 000340 000036
0001474 012737 016562 000024
0001502 012737 000340 000026
0001510 016767 010476 010466
0001516 005067 177444
0001522 005067 177442
0001526 112767 000001 177361
0001534 012767 001534 177344
0001542 012767 001542 177340

0001550 013746 000004
0001554 012737 001610 000004
0001562 012767 177570 177350
0001570 012767 177570 177344
0001576 022777 177777 177334
0001604 001012
0001606 000403

BEGIN1: CLR R5 ;NORMAL START
MOV #AB, @DRVKEY
CMPB #5, @41 ;CASSETTE DDP?
BNE BGNCMN ;GO BEGIN COMMON CODE IF NO
CMP #260, @TAVEC ;STANDARD VECTOR?
BNE BGNCMN ;GO BEGIN COMMON CODE IF NO
BR BEGIN3 ;GET DRIVES AND ADDRESSES
BEGIN2: MOV #1, R5 ;ASK FOR DRIVES FLAG
BR BGNCMN ;BEGIN COMMON CODE
BEGIN3: MOV #2, R5 ;ASK FOR DRIVES AND ADDRESSES
BR BGNCMN
BEGIN4: MOV #3, R5
BGNCMN:
:SBTTL INITIALIZE THE COMMON TAGS
:;CLEAR THE COMMON TAGS (\$CMTAG) AREA
MOV \$CMTAG, R6 ;:FIRST LOCATION TO BE CLEARED
CLR (R6)+ ;:CLEAR MEMORY LOCATION
CMP #SWR, R6 ;:DONE?
BNE -6 ;:LOOP BACK IF NO
MOV #STACK, SP ;:SETUP THE STACK POINTER
:;INITIALIZE A FEW VECTORS
MOV \$SCOPE, @IOTVEC ;:IOT VECTOR FOR SCOPE ROUTINE
MOV #340, @IOTVEC+2 ;:LEVEL 7
MOV \$ERROR, @EMTVEC ;:EMT VECTOR FOR ERROR ROUTINE
MOV #340, @EMTVEC+2 ;:LEVEL 7
MOV \$TRAP, @TRAPVEC ;:TRAP VECTOR FOR TRAP CALLS
MOV #340, @TRAPVEC+2 ;:LEVEL 7
MOV \$PWRDN, @PWRVEC ;:POWER FAILURE VECTOR
MOV #340, @PWRVEC+2 ;:LEVEL 7
MOV \$ENDCT, \$EOPCT ;:SETUP END-OF-PROGRAM COUNTER
CLR \$TIMES ;:INITIALIZE NUMBER OF ITERATIONS
CLR \$ESCAPE ;:CLEAR THE ESCAPE ON ERROR ADDRESS
MOVB #1, \$ERMAX ;:ALLOW ONE ERROR PER TEST
MOV #, \$LPADR ;:INITIALIZE THE LOOP ADDRESS FOR SCOPE
MOV #, \$LPERR ;:SETUP THE ERROR LOOP ADDRESS
:;SIZE FOR A HARDWARE SWITCH REGISTER. IF NOT FOUND OR IT IS
:;EQUAL TO A "-1", SETUP FOR A SOFTWARE SWITCH REGISTER.
MOV @ERRVEC, -(SP) ;:SAVE ERROR VECTOR
MOV #645, @ERRVEC ;:SET UP ERROR VECTOR
MOV #DSWR, SWR ;:SETUP FOR A HARDWARE SWICH REGISTER
MOV #DDISP, DISPLAY ;:AND A HARDWARE DISPLAY REGISTER
CMP #-1, @SWR ;:TRY TO REFERENCE HARDWARE SWR
BNE 66\$;:BRANCH IF NO TIMEOUT TRAP OCCURRED
;:AND THE HARDWARE SWR IS NOT = -1
BR 65\$;:BRANCH IF NO TIMEOUT


```

001610 012716 001616 64$: MOV #65$, (SP) ;;SET UP FOR TRAP RETURN
001614 000000 RTI
001616 012767 000176 177314 65$: MOV #SWREG, SWR ;;POINT TO SOFTWARE SWR
001624 012767 000174 177310 MOV #DISPREG, DISPLAY
001632 012637 000004 66$: MOV (SP)+, @#ERRVEC ;;RESTORE ERROR VECTOR

.SBTTL TYPE PROGRAM NAME
;;TYPE THE NAME OF THE PROGRAM IF FIRST PASS
001636 005227 177777 INC #-1 ;;FIRST TIME?
001642 001036 BNE HERE ;;BRANCH IF NO
001644 022737 012232 000042 CMP #SENDAD, @#42 ;;ACT-11?
001652 001432 BEQ HERE ;;BRANCH IF YES
001654 104401 001712 TYPE MSGID ;;TYPE ASCIZ STRING

.SBTTL GET VALUE FOR SOFTWARE SWITCH REGISTER
001660 005737 000042 TST @#42 ;;ARE WE RUNNING UNDER XXDP/ACT?
001664 001006 BNE 67$ ;;BRANCH IF YES
001666 026727 177246 000176 CMP SWR, #SWREG ;;SOFTWARE SWITCH REG SELECTED?
001674 001005 BNE 68$ ;;BRANCH IF NO
001676 104405 GTSWR ;;GET SOFT-SWR SETTINGS
001700 000403 BR 68$
001702 112767 000001 177224 67$: MOVB #1, $AUTOB ;;SET AUTO-MODE INDICATOR
001710 68$: BR HERE ;;GET OVER THE ASCIZ
001710 000413 .MSGID: .ASCIZ <CRLF>/MAINDEC-11-DZTAB-C/<CRLF>
001740 HERE:
;*****
;*****
;THE CONTENTS OF R5 DETERMINES WHAT WILL BE DONE
;
; R5=3 MANUAL OPERATIONS
; R5=2 ASK FOR DRIVE(S) AND ADDRESSES (TACS AND VECTOR)
; R5=1 ASK FOR DRIVE(S)
; R5=0 DON'T ASK FOR ANYTHING
;*****
;*****
001740 010504 BEGINX: MOV R5, R4 ;;COPY R5
001742 005305 DEC R5 ;;ASK FOR DRIVES?
001744 002406 BLT CHKADR ;;BR IF NO
001746 004737 013254 JSR PC, @#ASKDRV ;;GO GET DRIVES TO BE TESTED
001752 005305 DEC R5 ;;ASK FOR ADDRESSES?
001754 002406 BLT CHKADR ;;BR IF NO
001756 004737 013364 JSR PC, @#ASKADR ;;GO GET TAIL ADDRESSES
;*****
;*****
;CHECK THAT "TACS" WILL RESPOND TO ADDRESSING
;
; I. TIMEOUT OCCURRED
; A. TYPE ERROR MESSAGE
; B. EXAMINE R4
; 1. R4>0 GOTO BEGINX
; 2. R4=0 EXAMINE (42)
; A. (42)=0 GOTO BEGINX
; B. (42)>0 GOTO SENDAD
    
```



```

:II.  TIMEOUT DIDN'T OCCUR
      A. CONTINUE
:*****
001762 012737 002000 000004 CHKADR: MOV    #16,0#ERRVEC      : IN CASE OF TIMEOUTS
001770 005000          CLR    R0              : USE AS A SWITCH
001772 005777 177210    TST   @TACSL          : SEE IF TAIL RESPONDS
001776 000402          BR    R5              : BR IF NO TIMEOUT
002000 005200    15:  INC    R0              : COME HERE ON TIMEOUT
002002 022626          CMP   (SP)+,(SP)+      : CLEANUP THE STACK
002004 012737 000006 000004 25:  MOV   #ERRVEC+2,0#ERRVEC : RESTORE TIMEOUT VECTOR
002012 005700          TST   R0              : DID A TIMEOUT OCCUR?
002014 001414          BEQ   J5              : BR IF NO
002016 104201          ERROR  R01          : TAIL FAILED TO RESPOND
002020 012705 000002    MOV   #2,R5              : DRIVES & ADDRESSES
002024 005704          TST   R4              : OPERATOR INPUTS?
002026 001344          BNE   BEGINX          : BR IF YES
002030 013700 000042    MOV   @#42,R0          : GET MONITOR RETURN ADDRESS
002034 001741          BEQ   BEGINX          : BR IF NO MONITOR
002036 000137 012232    JMP   @#SENDAD        : GO TO END
002040 005077 177152    CLR   @TAVEC+2
35:

```



```

655 002220 013777 001220 176770 START: MOV @#TAVEC+2,@TAVEC ;SETUP TA11 TRAP VECTOR
656 002226 005077 176766 CLR @TAVEC+2
657 002232 012737 000340 177776 MOV #340,@#PS ;LOCKOUT ALL I/O INT
658 002240 013704 001206 MOV @#TACSL,TACS ;SETUP TACS
659 002244 013705 001212 MOV @#TADBL,TADB ;SETUP TADB
660 002250 005737 001100 TST @#$PASS ;IF FIRST PASS SETUP FOR EXTRA LONG WAIT LOOPS
661 002254 001003 BNE IS ;OTHERWISE USE OLD VALUES
662 002256 012737 077777 013150 MOV #1CBIT15,@#MAXCNT
663 002264 005037 001102 IS: CLR @#$TSTNM ;ZERO THE TEST NUMBER
664 002270 005003 CLR DRIVE ;SET DRIVE TO "A"
665 002272 013701 001230 MOV @#DRVPT,R1 ;GET DRIVE POINTER
666 002276 121127 000101 CMPB (R1),#'A ;IS IT DRIVE "A"?
667 002302 001402 BEQ TDRV ;BR IF YES
668 002304 012703 000400 MOV #UNIT,DRIVE ;SET DRIVE TO "B"
669 002310 TDRV:
670 002310 104401 002316 TYPE ,65$ ;:TYPE ASCIZ STRING
671 002314 000411 BR 64$ ;:GET OVER THE ASCIZ
672 ;:65$: .ASCIZ <15><12>*TESTING DRIVE *
673 64$:
674 002340 112167 176670 MOVB (R1)+,CURDRV ;SETUP TO TYPE CURRENT DRIVE
675 002344 104401 001234 TYPE ,CURDRV
676 002350 104401 001177 TYPE ,$CRLF ;TYPE A CR & LF
677 002354 105711 TSTB (R1) ;LAST DRIVE BEEN SELECTED
678 002356 001002 BNE IS ;BR IF NO
679 002360 012701 001224 MOV #DRVKEY,R1 ;RESET DRIVE POINTER
680 002364 010137 001230 IS: MOV R1,@#DRVPT ;SAVE DRIVE POINTER FOR NEXT TIME
681 002370 005737 001232 TST @#ASKKEY ;GO START TESTING IF NO MANUAL
682 002374 002007 BGE 2$ ; OPERATIONS REQUESTED
683 002376 005000 CLR RD
684 002400 000000 HALT ;GIVE CONTROL TO THE OPERATOR
685 002402 022767 000176 176530 CMP #SWREG,SWR ;USING S/W SWITCH REG.?
686 002410 001001 BNE 20$ ;NO- GET OUT
687 002412 104405 GTSWR ;LET HIM CHANGE IT
688 002414 ;CONTINUE
689
690 002414 005737 000042 ;THIS CODE IS FOR ACT11 & DDP
691 002420 001406 2$: TST @#42 ;IS THERE A MONITOR?
692 002422 010314 BEQ 1$ ;GO START TESTING IF NO
693 002424 112714 000017 MOV DRIVE,@TACS ;IF YES SELECT DRIVE
694 002430 032714 000040 MOVB #REWIND!GO,@TACS ;SEND TAPE TO BOT
695 002434 001775 BIT #READY,@TACS ;WAIT ON READY
;FALL THRU IF READY=1

```

TA11 BASIC LOGIC TEST (PART 2) MAINDEC-11-DZTAB-C
DZTABO.NEW GET VALUE FOR SOFTWARE SWITCH REGISTER

700				
701				
702				
703				
704				
705				
706	002436	000004		
707	002440	012767	000001	176520
708	002446	005737	001100	
709	002452	001021		
710	002454	000005		
711	002456	010314		
712	002460	112714	000017	
713	002464	104412		
714	002466	112714	000001	
715	002472	104412		
716	002474	163737	013124	013150
717	002502	005237	013150	
718	002506	006137	013150	
719	002512	006137	013150	
720				
721				
722				
723				
724				
725				
726	002516	000004		
727	002520	012767	000012	176440
728	002526	012767	002554	176352
729	002534	012767	002656	176426
730	002542	000005		
731	002544	010314		
732	002546	112714	000017	
733	002552	104412		
734	002554	112714	000003	
735	002560	104413		
736	002562	105714		
737	002564	100401		
738	002566	104001		
739	002570	005015		
740	002572	005000		
741	002574	005001		
742	002576	105714		
743	002600	100405		
744	002602	062700	000010	
745	002606	005501		
746	002610	100372		
747	002612	104001		
748	002614	105714		
749	002616	100005		
750	002620	162700	000001	
751	002624	005601		
752	002626	100372		

////////////////////////////////////
////////////////////////////////////

THIS ISN'T A REAL TEST BUT A SMALL ROUTINE TO DETERMINE THE MAX.
TIME FOR THE WAIT LOOPS (WAIT FOR "READY" AND "TRANSFER REQUEST")

*TEST 1 ROUTINE TO DETERMINE TIME OF WAIT LOOPS

```

TST1:  SCOPE
      MOV     #1,$TIMES      ;;DO 1 ITERATION
      TST     @#$PASS        ;;IS THIS THE FIRST PASS?
      BNE     TST2           ;;BR IF NO
      RESET
      MOV     DRIVE,@TACS    ;SELECT THE DRIVE
      MOVB    #REWIND!GO,@TACS ;START A REWIND
      WAITREADY ;WAIT FOR READY
      MOVB    #WFG!GO,@TACS  ;WRITE A FILE GAP
      WAITREADY ;WAIT ON READY
      SUB     @#HGHTIM,@#MAXCNT ;GET THE TIME IT TOOK
      INC     @#MAXCNT        ;MAKE IT BIGGER
      ROL     @#MAXCNT
      ROL     @#MAXCNT

```

////////////////////////////////////
THE FOLLOWING TEST WILL CHECK TIMING ERRORS FOR BOTH "WRITE" AND "READ".
////////////////////////////////////

*TEST 2 TEST "TIMING ERROR" FOR "WRITE"

```

TST2:  SCOPE
      MOV     #10,$TIMES    ;;DO 10. ITERATIONS
      MOV     #7,$LPADR     ;;SET SCOPE LOOP ADDRESS
      MOV     #TST3,$ESCAPE ;;ESCAPE TO TEST 3 ON ERROR
      RESET
      MOV     DRIVE,@TACS   ;SELECT DRIVE
      MOVB    #REWIND!GO,@TACS ;GO TO "CLEAR LEADER"
      WAITREADY ;WAIT ON "READY"
      MOVB    #WRITE!GO,@TACS ;WRITE FUNCTION
      WAITXFER ;WAIT ON "XFER REQ"
      TSTB    @TACS         ;IS "XFER REQ" = 1
      BMI     1$           ;BR IF YES
      ERROR  1             ;NO "XFER REQ" OCCURRED
      CLR     @TADB        ;KNOCK DOWN "XFER REQ"
      CLR     R0           ;CLEAR COUNTER
      CLR     R1
      TSTB    @TACS       ;WAIT FOR "XFER REQ"
      BMI     3$           ;KEEP TRACK OF HOW LONG
      ADD     #10,R0       ;IT TAKES "XFER REQ" TO SET
      ADC     R1
      BPL     2$
      ERROR  1             ;"XFER REQ" FAILED TO SET
      TSTB    @TACS       ;SAMPLE "XFER REQ"
      BPL     4$           ;IF "XFER REQ" = 0 SOMETHING IS WRONG
      SUB     #1,R0        ;DOWN COUNT THE COUNTER
      SBC     R1
      BPL     3$           ;SO A "TIMING ERROR WILL OCCUR"

```



```

752 002630 000401 BR 5$
753 002632 104001 4$: ERROR 1 ;"XFER REQ" SHOULDN'T HAVE CLEARED
754 002634 105715 5$: TSTB @TADB ;CLEAR "XFER REQ"
755 002636 104412 WAITREADY ;WAIT FOR "READY"
756 002640 005714 TST @TACS ;IS "ERROR" BIT SET?
757 002642 100401 BMI 6$ ;BR IF YES
758 002644 104001 ERROR 1 ;"ERROR" BIT NOT SET
759 002646 032714 002000 6$: BIT #TIMERR,@TACS ;IS "TIMING ERROR" = 1?
760 002652 001001 BNE TST3 ;;BR IF YES
761 002654 104001 ERROR 1 ;"TIMING ERROR" NOT SET
762 *****
763 :*TEST 3 TEST "TIMING ERROR" FOR "READ"
764 *****
765 002656 000004 TST3: SCOPE
766 002660 012767 000012 176300 MOV #10,@TIMES ;;DO 10. ITERATIONS
767 002666 012767 002714 176212 MOV #7$,$LPADR ;;SET SCOPE LOOP ADDRESS
768 002674 012767 003062 176266 MOV #TST4,$ESCAPE ;;ESCAPE TO TEST 4 ON ERROR
769 002702 000005 RESET
770 002704 010314 MOV DRIVE,@TACS ;SELECT DRIVE
771 002706 112714 000017 MOVB #REWIND!GO,@TACS ;GO TO "CLEAR LEADER"
772 002712 104412 WAITREADY ;WAIT FOR "READY"
773 002714 112714 000003 7$: MOVB #WRITE!GO,@TACS ;WRITE
774 002720 012700 000006 MOV #6,R0 ;WRITE THIS MANY BYTES ON TAPE
775 002724 104413 WAITXFER ;WAIT FOR "XFER REQ"
776 002726 112715 000377 8$: MOVB #377,@TADB ;WRITE ONE BYTE
777 002732 104413 WAITXFER ;WAIT FOR "XFER REQ"
778 002734 005300 DEC R0 ;LAST BYTE OUT?
779 002736 003373 BGT 8$ ;BR IF NO
780 002740 052714 000020 BIS #ILBS,@TACS ;WRITE "CRC"
781 002744 104412 WAITREADY ;WAIT FOR "READY"
782 002746 112714 000017 MOVB #REWIND!GO,@TACS ;BACK OVER THE BLOCK
783 002752 104412 WAITREADY ;WAIT FOR "READY"
784 002754 112714 000005 MOVB #READ!GO,@TACS ;START A "READ"
785 002760 104413 WAITXFER ;WAIT FOR "XFER REQ"
786 002762 105714 TSTB @TACS ;IS "XFER REQ" SET
787 002764 100401 BMI 1$ ;BR IF YES
788 002766 104001 ERROR 1 ;"XFER REQ" DIDN'T SET
789 002770 105715 1$: TSTB @TADB ;KNOCK DOWN "XFER REQ"
790 002772 005000 CLR R0 ;CLEAR COUNTERS
791 002774 005001 CLR R1
792 002776 105714 2$: TSTB @TACS ;FIND OUT HOW LONG IT
793 003000 100405 BMI 3$ ;TAKES FOR "XFER REQ"
794 003002 062700 000012 ADD #10.,R0 ;TO SET
795 003006 005501 ADC R1
796 003010 100372 BPL 2$
797 003012 104001 ERROR 1 ;"XFER REQ" FAILED TO SET
798 003014 105714 3$: TSTB @TACS ;NOW WASTE MORE THAN THE ABOVE
799 003016 100005 BPL 4$ ;AMOUNT OF TIME SO THAT
800 003020 162700 000001 SUB #1,R0 ;A "TE" WILL OCCUR
801 003024 005601 SBC R1
802 003026 100372 BPL 3$
803 003030 000401 BR 5$
804 003032 104001 4$: ERROR 1 ;"XFER REQ" CLEARED SOME HOW
805 003034 105715 5$: TSTB @TADB ;KNOCK DOWN "XFER REQ"
806 003036 052714 000020 BIS #ILBS,@TACS ;STOP THE READ
807 003042 104412 WAITREADY ;WAIT ON "READY"

```

TAB: BASIC LOGIC TEST (PART 2) MAINDEC-11-DZTAB-C
DZTAB.C.NEW T3 TEST "TIMING ERROR" FOR "READ"

```

0008 003044 005714          TST      @TACS          ; IS "ERROR" SET?
0009 003046 100401          BMI      6$              ; BR IF YES
0010 003050 104001          ERROR   1                ; "ERROR" NOT = 1
0011 003052 032714 002000        6$: BIT      #TIMERR,@TACS ; IS THERE A "TIMING ERROR"
0012 003056 001001          BNE     TST4             ;:GO TO NEXT TEST IF YES
0013 003060 104001          ERROR   1                ; "TIMING ERROR" NOT SET
0014 *****
0015 :*TEST 4*                TEST "ERROR" OUTPUT OF STATUS ROM USING A "TIMING ERROR"
0016 *****
0017 TST4: SCOPE
0018 003062 000004          MOV     #10.,$TIMES     ;:DO 10. ITERATIONS
0019 003064 012767 000012 176074  MOV     #1$, $LPADR     ;:SET SCOPE LOOP ADDRESS
0020 003072 012767 003126 176006  MOV     #TST5,$ESCAPE  ;:ESCAPE TO TEST 5 ON ERROR
0021 003100 012767 003342 176062  RESET
0022 003106 000005          MOV     DRIVE,@TACS     ;:SELECT DRIVE
0023 003110 010314          MOVVB  #REWIND!GO,@TACS ;:GO TO BEGINNING OF TAPE
0024 003112 112714 000017  WAITREADY ;:WAIT ON "READY" TO SET
0025 003116 104412          MOVVB  #WFG!GO,@TACS   ;:GET ON THE OXIDE
0026 003120 112714 000001  WAITREADY ;:WAIT ON "READY" TO SET
0027 003124 104412          MOVVB  #WRITE!GO,@TACS ;:START A WRITE
0028 003126 112714 000003  WAITXFER ;:WAIT FOR "TRANSFER REQ"
0029 003132 104413          MOVVB  #377,@TADB      ;:WRITE ONE BYTE
0030 003134 112715 000377  CLR     RO              ;:CLEAR THE DOUBLE LENGTH COUNTER
0031 003140 005000          CLR     RI
0032 003142 005001          TSTB   @TACS           ;:CHECK FOR "XFER REQ"
0033 003144 105714 2$: BMI      3$              ;:BR IF SET
0034 003146 100405          ADD     #10.,RO        ;:COUNT UP UNTIL "XFER REQ"
0035 003150 062700 000012  ADC     RI              ;:SETS OR OVERFLOW OCCURS
0036 003154 005501          BPL    2$              ; "XFER REQ" FAILED TO SET
0037 003156 100372          ERROR  1                ;:CHECK "XFER REQ"
0038 003160 104001          TSTB   @TACS           ;:BR IF SET
0039 003162 105714 3$: BMI      4$              ;: "XFER REQ" SHOULDN'T HAVE CLEARED
0040 003164 100401          ERROR  1                ;:COUNT DOWN TO CAUSE A "TE"
0041 003166 104001          SUB     #1,RO
0042 003170 162700 4$: SBC     RI
0043 003174 005601          BPL    3$
0044 003176 100371          TSTB   @TADB          ;:CLEAR "XFER REQ"
0045 003200 105715  WAITREADY ;:WAIT FOR READY
0046 003202 104412          TST    @TACS           ;:MAKE SURE "ERROR" IS SET
0047 003204 005714          BMI    5$
0048 003206 100401          ERROR  1                ;:"ERROR" DIDN'T SET
0049 003210 104001          BIT    #TIMERR,@TACS   ;:MAKE SURE "TE" IS SET
0050 003212 032714 002000 6$: BNE     6$
0051 003216 001001          ERROR  1                ;:"TE" FAILED TO SET
0052 003220 104001          MOVVB  #WFG,@TACS      ;:CHECK "ERROR" WITH "WFG"
0053 003222 112714 000000  TST    @TACS           ;:SAMPLE THE "ERROR" BIT
0054 003226 005714          BPL    7$              ;:BR IF "ERROR" = 0
0055 003230 100001          ERROR  1                ;:"ERROR" NOT = 0
0056 003232 104001          MOVVB  #WRITE,@TACS   ;:CHECK "ERROR" WITH "WRITE"
0057 003234 112714 000002  TST    @TACS           ;:SAMPLE THE "ERROR" BIT
0058 003240 005714          BMI    8$              ;:BR IF "ERROR" = 1
0059 003242 100401          ERROR  1                ;:"ERROR" NOT = 1
0060 003244 104001          MOVVB  #READ,@TACS    ;:CHECK "ERROR" WITH "READ"
0061 003246 112714 000004  TST    @TACS           ;:SAMPLE THE "ERROR" BIT
0062 003252 005714          BMI    9$              ;:BR IF "ERROR" = 1
0063 003254 100401          ERROR  1                ;:"ERROR" NOT = 1
0064 003256 104001

```



```

864 003260 112714 000006 9%:   MOVB   #BSFG,@TACS   :CHECK "ERROR" WITH "BSFG"
865 003264 005714      TST   @TACS         :SAMPLE THE "ERROR" BIT
866 003266 100001      BPL   10%           :BR IF "ERROR" = 0
867 003270 104001      ERROR 1             : "ERROR" NOT = 0
868 003272 112714 000010 10%:   MOVB   #BSBG,@TACS   :CHECK "ERROR" WITH "BSBG"
869 003276 005714      TST   @TACS         :SAMPLE THE "ERROR" BIT
870 003300 100001      BPL   11%           :BR IF "ERROR" = 0
871 003302 104001      ERROR 1             : "ERROR" NOT = 0
872 003304 112714 000012 11%:   MOVB   #SFFG,@TACS   :CHECK "ERROR" WITH "SFFG"
873 003310 005714      TST   @TACS         :SAMPLE THE "ERROR" BIT
874 003312 100001      BPL   12%           :BR IF "ERROR" = 0
875 003314 104001      ERROR 1             : "ERROR" NOT = 0
876 003316 112714 000014 12%:   MOVB   #SFBG,@TACS   :CHECK "ERROR" WITH "SFBG"
877 003322 005714      TST   @TACS         :SAMPLE THE "ERROR" BIT
878 003324 100001      BPL   13%           :BR IF "ERROR" = 0
879 003326 104001      ERROR 1             : "ERROR" NOT = 0
880 003330 112714 000016 13%:   MOVB   #REWIND,@TACS :CHECK "ERROR" WITH "REWIND"
881 003334 005714      TST   @TACS         :SAMPLE THE "ERROR" BIT
882 003336 100001      BPL   TST5           ;;BR IF "ERROR" = 0
883 003340 104001      ERROR 1             : "ERROR" NOT = 0

```

////////////////////////////////////
 THE FOLLOWING TESTS CHECK THE INTERRUPT CIRCUITRY FOR:

- 1) INTERRUPT OCCURS AT THE PROPER "BR" LEVEL
- 2) "READY" WILL INTERRUPT
- 3) "TRANSFER REQUEST" WILL INTERRUPT
- 4) IMPROPER INTERRUPTS DO NOT OCCUR
- 5) WITH AN ERROR PENDING AND "XFER REQ." SET THE FOLLOWING SEQUENCE OCCURS
 - A) "XFER REQ" CAUSES AN INTERRUPT
 - B) CLEARING "XFER REQ" ALLOWS "READY" TO SET
 - C) "READY" CAUSES AN INTERRUPT

////////////////////////////////////

 : THIS TEST WILL SETUP THE TA11'S VECTOR ADDRESS AND AT ADDRESS 0
 : A JMP @PC+ IS STORED IN CASE THE VECTOR IS READ AS 0.
 : IT WILL THEN SET THE PRIORITY LEVEL TO 0 AND WASTE ENOUGH TIME
 : FOR AN INTERRUPT TO OCCUR.
 : AFTER THE TIME IS UP IT WILL RESTORE THE PS TO LEVEL 7
 : AND CHECK IF AN INTERRUPT OCCURRED.

 : *TEST 5 TEST INTERRUPT WITH READY = "1" AT LEVEL 0
 : *****

```

909 003342 000004      TST5:  SCOPE
910 003344 012767 003362 175534      MOV   #1$, $LPADR   ;;SET SCOPE LOOP ADDRESS
911 003352 012767 003520 175610      MOV   #6$, $ESCAPE ;;ESCAPE TO 6$ ON ERROR
912 003360 000005      RESET
913 003362 010314 1%:   MOV   DRIVE,@TACS
914 003364 012737 000340 177776      MOV   #340,@#PS    :LOCK OUT ALL INTERRUPTS
915 003372 012700 000000      MOV   #0,R0        :TEST AT PRIORITY LEVEL 0
916 003376 005001      CLR   R1           :CLEAR INTERRUPT KEY
917 003400 012702 000001      MOV   #1,R2        :SETUP TO WASTE A LITTLE TIME
918 003404 012777 003510 175604      MOV   #4$,@TAVEC   :INTERRUPT RETURN
919 003412 012777 000340 175600      MOV   #340,@TAVEC+2 :LOCK OUT THE WORLD IF INTERRUPT

```

K03

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950 003420 012737 000137 000000      MOV      #137, @#0      ;SETUP TO CATCH INTERRUPT IF
951 003426 012737 003514 000002      MOV      #55, @#2      ; IT GOES TO LOCATION 0
952 003434 112714 000100      MOV      #INT.EN, @TACS ;SET INTERRUPT ENABLE
953 003440 012737 000000 177776      MOV      #0*40, @#PS    ;SET TO PRIORITY LEVEL 0
954 003446 006302      2$: ASL      R0            ;KILL SOME TIME
955 003450 001376      BNE      2$
956 003452 012737 000340 177776      MOV      #340, @#PS     ;LOCK OUT THE WORLD
957 003460 005701      TST     R1            ;DID AN INTERRUPT OCCUR?
958 003462 001005      BNE     2$           ;BR IF YES
959 003464 022737 000000 001222      CMP     #0*40, @#TAPRIO ;WAS AN INTERRUPT EXPECTED?
960 003472 002012      BGE     5$           ;BR IF NO
961 003474 104006      ERROR   5$          ;INTERRUPT FAILED TO OCCUR AT PRIORITY LEVEL 0
962 003476 022737 000000 001222 3$: CMP     #0*40, @#TAPRIO ;INTERRUPT OCCURRED--SHOULD IT HAVE?
963 003504 002405      BLT     5$           ;BR IF YES
964 003506 104006      ERROR   5$          ;INTERRUPT OCCURRED AT PRIORITY LEVEL 0
965 003510 005201      4$: INC     R1            ;SET INTERRUPT KEY
966 003512 000002      RTI                      ;RETURN AFTER INTERRUPT
967 003514 022626      5$: CMP     (SP)+, (SP)+  ;POP THE STACK
968 003516 104006      ERROR   5$          ;INTERRUPT WENT TO LOCATION 0
969 003520 005014      6$: CLR     @TACS        ;CLEAR INTERRUPT ENABLE
970 003522 013777 001220 175466      MOV     @#TAVEC+2, @TAVEC ;SET TRAP CATCHER
971 003530 005077      CLR     @TAVEC+2
972 003534 005037      CLR     @#0
973 003540 005037      CLR     @#2
974 003544 000004      ;*****
975 003546 012767 003554 175332      ;THIS TEST WILL SETUP THE TA11'S VECTOR ADDRESS AND AT ADDRESS 0
976 003554 012767 003722 175406      ;A JMP @#(PC)+ IS STORED INCASE THE VECTOR IS READ AS 0.
977 003562 000005      ;IT WILL THEN SET THE PRIORITY LEVEL TO 1 AND WASTE ENOUGH TIME
978 003564 010314      ;FOR AN INTERRUPT TO OCCUR.
979 003566 012737 000340 177776      ;AFTER THE TIME IS UP IT WILL RESTORE THE PS TO LEVEL 7
980 003574 012700 000001      ;AND CHECK IF AN INTERRUPT OCCURRED.
981 003600 005001      ;*****
982 003602 012702 000001      ;*TEST 6 TEST INTERRUPT WITH READY = "1" AT LEVEL 1
983 003606 012777 003712 175402      ;*****
984 003614 012777 000340 175376      ;*****
985 003622 012737 000137 000000      ;*****
986 003630 012737 003716 000002      ;*****
987 003636 112714 000100      ;*****
988 003642 012737 000040 177776      ;*****
989 003650 006302      1$: ASL     R2            ;SCOPE
990 003652 001376      MOV     #1$, $LPADR    ;MOV #6$, $ESCAPE ;:SET SCOPE LOOP ADDRESS
991 003654 012737 000340 177776      MOV     #340, @#PS     ;:ESCAPE TO 6$ ON ERROR
992 003656 005701      RESET
993 003658 006302      MOV     DRIVE, @TACS   ;LOCK OUT ALL INTERRUPTS
994 003660 012700 000001      MOV     #1, R0        ;TEST AT PRIORITY LEVEL 1
995 003662 012700 000001      CLR     R1            ;CLEAR INTERRUPT KEY
996 003664 012702 000001      MOV     #1, R2        ;SETUP TO WASTE A LITTLE TIME
997 003666 012777 003712 175402      MOV     #4$, @TAVEC    ;INTERRUPT RETURN
998 003668 012777 000340 175376      MOV     #340, @TAVEC+2 ;LOCK OUT THE WORLD IF INTERRUPT
999 003670 012737 000137 000000      MOV     #137, @#0     ;SETUP TO CATCH INTERRUPT IF
1000 003672 012737 003716 000002      MOV     #55, @#2      ; IT GOES TO LOCATION 0
1001 003674 112714 000100      MOV     #INT.EN, @TACS ;SET INTERRUPT ENABLE
1002 003676 012737 000040 177776      MOV     #1*40, @#PS   ;SET TO PRIORITY LEVEL 1
1003 003678 006302      2$: ASL     R2            ;KILL SOME TIME
1004 003680 001376      BNE     2$
1005 003682 012737 000340 177776      MOV     #340, @#PS     ;LOCK OUT THE WORLD
1006 003684 005701      TST     R1            ;DID AN INTERRUPT OCCUR?

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976 003664 001005      BNE      3$      ;BR IF YES
977 003666 022737 000040 001222      CMP      #1*40, @#TAPRIO ;WAS AN INTERRUPT EXPECTED?
978 003674 002012      BGE      6$      ;BR IF NO
979 003676 104006      ERROR    6        ;INTERRUPT FAILED TO OCCUR AT PRIORITY LEVEL 1
980
981 003700 022737 000040 001222 3$:      CMP      #1*40, @#TAPRIO ;INTERRUPT OCCURRED--SHOULD IT HAVE?
982 003706 002405      BLT      6$      ;BR IF YES
983 003710 104006      ERROR    6        ;INTERRUPT OCCURRED AT PRIORITY LEVEL 1
984
985 003712 005201      4$:      INC      R1        ;SET INTERRUPT KEY
986 003714 000002      RTI                      ;RETURN AFTER INTERRUPT
987 003716 022626      5$:      CMP      (SP)+, (SP)+ ;POP THE STACK
988 003720 104006      ERROR    6        ;INTERRUPT WENT TO LOCATION 0
989
990 003722 005014      6$:      CLR      @TACS      ;CLEAR INTERRUPT ENABLE
991 003724 013777 001220 175264      MOV      @#TAVEC+2, @TAVEC ;SET TRAP CATCHER
992 003732 005077 175262      CLR      @TAVEC+2
993 003736 005037 000000      CLR      @#0
994 003742 005037 000002      CLR      @#2
995
996 ;*****
997 ;THIS TEST WILL SETUP THE TA11'S VECTOR ADDRESS AND AT ADDRESS 0
998 ;A JMP @#(PC)+ IS STORED IN CASE THE VECTOR IS READ AS 0.
999 ;IT WILL THEN SET THE PRIORITY LEVEL TO 2 AND WASTE ENOUGH TIME
1000 ;FOR AN INTERRUPT TO OCCUR.
1001 ;AFTER THE TIME IS UP IT WILL RESTORE THE PS TO LEVEL 7
1002 ;AND CHECK IF AN INTERRUPT OCCURRED.
1003
1004 ;*****
1005 ;*TEST 7 TEST INTERRUPT WITH READY = "1" AT LEVEL 2
1006 ;*****
1007
1008 ;TST7: SCOPE
1009      MOV      #1$, $LPADR      ;;SET SCOPE LOOP ADDRESS
1010      MOV      #6$, $ESCAPE    ;;ESCAPE TO 6$ ON ERROR
1011      RESET
1012      1$:      MOV      DRIVE, @TACS
1013      MOV      #340, @#PS      ;LOCK OUT ALL INTERRUPTS
1014      MOV      #2, R0          ;TEST AT PRIORITY LEVEL 2
1015      CLR      R1              ;CLEAR INTERRUPT KEY
1016      MOV      #1, R2          ;SETUP TO WASTE A LITTLE TIME
1017      MOV      #4$, @TAVEC    ;INTERRUPT RETURN
1018      MOV      #340, @TAVEC+2 ;LOCK OUT THE WORLD IF INTERRUPT
1019      MOV      #137, @#0      ;SETUP TO CATCH INTERRUPT IF
1020      MOV      #5$, @#2        ;IT GOES TO LOCATION 0
1021      MOV      #INT.EN, @TACS ;SET INTERRUPT ENABLE
1022      MOV      #2*40, @#PS    ;SET TO PRIORITY LEVEL 2
1023      2$:      ASL      R2        ;KILL SOME TIME
1024      BNE      2$
1025      MOV      #340, @#PS      ;LOCK OUT THE WORLD
1026      TST      R1              ;DID AN INTERRUPT OCCUR?
1027      BNE      3$              ;BR IF YES
1028      CMP      #2*40, @#TAPRIO ;WAS AN INTERRUPT EXPECTED?
1029      BGE      6$              ;BR IF NO
1030      ERROR    6                ;INTERRUPT FAILED TO OCCUR AT PRIORITY LEVEL 2
1031
1032      3$:      CMP      #2*40, @#TAPRIO ;INTERRUPT OCCURRED--SHOULD IT HAVE?
1033      BLT      6$              ;BR IF YES
1034      ERROR    6                ;INTERRUPT OCCURRED AT PRIORITY LEVEL 2

```



```

1032
1033 004114 005201          4$:   INC      R1          ;SET INTERRUPT KEY
1034 004116 000002          RTI          ;RETURN AFTER INTERRUPT
1035 004120 022626          5$:   CMP      (SP)+,(SP)+ ;POP THE STACK
1036 004122 104006          ERROR     6          ;INTERRUPT WENT TO LOCATION 0
1037
1038 004124 005014          6$:   CLR      @TACS        ;CLEAR INTERRUPT ENABLE
1039 004126 013777 001220 175062  MOV      @#TAVEC+2,@TAVEC ;SET TRAP CATCHER
1040 004134 005077 175060  CLR      @TAVEC+2
1041 004140 005037 000000  CLR      @#0
1042 004144 005037 000002  CLR      @#2
1043
1044      ;*****
1045      ;THIS TEST WILL SETUP THE TA11'S VECTOR ADDRESS AND AT ADDRESS 0
1046      ;A JMP @#(PC)+ IS STORED INCASE THE VECTOR IS READ AS 0.
1047      ;IT WILL THEN SET THE PRIORITY LEVEL TO 3 AND WASTE ENOUGH TIME
1048      ;FOR AN INTERRUPT TO OCCUR.
1049      ;AFTER THE TIME IS UP IT WILL RESTORE THE PS TO LEVEL 7
1050      ;AND CHECK IF AN INTERRUPT OCCURRED.
1051      ;*****
1052      ;*TEST 10      TEST INTERRUPT WITH READY = "1" AT LEVEL 3
1053      ;*****
1054      TST10:  SCOPE
1055      MOV      #1$, $LPADR      ;;SET SCOPE LOOP ADDRESS
1056      MOV      #6$, $ESCAPE    ;;ESCAPE TO 6$ ON ERROR
1057      RESET
1058      1$:   MOV      DRIVE,@TACS
1059      MOV      #340,@#PS        ;LOCK OUT ALL INTERRUPTS
1060      MOV      #3,R0           ;TEST AT PRIORITY LEVEL 3
1061      CLR      R1              ;CLEAR INTERRUPT KEY
1062      MOV      #1,R2           ;SETUP TO WASTE A LITTLE TIME
1063      MOV      #4$,@TAVEC      ;INTERRUPT RETURN
1064      MOV      #340,@TAVEC+2   ;LOCK OUT THE WORLD IF INTERRUPT
1065      MOV      #137,@#0       ;SETUP TO CATCH INTERRUPT IF
1066      MOV      #5$,@#2        ;IT GOES TO LOCATION 0
1067      MOV      #INT.EN,@TACS   ;SET INTERRUPT ENABLE
1068      MOV      #3*40,@#PS     ;SET TO PRIORITY LEVEL 3
1069      2$:   ASL      R2          ;KILL SOME TIME
1070      BNE     2$
1071      MOV      #340,@#PS      ;LOCK OUT THE WORLD
1072      TST     R1              ;DID AN INTERRUPT OCCUR?
1073      BNE     3$              ;BR IF YES
1074      CMP     #3*40,@#TAPRIO  ;WAS AN INTERRUPT EXPECTED?
1075      BGE     6$              ;BR IF NO
1076      ERROR   6               ;INTERRUPT FAILED TO OCCUR AT PRIORITY LEVEL 3
1077      3$:   CMP     #3*40,@#TAPRIO ;INTERRUPT OCCURRED--SHOULD IT HAVE?
1078      BLT     6$              ;BR IF YES
1079      ERROR   6               ;INTERRUPT OCCURRED AT PRIORITY LEVEL 3
1080
1081 004316 005201          4$:   INC      R1          ;SET INTERRUPT KEY
1082 004320 000002          RTI          ;RETURN AFTER INTERRUPT
1083 004322 022626          5$:   CMP      (SP)+,(SP)+ ;POP THE STACK
1084 004324 104006          ERROR     6          ;INTERRUPT WENT TO LOCATION 0
1085
1086 004326 005014          6$:   CLR      @TACS        ;CLEAR INTERRUPT ENABLE
1087 004330 013777 001220 174660  MOV      @#TAVEC+2,@TAVEC ;SET TRAP CATCHER

```



```

1088 004336 005077 174656
1089 004342 005037 000000
1090 004346 005037 000002
1091
1092
1093
1094
1095
1096
1097
1098
1099
1100
1101 004352 000004
1102 004354 012767 004372 174524
1103 004362 012767 004530 174600
1104 004370 000005
1105 004372 010314
1106 004374 012737 000340 177776
1107 004402 012700 000004
1108 004406 005001
1109 004410 012702 000001
1110 004414 012777 004520 174574
1111 004422 012777 000340 174570
1112 004430 012737 000137 000000
1113 004436 012737 004524 000002
1114 004444 112714 000100
1115 004450 012737 000200 177776
1116 004456 006302
1117 004460 001376
1118 004462 012737 000340 177776
1119 004470 005701
1120 004472 001005
1121 004474 022737 000200 001222
1122 004502 002012
1123 004504 104006
1124
1125 004506 022737 000200 001222
1126 004514 002405
1127 004516 104006
1128
1129 004520 005201
1130 004522 000002
1131 004524 022626
1132 004526 104006
1133
1134 004530 005014
1135 004532 013777 001220 174456
1136 004540 005077 174454
1137 004544 005037 000000
1138 004550 005037 000002
1139
1140
1141
1142
1143

```

```

CLR @TAVEC+2
CLR @#0
CLR @#2
*****
; THIS TEST WILL SETUP THE TA11'S VECTOR ADDRESS AND AT ADDRESS 0
; A JMP @ (PC)+ IS STORED IN CASE THE VECTOR IS READ AS 0.
; IT WILL THEN SET THE PRIORITY LEVEL TO 4 AND WASTE ENOUGH TIME
; FOR AN INTERRUPT TO OCCUR.
; AFTER THE TIME IS UP IT WILL RESTORE THE PS TO LEVEL 7
; AND CHECK IF AN INTERRUPT OCCURRED.
*****
*TEST 11 TEST INTERRUPT WITH READY = "1" AT LEVEL 4
*****
TST11: SCOPE
MOV #1$, $LPADR ;; SET SCOPE LOOP ADDRESS
MOV #6$, $ESCAPE ;; ESCAPE TO 6$ ON ERROR
RESET
1$: MOV DRIVE, @TACS
MOV #340, @#PS ;; LOCK OUT ALL INTERRUPTS
MOV #4, R0 ;; TEST AT PRIORITY LEVEL 4
CLR R1 ;; CLEAR INTERRUPT KEY
MOV #1, R2 ;; SETUP TO WASTE A LITTLE TIME
MOV #4$, @TAVEC ;; INTERRUPT RETURN
MOV #340, @TAVEC+2 ;; LOCK OUT THE WORLD IF INTERRUPT
MOV #137, @#0 ;; SETUP TO CATCH INTERRUPT IF
MOV #5$, @#2 ;; IT GOES TO LOCATION 0
MOVB #INT.EN, @TACS ;; SET INTERRUPT ENABLE
MOV #4*40, @#PS ;; SET TO PRIORITY LEVEL 4
2$: ASL R2 ;; KILL SOME TIME
BNE 2$
MOV #340, @#PS ;; LOCK OUT THE WORLD
TST R1 ;; DID AN INTERRUPT OCCUR?
BNE 3$ ;; BR IF YES
CMP #4*40, @#TAPRIO ;; WAS AN INTERRUPT EXPECTED?
BGE 6$ ;; BR IF NO
ERROR 6 ;; INTERRUPT FAILED TO OCCUR AT PRIORITY LEVEL 4
3$: CMP #4*40, @#TAPRIO ;; INTERRUPT OCCURRED--SHOULD IT HAVE?
BLT 6$ ;; BR IF YES
ERROR 6 ;; INTERRUPT OCCURRED AT PRIORITY LEVEL 4
4$: INC R1 ;; SET INTERRUPT KEY
RTI ;; RETURN AFTER INTERRUPT
5$: CMP (SP)+, (SP)+ ;; POP THE STACK
ERROR 6 ;; INTERRUPT WENT TO LOCATION 0
6$: CLR @TACS ;; CLEAR INTERRUPT ENABLE
MOV @#TAVEC+2, @TAVEC ;; SET TRAP CATCHER
CLR @TAVEC+2
CLR @#0
CLR @#2
*****
; THIS TEST WILL SETUP THE TA11'S VECTOR ADDRESS AND AT ADDRESS 0
; A JMP @ (PC)+ IS STORED IN CASE THE VECTOR IS READ AS 0.
; IT WILL THEN SET THE PRIORITY LEVEL TO 5 AND WASTE ENOUGH TIME
; FOR AN INTERRUPT TO OCCUR.

```


: AFTER THE TIME IS UP IT WILL RESTORE THE PS TO LEVEL 7
: AND CHECK IF AN INTERRUPT OCCURRED.

: TEST 12 TEST INTERRUPT WITH READY = "1" AT LEVEL 5

1171	004706	104006				1171	004706	104006			
1172						1172					
1173	004710	022737	000240	001222	3\$:	CMP	#5*40,0#TAPRIO				: INTERRUPT OCCURRED--SHOULD IT HAVE?
1174	004716	002405				BLT	6\$: BR IF YES
1175	004720	104006				ERROR	6				: INTERRUPT OCCURRED AT PRIORITY LEVEL 5
1176											
1177	004722	005201			4\$:	INC	R1				: SET INTERRUPT KEY
1178	004724	000002				RTI					: RETURN AFTER INTERRUPT
1179	004726	022626			5\$:	CMP	(SP)+,(SP)+				: POP THE STACK
1180	004730	104006				ERROR	6				: INTERRUPT WENT TO LOCATION 0
1181											
1182	004732	005014			6\$:	CLR	@TACS				: CLEAR INTERRUPT ENABLE
1183	004734	013777	001220	174254		MOV	@TAVEC+2,@TAVEC				: SET TRAP CATCHER
1184	004742	005077	174252			CLR	@TAVEC+2				
1185	004746	005037	000000			CLR	@#0				
1186	004752	005037	000002			CLR	@#2				
1187											
1188											
1189											
1190											
1191											
1192											
1193											
1194											
1195											
1196											
1197	004756	000004			TEST13:	SCOPE					
1198	004760	012767	004776	174120		MOV	#1\$, \$LPADR				: SET SCOPE LOOP ADDRESS
1199	004766	012767	005134	174174		MOV	#6\$, \$ESCAPE				: ESCAPE TO 6\$ ON ERROR

: THIS TEST WILL SETUP THE TA11'S VECTOR ADDRESS AND AT ADDRESS 0
: A JMP @PC)+ IS STORED IN CASE THE VECTOR IS READ AS 0.
: IT WILL THEN SET THE PRIORITY LEVEL TO 6 AND WASTE ENOUGH TIME
: FOR AN INTERRUPT TO OCCUR.
: AFTER THE TIME IS UP IT WILL RESTORE THE PS TO LEVEL 7
: AND CHECK IF AN INTERRUPT OCCURRED.

: TEST 13 TEST INTERRUPT WITH READY = "1" AT LEVEL 6

```

12000 004774 000005          RESET
12001 004776 010314          1$: MOV    DRIVE, @TACS
12002 005000 012737 000340 177776  MOV    #340, @#PS          ;LOCK OUT ALL INTERRUPTS
12003 005006 012700 000006          MOV    #6, R0             ;TEST AT PRIORITY LEVEL 6
12004 005012 005001          CLR    R1                 ;CLEAR INTERRUPT KEY
12005 005014 012702 000001          MOV    #1, R2             ;SETUP TO WASTE A LITTLE TIME
12006 005020 012777 005124 174170  MOV    #4$, @TAVEC        ;INTERRUPT RETURN
12007 005026 012777 000340 174164  MOV    #340, @TAVEC+2     ;LOCK OUT THE WORLD IF INTERRUPT
12008 005034 012737 000137 000000  MOV    #137, @#0          ;SETUP TO CATCH INTERRUPT IF
12009 005042 012737 005130 000002  MOV    #5$, @#2          ;IT GOES TO LOCATION 0
12010 005050 112714 000100          MOV    #INT.EN, @TACS     ;SET INTERRUPT ENABLE
12011 005054 012737 000300 177776  MOV    #6*40, @#PS        ;SET TO PRIORITY LEVEL 6
12012 005062 005302          2$: ASL    R2                ;KILL SOME TIME
12013 005064 001376          BNE   2$
12014 005066 012737 000340 177776  MOV    #340, @#PS        ;LOCK OUT THE WORLD
12015 005074 005701          TST   R1                 ;DID AN INTERRUPT OCCUR?
12016 005076 001005          BNE   3$                 ;BR IF YES
12017 005100 022737 000300 001222  CMP    #6*40, @#TAPRIO    ;WAS AN INTERRUPT EXPECTED?
12018 005106 002012          BGE   6$                 ;BR IF NO
12019 005110 104006          ERROR 6                  ;INTERRUPT FAILED TO OCCUR AT PRIORITY LEVEL 6
12020
12021 005112 022737 000300 001222  3$:  CMP    #6*40, @#TAPRIO    ;INTERRUPT OCCURRED--SHOULD IT HAVE?
12022 005120 002405          BLT   6$                 ;BR IF YES
12023 005122 104006          ERROR 6                  ;INTERRUPT OCCURRED AT PRIORITY LEVEL 6
12024
12025 005124 005201          4$:  INC    R1                ;SET INTERRUPT KEY
12026 005126 000002          RTI   ;RETURN AFTER INTERRUPT
12027 005130 022626          5$:  CMP    (SP)+, (SP)+      ;POP THE STACK
12028 005132 104006          ERROR 6                  ;INTERRUPT WENT TO LOCATION 0
12029
12030 005134 005014          6$:  CLR    @TACS             ;CLEAR INTERRUPT ENABLE
12031 005136 013777 001220 174052  MOV    @#TAVEC+2, @TAVEC ;SET TRAP CATCHER
12032 005144 005077 174050          CLR    @TAVEC+2
12033 005150 005037 000000          CLR    @#0
12034 005154 005037 000002          CLR    @#2
12035
12036          ;:*****
12037          ;THIS TEST WILL SETUP THE TA11'S VECTOR ADDRESS AND AT ADDRESS 0
12038          ;A JMP @PC+ IS STORED IN CASE THE VECTOR IS READ AS 0.
12039          ;IT WILL THEN SET THE PRIORITY LEVEL TO 7 AND WASTE ENOUGH TIME
12040          ;FOR AN INTERRUPT TO OCCUR.
12041          ;AFTER THE TIME IS UP IT WILL RESTORE THE PS TO LEVEL 7
12042          ;AND CHECK IF AN INTERRUPT OCCURRED.
12043          ;:*****
12044          ;*TEST 14 TEST INTERRUPT WITH READY = "1" AT LEVEL 7
12045          ;:*****
12046          ;*TEST 14: SCOPE
12047          MOV    #1$, $LPAOR    ;:SET SCOPE LOOP ADDRESS
12048          MOV    #6$, $ESCAPE ;:ESCAPE TO 6$ ON ERROR
12049          RESET
12050          1$: MOV    DRIVE, @TACS
12051          MOV    #340, @#PS          ;LOCK OUT ALL INTERRUPTS
12052          MOV    #7, R0             ;TEST AT PRIORITY LEVEL 7
12053          CLR    R1                 ;CLEAR INTERRUPT KEY
12054          MOV    #1, R2             ;SETUP TO WASTE A LITTLE TIME
12055          MOV    #4$, @TAVEC        ;INTERRUPT RETURN
12056          MOV    #340, @TAVEC+2     ;LOCK OUT THE WORLD IF INTERRUPT

```



```

1256 005236 012737 000137 000000      MOV      #137, @#0      ; SETUP TO CATCH INTERRUPT IF
1257 005244 012737 005332 000002      MOV      #5$, @#2      ; IT GOES TO LOCATION 0
1258 005252 112714 000100      MOV      #INT.EN, @TACS ; SET INTERRUPT ENABLE
1259 005256 012737 000340 177776      MOV      #7*40, @#PS    ; SET TO PRIORITY LEVEL 7
1260 005264 005302      2$:      ASL      R2            ; KILL SOME TIME
1261 005266 001376      BNE      2$
1262 005270 012737 000340 177776      MOV      #340, @#PS     ; LOCK OUT THE WORLD
1263 005276 005701      TST      R1            ; DID AN INTERRUPT OCCUR?
1264 005300 001005      BNE      3$           ; BR IF YES
1265 005302 022737 000340 001222      CMP      #7*40, @#TAPRIO ; WAS AN INTERRUPT EXPECTED?
1266 005310 002012      BGE      6$           ; BR IF NO
1267 005312 104006      ERROR    6            ; INTERRUPT FAILED TO OCCUR AT PRIORITY LEVEL 7
1268
1269 005314 022737 000340 001222 3$:      CMP      #7*40, @#TAPRIO ; INTERRUPT OCCURRED--SHOULD IT HAVE?
1270 005322 002405      BLT      6$           ; BR IF YES
1271 005324 104006      ERROR    6            ; INTERRUPT OCCURRED AT PRIORITY LEVEL 7
1272
1273 005326 005201      4$:      INC      R1            ; SET INTERRUPT KEY
1274 005330 000002      RTI                          ; RETURN AFTER INTERRUPT
1275 005332 022626      5$:      CMP      (SP)+, (SP)+    ; POP THE STACK
1276 005334 104006      ERROR    6            ; INTERRUPT WENT TO LOCATION 0
1277
1278 005336 005014      6$:      CLR      @TACS         ; CLEAR INTERRUPT ENABLE
1279 005340 013777 001220 173650      MOV      @#TAVEC+2, @TAVEC ; SET TRAP CATCHER
1280 005346 005077 173646      CLR      @TAVEC+2
1281 005352 005037 000000      CLR      @#0
1282 005356 005037 000002      CLR      @#2
1283
1284      ::*****
1285      ::*TEST 15 TEST INTERRUPT WITH "TRANSFER REQUEST" = 1
1286      ::*****
1287      †ST15: SCOPE
1288      MOV      #10, $TIMES      ;; DO 10 ITERATIONS
1289      MOV      #4$, $LPADR      ;; SET SCOPE LOOP ADDRESS
1290      MOV      #3$, $ESCAPE    ;; ESCAPE TO 3$ ON ERROR
1291      RESET
1292      MOV      DRIVE, @TACS     ; SELECT DRIVE
1293      MOV      #REWIND+GO, @TACS ; POSITION THE TAPE
1294      WAITREADY                ; GO WAIT FOR "READY" TO SET
1295      MOV      DRIVE, @TACS     ; SELECT DRIVE
1296      MOV      #WFG+GO, @TACS  ; START A "WRITE FILE GAP"
1297      WAITREADY                ; GO WAIT FOR "READY" TO SET
1298      MOV      #340, @#PS      ; LOCK OUT ALL INTERRUPTS
1299      MOV      #1, R1          ; SETUP TO WASTE SOME TIME
1300      MOV      #2$, @TAVEC     ; SETUP INTERRUPT RETURN
1301      MOV      #340, @TAVEC+2  ; PRIORITY "7" ON INTERRUPT
1302      MOV      #137, @#0       ; CATCH INTERRUPT IF IT GOES
1303      MOV      #5$, @#2        ; TO LOCATION 0
1304      MOV      #WRITE!INT.EN!GO, @TACS ; START A "WRITE" WITH "INT. EN."
1305      WAITXFER                ; GO WAIT ON "TRANSFER REQUEST" TO SET
1306      CLR      @#PS            ; LEVEL 0
1307      CLR      R0
1308      ASL      R1              ; KILL A LITTLE TIME
1309      BNE      1$
1310      MOV      #340, @#PS      ; LOCK OUT ALL INTERRUPTS
1311      ERROR    6              ; INTERRUPT FOR XFER REQ. FAILED
1312      CMP      (SP)+, (SP)+    ; POP THE STACK

```


E04

TA11 BASIC LOGIC TEST (PART 2) MAINDEC-11-DZTAB-C
DZTAB0.NEW T15

TEST INTERRUPT WITH "TRANSFER REQUEST" = 1

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SEQ 0043
SEQ 0043

```

1313 005524 104006          ERROR 6          : INTERRUPT WENT TO LOCATION 0
1313 005526 022626          2$: CMP (SP)+,(SP)+ : POP STACK
1314 005530 000005          3$: RESET          : CLEAR ALL
1315 005532 013777 001220 173456  MOV @#TAVEC+2,@TAVEC : SET TRAP CATCHER
1316 005540 005077 173454  CLR @TAVEC+2
1317 005544 005037 000000  CLR @#0
1318 005550 005037 000002  CLR @#2
:*****
: *TEST 16 TEST INTERRUPT WITH "READY" = 0 AND "XFER REQ" = 0
:*****
1320 005554 000004 1ST16: SCOPE
1321 005556 012767 000001 173402  MOV #1,$TIMES          ;;DO 1 ITERATION
1322 005564 012767 005724 173376  MOV #5,$$,$$ESCAPE    ;;ESCAPE TO 5$ ON ERROR
1323 005572 012767 005724 000064  MOV #5,$3$
1324 005600 012701 000001  MOV #1,$R1
1325 005604 012737 000340 177776  MOV #340,@#PS          : LOCK OUT THE WORLD
1326 005612 000005          RESET          : CLEAR THE WORLD
1327 005614 010314          MOV DRIVE,@TACS    : SELECT DRIVE
1328 005616 104412          WAITREADY         : GO WAIT FOR "READY" TO SET
1329 005620 012777 005714 173370  MOV #4$,@TAVEC        : RETURN IF INTERRUPT OCCURS
1330 005626 012777 000340 173364  MOV #340,@TAVEC+2     : LEVEL "7" IF INTERRUPT
1331 005634 012737 000137 000000  MOV #137,@#0          : CATCH INTERRUPT IF IT GOES TO
1332 005642 012737 005720 000002  MOV #7$,@#2           : LOCATION 0
1333 005650 112714 000117          MOVB #REWIND!INT.EN!GO,@TACS
1334 005654 032714 000240 1$: BIT #TR.REG!READY,@TACS : GIVE READY TIME TO CLEAR
1335 005660 001404          BEQ 2$
1336 005662 005327          DEC (PC)+
1337 005664 000000 3$: 0
1338 005666 001372          BNE 1$
1339 005670 104001          ERROR 1           : READY OR XFER REQ = "1"
1340 005672 005037 177776 2$: CLR @#PS          : ALLOW INTERRUPT
1341 005676 005000          CLR R0           : AT LEVEL 0
1342 005700 006301 6$: ASL R1           : GIVE INTERRUPT TIME TO COME IN
1343 005702 001376          BNE 6$
1344 005704 012737 000340 177776  MOV #340,@#PS          : LOCK OUT INTERRUPT
1345 005712 000404          BR 5$           : EXIT WITH NO ERRORS
1346 005714 022626 4$: CMP (SP)+,(SP)+     : POP STACK
1347 005716 104006          ERROR 6          : INTERRUPT OCCURRED
1348 005720 022626 7$: CMP (SP)+,(SP)+     : POP THE STACK
1349 005722 104006          ERROR 6          : INTERRUPTED TO LOCATION 0
1350 005724 000005 5$: RESET          : CLEAR THE WORLD
1351 005726 013777 001220 173262  MOV @#TAVEC+2,@TAVEC : SET TRAP CATCHER
1352 005734 005077 173260  CLR @TAVEC+2
1353 005740 005037 000000  CLR @#0
1354 005744 005037 000002  CLR @#2
:*****
: *TEST 17 TEST INTERRUPT WITH "XFER REQ" = 1 & "TIMING ERROR" = 1
:*****
1356 005750 000004 1ST17: SCOPE
1357 005752 012767 000012 173206  MOV #10,$TIMES        ;;DO 10. ITERATIONS
1358 005760 012767 006014 173120  MOV #1$,$LPADR        ;;SET SCOPE LOOP ADDRESS
1359 005766 012767 006222 173174  MOV #11$,$$ESCAPE    ;;ESCAPE TO 11$ ON ERROR
1360 005774 000005          RESET          : CLEAR ALL
1361 005776 010314          MOV DRIVE,@TACS    : SELECT DRIVE
1362 006000 112714 000017          MOVB #REWIND!GO,@TACS : GO TO "BOT"
1363 006004 104412          WAITREADY         : WAIT ON READY

```

F04

TA11 BASIC LOGIC TEST (PART 2) MAINDEC-11-DZTAB-C
 DZTABO.NEW T17 TEST INTERRUPT WITH "XFER REQ" = 1 & "TIMING ERROR" = 1

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SEQ 0044
SEQ 0044

1368	006006	112714	000001			MOV B #WFG!GO,@TACS	;GET ON OXIDE
1369	006012	104412				WAITREADY	
1370	006014	010314		18:		MOV DRIVE,@TACS	;SELECT DRIVE
1371	006016	112714	000003			MOV #WRITE!GO,@TACS	;START A WRITE
1372	006022	104413				WAITXFER	;WAIT ON "TRANSFER REQUEST"
1373	006024	112715	000377			MOV #377,@TADB	;WRITE A BYTE
1374	006030	005000				CLR R0	;ZERO THE COUNTER
1375	006032	005001				CLR R1	
1376	006034	105714		28:		TSTB @TACS	;CHECK "XFER REQ"
1377	006036	100405				BMI 3\$;BR IF SET
1378	006040	062700	000010			ADD #10,R0	;FIND OUT HOW LONG IT TAKES
1379	006044	005501				ADC R1	;FOR "XFER REQ" TO SET
1380	006046	100372				BPL 2\$	
1381	006050	104001				ERROR 1	; "XFER REQ" DIDN'T SET
1382	006052	105714		38:		TSTB @TACS	;CHECK "XFER REQ" IF IT IS = 0
1383	006054	100005				BPL 4\$;SOMETHING IS WRONG
1384	006056	162700	000001			SUB #1,R0	;DOWN COUNT THE COUNTER
1385	006062	005601				SBC R1	;SO A "TIMING ERROR" WILL OCCUR
1386	006064	100372				BPL 3\$	
1387	006066	000401				BR 5\$	
1388							
1389	006070	104001		48:		ERROR 1	; "XFER REQ" CLEARED
1390							
1391	006072	012777	006160	173116	58:	MOV #8\$,@TAVEC	;SETUP INTERRUPT RETURN
1392	006100	012777	000340	173112		MOV #340,@TAVEC+2	;PRIORITY "7" ON INTERRUPT
1393	006106	012737	000137	000000		MOV #137,@#0	;CATCH INTERRUPT IF IT GOES
1394	006114	012737	006154	000002		MOV #7\$,@#2	; TO LOCATION 0
1395	006122	012701	000001			MOV #1,R1	;SETUP TO WASTE SOME TIME
1396	006126	005037	177776			CLR @#PS	;ALLOW INTERRUPTS
1397	006132	005000				CLR R0	;AT LEVEL "0"
1398	006134	052714	000100			BIS #INT.EN,@TACS	;TURN ON "INTERRUPT ENABLE"
1399	006140	006301		68:		ASL R1	;GIVE INTERRUPT TIME TO OCCUR
1400	006142	001376				BNE 6\$	
1401	006144	012737	000340	177776		MOV #340,@#PS	;LOCK OUT INTERRUPTS
1402	006152	104006				ERROR 6	;FAILED TO INTERRUPT WITH "XFER REQ" = 1
1403							
1404	006154	022626		78:		CMP (SP)+,(SP)+	;POP THE STACK
1405	006156	104006				ERROR 6	;INTERRUPT WENT TO LOCATION 0
1406							
1407	006160	022626		88:		CMP (SP)+,(SP)+	;POP THE STACK
1408	006162	105715				TSTB @TADB	;KNOCK DOWN "XFER REQ"
1409	006164	104412				WAITREADY	;WAIT ON "READY" CAUSED BY "TIMING ERROR"
1410	006166	012777	006220	173022		MOV #10\$,@TAVEC	;SETUP INTERRUPT RETURN
1411	006174	012701	000001			MOV #1,R1	;SETUP TO WASTE TIME
1412	006200	005037	177776			CLR @#PS	;ALLOW INTERRUPTS
1413	006204	006301		98:		ASL R1	;GIVE INTERRUPTS TIME TO OCCUR
1414	006206	001376				BNE 9\$	
1415	006210	012737	000340	177776		MOV #340,@#PS	;LOCK OUT INTERRUPTS
1416	006215	104006				ERROR 6	; "INTERRUPT FAILED TO OCCUR FOR READY"
1417							
1418	006220	022626		108:		CMP (SP)+,(SP)+	;POP THE STACK
1419	006222	000005		118:		RESET	;CLEAR ALL
1420	006224	012777	001220	172764		MOV #TAVEC+2,@TAVEC	;RESTORE THE TRAP CATCHER
1421	006232	005077	172762			CLR @TAVEC+2	
1422	006236	005037	000000			CLR @#0	
1423	006242	005037	000002			CLR @#2	

THE FOLLOWING TEST ARE USED TO CHECK THE BASIC OPERATION OF THE SPACING FUNCTIONS

*TEST 20 TEST "BACK SPACE FILE GAP"

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```
TST20: SCOPE
MOV #10,$TIMES ;;DO 10 ITERATIONS
MOV #3$, $LPADR ;;SET SCOPE LOOP ADDRESS
MOV #TST21,$ESCAPE ;;ESCAPE TO TEST 21 ON ERROR
RESET
3$: MOV DRIVE,@TACS ;SELECT DRIVE
MOV #REWIND!GO,@TACS ;POSITION TAPE ON CLEAR LEADER
WAITREADY ;GO WAIT FOR "READY" TO SET
MOV #WFG!GO,@TACS ;WRITE A FILE GAP OFF OF CLEAR LEADER
WAITREADY ;GO WAIT FOR "READY" TO SET
MOV #WRITE!GO,@TACS ;PUT A DATA BLOCK ON THE TAPE
WAITXFER ;GO WAIT ON "TRANSFER REQUEST" TO SET
MOV #377,@TADB ;KNOCK DOWN XFER REQ
WAITXFER ;GO WAIT ON "TRANSFER REQUEST" TO SET
BIS #ILBS,@TACS ;WRITE "CRC"
WAITREADY ;GO WAIT FOR "READY" TO SET
MOV #BSFG!GO,@TACS ;BACK UP OVER THE DATA
WAITREADY ;GO WAIT FOR "READY" TO SET
BIT #FGAP,@TACS ;CHECK FOR FILE GAP
BNE 1$ ;BR IF "FILE GAP" =1
ERROR 1 ;ERROR--NO FILE GAP INDICATION
1$: TST @TACS ;IS "ERROR" =1?
BPL 2$ ;BR IF NO
ERROR 1 ;"ERROR" =1
2$: BIT #LEADER,@TACS ;IS "CLEAR LEADER" =1?
BEQ TST21 ;;BR IF NO
ERROR 1 ;"WFG" OR "BSFG" FAILED
```

*TEST 21 TEST "SPACE FORWARD FILE GAP" FUNCTION

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```
TST21: SCOPE
MOV #5,$TIMES ;;DO 5 ITERATIONS
MOV #1$, $LPADR ;;SET SCOPE LOOP ADDRESS
MOV #TST22,$ESCAPE ;;ESCAPE TO TEST 22 ON ERROR
RESET
MOV DRIVE,@TACS ;SELECT A DRIVE
MOV #REWIND!GO,@TACS ;POSITION THE TAPE
WAITREADY ;GO WAIT FOR "READY" TO SET
MOV #WFG!GO,@TACS ;WRITE FILE GAP OFF OF CLEAR LEADER
WAITREADY ;GO WAIT FOR "READY" TO SET
MOV #WRITE!GO,@TACS ;PUT SOME DATA ON THE TAPE
WAITXFER ;GO WAIT ON "TRANSFER REQUEST" TO SET
MOV #377,@TADB ;KNOCK DOWN XFER REQ
WAITXFER ;GO WAIT ON "TRANSFER REQUEST" TO SET
BIS #ILBS,@TACS ;WRITE CRC AND SHUT DOWN
WAITREADY ;GO WAIT FOR "READY" TO SET
MOV #WFG!GO,@TACS ;WRITE FILE GAP AFTER THE RECORD
WAITREADY ;GO WAIT FOR "READY" TO SET
1$: MOV #REWIND!GO,@TACS ;REPOSITION THE TAPE
```

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1480 006470 104412          WAITREADY          ;GO WAIT FOR "READY" TO SET
1481 006472 112714 000013  MOVB #SFFG!GO,@TACS ;SPACE OVER THE RECORD
1482 006476 104412          WAITREADY          ;GO WAIT FOR "READY" TO SET
1483 006500 032714 004000  BIT #FGAP,@TACS    ;SETTING IN A FILE GAP?
1484 006504 001001          BNE TST22          ;;BR IF YES
1485 006506 104001          ERROR 1          ;NO FILE GAP INDICATION
1486
1487 *****
1488 *TEST 22 TEST "BACK SPACE BLOCK GAP"
1489 *****
1490 TST22: SCOPE
1491 MOV #5,$TIMES      ;;DO 5 ITERATIONS
1492 MOV #1$, $LPADR   ;;SET SCOPE LOOP ADDRESS
1493 MOV #TST23,$ESCAPE ;;ESCAPE TO TEST 23 ON ERROR
1494
1495 1S:
1496 MOV DRIVE,@TACS   ;SELECT DRIVE
1497 MOVB #REWIND!GO,@TACS ;GO TO CLEAR LEADER
1498 WAITREADY        ;GO WAIT FOR "READY" TO SET
1499 MOVB #WFG!GO,@TACS ;WRITE A FILE GAP
1500 WAITREADY        ;GO WAIT FOR "READY" TO SET
1501 MOVB #WRITE!GO,@TACS ;WRITE A BLOCK OF DATA
1502 WAITXFER        ;GO WAIT ON "TRANSFER REQUEST" TO SET
1503 MOVB #377,@TADB  ;WRITE ONE BYTE
1504 WAITXFER        ;GO WAIT ON "TRANSFER REQUEST" TO SET
1505 BIS #ILBS,@TACS  ;WRITE "CRC"
1506 WAITREADY        ;GO WAIT FOR "READY" TO SET
1507 MOVB #BSBG!GO,@TACS ;BACK OVER THE DATA
1508 WAITREADY        ;GO WAIT FOR "READY" TO SET
1509 TST @TACS        ;CHECK FOR ERRORS
1510 BPL 2$          ;BR IF NONE
1511 ERROR 1          ;ERROR OCCURRED
1512 BIT #LEADER,@TACS ;IS CLEAR LEADER=1?
1513 BEQ TST23       ;;BR IF NO
1514 ERROR 1          ;ON CLEAR LEADER
1515 *****
1516 *TEST 23 TEST "SPACE FWD BLOCK GAP"
1517 *****
1518 TST23: SCOPE
1519 MOV #5,$TIMES      ;;DO 5 ITERATIONS
1520 MOV #1$, $LPADR   ;;SET SCOPE LOOP ADDRESS
1521 MOV #TST24,$ESCAPE ;;ESCAPE TO TEST 24 ON ERROR
1522
1523 1S:
1524 MOV DRIVE,@TACS   ;GO TO CLEAR LEADER
1525 MOVB #REWIND!GO,@TACS ;GO WAIT FOR "READY" TO SET
1526 WAITREADY        ;GO WAIT FOR "READY" TO SET
1527 MOVB #WFG!GO,@TACS ;PUT FILE GAP ON TAPE
1528 WAITREADY        ;GO WAIT FOR "READY" TO SET
1529 MOVB #WRITE!GO,@TACS ;WRITE ONE BYTE BLOCK
1530 WAITXFER        ;GO WAIT ON "TRANSFER REQUEST" TO SET
1531 MOVB #377,@TADB  ;GO WAIT ON "TRANSFER REQUEST" TO SET
1532 BIS #ILBS,@TACS  ;WRITE "CRC"
1533 WAITREADY        ;GO WAIT FOR "READY" TO SET
1534 MOVB #WFG!GO,@TACS ;WRITE A FILE GAP AFTER THE DATA
1535 WAITREADY        ;GO WAIT FOR "READY" TO SET
1536 1S:
1537 MOVB #REWIND!GO,@TACS ;GO TO "CLEAR LEADER"
1538 WAITREADY        ;GO WAIT FOR "READY" TO SET

```


TAB1 BASIC LOGIC TEST (PART 2) MAINDEC-11-DZTAB-C
DZTAB-C.NEW TEST "SPACE FWD BLOCK GAP"

1570	006724	112714	000015	
1571	006730	104412		
1572	006732	032714	124000	
1573	006736	001401		
1574	006740	104001		
1575				
1576				
1577				
1578				
1579				
1580				
1581				
1582				
1583				
1584				
1585				
1586				
1587				
1588				
1589				
1590				
1591				
1570	006742	000004		
1571	006744	012767	000012	172214
1572	006752	012767	006770	172126
1573	006760	012767	007050	172202
1574	006766	000005		
1575	006770	010314		
1576	006772	112714	000017	
1577	006776	104412		
1578	007000	112714	000003	
1579	007004	104413		
1580	007006	012715	000377	
1581	007012	104413		
1582	007014	052714	000020	
1583	007020	104412		
1584	007022	112714	000007	
1585	007026	104412		
1586	007030	032714	004000	
1587	007034	001001		
1588	007036	104001		
1589	007040	032714	120000	
1590	007044	001401		
1591	007046	104001		
1570	007050	000004		
1571	007052	012767	000012	172106
1572	007060	012767	007106	172020
1573	007066	012767	007176	172074
1574	007074	000005		
1575	007076	010314		
1576	007100	112714	000017	
1577	007104	104412		
1578	007106	112714	000003	
1579	007112	104413		
1580	007114	112715	000377	
1581	007120	104413		
1582	007122	052714	000020	
1583	007126	104412		
1584	007130	112714	000001	
1585	007134	104412		
1586	007136	112714	000011	
1587	007142	104412		
1588	007144	112714	000015	

```

MOV     #SFBG!GO,@TACS      ;SEE IF SFBG WILL SPACE OVER THE BLOCK OF DATA
WAITREADY      ;GO WAIT FOR "READY" TO SET
BIT     #ERROR!LEADER!FGAP,@TACS ;"ERROR" SHOULD BE CLEAR
BEG     TST24              ;;SO SHOULD "CLEAR LEADER" AND "FILE GAP"
ERROR   1                  ;TAPE IS IN THE WRONG PLACE AFTER A SFBG

////////////////////////////////////////////////////////////////////
// THE FOLLOWING TESTS ARE USED TO CHECK THE "FILE GAP" CIRCUITS
////////////////////////////////////////////////////////////////////
*****
*TEST 24 TEST AUTOMATIC "WFG" WHEN WRITING OFF OF "CLEAR LEADER"
*****
TST24:  SCOPE
        MOV     #10,$TIMES      ;;DO 10. ITERATIONS
        MOV     #1$, $LPADR     ;;SET SCOPE LOOP ADDRESS
        MOV     #TST25,$ESCAPE ;;ESCAPE TO TEST 25 ON ERROR
        RESET
1$:     MOV     DRIVE,@TACS      ;SELECT DRIVE
        MOVB    #REWIND!GO,@TACS ;GO TO CLEAR LEADER
        WAITREADY      ;GO WAIT FOR "READY" TO SET
        MOVB    #WRITE!GO,@TACS  ;WRITE "AUTO. FILE GAP"
        WAITXFER      ;GO WAIT ON "TRANSFER REQUEST" TO SET
        MOV     #377,@TADB      ;WRITE "DATA"
        WAITXFER      ;GO WAIT ON "TRANSFER REQUEST" TO SET
        BIS     #ILBS,@TACS     ;WRITE "CRC"
        WAITREADY      ;GO WAIT FOR "READY" TO SET
        MOVB    #BSFG!GO,@TACS  ;BACK OVER THE DATA BLOCK
        WAITREADY      ;GO WAIT FOR "READY" TO SET
        BIT     #FGAP,@TACS     ;IN FILE GAP?
        BNE     2$              ;BR IF YES
        ERROR   1              ;NO FILE GAP INDICATION
2$:     BIT     #ERROR!LEADER,@TACS ;ERROR SHOULD BE CLEAR
        BEG     TST25          ;;SO SHOULD "CLEAR LEADER"
        ERROR   1              ;ERROR OR CLEAR LEADER ON A (1)
*****
*TEST 25 TEST "READ" INTO FILE GAP CAUSES AN ERROR
*****
TST25:  SCOPE
        MOV     #10,$TIMES      ;;DO 10. ITERATIONS
        MOV     #1$, $LPADR     ;;SET SCOPE LOOP ADDRESS
        MOV     #TST26,$ESCAPE ;;ESCAPE TO TEST 26 ON ERROR
        RESET
1$:     MOV     DRIVE,@TACS      ;SELECT DRIVE
        MOVB    #REWIND!GO,@TACS ;GO TO BEGINNING OF TAPE
        WAITREADY      ;WAIT ON "READY"
        MOVB    #WRITE!GO,@TACS  ;START A WRITE
        WAITXFER      ;WAIT ON "XFER REQ"
        MOV     #377,@TADB      ;WRITE ONE BYTE
        WAITXFER      ;WAIT ON "XFER REQ"
        BIS     #ILBS,@TACS     ;WRITE "CRC"
        WAITREADY      ;WAIT ON "READY"
        MOVB    #WFG!GO,@TACS   ;WRITE A "FILE GAP"
        WAITREADY      ;WAIT ON "READY"
        MOVB    #BSBG!GO,@TACS  ;BACK OVER THE DATA
        WAITREADY      ;WAIT ON "READY"
        MOV     #SFBG!GO,@TACS  ;SPACE OVER THE DATA BLOCK

```

TEST "READ" INTO FILE GAP CAUSES AN ERROR

```

1593 007150 104412          WAITREADY          ;WAIT ON "READY"
1593 007152 112714 000005  MOVB #READ!GO,@TACS ;START A "READ"
1594 007156 104412          WAITREADY          ;WAIT ON "READY"
1595 007160 005714          TST @TACS          ;DID AN "ERROR" OCCUR?
1596 007162 100401          BMI 2$            ;BR IF YES
1597 007164 104001          ERROR 1          ;ERROR FAILED TO SET
1598 007166 032714 004000 2$: BIT #FGAP,@TACS ;IS FILE GAP=1?
1599 007172 001001          BNE TST26        ;;BR IF YES
1600 007174 104001          ERROR 1          ;FILE GAP NOT=1
1601
1602 *****
1603 ;*TEST 26 TEST "SFBG" INTO FILE GAP CAUSES AN ERROR
1604 *****
1604 007176 000004  †TST26: SCOPE
1605 007200 012767 000012 171760  MOV #10,$TIMES ;;DO 10. ITERATIONS
1606 007206 012767 007234 171672  MOV #1$, $LPADR ;;SET SCOPE LOOP ADDRESS
1607 007214 012767 007324 171746  MOV #TST27,$ESCAPE ;;ESCAPE TO TEST 27 ON ERROR
1608 007222 000005          RESET
1609 007224 010314          MOV DRIVE,@TACS ;SELECT DRIVE
1610 007226 112714 000017  MOVB #REWIND!GO,@TACS ;GO TO BEGINNING OF TAPE
1611 007232 104412          WAITREADY        ;WAIT ON "READY"
1612 007234 112714 000003 15:  MOVB #WRITE!GO,@TACS ;START A WRITE
1613 007240 104413          WAITXFER        ;WAIT ON "XFER REQ"
1614 007242 112715 000377  MOVB #377,@TADB ;WRITE ONE BYTE
1615 007246 104413          WAITXFER        ;WAIT ON "XFER REQ"
1616 007250 052714 000020  BIS #ILBS,@TACS ;WRITE "CRC"
1617 007254 104412          WAITREADY        ;WAIT ON "READY"
1618 007256 112714 000001  MOVB #WFG!GO,@TACS ;WRITE A "FILE GAP"
1619 007262 104412          WAITREADY        ;WAIT ON "READY"
1620 007264 112714 000011  MOVB #BSBG!GO,@TACS ;BACK OVER THE DATA
1621 007270 104412          WAITREADY        ;WAIT ON "READY"
1622 007272 112714 000015  MOVB #SFBG!GO,@TACS ;SPACE OVER THE DATA BLOCK
1623 007276 104412          WAITREADY        ;WAIT ON "READY"
1624 007300 112714 000015  MOVB #SFBG!GO,@TACS ;START A "S"BG"
1625 007304 104412          WAITREADY        ;WAIT ON "READY"
1626 007306 005714          TST @TACS        ;DID AN "ERROR" OCCUR?
1627 007310 100401          BMI 2$            ;BR IF YES
1628 007312 104001          ERROR 1          ;ERROR FAILED TO SET
1629 007314 032714 004000 2$: BIT #FGAP,@TACS ;IS FILE GAP=1?
1630 007320 001001          BNE TST27        ;;BR IF YES
1631 007322 104001          ERROR 1          ;FILE GAP NOT=1
1632
1633 *****
1634 ;*TEST 27 TEST "ERROR" OUTPUT OF THE STATUS ROM WITH "FILE GAP=1"
1635 *****
1635 007324 000004  †TST27: SCOPE
1636 007326 012767 000012 171632  MOV #10,$TIMES ;;DO 10. ITERATIONS
1637 007334 012767 007430 171544  MOV #1$, $LPADR ;;SET SCOPE LOOP ADDRESS
1638 007342 012767 007574 171620  MOV #TST30,$ESCAPE ;;ESCAPE TO TEST 30 ON ERROR
1639 007350 000005          RESET
1640 007352 010314          MOV DRIVE,@TACS ;SELECT DRIVE
1641 007354 104412          WAITREADY        ;MAKE SURE READY IS SET
1642 007356 112714 000017  MOVB #REWIND!GO,@TACS ;GO TO BEGINNING OF TAPE
1643 007362 104412          WAITREADY        ;WAIT ON "READY"
1644 007364 112714 000001  MOVB #WFG!GO,@TACS ;GET OFF OF CLEAR LEADER
1645 007370 104412          WAITREADY        ;WAIT FOR READY
1646 007372 112714 000003  MOVB #WRITE!GO,@TACS ;START A WRITE
1647 007376 104413          WAITXFER        ;WAIT ON "XFER REQ"

```


TEST "ERROR" OUTPUT OF THE STATUS ROM WITH "FILE GAP=1"

```

1648 007400 112715 000377      MOVB      #377,@TADB      ;WRITE ONE BYTE
1649 007404 104413      WAITXFER      ;WAIT ON "XFER REQ"
1650 007406 112715 000377      MOVB      #377,@TADB      ;WRITE
1651 007412 104413      WAITXFER      ;WAIT ON "XFER REQ"
1652 007414 052714 000020      BIS       #ILBS,@TACS     ;WRITE "CRC"
1653 007420 104412      WAITREADY     ;WAIT ON "READY"
1654 007422 112714 000001      MOVB      #WFG!GO,@TACS   ;WRITE A "FILE GAP"
1655 007426 104412      WAITREADY     ;WAIT ON "READY"
1656 007430 112714 000017      13:      MOVB      #REWIND!GO,@TACS ;BACK OVER THE DATA
1657 007434 104412      WAITREADY     ;WAIT ON "READY"
1658 007436 112714 000013      MOVB      #SFFG!GO,@TACS  ;DO A SPACE FWD FILE GAP
1659 007442 104412      WAITREADY
1660 007444 032714 004000      BIT       #FGAP,@TACS     ;IS FILE GAP=1?
1661 007450 001001      BNE       2$           ;BR IF YES
1662 007452 104001      ERROR      1           ;FILE GAP NOT=1
1663                                ;NOW CHECK "ERROR" BIT FOR ALL FUNCTIONS
1664 007454 112714 000000      2$:      MOVB      #WFG,@TACS     ;CHECK "ERROR" WITH "WFG"
1665 007460 005714      TST       @TACS        ;SAMPLE THE "ERROR" BIT
1666 007462 100001      BPL       3$           ;BR IF "ERROR" = 0
1667 007464 104001      ERROR      1           ;"ERROR" NOT = 0
1668 007466 112714 000002      3$:      MOVB      #WRITE,@TACS   ;CHECK "ERROR" WITH "WRITE"
1669 007472 005714      TST       @TACS        ;SAMPLE THE "ERROR" BIT
1670 007474 100001      BPL       4$           ;BR IF "ERROR" = 0
1671 007476 104001      ERROR      1           ;"ERROR" NOT = 0
1672 007500 112714 000004      4$:      MOVB      #READ,@TACS    ;CHECK "ERROR" WITH "READ"
1673 007504 005714      TST       @TACS        ;SAMPLE THE "ERROR" BIT
1674 007506 100401      BMI       5$           ;BR IF "ERROR" = 1
1675 007510 104001      ERROR      1           ;"ERROR" NOT = 1
1676 007512 112714 000006      5$:      MOVB      #BSFG,@TACS    ;CHECK "ERROR" WITH "BSFG"
1677 007516 005714      TST       @TACS        ;SAMPLE THE "ERROR" BIT
1678 007520 100001      BPL       6$           ;BR IF "ERROR" = 0
1679 007522 104001      ERROR      1           ;"ERROR" NOT = 0
1680 007524 112714 000010      6$:      MOVB      #BSBG,@TACS    ;CHECK "ERROR" WITH "BSBG"
1681 007530 005714      TST       @TACS        ;SAMPLE THE "ERROR" BIT
1682 007532 100001      BPL       7$           ;BR IF "ERROR" = 0
1683 007534 104001      ERROR      1           ;"ERROR" NOT = 0
1684 007536 112714 000012      7$:      MOVB      #SFFG,@TACS    ;CHECK "ERROR" WITH "SFFG"
1685 007542 005714      TST       @TACS        ;SAMPLE THE "ERROR" BIT
1686 007544 100001      BPL       8$           ;BR IF "ERROR" = 0
1687 007546 104001      ERROR      1           ;"ERROR" NOT = 0
1688 007550 112714 000014      8$:      MOVB      #SFBG,@TACS    ;CHECK "ERROR" WITH "SFBG"
1689 007554 005714      TST       @TACS        ;SAMPLE THE "ERROR" BIT
1690 007556 100401      BMI       9$           ;BR IF "ERROR" = 1
1691 007560 104001      ERROR      1           ;"ERROR" NOT = 1
1692 007562 112714 000016      9$:      MOVB      #REWIND,@TACS   ;CHECK "ERROR" WITH "REWIND"
1693 007566 005714      TST       @TACS        ;SAMPLE THE "ERROR" BIT
1694 007570 100001      BPL       TST30        ;BR IF "ERROR" = 0
1695 007572 104001      ERROR      1           ;"ERROR" NOT = 0

```



```

1696
1697
1698
1699
1700
1701
1702
1703 007574 000004
1704 007576 012767 000012 171362
1705 007604 012767 007700 171274
1706 007612 012767 010000 171350
1707 007620 000005
1708 007622 010314
1709 007624 104412
1710 007626 112714 000017
1711 007632 104412
1712 007634 112714 000001
1713 007640 104412
1714 007642 112714 000003
1715 007646 104413
1716 007650 112715 000377
1717 007654 104413
1718 007656 112715 000377
1719 007662 104413
1720 007664 052714 000020
1721 007670 104412
1722 007672 112714 000001
1723 007676 104412
1724 007700 112714 000017
1725 007704 104412
1726 007706 112714 000013
1727 007712 104412
1728 007714 032714 004000
1729 007720 001001
1730 007722 104001
1731 007724 112714 000007
1732 007730 104412
1733 007732 032714 004000
1734 007736 001001
1735 007740 104001
1736 007742 032714 100000
1737 007746 001401
1738 007750 104001
1739 007752 112714 000007
1740 007756 104412
1741 007760 032714 100000
1742 007764 001001
1743 007766 104001
1744 007770 032714 020000
1745 007774 001001
1746 007776 104001

```

```

: ////////////////////////////////////////////////////////////////////
: ////////////////////////////////////////////////////////////////////
: THE FOLLOWING TESTS INSURE THAT BACKING INTO "CLEAR LEADER" CAUSES AN ERROR
: ////////////////////////////////////////////////////////////////////
: ////////////////////////////////////////////////////////////////////
: *****
: *TEST 30 TEST BACK-SPACE-FILE-GAP INTO CLEAR LEADER
: *****
TST30: SCOPE
MOV #10,$TIMES ;;DO 10. ITERATIONS
MOV #1$, $LPADR ;;SET SCOPE LOOP ADDRESS
MOV #TST31,$ESCAPE ;;ESCAPE TO TEST 31 ON ERROR
RESET
MOV DRIVE,$TACS ;SELECT DRIVE
WAITREADY ;MAKE SURE READY IS SET
MOVB #REWIND!GO,$TACS ;GO TO BEGINNING OF TAPE
WAITREADY ;WAIT ON "READY"
MOVB #WFG!GO,$TACS ;GET OFF OF CLEAR LEADER
WAITREADY ;WAIT FOR READY
MOVB #WRITE!GO,$TACS ;START A WRITE
WAITXFER ;WAIT ON "XFER REQ"
MOVB #377,$TADB ;WRITE ONE BYTE
WAITXFER ;WAIT ON "XFER REQ"
MOVB #377,$TADB ;WRITE
WAITXFER ;WAIT ON "XFER REQ"
BIS #ILBS,$TACS ;WRITE "CRC"
WAITREADY ;WAIT ON "READY"
MOVB #WFG!GO,$TACS ;WRITE A "FILE GAP"
WAITREADY ;WAIT ON "READY"
1$: MOVB #REWIND!GO,$TACS ;BACK OVER THE DATA
WAITREADY ;WAIT ON "READY"
MOVB #SFFG!GO,$TACS ;DO A SPACE FWD FILE GAP
WAITREADY
BIT #FGAP,$TACS ;IS FILE GAP=1?
BNE 2$ ;BR IF YES
ERROR 1 ;FILE GAP NOT=1
2$: MOVB #BSFG!GO,$TACS ;GO TO FIRST FILE GAP ON TAPE
WAITREADY ;GO WAIT FOR "READY" TO SET
BIT #FGAP,$TACS ;IN A FILE GAP?
BNE 3$ ;BR IF YES
ERROR 1 ;DIDN'T STOP IN A GAP
3$: BIT #ERROR,$TACS ;DID WE GET AN ERROR?
BEQ 4$ ;BR IF NO
ERROR 1 ;AN ERROR OCCURRED
4$: MOVB #BSFG!GO,$TACS ;BACK ONTO THE CLEAR LEADER
WAITREADY ;GO WAIT FOR "READY" TO SET
BIT #ERROR,$TACS ;CHECK FOR AN ERROR
BNE 5$ ;BR IF "ERROR" BIT IS SET
ERROR 1 ;"ERROR" BIT FAILED TO SET
5$: BIT #LEADER,$TACS ;CHECK FOR CLEAR LEADER
BNE TST31 ;;BR IF "CLEAR LEADER" = 1
ERROR 1 ;"ERROR" WASN'T DUE TO "CLEAR LEADER"

```



```

1747
1748
1749
1750 010000 000004
1751 010002 012767 000012 171156
1752 010010 012767 010104 171070
1753 010016 012767 010172 171144
1754 010024 000005
1755 010026 010314
1756 010030 104412
1757 010032 112714 000017
1758 010036 104412
1759 010040 112714 000001
1760 010044 104412
1761 010046 112714 000003
1762 010052 104413
1763 010054 112715 000377
1764 010060 104413
1765 010062 112715 000377
1766 010066 104413
1767 010070 052714 000020
1768 010074 104412
1769 010076 112714 000001
1770 010102 104412
1771 010104 112714 000017
1772 010110 104412
1773 010112 112714 000013
1774 010116 104412
1775 010120 032714 004000
1776 010124 001001
1777 010126 104001
1778 010130 112714 000011
1779 010134 104412
1780 010136 032714 124000
1781 010142 001401
1782 010144 104001
1783 010146 112714 000011
1784 010152 104412
1785 010154 005714
1786 010156 100401
1787 010160 104001
1788 010162 032714 020000
1789 010166 001001
1790 010170 104001
1791
1792
1793
1794
1795
1796
1797
1798 010172 000004
1799 010174 012767 000012 170764
1800 010202 012767 010230 170676
1801 010210 012767 010330 170752
1802 010216 000005

```

```

*****
*TEST 31 TEST BACK-SPACE-BLOCK-GAP INTO CLEAR LEADER
*****
TST31: SCOPE
MOV #10,$TIMES ;;DO 10. ITERATIONS
MOV #1$, $LPADR ;;SET SCOPE LOOP ADDRESS
MOV #TST32,$ESCAPE ;;ESCAPE TO TEST 32 ON ERROR
RESET
MOV DRIVE,@TACS ;SELECT DRIVE
WAITREADY ;MAKE SURE READY IS SET
MOVB #REWIND!GO,@TACS ;GO TO BEGINNING OF TAPE
WAITREADY ;WAIT ON "READY"
MOVB #WFG!GO,@TACS ;GET OFF OF CLEAR LEADER
WAITREADY ;WAIT FOR READY
MOVB #WRITE!GO,@TACS ;START A WRITE
WAITXFER ;WAIT ON "XFER REQ"
MOVB #377,@TADB ;WRITE ONE BYTE
WAITXFER ;WAIT ON "XFER REQ"
MOVB #377,@TADB ;WRITE
WAITXFER ;WAIT ON "XFER REQ"
BIS #ILBS,@TACS ;WRITE "CRC"
WAITREADY ;WAIT ON "READY"
MOVB #WFG!GO,@TACS ;WRITE A "FILE GAP"
WAITREADY ;WAIT ON "READY"
MOVB #REWIND!GO,@TACS ;BACK OVER THE DATA
WAITREADY ;WAIT ON "READY"
MOVB #SFFG!GO,@TACS ;DO A SPACE FWD FILE GAP
WAITREADY
BIT #FGAP,@TACS ;IS FILE GAP=1?
BNE 2$ ;BR IF YES
ERROR 1 ;FILE GAP NOT=1
2$: MOVB #BSBG!GO,@TACS ;GO TO FIRST FILE GAP ON TAPE
WAITREADY ;GO WAIT FOR "READY" TO SET
BIT #ERROR!FGAP!LEADER,@TACS ;DID WE GET ANY ERROR?
BEG 3$ ;BR IF NO
ERROR 1 ;AN ERROR OCCURRED
3$: MOVB #BSBG!GO,@TACS ;BSBG WHILE IN A FILE GAP
WAITREADY ;GO WAIT FOR "READY" TO SET
TST @TACS ;"ERROR" BIT SHOULD BE SET
BMI 4$ ;BR IF IT IS
ERROR 1 ;"ERROR" BIT WASN'T SET
4$: BIT #LEADER,@TACS ;"CLEAR LEADER" SHOULD BE SET
BNE TST32 ;;BR IF "CLEAR LEADER" = 1
ERROR 1 ;"CLEAR LEADER" NOT SET

```

```

//
// THE FOLLOWING TESTS INSURE THAT DATA CAN BE WRITING ONTO AND READ FROM THE TAPE
//
//
*****
*TEST 32 TEST "WRITE" 377 & 0 "READ" 377 & 0
*****

```

```

TST32: SCOPE
MOV #10,$TIMES ;;DO 10. ITERATIONS
MOV #3$, $LPADR ;;SET SCOPE LOOP ADDRESS
MOV #TST33,$ESCAPE ;;ESCAPE TO TEST 33 ON ERROR
RESET

```



```

1880 010442 112714 000011 3$: MOVB #BBSG!GO,@TACS ;GO TO BEGINNING OF BLOCK
1881 010444 104412 WAITREADY ;WAIT ON "READY" TO SET
1882 010446 112714 000005 MOVB #READ!GO,@TACS ;START A "READ"
1883 010448 012700 000377 MOV #377,R0 ;FIRST DATA PATTERN AND COUNTER
1884 010450 104413 4$: WAITXFER ;WAIT ON "XFER REQ" TO SET
1885 010452 011501 MOV @TADB,R1 ;READ A BYTE FROM TAPE
1886 010454 020001 CMP R0,R1 ;IS IT VALID?
1887 010456 001401 BEQ 5$ ;BR IF YES
1888 010458 104005 ERROR 5$ ;BAD DATA READ FROM TAPE
1889 010460 005300 5$: DEC R0 ;NEXT PATTERN
1890 010462 002371 BGE 4$ ;BR IF MORE TO READ
1891 010464 104413 WAITXFER ;WAIT ON "XFER REQ" TO SET
1892 010466 052714 000020 BIS #ILBS,@TACS ;SHUT DOWN THE "READ" OPERATION
1893 010468 104412 WAITREADY ;WAIT ON "READY" TO SET
1894 010470 032714 140000 BIT #ERROR!CRCERR,@TACS ;ERROR AND CRCERR SHOULD BE = 0
1895 010472 001401 BEQ TST34 ;;BR IF THEY ARE
1896 010474 104001 ERROR 1 ;(ERROR ! CRCERR) = 1
1897 ////////////////////////////////////////////////////
1898 ////////////////////////////////////////////////////
1899 //THE FOLLOWING TESTS ARE USED TO INSURE THAT THE "CRC" CIRCUITRY FUNCTIONS PROPERLY
1900 ////////////////////////////////////////////////////
1901 ////////////////////////////////////////////////////
1902 //*****
1903 //*TEST 34 TEST "ERROR" WITH "CRCERR" = 1
1904 //*****
1905 TST34: SCOPE
1906 010516 000004 MOV #10, $TIMES ;;DO 10. ITERATIONS
1907 010520 012767 000012 170440 MOV #20$, $LPADR ;;SET SCOPE LOOP ADDRESS
1908 010526 012767 010562 170352 MOV #TST35, $ESCAPE ;;ESCAPE TO TEST 35 ON ERROR
1909 010534 012767 010776 170426 RESET
1910 010542 000005 MOV DRIVE,@TACS ;SELECT DRIVE
1911 010544 010314 MOVB #REWIND!GO,@TACS ;GO TO CLEAR LEADER
1912 010546 112714 000017 WAITREADY ;WAIT ON "READY"
1913 010552 104412 MOVB #WFG!GO,@TACS ;GET OFF OF CLEAR LEADER
1914 010554 112714 000001 WAITREADY ;WAIT FOR READY
1915 010556 104412 20$: MOVB #WRITE!GO,@TACS ;START A WRITE
1916 010562 112714 000003 MOV #6,R0 ;SETUP FOR 6 BYTES
1917 010566 012700 000006 1$: WAITXFER ;WAIT ON "XFER REQ"
1918 010572 104413 DEC R0 ;DOWN COUNT
1919 010574 005300 BLT 2$ ;DONE?
1920 010576 002403 MOVB #377,@TADB ;NO--WRITE ON TAPE
1921 010600 112715 000377 BR 1$ ;LOOP
1922 010604 000772 2$: BIS #ILBS,@TACS ;WRITE "CRC"
1923 010606 052714 000020 WAITREADY ;WAIT ON "READY"
1924 010612 104412 MOVB #BBSG!GO,@TACS ;BACK OVER THE DATA BLOCK
1925 010614 112714 000011 WAITREADY ;WAIT ON "READY"
1926 010620 104412 MOVB #READ!GO,@TACS ;START A "READ"
1927 010622 112714 000005 MOV #3,R0 ;DO 3 BYTES
1928 010626 012700 000003 3$: WAITXFER ;WAIT FOR "XFER REQ"
1929 010632 104413 DEC R0 ;COUNT # OF BYTES
1930 010634 005300 BLT 4$
1931 010636 002402 TSTB @TADB ;CLEAR "XFER REQ"
1932 010640 105715 BR 3$
1933 010642 000773 4$: BIS #ILBS,@TACS ;DO "ILBS"
1934 010644 052714 000020 WAITREADY ;WAIT ON "READY"
1935 010650 104412 TST @TACS ;CHECK FOR "ERROR"
1936 010652 005714 BMI 5$ ;BR IF "ERROR"
1937 010654 100401

```



```

1915 010656 104001          ERROR 1          ;"ERROR" BIT NOT SET
1916 010660 032714 040000 58:  BIT #CRCERR,@TACS ;CHECK FOR "CRC" ERROR
1917 010664 001001          BNE 6$          ;BR IF "CRC" ERROR
1918 010666 104001          ERROR 1          ;NO "CRC" ERROR
1919
1920 010670 112714 000000 6$:  MOVB #WFG,@TACS ;CHECK "ERROR" WITH "WFG"
1921 010674 005714          TST @TACS      ;SAMPLE THE "ERROR" BIT
1922 010676 100001          BPL 7$        ;BR IF "ERROR" = 0
1923 010700 104001          ERROR 1          ;"ERROR" NOT = 0
1924 010702 112714 000002 7$:  MOVB #WRITE,@TACS ;CHECK "ERROR" WITH "WRITE"
1925 010706 005714          TST @TACS      ;SAMPLE THE "ERROR" BIT
1926 010710 100401          BMI 8$        ;BR IF "ERROR" = 1
1927 010712 104001          ERROR 1          ;"ERROR" NOT = 1
1928 010714 112714 000006 8$:  MOVB #BSFG,@TACS ;CHECK "ERROR" WITH "BSFG"
1929 010720 005714          TST @TACS      ;SAMPLE THE "ERROR" BIT
1930 010722 100001          BPL 9$        ;BR IF "ERROR" = 0
1931 010724 104001          ERROR 1          ;"ERROR" NOT = 0
1932 010726 112714 000010 9$:  MOVB #BSBG,@TACS ;CHECK "ERROR" WITH "BSBG"
1933 010732 005714          TST @TACS      ;SAMPLE THE "ERROR" BIT
1934 010734 100001          BPL 10$       ;BR IF "ERROR" = 0
1935 010736 104001          ERROR 1          ;"ERROR" NOT = 0
1936 010740 112714 000012 10$: MOVB #SFFG,@TACS ;CHECK "ERROR" WITH "SFFG"
1937 010744 005714          TST @TACS      ;SAMPLE THE "ERROR" BIT
1938 010746 100001          BPL 11$       ;BR IF "ERROR" = 0
1939 010750 104001          ERROR 1          ;"ERROR" NOT = 0
1940 010752 112714 000014 11$: MOVB #SFBG,@TACS ;CHECK "ERROR" WITH "SFBG"
1941 010756 005714          TST @TACS      ;SAMPLE THE "ERROR" BIT
1942 010760 100001          BPL 12$       ;BR IF "ERROR" = 0
1943 010762 104001          ERROR 1          ;"ERROR" NOT = 0
1944 010764 112714 000016 12$: MOVB #REWIND,@TACS ;CHECK "ERROR" WITH "REWIND"
1945 010770 005714          TST @TACS      ;SAMPLE THE "ERROR" BIT
1946 010772 100001          BPL TST35     ;;BR IF "ERROR" = 0
1947 010774 104001          ERROR 1          ;"ERROR" NOT = 0
1948
1949
1950 *****
1951 *****
1952 *****
1953 *****
1954 *****
1955 *****
1956 *****
1957 *****
1958 *****
1959 *****
1960 *****
1961 *****
1962 *****
1963 *****
1964 *****
1965 *****
1966 *****
1967 *****
1968 *****
1969 *****
1970 *****

```

```

*TEST 35 TEST "DATA OF 0 GIVES CRC OF 0"
*****

```

```

TST35: SCOPE
MOV #5,$TIMES ;;DO 5 ITERATIONS
MOV #6,$SLPADR ;;SET SCOPE LOOP ADDRESS
MOV #TST36,$ESCAPE ;;ESCAPE TO TEST 36 ON ERROR
RESET
MOV DRIVE,@TACS ;SELECT DRIVE
MOVB #REWIND!GO,@TACS ;GO TO "CLEAR LEADER"
WAITREADY ;WAIT ON "READY"
5$:  MOVB #WRITE!GO,@TACS ;WRITE THE DATA
WAITXFER ;WAIT ON "XFER REQ"
CLRB @TADB ;1ST BYTE = 0
WAITXFER ;WAIT ON "XFER REQ"
CLRB @TADB ;2ND BYTE = 0
WAITXFER ;WAIT ON "XFER REQ"
BIS #ILBS,@TACS ;WRITE "CRC"
WAITREADY ;WAIT ON "READY"
MOVB #BSBG!GO,@TACS ;BACK OVER THE DATA
WAITREADY ;WAIT ON "READY"
MOVB #READ!GO,@TACS ;START A "READ"
WAITXFER ;WAIT ON "XFER REQ" FIRST

```


TAB BASIC LOGIC TEST (PART 2) MAINDEC-11-DZTAB-C
DZTAB.C.NEW T35 TEST "DATA OF 0 GIVES CRC OF 0"

```

0111074 005000
0111076 011501
0111078 001401
0111080 104005
0111082 104413
0111084 104413
0111086 011501
0111088 001401
0111090 104005
0111092 104413
0111094 011501
0111096 001401
0111098 104005
0111100 104413
0111102 011501
0111104 001401
0111106 104005
0111108 104413
0111110 011501
0111112 001401
0111114 104005
0111116 104412
0111118 005714
0111120 100401
0111122 104001
0111124 032714
0111126 001001
0111128 104001

```

```

CLR R0
MOV @TAB,R1
BEQ 1$
ERROR 5
1$: WAITXFER
MOV @TAB,R1
BEQ 2$
ERROR 5
2$: WAITXFER
MOV @TAB,R1
BEQ 3$
ERROR 5
3$: WAITXFER
MOV @TAB,R1
BEQ 4$
ERROR 5
4$: BIS #ILBS,@TACS
WAITREADY
TST @TACS
BMI 5$
ERROR 1
5$: BIT #CRCERR,@TACS
BNE TST35
ERROR 1

```

```

;SET R0 TO WHAT THE DATA SHOULD BE
;READ DATA BYTE
;BR IF DATA = 0
;1ST BYTE NOT = 0
;WAIT ON "XFER REQ"
;READ SECOND DATA BYTE
;BR IF 2ND BYTE = 0
;DATA WASN'T = 0
;WAIT ON "XFER REQ"
;READ FIRST CRC BYTE
;BR IF 1ST CRC BYTE = 0
;1ST BYTE OF CRC BAD
;WAIT ON "XFER REQ"
;READ SECOND CRC BYTE
;BR IF 2ND CRC BYTE = 0
;2ND BYTE OF CRC BAD
;WAIT FOR "READY"
;"ERROR" SHOULD BE = 1
;BR IF "ERROR" IS = 1
;IS THE ERROR A "CRC" ERROR?
;;GO TO NEXT TEST IF YES
;THE ERROR WASN'T A "CRC" ERROR

```

*TEST 36 TEST "CRC" CIRCUIT USING A COUNT PATTERN

```

0111160 000004
0111162 012767
0111164 012767
0111166 012767
0111168 000005
0111170 010314
0111172 112714
0111174 104412
0111176 112714
0111178 104412
0111180 012700
0111182 112714
0111184 104413
0111186 010015
0111188 104413
0111190 011501
0111192 020001
0111194 001401
0111196 104005
0111198 005300
011200 002370
011202 052714
011204 104412
011206 005714
011208 100001
011210 104001
011212 112714
011214 104412
011216 112714

```

```

TST36: SCOPE
MOV #10,$TIMES
MOV #3$,$LPADR
MOV #TST37,$ESCAPE
RESET
MOV DRIVE,@TACS
MOVB #REWIND!GO,@TACS
WAITREADY
MOVB #WFG!GO,@TACS
WAITREADY
MOV #377,R0
MOVB #WRITE!GO,@TACS
WAITXFER
1$: MOV R0,@TAB
WAITXFER
MOV @TAB,R1
CMP R0,R1
BEQ 2$
ERROR 5
2$: DEC R0
BGE 1$
BIS #ILBS,@TACS
WAITREADY
TST @TACS
BPL 3$
ERROR 1
3$: MOVB #BSBG!GO,@TACS
WAITREADY
MOVB #READ!GO,@TACS

```

```

;;DO 10. ITERATIONS
;;SET SCOPE LOOP ADDRESS
;;ESCAPE TO TEST 37 ON ERROR
;CLEAR ALL
;SELECT DRIVE
;GO TO "BOT"
;WAIT ON "READY" TO SET
;GET ON OXIDE
;WAIT ON "READY" TO SET
;FIRST DATA PATTERN AND COUNTER
;START A WRITE
;WAIT ON "XFER REQ" TO SET
;WRITE A BYTE ON TAPE
;WAIT ON "XFER REQ" TO SET
;GET BACK THE LAST BYTE WRITTEN
;AND CHECK IT
;BR IF IT LOOKS GOOD
;THE DATA IN TAB WAS BAD
;NEXT PATTERN
;BR IF MORE TO DO
;WRITE THE "CRC"
;WAIT ON "READY" TO SET
;ANY ERRORS OCCUR?
;BR IF NO
;ERROR OCCURRED DURING WRITE
;BACK OVER THE DATA BLOCK
;WAIT ON "READY" TO SET
;START A "READ"

```

000020

040000

000012 167776
011272 167710
011412 167764

000017

000001

000377
000003

000020

000011

000005


```

2027 011304 012700 000377      MOV      #377,R0      ;FIRST DATA PATTERN AND COUNTER
2028 011310 005037 013774      CLR      @#CRC.WD    ;INITIALIZES THE "CRC WORD"
2029 011314 004737 013654      JSR      PC,@#DO.CRC ;COMBINE THIS BYTE [R0] WITH THE "CRC WORD"
2030 011320 104413      WAITXFER            ;WAIT ON "XFER REQ" TO SET
2031 011322 011501      MOV      @TADB,R1    ;READ A BYTE FROM TAPE
2032 011324 020001      CMP      R0,R1      ;IS IT VALID?
2033 011326 001401      BEQ      US$        ;BR IF YES
2034 011330 104005      ERROR    US$        ;BAD DATA READ FROM TAPE
2035 011332 005300      DEC      R0          ;NEXT PATTERN
2036 011334 002367      BGE      4$         ;BR IF MORE TO READ
2037 011336 005000      CLR      R0          ;GET LOW BYTE OF "CRC WORD"
2038 011340 153700 013774      BISB    @#CRC.WD,R0
2039 011344 104413      WAITXFER            ;WAIT ON "XFER REQ" TO SET
2040 011346 052714 000020      BIS     #1LBS,@TACS ;SHUT DOWN THE "READ" OPERATION
2041 011352 011501      MOV      @TADB,R1    ;PICK FIRST BYTE OF TU60 "CRC"
2042 011354 020001      CMP      R0,R1      ;IS IT GOOD?
2043 011356 001401      BEQ      6$         ;BR IF YES
2044 011360 104005      ERROR    US$        ;FIRST BYTE OF TU60 "CRC" IS BAD
2045 011362 005000      CLR      R0          ;GET HIGH BYTE OF "CRC WORD"
2046 011364 153700 013775      BISB    @#CRC.WD+1,R0
2047 011370 104412      WAITREADY           ;WAIT ON "READY" TO SET
2048 011372 011501      MOV      @TADB,R1    ;READ THE 2ND BYTE OF TU60 "CRC"
2049 011374 020001      CMP      R0,R1      ;IS IT GOOD?
2050 011376 001401      BEQ      7$         ;BR IF YES
2051 011400 104005      ERROR    5          ;HIGH BYTE OF "CRC" IS BAD
2052 011402 032714 140000 7$: BIT     #ERROR!CRCERR,@TACS ;ERROR AND CRCERR SHOULD BE = 0
2053 011406 001401      BEQ      TST37      ;BR IF THEY ARE
2054 011410 104001      ERROR    1          ;(ERROR ! CRCERR) = 1

```

```

*****
: THIS TEST REQUIRES BOTH DRIVES
: FOR THE FOLLOWING DESCRIPTION DRIVE "1" IS THE DRIVE UNDER TEST
: AND DRIVE "2" IS THE OTHER DRIVE

```

- ```

: TEST DESCRIPTION
: 1) REWIND DRIVE 1
: 2) WAIT FOR READY TO SET
: 3) START DRIVE 2 REWINDING
: 4) WAIT FOR READY TO CLEAR
: 5) SELECT DRIVE 1 AND CHECK THAT READY IS STILL SET
: 6) SELECT DRIVE 2 AND CHECK THAT READY IS STILL CLEAR

```

```

*TEST 37 TRY TO HANG "READY" ON "REWIND"

```

```

2071 011412 000004 TST37: SCOPE
2072 011414 012767 000005 167544 MOV #5,$TIMES ;;DO 5 ITERATIONS
2073 011422 012767 011436 167456 MOV #1$, $LPADR ;;SET SCOPE LOOP ADDRESS
2074 011430 012767 011552 167532 MOV #TST40,$ESCAPE ;;ESCAPE TO TEST 40 ON ERROR
2075 011436 105737 001225 1$: TSTB @#DRVKEY+1 ;TESTING TWO DRIVES?
2076 011442 001443 BEQ TST40 ;;BR IF NO
2077 011444 012701 000001 MOV #1,R1 ;SET TIMER
2078 011450 010302 MOV DRIVE,R2 ;SET R2 TO THE OTHER DRIVE
2079 011452 062702 000400 ADD #UNIT,R2
2080 011456 042702 177377 BIC #↑CUNIT,R2
2081 011462 000005 RESET
2082 011464 010314 MOV DRIVE,@TACS ;CLEAR ALL
 ;SELECT 1ST DRIVE

```



F05

```

000003 011466 104412 WAITREADY ;WAIT ON "READY"
000004 011470 010214 MOV R2,@TACS ;SELECT 2ND DRIVE
000005 011472 104412 WAITREADY ;WAIT ON "READY"
000006 011474 112714 000017 MOVB #REWIND!GO,@TACS ;SEND 2ND DRIVE TO BOT
000007 011500 006301 33: ASL R1 ;GIVE "READY" TIME TO CLEAR
000008 011502 001001 BNE 33
000009 011504 104001 ERROR 1 ;"READY" FAILED TO CLEAR
;NOTE: THIS ERROR OCCURRED ON THE
;2ND DRIVE. I.E. IF TESTING DRIVE A
;THEN DRIVE B FAILED
;WAIT FOR "READY" TO CLEAR
000010 011506 032714 000040 33: BIT #READY,@TACS
000011 011512 001272 BNE 23
000012 011514 010214 MOV DRIVE,@TACS ;SELECT 1ST DRIVE
000013 011516 112714 000016 MOVB #REWIND,@TACS ;LOAD A "REWIND"
000014 011522 032714 000040 BIT #READY,@TACS ;CHECK "READY"
000015 011526 001001 BNE 43 ;BR IF STILL SET
000016 011530 104001 ERROR 1 ;"READY" WAS CLEAR
000017 011532 010214 43: MOV R2,@TACS ;SELECT 2ND DRIVE
000018 011534 112714 000016 MOVB #REWIND,@TACS ;LOAD A REWIND
000019 011540 032714 000040 BIT #READY,@TACS ;CHECK "READY"
000020 011544 001401 BEQ 53 ;BR IF STILL CLEAR
000021 011546 104001 ERROR 1 ;"READY" WAS SET
000022 011550 104412 53: WAITREADY ;WAIT ON "READY"
;*****
;THIS TEST REQUIRES BOTH DRIVES
;FOR THE FOLLOWING DESCRIPTION DRIVE "1" IS THE DRIVE UNDER TEST
;AND DRIVE "2" IS THE OTHER DRIVE
;TEST DESCRIPTION
;1) REWIND DRIVE 1
;2) WFG ON DRIVE 1
;3) START A REWIND ON DRIVE 2
;4) WHILE DRIVE 2 IS REWINDING WRITE DATA ON DRIVE 1
;5) CHECK FOR ERRORS
;6) START A REWIND ON DRIVE 2
;7) WHILE DRIVE 2 IS REWINDING READ DATA FROM DRIVE 1
;8) CHECK FOR ERRORS
;*****
;*TEST 40 TRY TO GLITCH THE "POWER SUPPLY"
;*****
1204 011552 000004 TST40: SCOPE
1205 011554 012767 000005 167404 MOV #5,$TIMES ;;DO 5 ITERATIONS
1206 011562 012767 011576 167316 MOV #1,$SLPADR ;;SET SCOPE LOOP ADDRESS
1207 011570 012767 012134 167372 MOV #TST41,$ESCAPE ;;ESCAPE TO TEST 41 ON ERROR
1208 011576 105737 001225 13: TSTB @#DRVKEY+1 ;TESTING TWO DRIVES?
1209 011602 001554 BEQ TST41 ;;BR IF NO
1210 011604 012701 000001 MOV #1,R1 ;SET TIMER
1211 011610 010302 MOV DRIVE,R2 ;SET R2 TO THE OTHER DRIVE
1212 011612 062702 000400 ADD #UNIT,R2
1213 011616 042702 177377 BIC #1CUNIT,R2
1214 011622 000005 RESET ;CLEAR ALL
1215 011624 010314 MOV DRIVE,@TACS ;SELECT 1ST DRIVE
1216 011626 112714 000017 MOVB #REWIND!GO,@TACS ;REWIND TO BOT
1217 011632 104412 WAITREADY ;WAIT ON "READY"
1218 011634 112714 000001 MOVB #WFG!GO,@TACS ;GET ON OXIDE

```



|     |        |        |        |     |                       |  |                                             |
|-----|--------|--------|--------|-----|-----------------------|--|---------------------------------------------|
| 139 | 011640 | 104412 |        |     | WAITREADY             |  | :WAIT ON "READY"                            |
| 140 | 011642 | 010214 |        |     | MOV R2,@TACS          |  | :SELECT 2ND DRIVE                           |
| 141 | 011644 | 104412 |        |     | WAITREADY             |  | :WAIT ON "READY"                            |
| 142 | 011646 | 112714 | 000017 |     | MOVB #REWIND!GO,@TACS |  | :START A "REWIND"                           |
| 143 | 011652 | 006301 |        | 28: | ASL R1                |  | :GIVE "READY" TIME TO CLEAR                 |
| 144 | 011654 | 001001 |        |     | BNE 3\$               |  |                                             |
| 145 | 011656 | 104001 |        |     | ERROR 1               |  | : "READY" FAILED TO CLEAR                   |
| 146 |        |        |        |     |                       |  | :NOTE: THIS ERROR OCCURRED ON THE           |
| 147 |        |        |        |     |                       |  | :2ND DRIVE. I.E. IF TESTING DRIVE A         |
| 148 |        |        |        |     |                       |  | :THEN DRIVE B FAILED                        |
| 149 |        |        |        |     |                       |  | :WAIT FOR "READY" TO CLEAR                  |
| 150 | 011660 | 032714 | 000040 | 38: | BIT #READY,@TACS      |  |                                             |
| 151 | 011664 | 001372 |        |     | BNE 2\$               |  |                                             |
| 152 | 011666 | 010314 |        |     | MOV DRIVE,@TACS       |  | :SELECT 1ST DRIVE                           |
| 153 | 011670 | 112714 | 000003 |     | MOVB #WRITE!GO,@TACS  |  | :WRITE WHILE THE OTHER DRIVE IS "REWINDING" |
| 154 | 011674 | 104413 |        |     | WAITXFER              |  | :WAIT ON "XFER REQ"                         |
| 155 | 011676 | 112715 | 000377 |     | MOVB #377,@TADB       |  | :WRITE 1ST BYTE                             |
| 156 | 011702 | 104413 |        |     | WAITXFER              |  | :WAIT ON "XFER REQ"                         |
| 157 | 011704 | 105015 |        |     | CLRB @TADB            |  | :WRITE 2ND BYTE                             |
| 158 | 011706 | 104413 |        |     | WAITXFER              |  | :WAIT ON "XFER REQ"                         |
| 159 | 011710 | 052714 | 000020 |     | BIS #ILBS,@TACS       |  | :WRITE "CRC" & SHUT DOWN                    |
| 160 | 011714 | 104412 |        |     | WAITREADY             |  | :WAIT ON "READY"                            |
| 161 | 011716 | 005714 |        |     | TST @TACS             |  | :ANY ERROR?                                 |
| 162 | 011720 | 100001 |        |     | BPL 4\$               |  | :BR IF NO                                   |
| 163 | 011722 | 104001 |        |     | ERROR 1               |  | :ERROR OCCURRED DURING "WRITE"              |
| 164 | 011724 | 112714 | 000011 | 48: | MOVB #BSBG!GO,@TACS   |  | :POSITION TAPE FOR "READ"                   |
| 165 | 011730 | 104412 |        |     | WAITREADY             |  | :WAIT ON "READY"                            |
| 166 | 011732 | 005714 |        |     | TST @TACS             |  | :ANY ERROR?                                 |
| 167 | 011734 | 100001 |        |     | BPL 5\$               |  | :BR IF NO                                   |
| 168 | 011736 | 104001 |        |     | ERROR 1               |  | :ERROR OCCURRED DURING "BSBG"               |
| 169 | 011740 | 112714 | 000005 | 58: | MOVB #READ!GO,@TACS   |  | :START A "READ"                             |
| 170 | 011744 | 104413 |        |     | WAITXFER              |  | :WAIT ON "XFER REQ"                         |
| 171 | 011746 | 012700 | 000377 |     | MOV #377,R0           |  | :FIRST DATA PATTERN                         |
| 172 | 011752 | 011501 |        |     | MOV @TADB,R1          |  | :PICKUP FIST BYTE                           |
| 173 | 011754 | 020001 |        |     | CMP R0,R1             |  | :IS IT GOOD?                                |
| 174 | 011756 | 001401 |        |     | BEG 6\$               |  | :BR IF NO                                   |
| 175 | 011760 | 104005 |        |     | ERROR 5               |  | :1ST DATA BYTE IS BAD                       |
| 176 | 011762 | 104413 |        | 68: | WAITXFER              |  | :WAIT ON "XFER REQ"                         |
| 177 | 011764 | 005000 |        |     | CLR R0                |  | :2ND DATA PATTERN                           |
| 178 | 011766 | 011501 |        |     | MOV @TADB,R1          |  | :PICKUP 2ND BYTE                            |
| 179 | 011770 | 001401 |        |     | BEG 7\$               |  | :BR IF IT IS GOOD                           |
| 180 | 011772 | 104005 |        |     | ERROR 5               |  | :2ND BYTE IS BAD                            |
| 181 | 011774 | 104413 |        | 78: | WAITXFER              |  | :WAIT ON "XFER REQ"                         |
| 182 | 011776 | 052714 | 000020 |     | BIS #ILBS,@TACS       |  | :SHUT DOWN                                  |
| 183 | 012002 | 104412 |        |     | WAITREADY             |  | :WAIT ON "READY"                            |
| 184 | 012004 | 005714 |        |     | TST @TACS             |  | :ERROR?                                     |
| 185 | 012006 | 100001 |        |     | BPL 8\$               |  | :BR IF NO                                   |
| 186 | 012010 | 104001 |        |     | ERROR 1               |  | :ERROR OCCURRED                             |
| 187 | 012012 | 112714 | 000011 | 88: | MOVB #BSBG!GO,@TACS   |  | :POSITION FOR NEXT "READ"                   |
| 188 | 012016 | 104412 |        |     | WAITREADY             |  | :WAIT ON "READY"                            |
| 189 | 012020 | 005714 |        |     | TST @TACS             |  | :ERROR?                                     |
| 190 | 012022 | 100001 |        |     | BPL 9\$               |  | :BR IF NO                                   |
| 191 | 012024 | 104001 |        |     | ERROR 1               |  | :ERROR OCCURRED DURING "BSBG"               |
| 192 | 012026 | 010214 |        | 98: | MOV R2,@TACS          |  | :SELECT 2ND DRIVE                           |
| 193 | 012030 | 104412 |        |     | WAITREADY             |  | :WAIT ON "READY"                            |
| 194 | 012032 | 112714 | 000017 |     | MOVB #REWIND!GO,@TACS |  | :START A "REWIND"                           |
|     | 012036 | 012701 | 000001 |     | MOV #1,R1             |  | :SET THE TIMER                              |

```

012042 005201 105: INC R1 ;GIVE "READY" TIME TO CLEAR
012044 001001 BNE 105
012046 104001 ERROR 1 ;"READY" FAILED TO CLEAR
;NOTE: THIS ERROR OCCURRED ON THE
;2ND DRIVE. I.E. IF TESTING DRIVE A
;THEN DRIVE B FAILED
;WAIT FOR "READY" TO CLEAR

012050 032714 000040 115: BIT #READY,@TACS
012054 001372 BNE 105
012056 010314 MOV DRIVE,@TACS ;SELECT 1ST DRIVE
012060 104412 WAITREADY ;WAIT ON "READY"
012062 112714 000005 MOVB #READ!GO,@TACS ;"READ" WHILE OTHER DRIVE IS "REWINDING"
012066 104413 WAITXFER ;WAIT ON "XFER REQ"
012070 012700 000377 MOV #377,R0 ;1ST DATA PATTERN
012074 011501 MOV @TADB,R1 ;READ 1ST BYTE
012076 020001 CMP R0,R1 ;IS IT GOOD?
012100 001401 BEQ 125 ;BR IF NO
012102 104005 ERROR 5 ;1ST BYTE FAILED
012104 104413 125: WAITXFER ;WAIT ON "XFER REQ"
012106 005000 CLR R0 ;2ND DATA PATTERN
012110 011501 MOV @TADB,R1 ;READ 2ND BYTE
012112 001401 BEQ 135 ;BR IF IT IS GOOD
012114 104005 ERROR 5 ;2ND BYTE FAILED
012116 104413 135: WAITXFER ;WAIT ON "XFER REQ"
012120 052714 000020 BIS #ILBS,@TACS ;SHUT DOWN
012124 104412 WAITREADY ;WAIT ON "READY"
012126 005714 TST @TACS ;ERROR?
012130 100001 BPL TST41 ;;BR IF NO
012132 104001 ERROR 1 ;ERROR OCCURRED

;*****
;*TEST 41 END OF TEST CODE
;*****
TST41: SCOPE
MOV #1,$TIMES ;;DO 1 ITERATION
RESET
MOV DRIVE,@TACS ;SELECT DRIVE
MOVB #REWIND!GO,@TACS
WAITREADY
.SETTL END OF PASS ROUTINE

;*****
;*INCREMENT THE PASS NUMBER ($PASS)
;*TYPE "END PASS"
;*IF THERES A MONITOR GO TO IT
;*IF THERE ISN'T JUMP TO START
;*IF IT IS DESIRED TO HAVE A BELL INDICATE THE "END OF PASS" LOCATION
;*SENDMG CAN BE CHANGED TO 7.

$EOP: SCOPE
CLR $STNM ;;ZERO THE TEST NUMBER
CLR $TIMES ;;ZERO THE NUMBER OF ITERATIONS
INC $PASS ;;INCREMENT THE PASS NUMBER
BIC #100000,$PASS ;;DON'T ALLOW A NEG. NUMBER
DEC (PC)+ ;;LOOP?
$EOPCT: .WORD 1
BGT $DOAGN ;;YES

```



TAB: BASIC LOGIC TEST (PART 2) MAINDEC-11-DZTAB-C  
DZTAB.C.NEW END OF PASS ROUTINE

|          |          |          |  |  |  |
|----------|----------|----------|--|--|--|
| 00000001 | 01000000 | 00000000 |  |  |  |
| 00000002 | 01000000 | 00000000 |  |  |  |
| 00000003 | 01000000 | 00000000 |  |  |  |
| 00000004 | 01000000 | 00000000 |  |  |  |
| 00000005 | 01000000 | 00000000 |  |  |  |
| 00000006 | 01000000 | 00000000 |  |  |  |
| 00000007 | 01000000 | 00000000 |  |  |  |
| 00000008 | 01000000 | 00000000 |  |  |  |
| 00000009 | 01000000 | 00000000 |  |  |  |
| 00000010 | 01000000 | 00000000 |  |  |  |
| 00000011 | 01000000 | 00000000 |  |  |  |
| 00000012 | 01000000 | 00000000 |  |  |  |
| 00000013 | 01000000 | 00000000 |  |  |  |
| 00000014 | 01000000 | 00000000 |  |  |  |
| 00000015 | 01000000 | 00000000 |  |  |  |
| 00000016 | 01000000 | 00000000 |  |  |  |
| 00000017 | 01000000 | 00000000 |  |  |  |
| 00000018 | 01000000 | 00000000 |  |  |  |
| 00000019 | 01000000 | 00000000 |  |  |  |
| 00000020 | 01000000 | 00000000 |  |  |  |
| 00000021 | 01000000 | 00000000 |  |  |  |
| 00000022 | 01000000 | 00000000 |  |  |  |
| 00000023 | 01000000 | 00000000 |  |  |  |
| 00000024 | 01000000 | 00000000 |  |  |  |
| 00000025 | 01000000 | 00000000 |  |  |  |
| 00000026 | 01000000 | 00000000 |  |  |  |
| 00000027 | 01000000 | 00000000 |  |  |  |
| 00000028 | 01000000 | 00000000 |  |  |  |
| 00000029 | 01000000 | 00000000 |  |  |  |
| 00000030 | 01000000 | 00000000 |  |  |  |
| 00000031 | 01000000 | 00000000 |  |  |  |
| 00000032 | 01000000 | 00000000 |  |  |  |
| 00000033 | 01000000 | 00000000 |  |  |  |
| 00000034 | 01000000 | 00000000 |  |  |  |
| 00000035 | 01000000 | 00000000 |  |  |  |
| 00000036 | 01000000 | 00000000 |  |  |  |
| 00000037 | 01000000 | 00000000 |  |  |  |
| 00000038 | 01000000 | 00000000 |  |  |  |
| 00000039 | 01000000 | 00000000 |  |  |  |
| 00000040 | 01000000 | 00000000 |  |  |  |
| 00000041 | 01000000 | 00000000 |  |  |  |
| 00000042 | 01000000 | 00000000 |  |  |  |
| 00000043 | 01000000 | 00000000 |  |  |  |
| 00000044 | 01000000 | 00000000 |  |  |  |
| 00000045 | 01000000 | 00000000 |  |  |  |
| 00000046 | 01000000 | 00000000 |  |  |  |
| 00000047 | 01000000 | 00000000 |  |  |  |
| 00000048 | 01000000 | 00000000 |  |  |  |
| 00000049 | 01000000 | 00000000 |  |  |  |
| 00000050 | 01000000 | 00000000 |  |  |  |
| 00000051 | 01000000 | 00000000 |  |  |  |
| 00000052 | 01000000 | 00000000 |  |  |  |
| 00000053 | 01000000 | 00000000 |  |  |  |
| 00000054 | 01000000 | 00000000 |  |  |  |
| 00000055 | 01000000 | 00000000 |  |  |  |
| 00000056 | 01000000 | 00000000 |  |  |  |
| 00000057 | 01000000 | 00000000 |  |  |  |
| 00000058 | 01000000 | 00000000 |  |  |  |
| 00000059 | 01000000 | 00000000 |  |  |  |
| 00000060 | 01000000 | 00000000 |  |  |  |
| 00000061 | 01000000 | 00000000 |  |  |  |
| 00000062 | 01000000 | 00000000 |  |  |  |
| 00000063 | 01000000 | 00000000 |  |  |  |
| 00000064 | 01000000 | 00000000 |  |  |  |
| 00000065 | 01000000 | 00000000 |  |  |  |
| 00000066 | 01000000 | 00000000 |  |  |  |
| 00000067 | 01000000 | 00000000 |  |  |  |
| 00000068 | 01000000 | 00000000 |  |  |  |
| 00000069 | 01000000 | 00000000 |  |  |  |
| 00000070 | 01000000 | 00000000 |  |  |  |
| 00000071 | 01000000 | 00000000 |  |  |  |
| 00000072 | 01000000 | 00000000 |  |  |  |
| 00000073 | 01000000 | 00000000 |  |  |  |
| 00000074 | 01000000 | 00000000 |  |  |  |
| 00000075 | 01000000 | 00000000 |  |  |  |
| 00000076 | 01000000 | 00000000 |  |  |  |
| 00000077 | 01000000 | 00000000 |  |  |  |
| 00000078 | 01000000 | 00000000 |  |  |  |
| 00000079 | 01000000 | 00000000 |  |  |  |
| 00000080 | 01000000 | 00000000 |  |  |  |
| 00000081 | 01000000 | 00000000 |  |  |  |
| 00000082 | 01000000 | 00000000 |  |  |  |
| 00000083 | 01000000 | 00000000 |  |  |  |
| 00000084 | 01000000 | 00000000 |  |  |  |
| 00000085 | 01000000 | 00000000 |  |  |  |
| 00000086 | 01000000 | 00000000 |  |  |  |
| 00000087 | 01000000 | 00000000 |  |  |  |
| 00000088 | 01000000 | 00000000 |  |  |  |
| 00000089 | 01000000 | 00000000 |  |  |  |
| 00000090 | 01000000 | 00000000 |  |  |  |
| 00000091 | 01000000 | 00000000 |  |  |  |
| 00000092 | 01000000 | 00000000 |  |  |  |
| 00000093 | 01000000 | 00000000 |  |  |  |
| 00000094 | 01000000 | 00000000 |  |  |  |
| 00000095 | 01000000 | 00000000 |  |  |  |
| 00000096 | 01000000 | 00000000 |  |  |  |
| 00000097 | 01000000 | 00000000 |  |  |  |
| 00000098 | 01000000 | 00000000 |  |  |  |
| 00000099 | 01000000 | 00000000 |  |  |  |
| 00000100 | 01000000 | 00000000 |  |  |  |

```

MOV (PC)+,2(PC)+ ;;RESTORE COUNTER
SENDCT: .WORD 1
 .SEOPCT
 .SENDMG ;;TYPE "END PASS"
SGET42: MOV 2#42,RO ;;GET MONITOR ADDRESS
 .BREQ $DOAGN ;;BRANCH IF NO MONITOR
SENDAD: .RESET ;;CLEAR THE WORLD
 JSR PC,(RO) ;;GO TO MONITOR
 NOP
 NOP ;;SAVE ROOM
 NOP ;;FOR
 NOP ;;ACT11
$DOAGN: .JMP 2(PC)+ ;;RETURN
$RTNAD: .WORD START
$ENULL: .BYTE -1,-1,0 ;;NULL CHARACTER STRING
$ENDMG: .ASCIZ <15><12>/END PASS/

```

.SBTTL SCOPE HANDLER ROUTINE

```

*THIS ROUTINE CONTROLS THE LOOPING OF SUBTESTS. IT WILL INCREMENT
*AND LOAD THE TEST NUMBER($TSTNM) INTO THE DISPLAY REG.(DISPLAY<7:0>)
*AND LOAD THE ERROR FLAG ($ERFLG) INTO DISPLAY<15:08>
*THE SWITCH OPTIONS PROVIDED BY THIS ROUTINE ARE:
*SW14=1 LOOP ON TEST
*SW11=1 INHIBIT ITERATIONS
*SW09=1 LOOP ON ERROR
*SW08=1 LOOP ON TEST IN SWR<7:0>
*CALL
* SCOPE ;;SCOPE=IOT

```

```

$SCOPE:
CKSWR ;;TEST FOR CHANGE IN SOFT-SWR
1$: BIT #BIT14,$SWR ;;LOOP ON PRESENT TEST?
 BNE $OVER ;;YES IF SW14=1
*****START OF CODE FOR THE XOR TESTER*****
$XTSTR: BR 6$
 MOV 2$ERRVEC,-(SP) ;;IF RUNNING ON THE "XOR" TESTER CHANGE
 MOV 5$,2$ERRVEC ;;THIS INSTRUCTION TO A "NOP" (NOP=240)
 TST 2$177060 ;;SAVE THE CONTENTS OF THE ERROR VECTOR
 MOV (SP)+,2$ERRVEC ;;SET FOR TIMEOUT
 BR 5$VLAD ;;TIME OUT ON XOR?
 BR 5$;;RESTORE THE ERROR VECTOR
 CMP (SP)+,(SP)+ ;;GO TO THE NEXT TEST
 MOV (SP)+,2$ERRVEC ;;CLEAR THE STACK AFTER A TIME OUT
 BR 7$;;RESTORE THE ERROR VECTOR
6$: *****END OF CODE FOR THE XOR TESTER*****
 BIT #BIT08,$SWR ;;LOOP ON SPEC. TEST?
 BEQ 2$;;BR IF NO
 CMPB 2$SWR,$TSTNM ;;ON THE RIGHT TEST? SWR<7:0>
 BEQ $OVER ;;BR IF YES
 TSTB $ERFLG ;;HAS AN ERROR OCCURRED?
 BEQ 3$;;BR IF NO
 CMPB $ERMAX,$ERFLG ;;MAX. ERRORS FOR THIS TEST OCCURRED?
 BHI 3$;;BR IF NO
 BIT #BIT09,$SWR ;;LOOP ON ERROR?
 BEQ 4$;;BR IF NO
7$: MOV $LPERR,$LPADR ;;SET LOOP ADDRESS TO LAST SCOPE
 BR $OVER
4$: CLRB $ERFLG ;;ZERO THE ERROR FLAG
 CLR $TIMES ;;CLEAR THE NUMBER OF ITERATIONS TO MAKE
 BR 1$;;ESCAPE TO THE NEXT TEST
3$: BIT #BIT11,$SWR ;;INHIBIT ITERATIONS?
 BNE 1$;;BR IF YES
 TST $PASS ;;IF FIRST PASS OF PROGRAM
 BEQ 1$;;INHIBIT ITERATIONS
 INC $ICNT ;;INCREMENT ITERATION COUNT
 CMP $TIMES,$ICNT ;;CHECK THE NUMBER OF ITERATIONS MADE
 BGE $OVER ;;BR IF MORE ITERATION REQUIRED
1$: MOV #1,$ICNT ;;REINITIALIZE THE ITERATION COUNTER
 MOV $MXCNT,$TIMES ;;SET NUMBER OF ITERATIONS TO DO
$SVLAD: INCB $TSTNM ;;COUNT TEST NUMBERS
 MOV (SP),$LPADR ;;SAVE SCOPE LOOP ADDRESS

```

```

000000
000001
000002
000003
000004
000005
000006
000007
000008
000009
000010
000011
000012
000013
000014
000015
000016
000017
000018
000019
000020
000021
000022
000023
012264
012264 104406
012266 032777 040000 166644
012274 001111
012276 000416
012300 013746 000004
012304 012737 012324 000004
012312 005737 177060
012316 012637 000004
012322 000463
012324 022626
012326 012637 000004
012332 000423
012334
012334 032777 000400 166576
012342 001404
012344 127767 166570 166530
012352 001462
012354 105767 166523
012360 001421
012362 126767 166527 166513
012370 101015
012372 032777 001000 166540
012400 001404
012402 016767 166502 166476
012410 000443
012412 105067 166465
012416 005067 166544
012422 000415
012424 032777 004000 166506
012432 001011
012434 005767 166440
012440 001406
012442 005267 166436
012446 026767 166514 166430
012454 002021
012456 012767 000001 166420
012464 016767 000044 166474
012472 105267 166404
012476 011667 166404

```



|      |        |        |        |        |               |                  |                                         |
|------|--------|--------|--------|--------|---------------|------------------|-----------------------------------------|
| 2324 | 012502 | 011667 | 166402 |        | MOV           | (SP), SLPERR     | :: SAVE ERROR LOOP ADDRESS              |
| 2325 | 012506 | 005067 | 166456 |        | CLR           | \$ESCAPE         | :: CLEAR THE ESCAPE FROM ERROR ADDRESS  |
| 2326 | 012512 | 112767 | 000001 | 166375 | MOVB          | #1, \$ERMAX      | :: ONLY ALLOW ONE(1) ERROR ON NEXT TEST |
| 2327 | 012520 | 016777 | 166356 | 166414 | SOVER: MOV    | \$STNM, @DISPLAY | :: DISPLAY TEST NUMBER                  |
| 2328 | 012526 | 016716 | 166354 |        | MOV           | SLPADR, (SP)     | :: FUDGE RETURN ADDRESS                 |
| 2329 | 012532 | 000002 |        |        | RTI           |                  | :: FIXES PS                             |
| 2330 | 012534 | 003720 |        |        | SMXCNT: 2000. |                  | :: MAX. NUMBER OF ITERATIONS            |

.SBTTL ERROR HANDLER ROUTINE

```

*THIS ROUTINE WILL INCREMENT THE ERROR FLAG AND THE ERROR COUNT,
*SAVE THE ERROR ITEM NUMBER AND THE ADDRESS OF THE ERROR CALL
*AND GO TO TYPERR ON ERROR
*THE SWITCH OPTIONS PROVIDED BY THIS ROUTINE ARE:
*SW15=1 HALT ON ERROR
*SW13=1 INHIBIT ERROR TYPEOUTS
*SW10=1 BELL ON ERROR
*SW09=1 LOOP ON ERROR
*CALL ERROR N ;;ERROR=EMT AND N=ERROR ITEM NUMBER

```

```

$ERROR: CKSWR ;;TEST FOR CHANGE IN SOFT-SWR
 MOV @TACS,@#$REGD ;;SAVE THE STATUS REG.
 MOV @TADB,@#$REG1 ;;SAVE THE DATA BUFFER
 MOV RO,@#$GDDAT ;;RO WILL CONTAIN THE GOOD DATA
 MOV R1,@#$BDDAT ;;R1 WILL CONTAIN THE BAD DATA
7$: INCB $ERFLG ;;SET THE ERROR FLAG
 BEQ 7$;;DON'T LET THE FLAG GO TO ZERO
 MOV $STNM,@DISPLAY ;;DISPLAY TEST NUMBER AND ERROR FLAG
 BIT #BIT10,@SWR ;;BELL ON ERROR?
 BEQ 1$;;NO - SKIP
 TYPE $BELL ;;RING BELL
1$: INC $ERTTL ;;COUNT THE NUMBER OF ERRORS
 MOV (SP),$ERRPC ;;GET ADDRESS OF ERROR INSTRUCTION
 SUB #2,$ERRPC
 MOVB @ERRPC,$ITEMB ;;STRIP AND SAVE THE ERROR ITEM CODE
 BIT #BIT13,@SWR ;;SKIP TYPEOUT IF SET
 BNE 20$;;SKIP TYPEOUTS
 JSR PC,TYPERR ;;GO TO USER ERROR ROUTINE
 TYPE ,SCLF
20$: 2$: TST @SWR ;;HALT ON ERROR
 BPL 3$;;SKIP IF CONTINUE
 HALT ;;HALT ON ERROR!
2369: CKSWR ;;TEST FOR CHANGE IN SOFT-SWR
2370: BIT #BIT09,@SWR ;;LOOP ON ERROR SWITCH SET?
 BEQ 4$;;BR IF NO
 MOV $LPERR,(SP) ;;FUDGE RETURN FOR LOOPING
2373: 4$: TST $ESCAPE ;;CHECK FOR AN ESCAPE ADDRESS
 BEQ 5$;;BR IF NONE
 MOV $ESCAPE,(SP) ;;FUDGE RETURN ADDRESS FOR ESCAPE
2376: 5$: CMP #SENDAD,@#42 ;;ACT-11 AUTO-ACCEPT?
 BNE 6$;;BRANCH IF NO
2379: 6$: HALT ;;YES
2381: RTI ;;RETURN

```

```

2331
2332
2333
2334
2335
2336
2337
2338
2339
2340
2341
2342
2343
2344
2345
2346 012536 104406
2347 012536 011437 001162
2348 012540 011537 001164
2349 012544 010037 001124
2350 012550 010137 001126
2351 012554 010137 001126
2352 012560 105267 166317
2353 012564 001775
2354 012566 016777 166310 166346
2355 012574 032777 002000 166336
2356 012602 001402
2357 012604 104401 001172
2358 012610 005267 166276
2359 012614 011667 166276
2360 012620 162767 000002 166270
2361 012626 117767 166264 166260
2362 012634 032777 020000 166276
2363 012642 001004
2364 012644 004767 000060
2365 012650 104401 001177
2366 012654
2367 012654 005777 166260
2368 012660 100002
2369 012662 000000
2370 012664 104406
2371 012666 032777 001000 166244
2372 012674 001402
2373 012676 016716 166206
2374 012702 005767 166262
2375 012706 001402
2376 012710 016716 166254
2377 012714
2378 012714 022737 012232 000042
2379 012722 001001
2380 012724 000000
2381 012726 000002
2382

```



```

2383 ::*****
2384 ;THIS ROUTINE WILL TYPEOUT THE ERROR MESSAGES
2385
2386 012730 104401 001177 TYPERR: TYPE $CRLF ;TYPE A CARRIAGE RETURN & LINE FEED
2387 012734 010046 MOV RD, -(SP) ;SAVE RD
2388 012736 113700 001114 MOVB @#$ITEMB, RD ;PICKUP THE ITEM INDEX
2389 012742 005300 DEC RD ;ADJUST THE INDEX
2390 012744 006300 ASL RD ;SO IT WILL WORK FOR
2391 012746 006300 ASL RD ;THE ERROR TABLE
2392 012750 006300 ASL RD
2393 012752 062700 001236 ADD #$ERRTB, RD ;FORM THE TABLE POINTER
2394 012756 012067 000002 MOV (RD)+, 1$;PICKUP "ERROR MESSAGE" POINTER
2395 012762 104401 TYPE "ERROR MESSAGE"
2396 012764 000000 1$: 0 ;"ERROR MESSAGE POINTER" GOES HERE
2397 012766 104401 001177 TYPE $CRLF
2398 012772 012067 000004 MOV (RD)+, 2$;PICKUP "DATA HEADER" POINTER
2399 012776 001404 BEQ 3$
2400 013000 104401 TYPE "DATA HEADER"
2401 013002 000000 2$: 0 ;"DATA HEADER" POINTER GOES HERE
2402 013004 104401 001177 TYPE $CRLF
2403 013010 012000 3$: MOV (RD)+, RD ;PICKUP "DATA POINTER"
2404 013012 001004 BNE 5$;IF THERE IS DATA TO TYPE GO DO IT
2405 013014 012600 4$: MOV (SP)+, RD ;RESTORE RD
2406 013016 104401 001177 TYPE $CRLF
2407 013022 000207 RTS PC ;RETURN
2408 013024 5$:
2409 013024 013046 MOV @ (RD)+, -(SP) ;:SAVE @ (RD)+ FOR TYPEOUT
2410
2411 013026 104402 TYP0C ;:TYPE DATA
2412 013030 005710 TST (RD) ;:GO TYPE--OCTAL ASCII(ALL DIGITS)
2413 013032 001770 BEQ 4$;:TERMINATOR?
2414 013034 104401 013042 TYPE 6$;:BR IF YES
2415 013040 000771 BR 5$;:TYPE 2 SPACES
2416 013042 020040 000 6$: .ASCIZ / / ;:LOOP
2417 013046 .EVEN ;:ASCII STRING OF 2 SPACES

```



```

2418 ;:*****
2419 ;ROUTINE TO WAIT ON THE READY BIT
2420
2421 WAIT.ON.READY:
2422 013046 005067 000044 CLR WAIT2 ;SETUP MAX. TIME TO WAIT ON "READY"
2423 013052 015767 000072 000044 MOV MAXCNT,HGHTIM
2424 013060 012637 001202 MOV (SP)+,a#SAVPC ;GET THE PC OF THE WAITREADY INSTRUCTION
2425 013064 162737 000002 001202 SUB #2,a#SAVPC
2426 013072 012637 001204 MOV (SP)+,a#SAVPS ;SAVE THE PS
2427 013076 032714 000040 WAIT1: BIT #READY,aTACS ;READY=1?
2428 013102 001013 BNE WAIT3 ;GO ON IF YES
2429 013104 105714 TSTB aTACS ;CHECK TRANSFER REQUEST
2430 013106 100002 BPL WAIT4
2431 013110 104004 ERROR 4 ;"TRANSFER REQUEST" SET WHILE WAITING ON "READY"
2432 013112 000407 BR WAIT3
2433 013114 005227 WAIT4: INC (PC)+ ;COUNT FAST COUNTER
2434 013116 000000 WAIT2: 0
2435 013120 001366 BNE WAIT1 ;GO CHECK "READY" AGAIN
2436 013122 005327 DEC (PC)+ ;COUNT LOOP COUNTER
2437 013124 000000 HGHTIM: 0
2438 013126 003363 BGT WAIT1 ;GO LOOP AGAIN
2439 013130 104002 ERROR 2 ;"READY" FAILED TO SET
2440 013132 013746 001204 WAIT3: MOV a#SAVPS,-(SP) ;GET THE STATUS BACK
2441 013136 013746 001202 MOV a#SAVPC,-(SP) ;GET THE PC
2442 013142 062716 000002 ADD #2,(SP)
2443 013146 000002 RTI
2444 013150 000000 MAXCNT: 0
2445
2446 ;:*****
2447 ;ROUTINE TO WAIT ON "TRANSFER REQUEST"
2448
2449 WAIT.FOR.XFER.REQ:
2450 013152 005067 000044 CLR 2$;SETUP WASTE TIME LOOP
2451 013156 013767 013150 000044 MOV a#MAXCNT,3$
2452 013164 012637 001202 MOV (SP)+,a#SAVPC ;GET THE PC OF THE WAITXFER INSTRUCTION
2453 013170 162737 000002 001202 SUB #2,a#SAVPC
2454 013176 012637 001204 MOV (SP)+,a#SAVPS ;SAVE THE PS
2455 013202 105714 1$: TSTB aTACS ;CHECK XFER REQ
2456 013204 100414 BMI 4$;EXIT IF SET
2457 013206 032714 000040 BIT #READY,aTACS ;LOOK AT READY
2458 013212 001402 BEQ 5$;BR IF "READY" ISN'T SET
2459 013214 104004 ERROR 4 ;"READY" SET WHILE WAITING FOR "XFER REQ"
2460 013216 000407 BR 4$
2461 013220 005227 5$: INC (PC)+ ;COUNT
2462 013222 000000 2$: 0
2463 013224 001366 BNE 1$;BR IF MORE TO DO
2464 013226 005327 DEC (PC)+
2465 013230 000000 3$: 0
2466 013232 003363 BGT 1$
2467 013234 104003 ERROR 3 ;"TRANSFER REQUEST" FAILED TO SET
2468 013236 013746 001204 4$: MOV a#SAVPS,-(SP) ;GET THE STATUS BACK
2469 013242 013746 001202 MOV a#SAVPC,-(SP) ;GET THE PC
2470 013246 062716 000002 ADD #2,(SP)
2471 013252 000002 RTI ;GO BACK

```







```

013452 104411 RDOCT
013454 012600 MOV (SP)+,RO
013456 001411 BEQ 5$
013458 020027 001000 CMP RO,#1000 ;MAKE SURE ADDRESS IS IN VECTOR AREA
013460 103370 BHS 3$
013462 010037 001216 MOV RO,2$TAVEC ;SAVE AS VECTOR ADDRESS
013464 062700 000002 ADD #2,RO
013466 010037 001220 MOV RO,2$TAVEC+2
013468 104401 017000 5$: TYPE ,MSGPRI ;ASK FOR PRIORITY
013470 104411 RDOCT
013472 012600 MOV (SP)+,RO
013474 001413 BEQ 5$;IF "0" USE OLD VALUE
013476 020027 000007 CMP RO,#7 ;MAKE SURE ITS VALID
013478 101370 BHI 5$
013480 000300 SWAB RO ;PUT INTO HIGH BYTE
013482 006200 ASR RO ;AND SHIFT
013484 006200 ASR RO ;INTO PROPER
013486 006200 ASR RO ;POSITION
013488 042700 177437 BIC #C(340),RO ;SAVE ONLY PRIORITY BITS
013490 010037 001222 MOV RO,2$TAPRIO ;STORE IT AWAY
013492 104401 017012 6$: TYPE ,TACS ;TACS=""
013494 016746 165434 MOV TACSL,-(SP) ;;SAVE TACSL FOR TYPEOUT
013496 104402 TYPOC ;;GO TYPE--OCTAL ASCII(ALL DIGITS)
013498 104401 017020 TYPE ,MTADB ;"TADB=""
013500 016746 165426 MOV TADBL,-(SP) ;;SAVE TADBL FOR TYPEOUT
013502 104402 TYPOC ;;GO TYPE--OCTAL ASCII(ALL DIGITS)
013504 104401 017027 TYPE ,MTAVEC ;"VECTOR=""
013506 016746 165420 MOV TAVEC,-(SP) ;;SAVE TAVEC FOR TYPEOUT
013508 104402 TYPOC ;;GO TYPE--OCTAL ASCII(ALL DIGITS)
013510 104401 017040 TYPE ,MTAPRI ;"PRIORITY=""
013512 016746 165412 MOV TAPRIO,-(SP) ;;SAVE TAPRIO FOR TYPEOUT
013514 104402 TYPOC ;;GO TYPE--OCTAL ASCII(ALL DIGITS)
013516 104401 017053 TYPE ,MSGOK ;"OK?"
013518 104407 RDCHR ;GO READ ONE CHARACTER
013520 012600 MOV (SP)+,RO ;GET IT
013522 022700 000015 CMP #15,RO ;IS IT "CR"?
013524 001406 BEQ 7$;BRANCH IF YES
013526 022700 000131 CMP #'Y,RO ;IS IT "Y"?
013528 001403 BEQ 7$;IT WAS
013530 104401 001176 TYPE ,SQUES ;TYPE "?"
013532 000651 BR 1$;AND LET HIM CORRECT THEM
013534 104401 017061 7$: TYPE ,MYES ;TYPE OUT "YES"
013536 012600 MOV (SP)+,RO ;RESTORE RO
013538 000207 RTS PC ;AND RETURN

```





THE FOLLOWING ROUTINES CAN BE USED TO MAKE ADJUSTMENTS TO THE TU60  
NOTE: \*\*\* BEFORE USING ANY OF THE ROUTINES LOAD AND START AT 214 \*\*\*

WRITE FILE GAPS FROM "BOT" TO "EOT"

START AT 220  
THIS ROUTINE CAN BE USED TO ADJUST THE "WRITE GAP MONO" AND  
THE "WRITE DELAY MONO".

013776 012706 001100  
014002 013704 001206  
014006 013705 001212  
014012 000005  
014014 012737 013776 001110  
014022 004737 015002  
014026 010314  
014030 112714 000017  
014034 032714 000040  
014040 001775  
014042 112714 000001  
014046 104412  
014050 032714 020000  
014054 001772  
014056 000000  
014060 000746  
014062 004737 014544  
014066 012706 001100  
014072 013704 001206  
014076 013705 001212  
014102 000005  
014104 012737 014066 001110  
014112 004737 015002  
014116 010314  
014120 112714 000017  
014124 032714 000040  
014130 001775

```
WFGSUB: MOV #STACK, SP ;KEEP THE STACK OUT OF THE WAY
MOV @#TACSL, TACS ;SETUP THE TAI1 STATUS AND
MOV @#TADBL, TADB ;DATA BUFFER REGISTERS
RESET ;RESET THE WORLD
MOV #WFGSUB, @#SLPERR ;SETUP THE LOOP ON ERROR ADDRESS
JSR PC, @#NXTDRV ;GO SETUP FOR NEXT DRIVE
MOV DRIVE, @TACS ;SELECT DRIVE
MOVB #REWIND!GO, @TACS ;SEND TAPE TO "BOT"
100$: BIT #READY, @TACS ;WAIT ON READY
BEQ 100$
1$: MOVB #WFG!GO, @TACS ;WRITE A FILE GAP
WAITREADY ;WAIT ON READY
BIT #LEADER, @TACS ;AT "CLEAR LEADER"?
BEQ 1$;BR IF NO
HALT ;STOP IF YES
BR WFGSUB ;LOOP ON CONT.
```

WRITE CONTINUOUS BLOCKS OF DATA

START AT 224  
THE PROGRAM WILL HALT THREE(3) TIMES  
AFTER EACH HALT SET THE SWR AND PRESS CONTINUE  
HALT 1 --- SWR<7:0> = NUMBER OF BYTES PER BLOCK  
HALT 2 --- SWR<7:0> = PATTERN DESIRED  
HALT 3 --- SWR<15:0> = OPERATIONAL SWITCH SETTINGS  
THIS ROUTINE CAN BE USED TO ADJUST THE "GAP TIME MONO"  
\*\* IF USING SOFTWARE SWITCH REGISTER, AFTER  
EACH HALT OPERATOR WILL BE PROMPTED  
FOR THE VALUE WITH "SWR=XXXXXX NEW="

```
WRTSUB: JSR PC, @#SETBUF ;GET BLOCK SIZE AND PATTERN
WLOOP: MOV #STACK, SP ;KEEP THE STACK OUT OF THE WAY
MOV @#TACSL, TACS ;SETUP THE TAI1 STATUS AND
MOV @#TADBL, TADB ;DATA BUFFER REGISTERS
RESET ;RESET THE WORLD
MOV #WLOOP, @#SLPERR ;SETUP THE LOOP ON ERROR ADDRESS
JSR PC, @#NXTDRV ;GO SETUP FOR NEXT DRIVE
MOV DRIVE, @TACS ;SELECT DRIVE
MOVB #REWIND!GO, @TACS ;SEND TAPE TO "BOT"
100$: BIT #READY, @TACS ;WAIT ON READY
BEQ 100$
```



```

014132 004737 014650 1S: JSR PC,@#WRTBLK ;WRITE A BLOCK
014136 032714 020000 BIT #LEADER,@TACS ;AT "CLEAR LEADER"?
014140 001775 BEQ 1S ;BR IF NO
014144 000000 HALT ;STOP IF "EOT"
014146 000747 BR WLOOP ;LOOP IF CONT.

:*****
: READ CONTINUOUS BLOCKS OF DATA
: START AT 230
: THIS ROUTINE CAN BE USED TO ADJUST THE "SIGNAL MONO"
: AND THE "THRESHOLD POT".
:*****
RDSUB: MOV #STACK,SP ;KEEP THE STACK OUT OF THE WAY
MOV @#TACSL,TACS ;SETUP THE TA11 STATUS AND
MOV @#TADBL,TADB ;DATA BUFFER REGISTERS
RESET ;RESET THE WORLD
MOV #RDSUB,@#SLPERR ;SETUP THE LOOP ON ERROR ADDRESS
JSR PC,@#NXTDRV ;GO SETUP FOR NEXT DRIVE
MOV DRIVE,@TACS ;SELECT DRIVE
MOVB #REWIND!GO,@TACS ;SEND TAPE TO "BOT"
100$: BIT #READY,@TACS ;WAIT ON READY
BEQ 100$
1S: JSR PC,@#RDBLK ;READ A BLOCK
BIT #LEADER,@TACS ;AT "CLEAR LEADER"?
BNE RDSUB ;BR IF YES---LOOP
BR 1S

:*****
: WRITE A FILE GAP AND A BLOCK OF DATA FROM BOT TO EOT
: START AT 234
: THE PROGRAM WILL HALT THREE(3) TIMES
: AFTER EACH HALT SET THE SWR AND PRESS CONTINUE
: HALT 1 --- SWR<7:0> = NUMBER OF BYTES PER BLOCK
: HALT 2 --- SWR<7:0> = PATTERN DESIRED
: HALT 3 --- SWR<15:0> = OPERATIONAL SWITCH SETTINGS
: ** IF USING SOFTWARE SWITCH REGISTER, AFTER
: EACH HALT OPERATOR WILL BE PROMPTED
: FOR THE VALUE WITH "SWR=XXXXXX NEW="
: AND THE "GAP TIME MONO".
: THIS ROUTINE SUPPORTS THE S/W SWITCH REG FUNCTIONS
:*****
WGPBLK: JSR PC,@#SETBUF ;GET BLOCK SIZE AND PATTERN
WGBL0P: MOV #STACK,SP ;KEEP THE STACK OUT OF THE WAY
MOV @#TACSL,TACS ;SETUP THE TA11 STATUS AND
MOV @#TADBL,TADB ;DATA BUFFER REGISTERS
RESET ;RESET THE WORLD
MOV #WGBL0P,@#SLPERR ;SETUP THE LOOP ON ERROR ADDRESS
JSR PC,@#NXTDRV ;GO SETUP FOR NEXT DRIVE
MOV DRIVE,@TACS ;SELECT DRIVE
MOVB #REWIND!GO,@TACS ;SEND TAPE TO "BOT"
100$: BIT #READY,@TACS ;WAIT ON READY
BEQ 100$
1S: MOVB #WFG!GO,@TACS ;WRITE A FILE GAP
WAITREADY ;WAIT ON READY

```

```

014306 032714 020000
014312 001005
014314 004737 014660
014320 032714 020000
014324 001765
014326 000000
014330 000741

```

23:

```

BIT #LEADER,@TACS ;AT "CLEAR LEADER"?
BNE 100$;BR IF YES
JSR PC,@#WRTBLK ;WRITE A BLOCK
BIT #LEADER,@TACS ;AT "CLEAR LEADER"?
BEQ 1$;BR IF NO
HALT ;STOP AT "EOT"
BR WGBLOP ;START OVER ON CONT.

```

```

: READ A BLOCK OF DATA AND A FILE GAP
: START AT 240
: THIS ROUTINE IS USED AFTER "WRITE A BLOCK AND A FILE GAP" ROUTINE
: IT CAN BE USED TO ADJUST THE "SIGNAL MONO", THE THRESHOLD POT"
: AND THE "TAPE BLANK MONO".

```

```

014332 012706 001100
014336 013704 001206
014342 013705 001212
014346 000005
014350 012737 014332 001110
014356 004737 015002
014362 010314
014364 112714 000017
014370 032714 000040
014374 001775
014376 004737 014722
014402 032714 020000
014406 001351
014410 112714 000015
014414 104412
014416 032714 020000
014422 001343
014424 000764

```

```

RGPBLK: MOV #STACK,SP ;KEEP THE STACK OUT OF THE WAY
MOV @#TACSL,TACS ;SETUP THE TAIL STATUS AND
MOV @#TADBL,TADB ;DATA BUFFER REGISTERS
RESET ;RESET THE WORLD
MOV #RGPBLK,@#SLPERR ;SETUP THE LOOP ON ERROR ADDRESS
JSR PC,@#NXTDRV ;GO SETUP FOR NEXT DRIVE
MOV DRIVE,@TACS ;SELECT DRIVE
MOVB #REWIND!GO,@TACS ;SEND TAPE TO "BOT"
100$: BIT #READY,@TACS ;WAIT ON READY
BEQ 100$
1$: JSR PC,@#RDBLK ;READ A BLOCK OF DATA
BIT #LEADER,@TACS ;AT "CLEAR LEADER"?
BNE RGPBLK ;BR IF YES
MOVB #SFBG!GO,@TACS ;GET INTO A FILE GAP
WAITREADY
BIT #LEADER,@TACS ;AT "CLEAR LEADER"?
BNE RGPBLK ;BR IF YES
BR 1$;LOOP

```

```

: SPACE FORWARD FILE GAP FROM "BOT" TO "EOT"
: START AT 244
: THIS ROUTINE CAN BE USED AFTER "WRITE FILE GAP" FOR LOW SPEED
: SPACE FOWARD (TAPE BLANK MONO CAN BE ADJUSTED), OR AFTER READ OR
: WRITE A FILE GAP AND A BLOCK OF DATA FOR HIGH SPEED SPACE FORWARD
: (SIGNAL MONO CAN BE CHECKED).

```

```

014426 012706 001100
014432 013704 001206
014436 013705 001212
014442 000005
014444 012737 014426 001110
014452 004737 015002
014456 010314
014460 112714 000017
014464 032714 000040
014470 001775
014472 112714 000013
014476 104412

```

```

SFFGSB: MOV #STACK,SP ;KEEP THE STACK OUT OF THE WAY
MOV @#TACSL,TACS ;SETUP THE TAIL STATUS AND
MOV @#TADBL,TADB ;DATA BUFFER REGISTERS
RESET ;RESET THE WORLD
MOV #SFFGSB,@#SLPERR ;SETUP THE LOOP ON ERROR ADDRESS
JSR PC,@#NXTDRV ;GO SETUP FOR NEXT DRIVE
MOV DRIVE,@TACS ;SELECT DRIVE
MOVB #REWIND!GO,@TACS ;SEND TAPE TO "BOT"
100$: BIT #READY,@TACS ;WAIT ON READY
BEQ 100$
1$: MOVB #SFFG!GO,@TACS ;SPACE INTO A FILE GAP
WAITREADY ;WAIT ON READY

```



```

014500 032714 020000 BIT #LEADER,@TACS ;AT "CLEAR LEADER"?
014504 001772 BEQ 1$;BR IF NO
014506 000000 HALT ;STOP AT "EOT"
014510 000746 BR SFFGSB ;LOOP ON CONT.

;*****
; BACK SPACE FILE GAP
; START AT 250
; THIS ROUTINE CAN BE USED TO ADJUST OR CHECK THE "SIGNAL MONO".
;*****
BSFGSB: RESET ;RESET THE WORLD
MOV #BSFGSB,@$LPERR ;LOOP ON ERROR ADDRESS
MOV DRIVE,@TACS ;SELECT DRIVE
1$: MOVB #BSFG!GO,@TACS ;BACK SPACE A FILE GAP
WAITREADY ;WAIT ON READY
BIT #LEADER,@TACS ;AT "CLEAR LEADER"?
BEQ 1$;BR IF NO
HALT ;STOP AT BOT
BR BSFGSB ;START OVER ON CONT.

;*****
; SETUP BLOCK SIZE AND PATTERN FOR SUBROUTINES
;*****
SETBUF: CLR RO
HALT ;OPERATOR PUTS BYTE COUNT IN SWR<7:0>
CMP #SWREG,SWR ;USING S/W SWITCH REG.?
BNE 20$;NO- GET OUT
GTSWR ;LET HIM CHANGE IT
20$: CONTINUE
BISB @SWR,RO ;PICKUP THE BYTE COUNT
BNE 2$;BR IF NON-ZERO
TSTB @SWR+1 ;CHECK IF GREATER THAN 377
BEQ 1$;BR IF NO
MOV #376,RO ;SET FOR MAX ALLOWED
1$: INC RO ;MAKE IT 377 OR 1
2$: MOV RO,@$BLKLIM ;SETUP THE BLOCK LIMIT
CLR @$PATRN
HALT ;OPERATOR PUTS PATTERN IN SWR<7:0>
CMP #SWREG,SWR ;USING S/W SWITCH REG.?
BNE 21$;NO- GET OUT
GTSWR ;LET HIM CHANGE IT
21$: CONTINUE
MOVB @SWR,@$PATRN ;PICK UP THE PATTERN
HALT ;SET OPERATIONAL SWITCHES
CMP #SWREG,SWR ;USING S/W SWITCH REG.?
BNE 22$;NO- GET OUT
GTSWR ;LET HIM CHANGE IT
22$: CONTINUE
RTS PC ;RETURN
BLKLIM: 0 ;READ AND WRITE BLOCK SIZE
PATRN: 0 ;PATTERN TO GO ON THE TAPE

;*****

```

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014660 013701 014654  
014664 112714 000003  
014670 104413  
014672 032714 000040  
014676 001010  
014700 005301  
014702 002403  
014704 113715 014656  
014710 000767  
014712 052714 000020  
014716 104412  
014720 000207

```
WRITE ROUTINE FOR THE MANUAL OPERATIONS

WRTBLK: MOV @#BLK LIM,R1 ;PICKUP THE BLOCK SIZE
 MOVB #WRITE!GO,@TACS ;START A WRITE
1$: WAITXFER ;WAIT ON TRANSFER REQUEST
 BIT #READY,@TACS ;DID READY SET?
 BNE 3$;BR IF YES
 DEC R1 ;COUNT THIS REQUEST
 BLT 2$;BR IF TIME FOR ILBS
 MOVB @#PATTRN,@TADB ;PUT DATA ON TAPE
 BR 1$;LOOP
2$: BIS #ILBS,@TACS ;WRITE CRC AND SHUT DOWN
 WAITREADY ;WAIT ON THE READY FLAG
3$: RTS PC
```

014722 013702 014654  
014726 013700 014656  
014732 112714 000005  
014736 104413  
014740 032714 000040  
014744 001012  
014746 005302  
014750 002405  
014752 011501  
014754 120001  
014756 001767  
014760 104005  
014762 000406  
014764 052714 000020  
014770 104412  
014772 005714  
014774 100001  
014776 104001  
015000 000207

```
READ ROUTINE FOR THE MANUAL OPERATIONS

ROBLK: MOV @#BLK LIM,R2 ;PICKUP THE BLOCK SIZE
 MOV @#PATTRN,R0 ;USE THIS DATA PATTERN TO COMPARE TO
 MOVB #READ!GO,@TACS ;START A READ
1$: WAITXFER ;WAIT ON TRANSFER REQUEST
 BIT #READY,@TACS ;IS READY SET?
 BNE 3$;BR IF YES
 DEC R2 ;COUNT THIS REQUEST
 BLT 2$;BR IF TIME FOR ILBS
 MOV @TADB,R1 ;READ THE DATA BUFFER
 CMPB R0,R1 ;CHECK THE DATA
 BEQ 1$;BR IF OK
 ERROR 5 ;BAD DATA
 BR 4$;GET OUT
2$: BIS #ILBS,@TACS ;READ ILBS
 WAITREADY ;WAIT ON READY
3$: TST @TACS ;CHECK FOR ERROR
 BPL 4$;BR IF NONE
 ERROR 1 ;ERROR OCCURRED
4$: RTS PC ;RETURN
```

015002 105777 164132  
015006 100416  
015010 005003  
015012 013701 001230  
015016 122127 000101  
015022 001402  
015024 012703 000400  
015030 105711  
015032 001002  
015034 012701 001224  
015040 010137 001230  
015044 000207

```
ROUTINE TO CHANGE DRIVES

NXTDRV: TSTB @SWR ;IS SW07 ON A (1)?
 BMI 3$;BR IF YES
 CLR DRIVE ;SET DRIVE TO "A"
 MOV @#DRV PNT,R1 ;GET DRIVE POINTER
 CMPB (R1)+,#'A ;IS IT DRIVE "A"?
 BEQ 1$;BR IF YES
 MOV #UNIT,DRIVE ;SET DRIVE TO "B"
1$: TSTB (R1) ;LAST DRIVE BEEN SELECTED
 BNE 2$;BR IF NO
 MOV #DRVKEY,R1 ;RESET DRIVE POINTER
2$: MOV R1,@#DRV PNT ;SAVE DRIVE POINTER FOR NEXT TIME
3$: RTS PC ;GO BACK
```





\$.SBTTL TYPE ROUTINE

\*\*\*\*\*  
\*ROUTINE TO TYPE ASCIZ MESSAGE. MESSAGE MUST TERMINATE WITH A 0 BYTE.  
\*THE ROUTINE WILL INSERT A NUMBER OF NULL CHARACTERS AFTER A LINE FEED.  
\*NOTE1: \$NULL CONTAINS THE CHARACTER TO BE USED AS THE FILLER CHARACTER.  
\*NOTE2: \$FILLS CONTAINS THE NUMBER OF FILLER CHARACTERS REQUIRED.  
\*NOTE3: \$FILLC CONTAINS THE CHARACTER TO FILL AFTER.  
\*

\*CALL:  
\*1) USING A TRAP INSTRUCTION  
\* TYPE ,MESADR ;;MESADR IS FIRST ADDRESS OF AN ASCIZ STRING  
\*OR  
\* TYPE  
\* MESADR  
\*

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\$TYPE: TSTB \$TFPLG ;; IS THERE A TERMINAL?  
BPL 1\$ ;; BR IF YES  
HALT ;; HALT HERE IF NO TERMINAL  
BR 3\$ ;; LEAVE  
1\$: MOV RO, -(SP) ;; SAVE RO  
MOV 22(SP), RO ;; GET ADDRESS OF ASCIZ STRING  
2\$: MOVB (RO)+, -(SP) ;; PUSH CHARACTER TO BE TYPED ONTO STACK  
BNE 4\$ ;; BR IF IT ISN'T THE TERMINATOR  
TST (SP)+ ;; IF TERMINATOR POP IT OFF THE STACK  
60\$: MOV (SP)+, RO ;; RESTORE RO  
3\$: ADD #2, (SP) ;; ADJUST RETURN PC  
RTI ;; RETURN  
4\$: CMPB #HT, (SP) ;; BRANCH IF <HT>  
BEQ 8\$  
CMPB #CRLF, (SP) ;; BRANCH IF NOT <CRLF>  
BNE 5\$  
TST (SP)+ ;; POP <CR><LF> EQUIV  
TYPE ;; TYPE A CR AND LF  
\$CRLF  
CLRB \$CHARCNT ;; CLEAR CHARACTER COUNT  
BR 2\$ ;; GET NEXT CHARACTER  
5\$: JSR PC, \$TYPEC ;; GO TYPE THIS CHARACTER  
6\$: CMPB \$FILLC, (SP)+ ;; IS IT TIME FOR FILLER CHARS.?  
BNE 2\$ ;; IF NO GO GET NEXT CHAR.  
MOV \$NULL, -(SP) ;; GET # OF FILLER CHARS. NEEDED  
AND THE NULL CHAR.  
7\$: DECB 1(SP) ;; DOES A NULL NEED TO BE TYPED?  
BLT 6\$ ;; BR IF NO--GO POP THE NULL OFF OF STACK  
JSR PC, \$TYPEC ;; GO TYPE A NULL  
DECB \$CHARCNT ;; DO NOT COUNT AS A COUNT  
BR 7\$ ;; LOOP

;HORIZONTAL TAB PROCESSOR

8\$: MOVB #' (SP) ;; REPLACE TAB WITH SPACE  
9\$: JSR PC, \$TYPEC ;; TYPE A SPACE  
BITB #7, \$CHARCNT ;; BRANCH IF NOT AT  
BNE 9\$ ;; TAB STOP  
TST (SP)+ ;; POP SPACE OFF STACK



```

2981 015320 000724 BR 2$;;GET NEXT CHARACTER
2982 015322 105777 163622 $TYPEC: TSTB 2$TPS ;;WAIT UNTIL PRINTER IS READY
2983 015326 100375 BPL $TYPEC
2984 015330 116677 000002 163614 MOVB 2(SP),2$TPB ;;LOAD CHAR TO BE TYPED INTO DATA REG.
2985 015336 122766 000015 000002 CMPB #CR,2(SP) ;;IS CHARACTER A CARRIAGE RETURN?
2986 015344 001003 BNE 1$;;BRANCH IF NO
2987 015346 105067 000014 CLRB $CHARCNT ;;YES--CLEAR CHARACTER COUNT
2988 015352 000406 BR $TYPEX ;;EXIT
2989 015354 122766 000012 000002 1$: CMPB #LF,2(SP) ;;IS CHARACTER A LINE FEED?
2990 015362 001402 BEQ $TYPEX ;;BRANCH IF YES
2991 015364 105227 INCB (PC)+ ;;COUNT THE CHARACTER
2992 015366 000000 $CHARCNT: .WORD 0 ;;CHARACTER COUNT STORAGE
2993 015370 000207 $TYPEX: RTS PC

.SBTTL READ AN OCTAL NUMBER FROM THE TTY

;;*****
;;*THIS ROUTINE WILL READ AN OCTAL (ASCII) NUMBER FROM THE TTY AND
;;*CHANGE IT TO BINARY.
;;*CALL:
;;* RDOCT ;;READ AN OCTAL NUMBER
;;* RETURN HERE ;;LOW ORDER BITS ARE ON TOP OF THE STACK
;;* ;;HIGH ORDER BITS ARE IN $HIOCT

3005 015372 011646 000004 000002 $RDOCT: MOV (SP),-(SP) ;;PROVIDE SPACE FOR THE
3006 015374 016666 MOV 4(SP),2(SP) ;;INPUT NUMBER
3007 015402 010046 MOV R0,-(SP) ;;PUSH R0 ON STACK
3008 015404 010146 MOV R1,-(SP) ;;PUSH R1 ON STACK
3009 015406 010246 MOV R2,-(SP) ;;PUSH R2 ON STACK
3010 015410 104410 1$: RDLIN ;;READ AN ASCII LINE
3011 015412 012600 MOV (SP)+,R0 ;;GET ADDRESS OF 1ST CHARACTER
3012 015414 005001 CLR R1 ;;CLEAR DATA WORD
3013 015416 005002 CLR R2
3014 015420 112046 2$: MOVB (R0)+,-(SP) ;;PICKUP THIS CHARACTER
3015 015422 001412 BEQ 3$;;IF ZERO GET OUT
3016 015424 006301 ASL R1 ;;*2
3017 015426 006102 ROL R2 ;;*4
3018 015430 006301 ASL R1 ;;*4
3019 015432 006102 ROL R2 ;;*8
3020 015434 006301 ASL R1 ;;*8
3021 015436 006102 ROL R2
3022 015440 042716 177770 BIC #1C7,(SP) ;;STRIP THE ASCII JUNK
3023 015444 062601 ADD (SP)+,R1 ;;ADD IN THIS DIGIT
3024 015446 000764 BR 2$;;LOOP
3025 015450 005726 3$: TST (SP)+ ;;CLEAN TERMINATOR FROM STACK
3026 015452 010166 000012 MOV R1,12(SP) ;;SAVE THE RESULT
3027 015456 010267 000010 MOV R2,$HIOCT
3028 015462 012602 MOV (SP)+,R2 ;;POP STACK INTO R2
3029 015464 012601 MOV (SP)+,R1 ;;POP STACK INTO R1
3030 015466 012600 MOV (SP)+,R0 ;;POP STACK INTO R0
3031 015470 000002 RTI
3032 015472 000000 $HIOCT: .WORD 0 ;;HIGH ORDER BITS GO HERE
.SBTTL TTY INPUT ROUTINE

;;*****
.ENABLE LSB

```



```

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3043 015474 022767 000176 163436 $CKSWR: CMP #SWREG,SWR ;; IS THE SOFT-SWR SELECTED?
3044 015502 001074 BNE 15$;; BRANCH IF NO
3045 015504 105777 163434 TSTB @STKS ;; CHAR THERE?
3046 015510 100071 BPL 15$;; IF NO, DON'T WAIT AROUND
3047 015512 117746 163430 MOVB @STKB,-(SP) ;; SAVE THE CHAR
3048 015516 042716 177600 BIC #1C177,(SP) ;; STRIP-OFF THE ASCII
3049 015522 022726 000007 CMP #7,(SP)+ ;; IS IT A CONTROL G?
3050 015526 001062 BNE 15$;; NO, RETURN TO USER
3051 015530 126727 163400 000001 CMPB $AUTOB,#1 ;; ARE WE RUNNING IN AUTO-MODE?
3052 015536 001456 BEQ 15$;; BRANCH IF YES
3053
3054 015540 104401 016221 $GTSWR: TYPE , $CNTLG ;; ECHO THE CONTROL-G (↑G)
3055 015544 104401 016226 TYPE $MSWR ;; TYPE CURRENT CONTENTS
3056 015550 016746 162422 MOV SWREG,-(SP) ;; SAVE SWREG FOR TYPEOUT
3057 015554 104402 TYPOC ;; GO TYPE--OCTAL ASCII(ALL DIGITS)
3058 015556 104401 016237 TYPE , $MNEW ;; PROMPT FOR NEW SWR
3059 015562 005046 19$: CLR -(SP) ;; CLEAR COUNTER
3060 015564 005046 7$: CLR -(SP) ;; THE NEW SWR
3061 015566 105777 163352 TSTB @STKS ;; CHAR THERE?
3062 015572 100375 BPL 7$;; IF NOT TRY AGAIN
3063
3064 015574 117746 163346 MOVB @STKB,-(SP) ;; PICK UP CHAR
3065 015600 042716 177600 BIC #1C177,(SP) ;; MAKE IT 7-BIT ASCII
3066
3067
3068
3069 015604 021627 000025 9$: CMP (SP),#25 ;; IS IT A CONTROL-U?
3070 015610 001005 BNE 10$;; BRANCH IF NOT
3071 015612 104401 016214 TYPE , $CNTLU ;; YES, ECHO CONTROL-U (↑U)
3072 015616 062706 000006 20$: ADD #6,SP ;; IGNORE PREVIOUS INPUT
3073 015622 000757 BR 19$;; LET'S TRY IT AGAIN
3074
3075
3076 015624 021627 000015 10$: CMP (SP),#15 ;; IS IT A <CR>?
3077 015630 001022 BNE 16$;; BRANCH IF NO
3078 015632 005766 000004 TST 4(SP) ;; YES, IS IT THE FIRST CHAR?
3079 015636 001403 BEQ 11$;; BRANCH IF YES
3080 015640 016677 000002 163272 MOV 2(SP),@SWR ;; SAVE NEW SWR
3081 015646 062706 000006 11$: ADD #6,SP ;; CLEAR UP STACK
3082 015652 104401 001177 14$: TYPE , $CRLF ;; ECHO <CR> AND <LF>
3083 015656 126727 163253 000001 CMPB $INTAG,#1 ;; RE-ENABLE TTY KBD INTERRUPTS?
3084 015664 001003 BNE 15$;; BRANCH IF NOT
3085 015666 012777 000100 163250 MOV #100,@STKS ;; RE-ENABLE TTY KBD INTERRUPTS
3086 015674 000002 15$: RTI ;; RETURN
3087 015676 004767 177420 16$: JSR PC,$TYPEC ;; ECHO CHAR
3088 015702 021627 000060 CMP (SP),#60 ;; CHAR < 0?
3089 015706 002420 BLT 18$;; BRANCH IF YES
3090 015710 021627 000067 CMP (SP),#67 ;; CHAR > 7?
3091 015714 003015 BGT 18$;; BRANCH IF YES
3092 015716 042726 000060 BIC #60,(SP)+ ;; STRIP-OFF ASCII

```



```

3093 015722 005766 000002 TST 2(SP) ;; IS THIS THE FIRST CHAR
3094 015726 001403 BEQ 17$;; BRANCH IF YES
3095 015730 006316 ASL (SP) ;; NO, SHIFT PRESENT
3096 015732 006316 ASL (SP) ;; CHAR OVER TO MAKE
3097 015734 006316 ASL (SP) ;; ROOM FOR NEW ONE.
3098 015736 005266 000002 17$: INC 2(SP) ;; KEEP COUNT OF CHAR
3099 015742 056616 177776 BIS -2(SP), (SP) ;; SET IN NEW CHAR
3100 015746 000707 BR 7$;; GET THE NEXT ONE
3101 015750 104401 001176 18$: TYPE $QUES ;; TYPE ?<CR><LF>
3102 015754 000720 BR 20$;; SIMULATE CONTROL-U
3103 .DSABL LSB
3104
3105
3106 ;;*****
3107 ;;*THIS ROUTINE WILL INPUT A SINGLE CHARACTER FROM THE TTY
3108 ;;*CALL:
3109 ;;*
3110 ;;* RDCHR ;; INPUT A SINGLE CHARACTER FROM THE TTY
3111 ;;* RETURN HERE ;; CHARACTER IS ON THE STACK
3112 ;;*
3113 ;;*
3114 015756 011646 $RDCHR: MOV (SP), -(SP) ;; PUSH DOWN THE PC
3115 015760 016666 000004 000002 MOV 4(SP), 2(SP) ;; SAVE THE PS
3116 015766 105777 163152 1$: TSTB 2$TKS ;; WAIT FOR
3117 015772 100375 BPL 1$;; A CHARACTER
3118 015774 117766 163146 000004 MOVB 2$TKB, 4(SP) ;; READ THE TTY
3119 016002 042766 177600 000004 BIC #1C<177>, 4(SP) ;; GET RID OF JUNK IF ANY
3120 016010 026627 000004 000023 CMP 4(SP), #23 ;; IS IT A CONTROL-S?
3121 016016 001013 BNE 3$;; BRANCH IF NO
3122 016020 105777 163120 2$: TSTB 2$TKS ;; WAIT FOR A CHARACTER
3123 016024 100375 BPL 2$;; LOOP UNTIL ITS THERE
3124 016026 117746 163114 MOVB 2$TKB, -(SP) ;; GET CHARACTER
3125 016032 042716 177600 BIC #1C177, (SP) ;; MAKE IT 7-BIT ASCII
3126 016036 022627 000021 CMP (SP)+, #21 ;; IS IT A CONTROL-Q?
3127 016042 001366 BNE 2$;; IF NOT DISCARD IT
3128 016044 000750 BR 1$;; YES, RESUME
3129 016046 026627 000004 000140 3$: CMP 4(SP), #140 ;; IS IT UPPER CASE?
3130 016054 002407 BLT 4$;; BRANCH IF YES
3131 016056 026627 000004 000175 CMP 4(SP), #175 ;; IS IT A SPECIAL CHAR?
3132 016064 003003 BGT 4$;; BRANCH IF YES
3133 016066 042766 000040 000004 BIC #40, 4(SP) ;; MAKE IT UPPER CASE
3134 016074 000002 4$: RTI ;; GO BACK TO USER
3135 ;;*****
3136 ;;*THIS ROUTINE WILL INPUT A STRING FROM THE TTY
3137 ;;*CALL:
3138 ;;*
3139 ;;* RDLIN ;; INPUT A STRING FROM THE TTY
3140 ;;* RETURN HERE ;; ADDRESS OF FIRST CHARACTER WILL BE ON THE STACK
3141 ;;*
3142 ;;*
3142 016076 010346 $RDLIN: MOV R3, -(SP) ;; SAVE R3
3143 016100 012703 016204 1$: MOV #1TTYIN, R3 ;; GET ADDRESS
3144 016104 022703 016214 2$: CMP #1TTYIN+8., R3 ;; BUFFER FULL?
3145 016110 101405 BLOS 4$;; BR IF YES
3146 016112 104407 RDCHR ;; GO READ ONE CHARACTER FROM THE TTY
3147 016114 112613 MOVB (SP)+, (R3) ;; GET CHARACTER
3148 016116 122713 000177 10$: CMPB #177, (R3) ;; IS IT A RUBOUT

```



```
016174 001000 001176 45: BNF 35 ::SKIP IF NOT
016175 104401 000044 46: TYPE 188888 ::TYPE A '?'
016176 000760 016202 35: BR 188888 ::CLEAR THE BUFFER AND LOOP
016177 111367 000018 46: MOV (R3),95 ::ECHO THE CHARACTER
016178 104401 000018 46: TYPE 188888 ::CHECK FOR RETURN
016179 122720 000018 46: CMP BPT 188888, (R3)+ ::LOOP IF NOT RETURN
016180 001335 177777 46: BNF 188888 ::CLEAR RETURN (THE 15)
016181 105063 001200 46: CLRB -1(R3) ::TYPE A LINE FEED
016182 104401 000004 000002 46: TYPE 188888 ::RESTORE R3
016183 012603 000004 000004 46: MOV (SP)+,R3 ::ADJUST THE STACK AND PUT ADDRESS OF THE
016184 011646 016204 000004 46: MOV (SP),-(SP) :: FIRST ASCII CHARACTER ON IT
016185 016666 016204 000004 46: MOV 4(SP),2(SP)
016186 012766 000002 46: MOV #TTYIN,4(SP)
016187 000002 95: RTI ::RETURN
016188 000000 000000 46: .BYTE 0 ::STORAGE FOR ASCII CHAR. TO TYPE
016189 000000 000000 46: .BYTE 0 ::TERMINATOR
016190 000010 000000 46: .BLKB 8 ::RESERVE 8 BYTES FOR TTY INPUT
016191 052536 005015 000012 46: $TTYIN: .ASCIZ /?U/<15><12> ::CONTROL "U"
016192 136 006507 020122 46: $CNTLU: .ASCIZ /?G/<15><12> ::CONTROL "G"
016193 005015 053523 020122 46: $MSWR: .ASCIZ <15><12>/SWR = /
016194 020075 000 46: $MNEW: .ASCIZ / NEW = /
016195 040 047040 053505 46:
016196 036440 000040 46:
016197 46: .SBTTL BINARY TO OCTAL (ASCII) AND TYPE
016198 46: *****
016199 46: *THIS ROUTINE IS USED TO CHANGE A 16-BIT BINARY NUMBER TO A 6-DIGIT
016200 46: *OCTAL (ASCII) NUMBER AND TYPE IT.
016201 46: *$TYPOS---ENTER HERE TO SETUP SUPPRESS ZEROS AND NUMBER OF DIGITS TO TYPE
016202 46: *CALL:
016203 46: * MOV NUM, -(SP) ;;NUMBER TO BE TYPED
016204 46: * TYPOS ;;CALL FOR TYPEOUT
016205 46: * .BYTE N ;;N=1 TO 6 FOR NUMBER OF DIGITS TO TYPE
016206 46: * .BYTE M ;;M=1 OR 0
016207 46: * ;;I=TYPE LEADING ZEROS
016208 46: * ;;O=SUPPRESS LEADING ZEROS
016209 46: *
016210 46: *$TYPON---ENTER HERE TO TYPE OUT WITH THE SAME PARAMETERS AS THE LAST
016211 46: *$TYPOS OR $TYPOC
016212 46: *CALL:
016213 46: * MOV NUM, -(SP) ;;NUMBER TO BE TYPED
016214 46: * TYPON ;;CALL FOR TYPEOUT
016215 46: *
016216 46: *$TYPOC---ENTER HERE FOR TYPEOUT OF A 16 BIT NUMBER
016217 46: *CALL:
016218 46: * MOV NUM, -(SP) ;;NUMBER TO BE TYPED
016219 46: * TYPOC ;;CALL FOR TYPEOUT
016220 46: *
016221 46: *$TYPOS: MOV 2(SP), -(SP) ;;PICKUP THE MODE
016222 46: * MOV 1(SP), $OFILL ;;LOAD ZERO FILL SWITCH
016223 46: * MOV (SP)+, $MODE+1 ;;NUMBER OF DIGITS TO TYPE
016224 46: * ADD #2, (SP) ;;ADJUST RETURN ADDRESS
016225 46: * BR $TYPON
016226 46: *$TYPOC: MOV #1, $OFILL ;;SET THE ZERO FILL SWITCH
016227 46: * MOV #6, $MODE+1 ;;SET FOR SIX(6) DIGITS
016228 46: *$TYPON: MOV #5, $OCNT ;;SET THE ITERATION COUNT
```



|        |        |        |               |       |              |                                    |
|--------|--------|--------|---------------|-------|--------------|------------------------------------|
| 000005 | 016316 | 010346 |               | MOV   | R3,-(SP)     | ::SAVE R3                          |
| 000006 | 016320 | 010446 |               | MOV   | R4,-(SP)     | ::SAVE R4                          |
| 000007 | 016322 | 010546 |               | MOV   | R5,-(SP)     | ::SAVE R5                          |
| 000008 | 016324 | 116704 | 000145        | MOVB  | \$OMODE+1,R4 | ::GET THE NUMBER OF DIGITS TO TYPE |
| 000009 | 016326 | 005404 |               | NEG   | R4           |                                    |
| 000010 | 016328 | 062704 | 000006        | ADD   | #6,R4        | ::SUBTRACT IT FOR MAX. ALLOWED     |
| 000011 | 016326 | 110467 | 000132        | MOVB  | R4,\$OMODE   | ::SAVE IT FOR USE                  |
| 000012 | 016342 | 116704 | 000125        | MOVB  | \$OFILL,R4   | ::GET THE ZERO FILL SWITCH         |
| 000013 | 016346 | 016605 | 000012        | MOV   | 100(SP),R5   | ::PICKUP THE INPUT NUMBER          |
| 000014 | 016352 | 005002 |               | CLR   | R4           | ::CLEAR THE OUTPUT WORD            |
| 000015 | 016354 | 006105 |               | ROL   | R4           | ::ROTATE MSB INTO "C"              |
| 000016 | 016356 | 000404 |               | BR    | 28           | ::GO DO MSB                        |
| 000017 | 016360 | 006105 |               | ROL   | R4           | ::FORM THIS DIGIT                  |
| 000018 | 016362 | 006105 |               | ROL   | R4           |                                    |
| 000019 | 016364 | 006105 |               | ROL   | R4           |                                    |
| 000020 | 016366 | 010503 |               | MOV   | R5,R3        |                                    |
| 000021 | 016370 | 006103 |               | ROL   | R3           |                                    |
| 000022 | 016372 | 105367 | 000076        | DECB  | \$OMODE      | ::GET LSB OF THIS DIGIT            |
| 000023 | 016376 | 100016 |               | BPL   | 75           | ::TYPE THIS DIGIT?                 |
| 000024 | 016400 | 042703 | 177770        | BIC   | #177770,R3   | ::BR IF NO                         |
| 000025 | 016404 | 001002 |               | BNE   | 45           | ::GET RID OF JUNK                  |
| 000026 | 016406 | 005704 |               | TST   | R4           | ::TEST FOR 0                       |
| 000027 | 016410 | 001403 |               | BEG   | 55           | ::SUPPRESS THIS 0?                 |
| 000028 | 016412 | 005204 |               | INC   | R4           | ::BR IF YES                        |
| 000029 | 016414 | 052703 | 000060        | BIS   | #'0,R3       | ::DON'T SUPPRESS ANYMORE 0'S       |
| 000030 | 016420 | 052703 | 000040        | BIS   | #'0,R3       | ::MAKE THIS DIGIT ASCII            |
| 000031 | 016424 | 110367 | 000040        | MOVB  | R3,R5        | ::MAKE ASCII IF NOT ALREADY        |
| 000032 | 016430 | 104401 | 016470        | TYPE  | 85           | ::SAVE FOR TYPING                  |
| 000033 | 016434 | 105367 | 000032        | DECB  | \$OCNT       | ::GO TYPE THIS DIGIT               |
| 000034 | 016440 | 003347 |               | BGT   | 25           | ::COUNT BY 1                       |
| 000035 | 016442 | 002402 |               | PLT   | 65           | ::BR IF MORE TO DO                 |
| 000036 | 016444 | 005204 |               | INC   | R4           | ::BR IF DONE                       |
| 000037 | 016446 | 000744 |               | BR    | 25           | ::INSURE LAST DIGIT ISN'T A BLANK  |
| 000038 | 016450 | 012605 |               | MOV   | (SP)+,R5     | ::GO DO THE LAST DIGIT             |
| 000039 | 016452 | 012604 |               | MOV   | (SP)+,R4     | ::RESTORE R5                       |
| 000040 | 016454 | 012603 |               | MOV   | (SP)+,R3     | ::RESTORE R4                       |
| 000041 | 016456 | 016666 | 000002 000004 | MOV   | 2(SP),4(SP)  | ::RESTORE R3                       |
| 000042 | 016464 | 012616 |               | MOV   | (SP)+,(SP)   | ::SET THE STACK FOR RETURNING      |
| 000043 | 016466 | 000002 |               | RTI   |              | ::RETURN                           |
| 000044 | 016470 | 000    |               | .BYTE | 0            | ::STORAGE FOR ASCII DIGIT          |
| 000045 | 016471 | 000    |               | .BYTE | 0            | ::TERMINATOR FOR TYPE ROUTINE      |
| 000046 | 016472 | 000    |               | .BYTE | 0            | ::OCTAL DIGIT COUNTER              |
| 000047 | 016473 | 000    |               | .BYTE | 0            | ::ZERO FILL SWITCH                 |
| 000048 | 016474 | 000000 |               | .WORD | 0            | ::NUMBER OF DIGITS TO TYPE         |

.SBTTL TRAP DECODER

\*\*\*\*\*  
 \*THIS ROUTINE WILL PICKUP THE LOWER BYTE OF THE "TRAP" INSTRUCTION  
 \*AND USE IT TO INDEX THROUGH THE TRAP TABLE FOR THE STARTING ADDRESS  
 \*OF THE DESIRED ROUTINE. THEN USING THE ADDRESS OBTAINED IT WILL  
 \*GO TO THAT ROUTINE.

016476 010046  
 016500 016600 000002  
 016504 005740  
 016506 111000  
 016510 006300  
 016512 016000 016532  
 016516 000200

```

$TRAP: MOV RO, -(SP) ;; SAVE RO
 MOV 2(SP), RO ;; GET TRAP ADDRESS
 TST -(RO) ;; BACKUP BY 2
 MOVB (RO), RO ;; GET RIGHT BYTE OF TRAP
 ASL RO ;; POSITION FOR INDEXING
 MOV $TRAPD(RO), RO ;; INDEX TO TABLE
 RTS RO ;; GO TO ROUTINE

```

::THIS IS USE TO HANDLE THE "GETPRI" MACRO

016520 011646  
 016522 016666 000004 000002  
 016530 000002

```

$TRAP2: MOV (SP), -(SP) ;; MOVE THE PC DOWN
 MOV 4(SP), 2(SP) ;; MOVE THE PSW DOWN
 RTI ;; RESTORE THE PSW

```

.SBTTL TRAP TABLE

\*THIS TABLE CONTAINS THE STARTING ADDRESSES OF THE ROUTINES CALLED  
 \*BY THE "TRAP" INSTRUCTION.

016532 016520  
 016534 015152  
 016536 016274  
 016540 016250  
 016542 016310  
 016544 015544  
 016546 015474  
 016550 015756  
 016552 016076  
 016554 015372  
 016556 013046  
 016560 013152

```

ROUTINE

$TRPAD: .WORD $TRAP2
 $TYPE ;; CALL=TYPE TRAP+1(104401) TTY TYPEOUT ROUTINE
 $TYPOC ;; CALL=TYPOC TRAP+2(104402) TYPE OCTAL NUMBER (WITH LEADING ZEROS)
 $TYPOS ;; CALL=TYPOS TRAP+3(104403) TYPE OCTAL NUMBER (NO LEADING ZEROS)
 $TYPON ;; CALL=TYPON TRAP+4(104404) TYPE OCTAL NUMBER (AS PER LAST CALL)

 $GTSWR ;; CALL=GTSWR TRAP+5(104405) GET SOFT-SWR SETTING

 $CKSWR ;; CALL=CKSWR TRAP+6(104406) TEST FOR CHANGE IN SOFT-SWR
 $RDCHR ;; CALL=RDCHR TRAP+7(104407) TTY TYPEIN CHARACTER ROUTINE
 $RDLIN ;; CALL=RDLIN TRAP+10(104410) TTY TYPEIN STRING ROUTINE
 $RDOCT ;; CALL=RDOCT TRAP+11(104411) READ AN OCTAL NUMBER FROM TTY
 WAIT.ON.READY ;; CALL=WAITREADY TRAP+12(104412) WAIT ON THE READY BIT TO
 WAIT.FOR.XFER ;; CALL=WAITXFER TRAP+13(104413) WAIT ON XFER REQ.

```



.SBTTL POWER DOWN AND UP ROUTINES

\*\*\*\*\*  
 :POWER DOWN ROUTINE

33303 016562 012737 016726 000024  
 33304 016570 012737 000340 000026  
 33305 016576 010046  
 33306 016600 010146  
 33307 016602 010246  
 33308 016604 010346  
 33309 016606 010446  
 33310 016610 010546  
 33311 016612 017746 162322  
 33312 016616 010667 000110  
 33313 016622 012737 016634 000024  
 33314 016630 000000  
 33315 016632 000776

```
$PWRDN: MOV $ILLUP, @#PWRVEC ;; SET FOR FAST UP
 MOV #340, @#PWRVEC+2 ;; PRIO:7
 MOV R0, -(SP) ;; PUSH R0 ON STACK
 MOV R1, -(SP) ;; PUSH R1 ON STACK
 MOV R2, -(SP) ;; PUSH R2 ON STACK
 MOV R3, -(SP) ;; PUSH R3 ON STACK
 MOV R4, -(SP) ;; PUSH R4 ON STACK
 MOV R5, -(SP) ;; PUSH R5 ON STACK
 MOV @SWR, -(SP) ;; PUSH @SWR ON STACK
 MOV SP, $SAVR6 ;; SAVE SP
 MOV #PWRUP, @#PWRVEC ;; SET UP VECTOR
 HALT
 BR .-2 ;; HANG UP
```

\*\*\*\*\*  
 :POWER UP ROUTINE

33316 016634 012737 016726 000024  
 33317 016642 016706 000064  
 33318 016646 005067 000060  
 33319 016652 005267 000054  
 33320 016656 001375  
 33321 016660 012677 162254  
 33322 016664 012605  
 33323 016666 012604  
 33324 016670 012603  
 33325 016672 012602  
 33326 016674 012601  
 33327 016676 012600  
 33328 016700 012737 016562 000024  
 33329 016706 012737 000340 000026  
 33330 016714 104401  
 33331 016716 016734 \$PWRMG: .WORD \$POWER  
 33332 016720 012716 MOV (PC)+, (SP) ;; RESTART AT PWRST  
 33333 016722 002206 \$PWRAD: .WORD PWRST ;; RESTART ADDRESS  
 33334 016724 000002 RTI  
 33335 016726 000000 \$ILLUP: HALT ;; THE POWER UP SEQUENCE WAS STARTED  
 33336 016730 000776 BR .-2 ;; BEFORE THE POWER DOWN WAS COMPLETE  
 33337 016732 000000 \$SAVR6: 0 ;; PUT THE SP HERE  
 33338 016734 005015 047520 042527 \$POWER: .ASCIZ <15><12>"POWER"  
 33339 016742 000122 .EVEN

```
$PWRUP: MOV $ILLUP, @#PWRVEC ;; SET FOR FAST DOWN
 MOV $SAVR6, SP ;; GET SP
 CLR $SAVR6 ;; WAIT LOOP FOR THE TTY
 INC $SAVR6 ;; WAIT FOR THE INC
 BNE $S ;; OF WORD
 MOV (SP)+, @SWR ;; POP STACK INTO @SWR
 MOV (SP)+, R5 ;; POP STACK INTO R5
 MOV (SP)+, R4 ;; POP STACK INTO R4
 MOV (SP)+, R3 ;; POP STACK INTO R3
 MOV (SP)+, R2 ;; POP STACK INTO R2
 MOV (SP)+, R1 ;; POP STACK INTO R1
 MOV (SP)+, R0 ;; POP STACK INTO R0
 MOV #PWRDN, @#PWRVEC ;; SET UP THE POWER DOWN VECTOR
 MOV #340, @#PWRVEC+2 ;; PRIO:7
 TYPE $POWER ;; REPORT THE POWER FAILURE
 MOV (PC)+, (SP) ;; POWER FAIL MESSAGE POINTER
 $PWRAD: .WORD PWRST ;; RESTART ADDRESS
 RTI
 $ILLUP: HALT ;; THE POWER UP SEQUENCE WAS STARTED
 BR .-2 ;; BEFORE THE POWER DOWN WAS COMPLETE
 $SAVR6: 0 ;; PUT THE SP HERE
 $POWER: .ASCIZ <15><12>"POWER"
 .EVEN
```

|      |        |        |        |        |                |                                    |
|------|--------|--------|--------|--------|----------------|------------------------------------|
| 3330 | 016744 | 042200 | 044522 | 042526 | MSGDRV: .ASCIZ | <CRLF>"DRIVE(S)?"                  |
| 3331 | 016752 | 051450 | 037451 | 000040 |                |                                    |
| 3332 | 016760 | 005015 | 040524 | 051503 | MSGASK: .ASCIZ | <15><12>/TACS?/                    |
| 3333 | 016766 | 000077 |        |        |                |                                    |
| 3334 | 016770 | 042526 | 052103 | 051117 | MSGVEC: .ASCIZ | /VECTOR?/                          |
| 3335 | 016776 | 000077 |        |        |                |                                    |
| 3336 | 017000 | 051120 | 047511 | 044522 | MSGPRI: .ASCIZ | /PRIORITY?/                        |
| 3337 | 017006 | 054524 | 000077 |        |                |                                    |
| 3338 | 017012 | 040524 | 051503 | 000075 | MTACS: .ASCIZ  | /TACS=/                            |
| 3339 | 017020 | 052040 | 042101 | 036502 | MTADB: .ASCIZ  | / TADB=/                           |
| 3340 | 017026 | 000    |        |        |                |                                    |
| 3341 | 017027 | 040    | 042526 | 052103 | MTAVEC: .ASCIZ | / VECTOR=/                         |
| 3342 | 017034 | 051117 | 000075 |        |                |                                    |
| 3343 | 017040 | 050040 | 044522 | 051117 | MTAPRI: .ASCIZ | / PRIORITY=/                       |
| 3344 | 017046 | 052111 | 036531 | 000    |                |                                    |
| 3345 | 017053 | 015    | 047412 | 037513 | MSGOK: .ASCIZ  | <15><12>/OK?/                      |
| 3346 | 017060 | 000    |        |        |                |                                    |
| 3347 | 017061 | 131    | 051505 | 000200 | MYES: .ASCIZ   | /YES/<CRLF>                        |
| 3348 | 017066 | 052123 | 052101 | 051525 | EM1: .ASCIZ    | /STATUS PROBLEM/                   |
| 3349 | 017074 | 050040 | 047522 | 046102 |                |                                    |
| 3350 | 017102 | 046505 | 000    |        |                |                                    |
| 3351 | 017105 | 042    | 042522 | 042101 | EM2: .ASCIZ    | /"READY" FAILED TO SET/            |
| 3352 | 017112 | 021131 | 043040 | 044501 |                |                                    |
| 3353 | 017120 | 042514 | 020104 | 047524 |                |                                    |
| 3354 | 017126 | 051440 | 052105 | 000    |                |                                    |
| 3355 | 017133 | 042    | 051124 | 047101 | EM3: .ASCIZ    | /"TRANSFER REQUEST" FAILED TO SET/ |
| 3356 | 017140 | 043123 | 051105 | 051040 |                |                                    |
| 3357 | 017146 | 050505 | 042525 | 052123 |                |                                    |
| 3358 | 017154 | 020042 | 040506 | 046111 |                |                                    |
| 3359 | 017162 | 042105 | 052040 | 020117 |                |                                    |
| 3360 | 017170 | 042523 | 000124 |        |                |                                    |
| 3361 | 017174 | 044124 | 020105 | 051127 | EM4: .ASCIZ    | /THE WRONG FLAG SET/               |
| 3362 | 017202 | 047117 | 020107 | 046106 |                |                                    |
| 3363 | 017210 | 043501 | 051440 | 052105 |                |                                    |
| 3364 | 017216 | 000    |        |        |                |                                    |
| 3365 | 017217 | 104    | 052101 | 020101 | EM5: .ASCIZ    | /DATA PROBLEM/                     |
| 3366 | 017224 | 051120 | 041117 | 042514 |                |                                    |
| 3367 | 017232 | 000115 |        |        |                |                                    |
| 3368 | 017234 | 047111 | 042524 | 051122 | EM6: .ASCIZ    | /INTERRUPT PROBLEM/                |
| 3369 | 017242 | 050125 | 020124 | 051120 |                |                                    |
| 3370 | 017250 | 041117 | 042514 | 000115 |                |                                    |
| 3371 | 017256 | 041520 | 020040 | 020040 | DH1: .ASCIZ    | /PC TACS/                          |
| 3372 | 017264 | 020040 | 040524 | 051503 |                |                                    |
| 3373 | 017272 | 000    |        |        |                |                                    |
| 3374 | 017273 | 120    | 020103 | 020040 | DH2: .ASCIZ    | /PC TACS WAIT ADDRESS/             |
| 3375 | 017300 | 020040 | 052040 | 041501 |                |                                    |
| 3376 | 017306 | 020123 | 020040 | 053440 |                |                                    |
| 3377 | 017314 | 044501 | 020124 | 042101 |                |                                    |
| 3378 | 017322 | 051104 | 051505 | 000123 |                |                                    |
| 3379 | 017330 | 041520 | 020040 | 020040 | DH5: .ASCIZ    | /PC TACS EXPECT RCV'D/             |
| 3380 | 017336 | 020040 | 040524 | 051503 |                |                                    |
| 3381 | 017344 | 020040 | 020040 | 054105 |                |                                    |
| 3382 | 017352 | 042520 | 052103 | 020040 |                |                                    |
| 3383 | 017360 | 041522 | 023526 | 000104 |                |                                    |
| 3384 | 017366 | 041520 | 020040 | 020040 | DH6: .ASCIZ    | /PC TACS BR LEVEL/                 |
| 3385 | 017374 | 020040 | 040524 | 051503 |                |                                    |



TA11 BASIC LOGIC TEST (PART 2) MAINDEC-11-DZTAB-C  
DATA C.NEW POWER DOWN AND UP ROUTINES

|        |        |        |        |        |                                        |
|--------|--------|--------|--------|--------|----------------------------------------|
| 017402 | 020040 | 020040 | 051102 |        |                                        |
| 017410 | 046040 | 053105 | 046105 |        |                                        |
| 017416 | 000000 |        |        |        |                                        |
|        | 017420 |        |        | .EVEN  |                                        |
| 017420 | 001116 | 001162 | 000000 | DT1:   | .WORD \$ERRPC,\$REGO,0                 |
| 017426 | 001116 | 001162 | 001202 | DT2:   | .WORD \$ERRPC,\$REGO,\$AVPC,0          |
| 017434 | 000000 |        |        |        |                                        |
| 017436 | 001116 | 001162 | 001124 | DT5:   | .WORD \$ERRPC,\$REGO,\$GDDAT,\$BDDAT,0 |
| 017444 | 001126 | 000000 |        |        |                                        |
| 017450 | 001116 | 001162 | 001124 | DT6:   | .WORD \$ERRPC,\$REGO,\$GDDAT,0         |
| 017456 | 000000 |        |        |        |                                        |
| 017460 | 001116 | 001206 | 000000 | DT201: | .WORD \$ERRPC,TACSL,0                  |
|        |        |        |        |        |                                        |
| 017466 | 001116 | 000000 |        | DT202: | .WORD \$ERRPC,0                        |
|        |        |        |        |        |                                        |
| 017472 | 040524 | 030461 | 043040 | EM201: | .ASCIZ "TA11 FAILED TO RESPOND"        |
| 017500 | 044501 | 042514 | 020104 |        |                                        |
| 017506 | 047524 | 051040 | 051505 |        |                                        |
| 017514 | 047520 | 042116 | 000    |        |                                        |
| 017521 | 116    | 020117 | 051104 | EM202: | .ASCIZ "NO DRIVE AVAILABLE"            |
| 017526 | 053111 | 020105 | 053101 |        |                                        |
| 017534 | 044501 | 040514 | 046102 |        |                                        |
| 017542 | 000105 |        |        |        |                                        |
| 017544 | 041520 | 020040 | 020040 | DH201: | .ASCIZ /PC TACS/                       |
| 017552 | 020040 | 040524 | 051503 |        |                                        |
| 017560 | 000    |        |        |        |                                        |
| 017561 | 120    |        |        |        |                                        |
|        | 000001 | 000103 |        | DH202: | .ASCIZ /PC/                            |
|        |        |        |        |        | .END                                   |

|     |        |        |        |        |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
|-----|--------|--------|--------|--------|--|--|--|--|--|--|--|--|--|--|--|--|--|--|--|--|
| 0   | 000000 | 000000 | 000000 | 000000 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| 1   | 000000 | 000000 | 000000 | 000000 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| 2   | 000000 | 000000 | 000000 | 000000 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| 3   | 000000 | 000000 | 000000 | 000000 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| 4   | 000000 | 000000 | 000000 | 000000 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| 5   | 000000 | 000000 | 000000 | 000000 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| 6   | 000000 | 000000 | 000000 | 000000 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| 7   | 000000 | 000000 | 000000 | 000000 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| 8   | 000000 | 000000 | 000000 | 000000 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| 9   | 000000 | 000000 | 000000 | 000000 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| 10  | 000000 | 000000 | 000000 | 000000 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| 11  | 000000 | 000000 | 000000 | 000000 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| 12  | 000000 | 000000 | 000000 | 000000 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| 13  | 000000 | 000000 | 000000 | 000000 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| 14  | 000000 | 000000 | 000000 | 000000 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| 15  | 000000 | 000000 | 000000 | 000000 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| 16  | 000000 | 000000 | 000000 | 000000 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| 17  | 000000 | 000000 | 000000 | 000000 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| 18  | 000000 | 000000 | 000000 | 000000 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| 19  | 000000 | 000000 | 000000 | 000000 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| 20  | 000000 | 000000 | 000000 | 000000 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| 21  | 000000 | 000000 | 000000 | 000000 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| 22  | 000000 | 000000 | 000000 | 000000 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| 23  | 000000 | 000000 | 000000 | 000000 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| 24  | 000000 | 000000 | 000000 | 000000 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| 25  | 000000 | 000000 | 000000 | 000000 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| 26  | 000000 | 000000 | 000000 | 000000 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| 27  | 000000 | 000000 | 000000 | 000000 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| 28  | 000000 | 000000 | 000000 | 000000 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| 29  | 000000 | 000000 | 000000 | 000000 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| 30  | 000000 | 000000 | 000000 | 000000 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| 31  | 000000 | 000000 | 000000 | 000000 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| 32  | 000000 | 000000 | 000000 | 000000 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| 33  | 000000 | 000000 | 000000 | 000000 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| 34  | 000000 | 000000 | 000000 | 000000 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| 35  | 000000 | 000000 | 000000 | 000000 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| 36  | 000000 | 000000 | 000000 | 000000 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| 37  | 000000 | 000000 | 000000 | 000000 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| 38  | 000000 | 000000 | 000000 | 000000 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| 39  | 000000 | 000000 | 000000 | 000000 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| 40  | 000000 | 000000 | 000000 | 000000 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| 41  | 000000 | 000000 | 000000 | 000000 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| 42  | 000000 | 000000 | 000000 | 000000 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| 43  | 000000 | 000000 | 000000 | 000000 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| 44  | 000000 | 000000 | 000000 | 000000 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| 45  | 000000 | 000000 | 000000 | 000000 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| 46  | 000000 | 000000 | 000000 | 000000 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| 47  | 000000 | 000000 | 000000 | 000000 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| 48  | 000000 | 000000 | 000000 | 000000 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| 49  | 000000 | 000000 | 000000 | 000000 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| 50  | 000000 | 000000 | 000000 | 000000 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| 51  | 000000 | 000000 | 000000 | 000000 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| 52  | 000000 | 000000 | 000000 | 000000 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| 53  | 000000 | 000000 | 000000 | 000000 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| 54  | 000000 | 000000 | 000000 | 000000 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| 55  | 000000 | 000000 | 000000 | 000000 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| 56  | 000000 | 000000 | 000000 | 000000 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| 57  | 000000 | 000000 | 000000 | 000000 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| 58  | 000000 | 000000 | 000000 | 000000 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| 59  | 000000 | 000000 | 000000 | 000000 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| 60  | 000000 | 000000 | 000000 | 000000 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| 61  | 000000 | 000000 | 000000 | 000000 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| 62  | 000000 | 000000 | 000000 | 000000 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| 63  | 000000 | 000000 | 000000 | 000000 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| 64  | 000000 | 000000 | 000000 | 000000 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| 65  | 000000 | 000000 | 000000 | 000000 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| 66  | 000000 | 000000 | 000000 | 000000 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| 67  | 000000 | 000000 | 000000 | 000000 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| 68  | 000000 | 000000 | 000000 | 000000 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| 69  | 000000 | 000000 | 000000 | 000000 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| 70  | 000000 | 000000 | 000000 | 000000 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| 71  | 000000 | 000000 | 000000 | 000000 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| 72  | 000000 | 000000 | 000000 | 000000 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| 73  | 000000 | 000000 | 000000 | 000000 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| 74  | 000000 | 000000 | 000000 | 000000 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| 75  | 000000 | 000000 | 000000 | 000000 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| 76  | 000000 | 000000 | 000000 | 000000 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| 77  | 000000 | 000000 | 000000 | 000000 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| 78  | 000000 | 000000 | 000000 | 000000 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| 79  | 000000 | 000000 | 000000 | 000000 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| 80  | 000000 | 000000 | 000000 | 000000 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| 81  | 000000 | 000000 | 000000 | 000000 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| 82  | 000000 | 000000 | 000000 | 000000 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| 83  | 000000 | 000000 | 000000 | 000000 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| 84  | 000000 | 000000 | 000000 | 000000 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| 85  | 000000 | 000000 | 000000 | 000000 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| 86  | 000000 | 000000 | 000000 | 000000 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| 87  | 000000 | 000000 | 000000 | 000000 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| 88  | 000000 | 000000 | 000000 | 000000 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| 89  | 000000 | 000000 | 000000 | 000000 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| 90  | 000000 | 000000 | 000000 | 000000 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| 91  | 000000 | 000000 | 000000 | 000000 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| 92  | 000000 | 000000 | 000000 | 000000 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| 93  | 000000 | 000000 | 000000 | 000000 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| 94  | 000000 | 000000 | 000000 | 000000 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| 95  | 000000 | 000000 | 000000 | 000000 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| 96  | 000000 | 000000 | 000000 | 000000 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| 97  | 000000 | 000000 | 000000 | 000000 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| 98  | 000000 | 000000 | 000000 | 000000 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| 99  | 000000 | 000000 | 000000 | 000000 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| 100 | 000000 | 000000 | 000000 | 000000 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| 101 | 000000 | 000000 | 000000 |        |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |











TA11 BASIC LOGIC TEST (PART 2) MAINDEC-11-DZTAB-C  
DZTABC.NEW CROSS REFERENCE TABLE -- USER SYMBOLS

|         |        |       |       |       |       |       |      |       |      |      |      |      |      |       |  |  |  |  |
|---------|--------|-------|-------|-------|-------|-------|------|-------|------|------|------|------|------|-------|--|--|--|--|
| TRAPVE= | 000034 | 177#  | 501*  | 502*  |       |       |      |       |      |      |      |      |      |       |  |  |  |  |
| TRTVEC= | 000014 | 172#  |       |       |       |       |      |       |      |      |      |      |      |       |  |  |  |  |
| TR.REQ= | 000200 | 201#  | 1336  |       |       |       |      |       |      |      |      |      |      |       |  |  |  |  |
| TST1    | 002436 | 691   | 704#  |       |       |       |      |       |      |      |      |      |      |       |  |  |  |  |
| TST10   | 004150 | 1053# |       |       |       |       |      |       |      |      |      |      |      |       |  |  |  |  |
| TST11   | 004352 | 1101# |       |       |       |       |      |       |      |      |      |      |      |       |  |  |  |  |
| TST12   | 004554 | 1149# |       |       |       |       |      |       |      |      |      |      |      |       |  |  |  |  |
| TST13   | 004756 | 1197# |       |       |       |       |      |       |      |      |      |      |      |       |  |  |  |  |
| TST14   | 005160 | 1245# |       |       |       |       |      |       |      |      |      |      |      |       |  |  |  |  |
| TST15   | 005362 | 1286# |       |       |       |       |      |       |      |      |      |      |      |       |  |  |  |  |
| TST16   | 005554 | 1322# |       |       |       |       |      |       |      |      |      |      |      |       |  |  |  |  |
| TST17   | 005750 | 1360# |       |       |       |       |      |       |      |      |      |      |      |       |  |  |  |  |
| TST2    | 002516 | 707   | 725#  |       |       |       |      |       |      |      |      |      |      |       |  |  |  |  |
| TST20   | 006246 | 1431# |       |       |       |       |      |       |      |      |      |      |      |       |  |  |  |  |
| TST21   | 006370 | 1434  | 1456  | 1461# |       |       |      |       |      |      |      |      |      |       |  |  |  |  |
| TST22   | 006510 | 1464  | 1484  | 1489# |       |       |      |       |      |      |      |      |      |       |  |  |  |  |
| TST23   | 006622 | 1492  | 1511  | 1516# |       |       |      |       |      |      |      |      |      |       |  |  |  |  |
| TST24   | 006742 | 1519  | 1539  | 1548# |       |       |      |       |      |      |      |      |      |       |  |  |  |  |
| TST25   | 007050 | 1551  | 1568  | 1573# |       |       |      |       |      |      |      |      |      |       |  |  |  |  |
| TST26   | 007176 | 1576  | 1599  | 1604# |       |       |      |       |      |      |      |      |      |       |  |  |  |  |
| TST27   | 007324 | 1607  | 1630  | 1635# |       |       |      |       |      |      |      |      |      |       |  |  |  |  |
| TST3    | 002656 | 728   | 760   | 765#  |       |       |      |       |      |      |      |      |      |       |  |  |  |  |
| TST30   | 007574 | 1638  | 1694  | 1703# |       |       |      |       |      |      |      |      |      |       |  |  |  |  |
| TST31   | 010000 | 1706  | 1745  | 1750# |       |       |      |       |      |      |      |      |      |       |  |  |  |  |
| TST32   | 010172 | 1753  | 1789  | 1798# |       |       |      |       |      |      |      |      |      |       |  |  |  |  |
| TST33   | 010330 | 1801  | 1833# |       |       |       |      |       |      |      |      |      |      |       |  |  |  |  |
| TST34   | 010516 | 1836  | 1874  | 1883# |       |       |      |       |      |      |      |      |      |       |  |  |  |  |
| TST35   | 010776 | 1886  | 1946  | 1951# |       |       |      |       |      |      |      |      |      |       |  |  |  |  |
| TST36   | 011160 | 1954  | 1993  | 1998# |       |       |      |       |      |      |      |      |      |       |  |  |  |  |
| TST37   | 011412 | 2001  | 2053  | 2071# |       |       |      |       |      |      |      |      |      |       |  |  |  |  |
| TST4    | 003062 | 768   | 812   | 817#  |       |       |      |       |      |      |      |      |      |       |  |  |  |  |
| TST40   | 011552 | 2074  | 2076  | 2124# |       |       |      |       |      |      |      |      |      |       |  |  |  |  |
| TST41   | 012134 | 2127  | 2129  | 2221  | 2226# |       |      |       |      |      |      |      |      |       |  |  |  |  |
| TST5    | 003342 | 820   | 882   | 909#  |       |       |      |       |      |      |      |      |      |       |  |  |  |  |
| TST6    | 003544 | 957#  |       |       |       |       |      |       |      |      |      |      |      |       |  |  |  |  |
| TST7    | 003746 | 1005# |       |       |       |       |      |       |      |      |      |      |      |       |  |  |  |  |
| TYPE =  | 104401 | 533   | 653   | 670   | 675   | 676   | 2254 | 2356  | 2364 | 2386 | 2395 | 2397 | 2400 | 2402  |  |  |  |  |
|         |        | 2406  | 2414  | 2480  | 2502  | 2513  | 2527 | 2536  | 2548 | 2551 | 2554 | 2557 | 2560 | 2567  |  |  |  |  |
|         |        | 2569  | 2959  | 3054  | 3055  | 3058  | 3071 | 3082  | 3101 | 3150 | 3153 | 3157 | 3232 | 3280# |  |  |  |  |
|         |        | 3327  |       |       |       |       |      |       |      |      |      |      |      |       |  |  |  |  |
| TYPERR  | 012730 | 2363  | 2386# |       |       |       |      |       |      |      |      |      |      |       |  |  |  |  |
| TYPOC = | 104402 | 2411  | 2550  | 2553  | 2556  | 2559  | 3057 | 3281# |      |      |      |      |      |       |  |  |  |  |
| TYPON = | 104404 | 3283# |       |       |       |       |      |       |      |      |      |      |      |       |  |  |  |  |
| TYPOS = | 104403 | 3282# |       |       |       |       |      |       |      |      |      |      |      |       |  |  |  |  |
| UNIT =  | 000400 | 200#  | 668   | 2079  | 2080  | 2132  | 2133 | 2887  | 2907 |      |      |      |      |       |  |  |  |  |
| WAITRE= | 104412 | 711   | 713   | 732   | 755   | 772   | 781  | 783   | 807  | 824  | 826  | 845  | 1293 | 1296  |  |  |  |  |
|         |        | 1330  | 1367  | 1369  | 1409  | 1438  | 1440 | 1446  | 1448 | 1468 | 1470 | 1476 | 1478 | 1480  |  |  |  |  |
|         |        | 1482  | 1496  | 1498  | 1504  | 1506  | 1523 | 1525  | 1531 | 1533 | 1535 | 1537 | 1555 | 1561  |  |  |  |  |
|         |        | 1563  | 1580  | 1586  | 1588  | 1590  | 1592 | 1594  | 1611 | 1617 | 1619 | 1621 | 1623 | 1625  |  |  |  |  |
|         |        | 1641  | 1643  | 1645  | 1653  | 1655  | 1657 | 1659  | 1709 | 1711 | 1713 | 1721 | 1723 | 1725  |  |  |  |  |
|         |        | 1727  | 1732  | 1740  | 1756  | 1758  | 1760 | 1768  | 1770 | 1772 | 1774 | 1779 | 1784 | 1805  |  |  |  |  |
|         |        | 1813  | 1815  | 1829  | 1840  | 1842  | 1855 | 1860  | 1872 | 1890 | 1892 | 1901 | 1903 | 1912  |  |  |  |  |
|         |        | 1958  | 1966  | 1968  | 1988  | 2005  | 2007 | 2020  | 2025 | 2047 | 2083 | 2085 | 2105 | 2137  |  |  |  |  |
|         |        | 2139  | 2141  | 2159  | 2164  | 2182  | 2187 | 2192  | 2204 | 2219 | 2231 | 2640 | 2726 | 2757  |  |  |  |  |
|         |        | 2782  | 2798  | 2851  | 2872  | 3291# |      |       |      |      |      |      |      |       |  |  |  |  |
| WAITXF= | 104413 | 734   | 775   | 777   | 785   | 828   | 1304 | 1372  | 1442 | 1444 | 1472 | 1474 | 1500 | 1502  |  |  |  |  |











| SYMBOL | ADDRESS  | DATA | DATA | DATA | DATA | DATA | DATA | DATA | DATA | DATA | DATA | DATA | DATA | DATA | DATA |
|--------|----------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|
| STRFB  | 001166   |      |      |      |      |      |      |      |      |      |      |      |      |      |      |
| STRFB  | = 001146 |      |      |      |      |      |      |      |      |      |      |      |      |      |      |
| STRFB  | = 001146 |      |      |      |      |      |      |      |      |      |      |      |      |      |      |
| STRFB  | = 000046 |      |      |      |      |      |      |      |      |      |      |      |      |      |      |
| STRFB  | 001150   |      |      |      |      |      |      |      |      |      |      |      |      |      |      |
| STRFB  | 001167   |      |      |      |      |      |      |      |      |      |      |      |      |      |      |
| STRFB  | 001160   |      |      |      |      |      |      |      |      |      |      |      |      |      |      |
| STRFB  | 016476   |      |      |      |      |      |      |      |      |      |      |      |      |      |      |
| STRFB  | 016520   |      |      |      |      |      |      |      |      |      |      |      |      |      |      |
| STRFB  | = 000014 |      |      |      |      |      |      |      |      |      |      |      |      |      |      |
| STRPAD | 016532   |      |      |      |      |      |      |      |      |      |      |      |      |      |      |
| WTSTNM | 001102   |      |      |      |      |      |      |      |      |      |      |      |      |      |      |
| WTYYIN | 016204   |      |      |      |      |      |      |      |      |      |      |      |      |      |      |
| WTYPEN | *****    |      |      |      |      |      |      |      |      |      |      |      |      |      |      |
| WTYPOS | *****    |      |      |      |      |      |      |      |      |      |      |      |      |      |      |
| WTYPC  | 015152   |      |      |      |      |      |      |      |      |      |      |      |      |      |      |
| WTYPC  | 015322   |      |      |      |      |      |      |      |      |      |      |      |      |      |      |
| WTYPC  | 015370   |      |      |      |      |      |      |      |      |      |      |      |      |      |      |
| WTYPC  | 016274   |      |      |      |      |      |      |      |      |      |      |      |      |      |      |
| WTYPC  | 016310   |      |      |      |      |      |      |      |      |      |      |      |      |      |      |
| WTYPC  | 016250   |      |      |      |      |      |      |      |      |      |      |      |      |      |      |
| EXTSTR | 012276   |      |      |      |      |      |      |      |      |      |      |      |      |      |      |
| SSGET4 | = 000000 |      |      |      |      |      |      |      |      |      |      |      |      |      |      |
| SOFILL | 016473   |      |      |      |      |      |      |      |      |      |      |      |      |      |      |
| SOCAT  | = *****  |      |      |      |      |      |      |      |      |      |      |      |      |      |      |
|        | = 017564 |      |      |      |      |      |      |      |      |      |      |      |      |      |      |

ERRORS DETECTED: 0  
DEFAULT GLOBALS GENERATED: 0

\*DZTAB, DZTAB/SOL/CRF: SYM=DSKM: SYSMAC.SML(400, 1066), DSKZ: DZTAB.C.NEW(400, 1237)  
RUN-TIME: 45 58 2 SECONDS  
RUN-TIME RATIO: 125/107=1.1  
CORE USED: 34K (68 PAGES)

