

RH11-RP04

MASSBUS INTERFACE/DCL
MD-11-DZRPS-D

EP-DZRPS-D-DL-B

SEP 1975

Copyright © 1975

digital

FICHE 1 OF 2

Made In U.S.A.

DZRPSD
SEQ

This microfiche sheet contains 100 individual frames of technical data. The top-left frame is labeled 'DZRPSD SEQ'. The frames contain a variety of content, including:

- Code listings: Blocks of alphanumeric characters, likely assembly or machine code.
- Tables: Grids of data with headers and rows of values.
- Diagrams: Schematic-like drawings with lines and labels.
- Textual descriptions: Short paragraphs of technical notes or comments.

The frames are arranged in a 10x10 grid, with each frame separated by a thin white border.

RH11-RP04

MASSBUS INTERFACE/DCL
MD-11-DZRPS-D

EP-DZRPS-D-DL-B

SEP 1975

Copyright © 1975

digital

FICHE 2 OF 2

Made In U.S.A.

This microfiche contains 100 individual frames of technical data. The frames are arranged in a grid of 10 rows and 10 columns. Each frame contains a page of text, likely code or technical specifications, which is too small to read clearly. The text is printed in white on a dark background. The frames are separated by thin white lines. The overall appearance is that of a standard microfiche card.

11 12 13 14 15 16 17 18 19 20
21 22 23 24 25 26 27 28 29 30
31 32 33 34 35 36 37 38 39 40
41 42 43 44 45 46 47 48 49 50
51 52 53 54 55 56 57 58 59 60
61 62 63 64 65 66 67 68 69 70
71 72 73 74 75 76 77 78 79 80
81 82 83 84 85 86 87 88 89 90
91 92 93 94 95 96 97 98 99 100

IDENTIFICATION

PRODUCT CODE: MAINDEC-11-DZRPS-D-D
PRODUCT NAME: RJPO4 DISKLESS CONTROLLER TEST-PART I (STATIC 1A)
DATE CREATED: OCT 1974, MAR 1975, MAY 1975, AUG 1975
MAINTAINER: DIAGNOSTIC ENGINEERING
AUTHOR: SUB MALLICK, PETE BLACKSTONE

THE INFORMATION IN THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE AND SHOULD NOT BE CONSTRUED AS A COMMITMENT BY DIGITAL EQUIPMENT CORPORATION. DIGITAL EQUIPMENT CORPORATION ASSUMES NO RESPONSIBILITY FOR ANY ERRORS THAT MAY APPEAR IN THIS MANUAL.

THE SOFTWARE DESCRIBED IN THIS DOCUMENT IS FURNISHED TO THE PURCHASER UNDER A LICENSE FOR USE ON A SINGLE COMPUTER SYSTEM AND CAN BE COPIED (WITH INCLUSION OF DIGITAL'S COPYRIGHT NOTICE) ONLY FOR USE IN SUCH SYSTEM, EXCEPT AS MAY OTHERWISE BE PROVIDED IN WRITING BY DIGITAL.

DIGITAL EQUIPMENT CORPORATION ASSUMES NO RESPONSIBILITY FOR THE USE OR RELIABILITY OF ITS SOFTWARE ON EQUIPMENT THAT IS NOT SUPPLIED BY DIGITAL.

COPYRIGHT (C) 1974, 1975 DIGITAL EQUIPMENT CORPORATION

CONTENTS

1. ABSTRACT
2. REQUIREMENTS
 - 2.1 EQUIPMENT
 - 2.2 STORAGE
 - 2.3 PRELIMINARY PROGRAMS
3. LOADING PROCEDURE
 - 3.1 METHOD
4. STARTING PROCEDURE
 - 4.1 CONTROL SWITCH SETTINGS
 - 4.2 STARTING ADDRESS OR ADDRESSES
 - 4.3 PROGRAM AND/OR OPERATOR ACTION
5. OPERATING PROCEDURE
 - 5.1 OPERATIONAL SWITCH SETTINGS
 - 5.2 SUB-ROUTINE ABSTRACTS
6. ERRORS
 - 6.1 'FATAL' ERRORS
7. RESTRICTIONS
8. MISCELLANEOUS
 - 8.1 EXECUTION TIME
 - 8.2 STACK POINTER
 - 8.3 OPERATOR SELECTABLE SCOPE LOOPS
 - 8.4 PROGRAM REVISION HISTORY
- 9.0 PROGRAM DESCRIPTION

1.0 ABSTRACT

THIS DIAGNOSTIC TESTS THE RH11 AND DCL OF AN RPO4 SUBSYSTEM. IT DOES NOT USE THE DISK SURFACE OR ANY SIGNALS FROM THE MDLI. IT REQUIRES THAT THE DCL CABLE BE PLUGGED INTO THE MDLI OR BE APPROPRIATELY TERMINATED. IF THE DISK IS POWERED UP, IT IS REQUIRED TO GET THE DISK TO THE "HEADS UNLOADED" POSITION. AFTER A SUCCESSFUL RUN (WITH NO ERRORS) OF THIS DIAGNOSTIC IT CAN BE ASSERTED THAT, "THAT PART OF THE DCL THAT HANDLES DATA OR DATA ASSOCIATED LOGIC IS WORKING PROPERLY". THIS IMPLIES THAT, THAT PART OF THE LOGIC WHICH HANDLES MECHANICAL COMMANDS OR ITS ASSOCIATED LOGIC IS NOT TESTED IN THIS DIAGNOSTIC. ALL DATA COMMANDS USE THE MAINTENANCE REGISTER IN THE MRAPAROUND MODE.

2.0 REQUIREMENTS

2.1 EQUIPMENT

PDP-11 COMPUTER WITH CONSOLE TELETYPE, AND A RPO4 DISK SYSTEM. THE RPO4 DISK SYSTEM WILL CONSIST OF AN RH11 CONTROLLER, A DISK CONTROL LOGIC (DCL). THE CABLE FROM THE DCL CAN BE CONNECTED TO THE MDLI BUT IF NOT THAT CABLE MUST BE PROPERLY TERMINATED.

2.2 STORAGE

THIS PROGRAM REQUIRES 16K WORDS OF MEMORY.

2.3 PRELIMINARY PROGRAMS

NONE
THIS CAN BE THE FIRST PROGRAM RUN ON THE SYSTEM.

3.0 LOADING PROCEDURE

USE STANDARD PROCEDURE FOR LOADING .ABS TAPES

4.0 STARTING PROCEDURE

4.1 CONTROL SWITCH SETTINGS

SEE SECTION 5.1

4.2 STARTING ADDRESS

START AT ADDRESS 200---FOR NORMAL RUN
START AT ADDRESS 210---FOR UNIT SELECTION

200 START
ALL SWITCHES MUST BE DOWN FOR WORST CASE RUN. WITH THIS STARTING ADDRESS ALL THE RPO4S ON THE SYSTEM WILL BE TESTED ONE AT A TIME BEFORE "END PASS" IS PRINTED OUT. TESTING WILL START WITH THE LOWEST UNIT NUMBER DRIVE THAT IS POWERED UP (THAT IS THE LOWEST UNIT NUMBER RHAS REGISTER THAT RESPONDS) THEN GO ON TO THE NEXT HIGHER UNIT NUMBER THAT IS POWERED UP.

210 START
ALL SWITCHES MUST BE DOWN FOR WORST CASE RUN. WITH THIS STARTING ADDRESS THE CONSOLE TELETYPE WILL ASK FOR THE UNIT

NUMBER TO BE TESTED. THEN ONLY THAT UNIT WILL BE TESTED **F01**
FOR EACH PASS OF THE PROGRAM.

4.3 PROGRAM AND/OR OPERATOR ACTION

1. LOAD THE PROGRAM INTO MEMORY.
2. SET STARTING ADDRESS ON THE SWITCH REGISTER
3. PRESS "LOAD ADDRESS".
4. SET "OPERATIONAL SWITCH SETTINGS" (SEE SECTION 5.1)
WORST CASE IS ALL SWITCHES DOWN.
5. PRESS "START".
6. FOR THE FIRST PASS EACH TEST WILL BE EXECUTED ONCE
ON THE DRIVES PRESENT OR DRIVE SELECTED BEFORE "END
PASS" IS PRINTED. THE FIRST PASS WILL REQUIRE OPERATOR
INTERVENTION IF THE PROGRAM IS NOT RUN UNDER AN "ACT-11"
MONITOR. THE SECOND AND SUBSEQUENT PASSES WILL EXECUTE
EACH TEST FOUR TIMES ON EACH DRIVES PRESENT OR DRIVE
SELECTED BEFORE "END PASS" IS PRINTED. THE SECOND
AND SUBSEQUENT PASSED DO NOT NEED ANY OPERATOR INTERVENTION.

5.0 OPERATING PROCEDURE

5.1 OPERATIONAL SWITCH SETTINGS

SWITCH DEFINITIONS ARE GIVEN IN SECTION 9 "OPERATIONAL
SWITCH SETTINGS" HOWEVER THE DETAIL DESCRIPTION ARE GIVEN
HERE.

SWITCH 15 - HALT ON ERROR

WHEN THIS SWITCH IS SET, IF THE PROGRAM FINDS AN ERROR
THEN THE APPROPRIATE INFORMATION WILL BE PRINTED OUT
AND THEN THE PROGRAM WILL HALT. AFTER THIS HALT, PRESSING
"CONTINUE" WILL CONTINUE WITH THE PROGRAM TILL THE NEXT
ERROR IS FOUND WHEN THE SAME THING WILL HAPPEN.

SWITCH 14 - LOOP ON TEST

WHEN THIS SWITCH IS SET THE PROGRAM WILL BEGIN TO LOOP
ON THE CURRENT TEST BEING EXECUTED. FOR EXAMPLE IF THIS
SWITCH IS SET WHEN THE PROGRAM IS IN TEST 10 THEN THE
PROGRAM WILL KEEP EXECUTING ALL OF TEST 10 REPEATEDLY.
ONE WAY TO BE SURE THAT THE PROGRAM IS IN THE EXPECTED
TEST IS TO SET THIS SWITCH DURING AN ERROR PRINTOUT OR
DURING A PROGRAM HALT.

SWITCH 13 - INHIBIT ERROR TYPEOUTS

WHEN THIS SWITCH IS SET FURTHER ERROR PRINTOUTS WILL
CEASE. HOWEVER OPERATOR INSTRUCTIONS SUCH AS "STOP DRIVE X"
WILL CONTINUE. AT THE END OF PASS "TOTAL NUMBER OF ERRORS
ON THIS PASS ON DRIVE X" WILL BE TRUE, THAT IS, ALTHOUGH
PRINTOUTS WERE INHIBITED IF THAT PASS FOUND 6 ERRORS,
IT WILL SAY SO.

SWITCH 11 - INHIBIT ITERATIONS

WHEN THIS SWITCH IS SET THE PROGRAM ON SECOND PASS WILL
NOT REPEAT EACH TEST FOUR TIMES BUT WILL DO EACH TEST
ONCE ONLY.

SWITCH 10 - BELL ON ERROR

GO1
WHEN THIS SWITCH IS SET, IF THE PROGRAM FINDS AN ERROR THE "BELL" OR "ALARM" WILL BE SOUNDED. THIS SWITCH IS USEFUL WHEN SWITCH 11 IS SET YET INFORMATION IS NEEDED WHEN ANY ERROR IS DETECTED. TAKE THE EXAMPLE OF A PROGRAM LOOPING ON A TEST WITH SWITCH 11 SET TO HELP SCOPING. THEN IF THIS SWITCH IS SET AND THE BELL OR ALARM SOUNDS IT MEANS THAT THE ERROR IS PRESENT BUT IF THE BELL OR ALARM STOPS IT MEANS THAT THE ERROR IS NOT PRESENT.

SWITCH 9 - LOOP ON ERROR
WHEN THIS SWITCH IS SET, IF THE PROGRAM FINDS AN ERROR THEN GENERALLY THE PROGRAM WILL LOOP BACK TO THE LAST EXECUTED "SCOPE" STATEMENT. IF ON THE SECOND TIME THROUGH AN ERROR IS FOUND IT WILL AGAIN LOOP BACK TO THAT "SCOPE" STATEMENT. THIS LOOPING WILL CONTINUE AS LONG AS THE ERROR IS PRESENT AND THIS SWITCH IS SET. HOWEVER IF THE ERROR IS NOT PRESENT AT ANY TIME THEN IT WILL CONTINUE NORMALLY WITH THE PROGRAM. EACH TIME THE ERROR IS ENCOUNTERED PRINTOUT WILL TAKE PLACE UNLESS SWITCH 11 IS ALSO SET. DURING BEGUG, USING A SCOPE, IT IS RECOMMENDED THAT SWITCH 11 IS ALSO SET.

NOTE: ALSO SEE SECTION 8.3

SWITCH 8 - LOOP ON TEST IN SMR (<7:0>
THIS IS A SPECIAL SWITCH. WHEN SET SWITCHES 0 THRU 7 HAVE ONE MEANING AND WHEN RESET SWITCHES 0 THRU 7 HAVE ANOTHER MEANING. THIS MEANS THAT ANY SETTING OF SWITCH 0 THRU 7 MUST BE DONE WITH SWITCH 8 IN THE APPROPRIATE POSITION. WHEN THIS SWITCH IS SET THEN SWITCHES 0 THRU 7 GIVE THE TEST NUMBER TO BE LOOPED ON. FOR EXAMPLE WITH SWITCH 8 SET AND SWITCH 3 SET THE PROGRAM WILL LOOP ON TEST 10. HOWEVER THIS SETTING MUST BE DONE AT THE BEGINNING OF THE PROGRAM THEN ALL THE TESTS FROM 1 TO 10 WILL BE EXECUTED AND THEN TEST 10 WILL BE REPEATED OVER AND OVER AGAIN. WHEN THIS SWITCH IS NOT SET THEN SWITCHES 0 THRU 7 HAVE THE MEANING ITS NAME INDICATES. FOR EXAMPLE SWITCH 7 IS "STOP FURTHER COMPARES: THAT IS IF SWITCH 8 IS NOT SET AND SWITCH 7 IS SET THEN WHEN A DATA ERROR IS DETECTED NO FURTHER COMPARES WILL BE DONE. FOR EXAMPLE IN A 256 WORD BUFFER IF ALL THE WORDS ARE IN ERROR THEN AFTER SEEING THE PRINTOUT FOR THE FIRST FEW WORDS SETTING SWITCH 7 ONLY WILL STOP FURTHER PRINTOUTS OF THIS ERROR AND GO ON WITH THE TEST RATHER THAN PRINT ALL THE 256 WORDS. HOWEVER IF THIS WAS DONE WITH SWITCH 11 THEN THE NEXT ERROR THAT THE PROGRAM DETECTS IN A SUBSEQUENT TEST WILL ALSO BE LOST. BUT WITH SWITCH 7, ONLY THIS GROUP OF DATA ERRORS ARE NOT PRINTED OUT. ANOTHER EXAMPLE OF SWITCH 8 BEING LOW IS WITH SWITCH 6, WHICH IS "ECC TEST-COMPARE END RESULT ONLY". THAT IS IF SWITCH 8 IS NOT SET AND SWITCH 6 IS SET THEN ON ECC TESTS (TEST 120 THRU TEST 134) INSTEAD OF COMPARING CONTENTS OF THE POSITION REGISTER AND PATTERN REGISTER AFTER EVERY CLOCK, COMPARES WILL ONLY BE DONE AT THE END OF ALL THE CLOCKS.

NOTE: ALSO SEE SECTION 8.3

SEQ 0006

SWITCH 7 - STOP FURTHER COMPARES IF SMOB IS LOW.
 IF SWITCH 8 IS SET AND THIS SWITCH IS ALSO SET THEN THIS SWITCH GIVES THE TEST NUMBER TO BE LOOPED ON AS INDICATED IN THE DESCRIPTION OF SWITCH 8. IF SWITCH 8 IS NOT SET AND THIS SWITCH IS SET THEN THE PROGRAM WILL DO AS THE NAME INDICATES. FOR EXAMPLE IN A 256 WORD BUFFER IF ALL THE WORDS ARE IN ERROR THEN AFTER SEEING THE ERROR PRINTOUTS FOR THE FIRST FEW WORDS THEN SETTING SWITCH 7 WITH SWITCH 8 NOT SET WILL STOP THE PRINTOUT OF ALL 256 WORDS BUT WILL NOT STOP THE PRINTOUT OF ANOTHER ERROR IN ANY SUBSEQUENT TEST. IT IS EXPECTED THAT SWITCH 7 AFTER BEING SET FOR A WHILE TO STOP PRINTING ALL THE 256 WORDS WILL BE RESET AGAIN TO ENABLE THE PRINTING OF OTHER DATA ERRORS.

SWITCH 6 - ECC TEST-COMPARE END RESULTS ONLY IF SMOB IS LOW
 IF SWITCH 8 IS SET AND THIS SWITCH IS ALSO SET THEN THIS SWITCH GIVES THE TEST NUMBER TO BE LOOPED ON AS INDICATED IN THE DESCRIPTION OF SWITCH 8. IF SWITCH 8 IS NOT SET AND THIS SWITCH IS SET THEN ON ECC TESTS (TEST 120 THRU TEST 134) INSTEAD OF COMPARING CONTENTS OF THE POSITION AND PATTERN REGISTERS AFTER EVERY CLOCK, COMPARES WILL BE DONE ONLY AT THE END OF ALL THE CLOCKS.

5.2 SUB-ROUTINE ABSTRACTS

SEE SECTION 9 "SUBROUTINES"

6.0 ERRORS

ERROR PRINTOUTS CONTAIN THE ERROR ADDRESS AND OTHER PERTINENT INFORMATION CONCERNING THE PARTICULAR FAILURE. THIS INFORMATION MAY BE THE CONTENTS OF RELEVANT RPO4 REGISTERS OR GOOD/RECEIVED DATA. IF THE ERROR OCCURRED IN A SUBROUTINE, THE ADDRESS OF THE SUBROUTINE CALL IS ALSO GIVEN. REFER TO THE PROGRAM LISTING AT THE STATED ADDRESS TO DETERMINE THE CAUSE OF THE ERROR.

6.1 'FATAL' ERRORS

IN THE EVENT THAT THE DISK DRIVE BECOMES UNAVAILABLE TO THE CONTROLLER, POWERS DOWN, OR CERTAIN CRITICAL STATUS BITS CANNOT BE CLEARED PRIOR TO THE START OF A TEST SEQUENCE - THIS INFORMATION WILL BE COMMUNICATED TO THE OPERATOR. IN ADDITION THE TTY BELL WILL RING AND THE PROGRAM WILL HALT.

IT IS SUGGESTED THAT IF THIS HAPPENS THE OPERATOR LOAD ADDRESS 200 (210) AND RESTART THE PROGRAM AS A FIRST ATTEMPT TO SOLVE THE PROBLEM. IF THE FAILURE CONTINUES TO OCCUR, LOOK IN THE TEST LISTING FOR THE 'HALT' INSTRUCTION AND REPLACE IT PLUS THE TWO WORDS ("TYPE CPHALT") ABOVE WITH 'NOP'S. WITH TTY ERROR PRINTOUTS INHIBITED A SCOPE LOOP CAN BE INITIATED FOR THE TEST IN QUESTION.

IT IS ALSO POSSIBLE TO CONTINUE FROM THE HALT POINT, BUT IT IS NOT RECOMMENDED AS ALL FOLLOWING TESTS WILL EXHIBIT THE SAME SYMPTOMS AND GIVE MISLEADING ERROR PRINTOUTS.

7.0 RESTRICTIONS

IF THERE IS A DRIVE CONNECTED THEN THE OPERATOR MUST HAVE THE DRIVE PORT SWITCH LOCKED EITHER ON PORT A OR PORT B BUT NEVER LEAVE IT IN THE PROGRAMMABLE STATE. IF THERE

IS NO DRIVE CONNECTED THEN THE CABLE NORMALLY GOING FROM **IO1**
THE DCL TO THE MDLI MUST BE PROPERLY TERMINATED.

B.0 MISCELLANEOUS

B.1 EXECUTION TIME

THE FIRST PASS OF THE PROGRAM WILL TAKE 30 SECONDS PER
DRIVE. SUBSEQUENT PASSES WILL TAKE 1 MINUTE.

B.2 STACK POINTER

THE STACK IS INITIALLY SET TO 1000

B.3 OPERATOR SELECTABLE SCOPE LOOPS

HERE IS A DETAILED EXPLANATION OF HOW THE LOOP ON ERROR WORKS.
FOR INSTRUCTIONS REGARDING USAGE OF THIS TECHNIQUE, HIT FC ANY
TIME WHILE THE PROGRAM IS RUNNING. ON HITTING AN ERROR IF THE
LOOP ON ERROR SWITCH IS SET, THE PROGRAM GOES BACK - USUALLY
BACK TO THE BEGINNING OF THE TEST.

WHEN THIS OPERATOR SELECTABLE SCOPE LOOP IS USED THEN THE POINT
THE PROGRAM GOES BACK TO CAN BE CHANGED.

THE RESTRICTIONS TO THE POINT WHERE THE PROGRAM CAN GO ARE: -

1. IT MUST BE WITHIN THE TEST UNDER CONSIDERATION
 2. LOOP ON ERROR SWITCH MUST BE SET
 3. THE ERROR MUST OCCUR WITHIN THE TEST UNDER CONSIDERATION
- IF THE ERROR DOES NOT OCCUR WITHIN THE TEST UNDER CONSIDERATION
THE PROGRAM WILL REVERT TO NORMAL OPERATION. HOWEVER, IF LOOP ON
TEST SWITCH IS SET AND THIS OPERATOR SELECTABLE SCOPE LOOP IS USED
THEN THE PROGRAM WILL LOOP BACK TO THE SELECTED POINT WHEN IT
COMES TO THE END OF THE TEST UNDER CONSIDERATION.

AFTER LOOPING FOR SOME TIME IF THE LOOP SWITCH IS PUT DOWN THEN
NORMAL OPERATION WILL CONTINUE.

B.4 PROGRAM REVISION HISTORY

REVISION B:

1. MOVED THE UNIBUS INIT TEST (T36) TO BE T25.
2. MOVED THE COMMAND PARITY ERROR TEST (MCPE -
T22) TO BE T11.
3. MOVED THE ILLEGAL FUNCTION TEST (ILF) T74 TO
BE T37.
4. MODIFIED DVA, DPR, RDY & DRY STATUS BIT TESTS
TO HALT THE PROGRAM IF THEY FAIL.
5. ADDED DELAY BEFORE ISSUING DIAGNOSTIC MODE
SECTOR PULSES (SP) TO ACCOUNT FOR THE SILO
FILLING DELAY BETWEEN ISSUING GO AND THE
RAISING OF RUN LINE.

REVISION C:

1. MODIFIED T23 (RHCS2 - 'NED' BIT TEST) SO THAT
BOTH RHAS AND RHCS2 UNITS TABLES ARE PRINTED
IN THEIR ENTIRETY.

SEQ 0008

2. MADE THE 2ND PORTION OF T23 (RHCS2 - 'CLR BIT) A SEPARATE TEST (T24). JO1
3. ADDED A WARNING TYPEOUT TO T4 REGARDING OPERATOR VERIFICATION OF DRIVES BEING TESTED.
4. ADDED REVISION DATE TO THE HEADER TYPE OUT.

REVISION D:

1. MODIFIED THE PROGRAM SO THAT WITH A LOAD AND START AT LOCATION 210, THE PROGRAM WILL LOOP ON TESTING THE SELECTED DRIVE EVEN IF THAT DRIVE FAILS TO RESPOND.
2. ENABLED THE TTY INTERRUPT BIT IN THE NON-STANDARD DEVICE ADDRESS RESTART ROUTINE.
3. MODIFIED THE ATTENTION WITH ERROR TEST (T70) SO THAT ALL BITS ARE TESTED IN RHER2 AND RHER3.
4. ADDED TESTS OF RHMC FOR ZERO AFTER A READ/WRITE HEADER AND DATA TO T55, T56, T57 & T61, T62, T63. THIS WAS DONE TO VERIFY PROPER COUNTDOWN OPERATION OF THE WORD COUNTER.

9.0 PROGRAM DESCRIPTION

THE FOLLOWING SECTIONS DESCRIBE EACH TEST AND SUBROUTINES IN DETAIL AND CAN ALSO BE USED AS AN INDEX TO THE LISTING. THE LEFT MOST COLUMN IS THE LINE NUMBER WITHIN THE LISTING WHERE THAT ITEM WILL BE FOUND.

DOCUMENT

MAINDEC-11-DZRPS-D, RJPO4 DISKLESS RH11 TEST-PART 1

COPYRIGHT 1975
DIGITAL EQUIPMENT CORPORATION
MAYNARD, MASS. 01754

TABLE OF CONTENTS

43 OPERATIONAL SWITCH SETTINGS
58 BASIC DEFINITIONS
164 TRAP CATCHER
171 STARTING ADDRESS(ES)
184 MEMORY MANAGEMENT DEFINITIONS
224 COMMON TAGS
280 ERROR POINTER TABLE
964 REGISTER ADDRESSES
1122
1123 ***DIAGNOSTIC CODE***
1124
1126 SETUP TESTS
1525 REGISTER TESTS
2450 SILO TESTS
2742 MORE REGISTER TESTS
2889 DCL COMMAND TESTS
4592 READ/WRITE ADDRESSING VIA RHM
5896 ERROR BIT FUNCTIONAL TESTS
7813
7814 ***SUBROUTINES***
7815
7820 END OF PASS ROUTINE

TABLE OF CONTENTS

7930	SAVE REGISTERS ROUTINE
7960	FLOAT 1 AND 0
8019	CLEAR MEMORY ROUTINE
8052	LOCAL TRAPS
8069	CLEAR DISK ROUTINE
8082	CHECK DISK STATUS ROUTINES
8212	SAVE ROUTINE
8240	WRITE CHECK ROUTINE
8284	COMPARE ROUTINE
8380	CRC GENERATION ROUTINE
8698	JAM CURRENT CYLINDER ROUTINE
8737	ECC GENERATION AND COMPARISON ROUTINE
9077	RH BASE ADDRESS CHANGE ROUTINE
9156	DISK SIMULATION
10168	SYSMAC LIBRARY ROUTINES
10172	SCOPE HANDLER ROUTINE
10246	CONVERT BINARY TO DECIMAL AND TYPE ROUTINE
10314	TYPE ROUTINE
10361	TTY INPUT ROUTINE
10464	READ AN OCTAL NUMBER FROM THE TTY
10518	ERROR HANDLER ROUTINE
10561	ERROR MESSAGE TYPEOUT ROUTINE

TABLE OF CONTENTS

10618 BINARY TO OCTAL (ASCII) AND TYPE
10696 TRAP DECODER
10711 TRAP TABLE
10732 POWER DOWN AND UP ROUTINES

2 COPYRIGHT (C) 1975
DIGITAL EQUIPMENT CORP.
MAYNARD, MASS. 01754

PROGRAM BY SUB MALLICK, PETE BLACKSTONE

THIS PROGRAM WAS ASSEMBLED USING THE PDP-11 MAINDEC SYSMAC
PACKAGE (MAINDEC-11-DZQAC-A3).

28 *****

32 *****

NOTE: MACROS BEGINNING WITH ".S" ARE SUPPLIED BY AN
EXTERNAL SYSMAC.SML SYSTEM MACRO PACKAGE WHICH
MUST BE MADE AVAILABLE TO THE SOURCE PROGRAM
AT ASSEMBLY TIME.

43

OPERATIONAL SWITCH SETTINGS

44

SWITCH	USE
15	HALT ON ERROR
14	LOOP ON TEST
13	INHIBIT ERROR TYPEOUTS
11	INHIBIT ITERATIONS
10	BELL ON ERROR
9	LOOP ON ERROR
8	LOOP ON TEST IN SMR<7:0>
7	STOP FURTHER COMPARES IF SMOB IS LOW
55	6 ECC TEST-COMPARE END RESULTS ONLY IF SMOB IS LOW


```

58 *****
   BASIC DEFINITIONS
   *****

60   INITIAL ADDRESS OF THE STACK POINTER *** 1000 ***
71   GENERAL PURPOSE REGISTER DEFINITIONS
83   PRIORITY LEVEL DEFINITIONS
93   "SWITCH REGISTER" SWITCH DEFINITIONS
121  DATA BIT DEFINITIONS (BIT00 TO BIT15)
149  BASIC "CPU" TRAP VECTOR ADDRESSES

164 *****
   TRAP CATCHER
   *****

167  ALL UNUSED LOCATIONS FROM 4 - 776 CONTAIN A ".+2,HALT"
      SEQUENCE TO CATCH ILLEGAL TRAPS AND INTERRUPTS
      LOCATION 0 CONTAINS 0 TO CATCH IMPROPERLY LOADED VECTORS

171 *****
   STARTING ADDRESS(ES)
   *****

178  STARTING ADDRESS 200 FOR NORMAL STARTS
      THIS WILL TEST ALL RPO4'S ON THE SYSTEM A SINGLE DRIVE AT A TIME
      STARTING ADDRESS 210 WILL TEST ONLY ONE SPECIFIED DRIVE

184 *****
   MEMORY MANAGEMENT DEFINITIONS
   *****

186  KT11 VECTOR ADDRESS
190  KT11 STATUS REGISTER ADDRESSES
197  KERNAL "I" PAGE DESCRIPTOR REGISTERS
208  KERNAL "I" PAGE ADDRESS REGISTERS
219  *****
222  *****

```


224 *****
COMMON TAGS

226 THIS TABLE CONTAINS VARIOUS COMMON STORAGE LOCATIONS
USED IN THE PROGRAM.

278 *****

280 *****
ERROR POINTER TABLE

282 THIS TABLE CONTAINS THE INFORMATION FOR EACH ERROR THAT CAN OCCUR.
THE INFORMATION IS OBTAINED BY USING THE INDEX NUMBER FOUND IN
LOCATION SITEMB. THIS NUMBER INDICATES WHICH ITEM IN THE TABLE IS PERTINENT.
NOTE1: IF SITEMB IS 0 THE ONLY PERTINENT DATA IS (SERRPC).
NOTE2: EACH ITEM IN THE TABLE CONTAINS 4 POINTERS EXPLAINED AS FOLLOWS:

288 EM ::POINTS TO THE ERROR MESSAGE
DH ::POINTS TO THE DATA HEADER
DT ::POINTS TO THE DATA
DF ::POINTS TO THE DATA FORMAT

723 *****

763 *****

964 *****
REGISTER ADDRESSES

1122 *****

1123 *****
DIAGNOSTIC CODE

1124

1126

SETUP TESTS

1221

TEST 1 REFERENCE EACH REGISTER
REFERENCE EACH REGISTER BY A MOVE INSTRUCTION

1265

TEST 2 RHCS2-CONTROL AND STATUS 2

1268

THIS PARTIALLY TESTS RHCS2 TO ENABLE DETERMINATION
OF THE NUMBER OF DRIVES PRESENT

1271

1290

TEST 3 PARTIAL TEST FOR RHAS FOR UNIT NUMBERS PRESENT

1308

TEST 4 TEST FOR DRIVES PRESENT USING RHAS AND RHCS2

1448

TEST 5 TEST SERIAL NUMBER AND DRIVE TYPE I
READ SERIAL NUMBER REGISTER AND DRIVE TYPE REGISTER
TYPE IT OUT AND PROCEED
TO LOOP HERE SET SWITCH 8 AND THIS TEST NO AND RESTART

1454

1493

TEST 6 CHECK MOL TO BE LOW

1496

MAKE SURE THAT DRIVE IS OFF LINE BEFORE STARTING PROGRAM
IF DRIVE IS ON LINE THEN AFTER TYPE OUT THE PROGRAM WILL
HANG FOR EVER WAITING FOR DRIVE TO GO OFF LINE

1500

1525

REGISTER TESTS

1527

TEST 7 RHCS2 - CONTROL AND STATUS 2
TEST LOADING AND READING OF ALL POSSIBLE BITS IN THE HARDWARE
REGISTERS. USE A PATTERN OF WALKING 0'S (-2,-3,-5 ETC.) AND
WALKING 1'S (1,2,4,10 ETC)

1552

TEST 10 RHCS1 - CONTROL AND STATUS 1 REGISTER
TEST LOADING AND READING OF ALL POSSIBLE BITS IN THE HARDWARE
REGISTERS. USE A PATTERN OF WALKING 0'S (-2,-3,-5 ETC) AND
WALKING 1'S (1,2,4,10 ETC)

1574

TEST 11 RHCS1 - BIT # 13 - MCPE
THIS FORCES A MASS BUS CONTROL PARITY ERROR
BY SETTING 'PAT', LOOKING FOR 'PAR', WRITING RHCS1
AND READING RHER1

1628

TEST 12 RHMC - WORD COUNT REGISTER
TEST LOADING AND READING OF ALL POSSIBLE BITS IN THE HARDWARE
REGISTERS. USE A PATTERN OF WALKING 0'S (-2,-3,-5 ETC.) AND
WALKING 1'S (1,2,4,10 ETC)

1652

TEST 13 RHBA - UNIBUS ADDRESS REGISTER
TEST LOADING AND READING OF ALL POSSIBLE BITS IN THE HARDWARE
REGISTERS. USE A PATTERN OF WALKING 0'S (-2,-3,-5 ETC.) AND
WALKING 1'S (1,2,4,10 ETC)

1676

TEST 14 RHER1 - ERROR REGISTER #1
TEST LOADING AND READING OF ALL POSSIBLE BITS IN THE HARDWARE
REGISTERS. USE A PATTERN OF WALKING 0'S (-2,-3,-5 ETC.) AND
WALKING 1'S (1,2,4,10 ETC)

1699

TEST 15 RHMR - MAINTENANCE REGISTER
BIT 0 (DMD) MUST BE SET BEFORE THE OTHER BITS
ARE READ WRITE
ONLY 5 LOW ORDER BITS ARE TESTED (R2 HAS 5)

1757 *****
 TEST 16 RHDST - DESIRED SECTOR/TRACK ADDRESS
 TEST LOADING AND READING OF ALL POSSIBLE BITS IN THE HARDWARE
 REGISTERS. USE A PATTERN OF WALKING 0'S (-2,-3,-5 ETC.) AND
 WALKING 1'S (1,2,4,10 ETC)

1783 *****
 TEST 17 RHER2 - ERROR REGISTER #2
 TEST LOADING AND READING OF ALL POSSIBLE BITS IN THE HARDWARE
 REGISTERS. USE A PATTERN OF WALKING 0'S (-2,-3,-5 ETC.) AND
 WALKING 1'S (1,2,4,10 ETC)

1809 *****
 TEST 20 RHOF - MARGIN/OFFSET REGISTER
 TEST LOADING AND READING OF ALL POSSIBLE BITS IN THE HARDWARE
 REGISTERS. USE A PATTERN OF WALKING 0'S (-2,-3,-5 ETC.) AND
 WALKING 1'S (1,2,4,10 ETC)

1834 *****
 TEST 21 RHCA - DESIRED CYLINDER REGISTER
 TEST LOADING AND READING OF ALL POSSIBLE BITS IN THE HARDWARE
 REGISTERS. USE A PATTERN OF WALKING 0'S (-2,-3,-5 ETC.) AND
 WALKING 1'S (1,2,4,10 ETC)

1855 *****
 TEST 22 RHER3 - ERROR REGISTER #3
 TEST LOADING AND READING OF ALL POSSIBLE BITS IN THE HARDWARE
 REGISTERS. USE A PATTERN OF WALKING 0'S (-2,-3,-5 ETC.) AND
 WALKING 1'S (1,2,4,10 ETC)

1879 *****
 OF THE TWENTY REGISTERS (4 IN RH11, 16 IN RPO4) ONLY 12 ARE
 CHECKED IN THE ABOVE TESTS
 TWO ARE ALREADY TESTED (SERIAL NO. AND DRIVE TYPE)
 THE OTHER 7 WHICH ARE RHD51, RHLA, RHCC, RHEC1, RHEC1, RHEC2
 ARE READ ONLY REGISTERS. ONE OR ZERO CANNOT BE WRITTEN

1889 *****
 TEST 23 CONTROL AND STATUS 2 (RHCS 2) - 'NED'
 THIS TESTS THE UNIT SELECT BITS #0-2 (US1-4)
 AND NON-EXISTENT DRIVE BIT #12 (NED)

1894 THE OTHER RHCS2 BITS ARE NOT TESTED HERE

1896 *****

2038 *****
TEST 24 CONTROL AND STATUS 2 (RHCS2) - 'CLR'

2048 *****

2289 *****
TEST 25 PACK ACKNOWLEDGE COMMAND TEST

2292 THE PACK ACKNOWLEDGE COMMAND WILL BE LOADED INTO RHCS1 WITH GO
THEN ALL REGISTERS WILL BE CHECKED
RH CLEAR WILL BE GIVEN
THEN ALL REGISTERS WILL BE CHECKED

2297 *****

2358 *****
TEST 26 UNIBUS INIT TEST

2361 ALL POSSIBLE REGISTER BITS ARE FILLED WITH ONES
A RESET COMMAND IS GIVEN
ALL REGISTERS ARE CHECKED

2365 *****

2450

SILO TESTS

2453 *****
TEST 27 SILO TST 1

2456 THIS TESTS THE SILO BUFFER IN THE RH11 CONTROLLER
A READ IS ATTEMPTED FROM AN EMPTY SILO
DATA LATE (DLT) (RHCS2), TRANSFER ERROR (TRE) (RHCS1),
SPECIAL CONDITION (SC) (RHCS1) SHOULD SET
THEN LOADING "1" INTO TRE SHOULD CLEAR DLT, TRE AND SC

2462 *****

2506 *****
TEST 30 SILO TEST 2

2509 THIS TESTS THE IR AND "OR" BITS OF RHCS2
AT THE BEGINNING IR SHOULD BE SET AND "OR" RESET
LOADING 0 IN SILO RESETS IR FOR ONLY 2 MICRO SECONDS
THIS TIME CANNOT BE CHECKED BUT IT IS CHECKED TO SEE IF
IT DOES GO DOWN OR NOT
THEN ALL 1 IS LOADED IN SILO "OR" SHOULD BECOME SET
IN 30 MICRO SECONDS AGAIN TIME IS NOT CHECKED

"OR" SHOULD BE SET
THE OUTPUT FROM THE SILO SHOULD BE 0 AND ALL ONES

2519 *****

2569 *****
TEST 31 SILO TEST 3

2572 THIS TESTS SILO BUFFER BY FILLING IT WITH A COUNT FROM
0 TO 65 AND THEN CHECKING IF IR IS DOWN AND "OR"
IS HIGH AND COMPARING THE SILO OUTPUT.

2576 *****

2636 *****
TEST 32 SILO TEST 4
NOW PUT 67 WORDS INTO SILO AND CHECK FOR DLT
EVEN AFTER THE 67TH. WORD INPUT THE FIRST WORD SHOULD NOT CHANGE

2641 *****

2667 *****
TEST 33 SILO TEST 5
THE SILO IS LOADED WITH 0,1,2,3 THEN AFTER
'OR' IS UP A CLR IN RHCS2 IS DONE THEN 4,
IS LOADED. AFTER 'OR' IS UP 2 READS FROM
SILO ARE DONE, ON THE LAST, 'DTL' IN RHCS2 SHOULD BE SET.

2674 *****

2742 *****
MORE REGISTER TESTS

2745 *****
TEST 34 TEST ODD BYTE INSTRUCTION ON RHCS1

2748 RDY (BIT 07) AND DVA (BIT 11) SHOULD ALWAYS BE SET

2750 *****

2780 *****
TEST 35 TEST ODD BYTE INSTRUCTION ON RHCS2

2783 IR (BIT 06) AND THE UNIT SELECT (BIT 0-2) WILL BE SET

2785 *****

2824 *****
TEST 36 ODD BYTE TEST ON RHMC

2827 IN THIS REGISTER NO BITS SHOULD BE PERMANENTLY SET

2829 *****

2857 *****
TEST 37 TEST ODD BYTE INSTRUCTION ON RHBA

2860 BIT 0 SHOULD ALWAYS BE 0

2862 *****

2889

DCL COMMAND TESTS

2891 *****

FOUR GENERAL REGISTERS WILL BE RESERVED FOR HARDWARE
R1=RHCS1 CONTROL AND STATUS1
R2=RHCS2 CONTROL AND STATUS2
R3=RHDS1 DRIVE STATUS 1
R4=RHER1 ERROR REGISTER1

2898 WHENEVER ANY OTHER USE IS MADE OF THESE REGISTERS
APPROPRIATE SAVING MUST BE DONE

2901 *****

2908 *****

ERROR REGISTER #01 (RHER1) TEST
BIT #1 (ILLEGAL REGISTER) CANNOT BE TESTED ON PDP11 THIS BIT
IS FOR PDP10 USE ONLY

2916 *****

TEST 40 TEST ILF BIT #0 IN REG. RHER1

2919 ALL 3 ILLEGAL FUNCTION CODES SHOULD SET - ATA,ERR,ILF - AND ARE TESTED
A GO WITHOUT CLEARING ILF ERR SHOULD SET - MXF,DLT,TRE - BITS AND THEY ARE ALSO

2922 *****

3012 *****

TEST 41 READ IN PRESET

3015 ALL POSSIBLE REGISTERS WILL BE FILLED WITH ONES
THE REGISTER CONTENTS WILL BE SAVED IN REINTO BUFFER
THE READ IN PRESET COMMAND WILL BE GIVEN
ALL REGISTERS WILL BE CHECKED

3020 *****

3090 *****

TEST 42 NO OPERATION FUNCTION TEST
ALL POSSIBLE REGISTERS ARE CLEARED THEN A "NOP"=0
IS GIVEN NO CHANGE SHOULD HAPPEN
ALL POSSIBLE REGISTERS ARE FILLED WITH ONES THEN A "NOP"
IS GIVEN NO CHANGE SHOULD HAPPEN

3097 *****

3225 *****

TEST 43 DRIVE CLEAR

3228 ALL WRITE BITS OF ALL REGISTERS EXCEPT RH08 ARE FILLED WITH
ONES EXCEPT FOR BIT #0 AND BIT #6 WHICH ARE "GO" AND
"ENABLE INTERRUPT" BITS
THEN A DRIVE CLEAR IS PERFORMED
THEN ALL REGISTERS EXCEPT RH08 ARE CHECKED

3234 *****

3502 *****

TEST 44 SEEK COMMAND TEST

3505 THE SEEK COMMAND WILL BE LOADED INTO RHCS1 WITH GO
THEN ALL REGISTERS WILL BE CHECKED
RH CLEAR WILL BE GIVEN
THEN ALL REGISTERS WILL BE CHECKED

3510 *****

3745 *****

TEST 45 UNLOAD COMMAND TEST

3748 THE UNLOAD COMMAND WILL BE LOADED INTO RHCS1 WITH GO
THEN ALL REGISTERS WILL BE CHECKED
RH CLEAR WILL BE GIVEN

3752 *****

3875 *****

TEST 46 OFFSET COMMAND TEST

3878 THE OFFSET COMMAND WILL BE LOADED INTO RHCS1 WITH GO
THEN ALL REGISTERS WILL BE CHECKED
RH CLEAR WILL BE GIVEN
THEN ALL REGISTERS WILL BE CHECKED

3883 *****

4018 *****

TEST 47 RETURN TO CENTER LINE COMMAND TEST

4021

THE RETURN TO CENTER LINE COMMAND WILL BE LOADED INTO RHCS1 WITH GO
THEN ALL REGISTERS WILL BE CHECKED
RH CLEAR WILL BE GIVEN
THEN ALL REGISTERS WILL BE CHECKED

4026 *****

4149 *****

TEST 50 RECALIBRATE COMMAND TEST

4152

THE RECALIBRATE COMMAND WILL BE LOADED INTO RHCS1 WITH GO
THEN ALL REGISTERS WILL BE CHECKED
RH CLEAR WILL BE GIVEN
THEN ALL REGISTERS WILL BE CHECKED

4157 *****

4279 *****

TEST 51 RELEASE COMMAND TEST

4282

THE RELEASE COMMAND WILL BE LOADED INTO RHCS1 WITH GO
THEN ALL REGISTERS WILL BE CHECKED
RH CLEAR WILL BE GIVEN
THEN ALL REGISTERS WILL BE CHECKED

4287 *****

4350 *****

TEST 52 MAKE CURRENT CYLINDER = 0

4364

TEST 53 LOOK AHEAD REGISTER

4367

A SEARCH COMMAND IS GIVEN FOR CYLINDER 0, TRACK 0, SECTOR 21.
THE LOOK AHEAD REGISTER IS CHECKED AFTER INDEX PULSE
THE EXTENSION FIELD IS CHECKED IN EACH SECTOR AFTER
128 BYTES THEN AGAIN AFTER 128 MORE BYTES THEN AGAIN AFTER 256 MORE BYTES
THE SECTOR COUNT FIELD IS CHECKED AFTER EACH SECTOR
AT THE END ALL REGISTERS ARE CHECKED

4374 *****

4577 *****

TEST 54 MAKE CURRENT CYLINDER = 0

4592

READ/WRITE ADDRESSING VIA RHMR

4595

TEST 55 WRITE HEADER AND DATA 1

4598

WRITE CYLINDER 0, FORMAT 16 BIT PER WORD
TRACK 0, SECTOR 0, KEYS 0, NUMBER OF WORDS 256 OF 0'S
AS EVERYTHING IS ZERO THIS PROVES VERY LITTLE
ANY ERROR LOGIC INDICATIONS ARE NOT CONCLUSIVE ON FIRST PASS
BECAUSE ERROR LOGIC HAS NOT BEEN CHECKED YET (ON FIRST PASS)

4604

4706

TEST 56 WRITE HEADER AND DATA 2

4709

WRITE CYLINDER 0, FORMAT 16 BITS PER WORD
TRACK 0, SECTOR 1, KEYS 0, NUMBER OF WORDS 256
OF ALL ONES.
ANY ERROR LOGIC INDICATIONS ARE NOT CONCLUSIVE ON FIRST PASS
BECAUSE ERROR LOGIC HAS NOT BEEN CHECKED YET (ON FIRST PASS)

4715

4827

TEST 57 WRITE HEADER AND DATA 3
WRITE CYLINDER 0 FORMAT 16 BITS PER WORD
TRACK 1, SECTOR 1, KEY 0, NUMBER OF WORDS 256
ALTERNATE ONES AND ZEROS (052525)
ANY ERROR LOGIC INDICATIONS ARE NOT CONCLUSIVE ON FIRST PASS
BECAUSE ERROR LOGIC HAS NOT BEEN CHECKED YET (ON FIRST PASS)

4835

4951

TEST 60 PROGRAM ERROR RHCS2 #10

4954

WRITE CYLINDER 0, FORMAT 16 BIT PER WORD
TRACK 0, SECTOR 0, KEYS 0, NUMBER OF WORDS 256 OF 0'S
WHILE GO BIT IS SET ANOTHER GO IS GIVEN THIS SHOULD SET
PROGRAM ERROR

4959

5020

THESE TESTS ARE THROUGH THE MAINTAINABILITY REGISTER - RHMR

5023 THE SECTOR GAP AND SYNC BYTE ARE ALWAYS READ AS ZEROS AND 144000 NO MATTER WHAT IS IN THE SIMULATED DISK AREA TAGGED SECGAP: AND MSSYNC:

5027 THE HEADER CONSISTING OF CYLINDER ADDRESS, SECTOR, TRACK AND THE KEYS ARE READ FROM LOCATION CYL:, SECTOR:, KEY1:, AND KEY2 AND NOT FROM HEADER: ON SIMULATED DISK

5032 CRC IS READ FROM SIMULATED DISK LOCATION MCRC: HEADER GAP IS ALWAYS READ AS ZEROS NO MATTER WHAT IS ON THE SIMULATED DISK AREA

5036 THE DATA SYNC IS READ FROM HDMSYN: ON SIMULATED DISK

5039 ALL DATA IS READ FROM SIMULATED DISK DISK: *****

5045 ***** TEST 61 READ HEADER AND DATA 1 READ CYLINDER 0 FORMAT 16 BITS PER WORD TRACK 0, SECTOR 0, KEYS 0, 256 WORDS OF 0 ANY ERROR LOGIC INDICATIONS ARE NOT CONCLUSIVE ON FIRST PASS BECAUSE ERROR LOGIC HAS NOT BEEN CHECKED YET (ON FIRST PASS)

5052 *****

5172 ***** TEST 62 READ HEADER AND DATA 2 READ CYLINDER 0 FORMAT 16 BITS PER WORD TRACK 0, SECTOR 1, KEYS 0, 256 WORDS OF 177777 ANY ERROR LOGIC INDICATIONS ARE NOT CONCLUSIVE ON FIRST PASS BECAUSE ERROR LOGIC HAS NOT BEEN CHECKED YET (ON FIRST PASS)

5179 *****

5298 ***** TEST 63 READ HEADER AND DATA 3 READ CYLINDER 0 FORMAT 16 BITS PER WORD TRACK 1, SECTOR 1, KEYS 0, 256 WORDS OF 052525 ANY ERROR LOGIC INDICATIONS ARE NOT CONCLUSIVE ON FIRST PASS BECAUSE ERROR LOGIC HAS NOT BEEN CHECKED YET (ON FIRST PASS)

5305 *****

5424 ***** TEST 64 WRITE DATA

5427 WRITE CYLINDER 0, FORMAT 16 BITS PER WORD
TRACK 0, SECTOR 0, KEYS 0, NUMBER OF WORDS 256 OF 377
ANY ERROR LOGIC INDICATION IS NOT CONCLUSIVE ON FIRST PASS
BECAUSE ERROR LOGIC HAS NOT BEEN CHECKED

5432 *****

5520 *****
TEST 65 READ DATA

5523 READ CYLINDER 0, FORMAT 16 BITS PER WORD
TRACK 0, SECTOR 1, KEYS 0, 10 WORDS OF 177400
ANY ERROR LOGIC INDICATION IS NOT CONCLUSIVE
BECAUSE ERROR LOGIC HAS NOT BEEN CHECKED YET (ON FIRST PASS)

5528 *****

5635 *****
TEST 66 WRITE CHECK HEADER AND DATA

5638 WRITE CHECK CYLINDER 0, FORMAT 16 BITS PER WORD
TRACK 1, SECTOR 1, KEYS 0, 36 WORDS AS SHOWN BELOW
ANY DEVICE LOGIC ERROR INDICATION IS NOT CONCLUSIVE ON FIRST PASS
BECAUSE ERROR LOGIC HAS NOT BEEN CHECKED YET (ON FIRST PASS)
ONLY RH WRITE CHECK ERROR (RHCS2 BIT 14) IS TESTED HERE

5766 *****
TEST 67 WRITE CHECK DATA

5769 WRITE CHECK DATA CYLINDER 0 FORMAT 16 BITS PER WORD
TRACK 1, SECTOR 1, KEYS 0, 32 WORDS OF DATA
ANY DEVICE LOGIC ERROR INDICATIONS ARE NOT CONCLUSIVE ON FIRST PASS
BECAUSE ERROR LOGIC HAS NOT YET (ON FIRST PASS) BEEN CHECKED
ONLY RH WRITE CHECK ERROR IS TESTED

5775 *****

5896

ERROR BIT FUNCTIONAL TESTS

5900 *****
TEST 70 ATTENTION WITH ERROR TEST

5903 THIS TESTS THE SETTING OF ATA BIT BOTH IN THE RHAS
AND THE RHDSI REGISTERS WITH THE SETTING OF EACH
ERROR BIT ON THE THREE ERROR REGISTERS.
IN EACH OF THE ABOVE CASES ERR IN RHDSI SHOULD
ALSO SET.

5909 "GO" SHOULD CLEAR ERR, ATA IN RHDS1 AND RHAS BUT NOT ERROR REG.
PUTTING "1" IN RHAS DRIVE POSITION CLEARS DRIVE BIT IN ATA IN RHDS1
UPPER BYTE OF RHAS IS INVALID

5913 *****

6034 *****
TEST 71 BUS ADDRESS INHIBIT

6037 READ CYLINDER0, FORMAT 16 BITS PER WORD
TRACK0, SECTOR 1, KEYS 0, 10 WORDS OF 177400
THIS IS DONE WITH BUS ADDRESS INHIBIT SET
ANY ERROR LOGIC INDICATION IS NOT CONCLUSIVE ON FIRST PASS
BECAUSE ERROR LOGIC HAS NOT BEEN CHECKED YET (ON FIRST PASS)

6043 *****

6148 *****
TEST 72 RHCS2 - BIT # 11 - NEM

6151 READ CYLINDER0, FORMAT 16 BITS PER WORD
TRACK0, SECTOR 1, KEYS 0, 1 WORD OF 177400
THIS IS DONE WITH BUS ADDRESS INHIBIT SET
BUS ADDRESS USED IS 760000 THIS IS ALWAYS NON EXISTANT
THIS SHOULD SET NEM

6157 *****

6240 *****
TEST 73 WRITE CHECK ERROR

6243 WRITE CHECK DATA CYLINDER 0 FORMAT 16 BITS PER WORD
TRACK 1, SECTOR 1, KEYS 0, 32 WORDS OF DATA
FIFTH WORD IS CHANGED ON DISK TO GIVE WRITE CHECK ERROR
ANY DEVICE LOGIC ERROR INDICATIONS ARE NOT CONCLUSIVE
ON FIRST PASS
BECAUSE ERROR LOGIC HAS NOT YET BEEN CHECKED
ONLY RH WRITE CHECK ERROR IS TESTED

6251 *****

6387 *****
TEST 74 ERROR REGISTER #1-BIT 4 -FORMAT ERROR
THE SIMULATED DISK IS FILLED WITH CYLINDER 0 TRACK 1
SECTOR 0 FORMAT=16 BITS PER WORD AND 4 WORDS
OF 125252, A READ HEADER AND DATA COMMAND IS GIVEN WITH 16 BITS
PER WORD FORMAT, FER=BIT4 SHOULD SET BUT THE
READ SHOULD BE COMPLETE

6395 *****

6530 *****
TEST 75 ERROR REGISTER #1-BIT 4 -FORMAT ERROR

6533 THE SIMULATED DISK HEADER IS FILLED WITH CYLINDER 0
TRACK 0, SECTOR 0 FORMAT 18 BITS PER WORD
A WRITE DATA COMMAND IS GIVEN WITH SAME HEADER
EXCEPT FORMAT BIT. THE DATA SHOULD NOT BE WRITTEN.

6538 *****

6630 *****
TEST 76 RHER1 - BIT #2 - REG. MODIFICATION REFUSED

6633 IN THIS TEST THE REGISTERS ARE IN TWO GROUPS
FIRST - RHCS1, RHDST, RHOF, RHCA, RHER1, RHER2, RHER3 - SETS RMR
SECOND - RHRM, RHAS - DOES NOT SET RMR
IF WRITING IS ATTEMPTED DURING AN OPERATION

ONLY ONE REGISTER IS WRITTEN INTO THAT IS RHCA

- 1 THE REGISTERS CONTENTS ARE SAVED IN "REINTO" BUFFER
- 2 WRITE HEADER AND DATA IS STARTED
- 3 ATTEMPT IS MADE TO WRITE INTO REGISTERS
- 4 ALL REGISTERS ARE COMPARED

6647 *****

6733 *****
TEST 77 MAKE CURRENT CYLINDER = 1

6747 *****
TEST 100 ERROR REG1 - BIT #7 - HEADER COMPARE ERROR

6750 THE SIMULATED DISK IS SET TO READ CYLINDER=0, TRACK=1
SECTOR=1, KEYS=1, 256 WORDS OF 177400
A READ HEADER AND DATA COMMAND IS GIVEN TO READ
CYLINDER=1, TRACK=1, SECTOR=1, KEY1=1, KEY2=1
REINTO BUFFER IS FILLED WITH 0
MRFROM IS FILLED WITH 10000,401,1,1,1, AND ALL 177400
AFTER THE READ THE REINTO BUFFER IS EXPECTED TO
HAVE WHAT IS IN MRFROM - 10000,401,1,1 AND ALL 177400

6759 *****

6813 *****
TEST 101 MAKE CURRENT CYLINDER = 0

6827 *****
TEST 102 ERROR REG1 - BIT #7 - HEADER COMPARE ERROR

6830 THE SIMULATED DISK IS SET TO READ CYLINDER=0, TRACK=1
SECTOR=1, KEYS=1, 256 WORDS OF 177400
A READ HEADER AND DATA COMMAND IS GIVEN TO READ
CYLINDER=0, TRACK=0, SECTOR=1, KEY1=1, KEY2=1
REINTO BUFFER IS FILLED WITH 0
MRFROM IS FILLED WITH 10000,401,1,1,1, AND ALL 177400
AFTER THE READ THE REINTO BUFFER IS EXPECTED TO
HAVE WHAT IS IN MRFROM - 10000,401,1,1 AND ALL 177400

6839 *****

6893 *****
TEST 103 MAKE CURRENT CYLINDER = 1

6907 *****
TEST 104 ERROR REG.1 - BIT #7 - HEADER COMPARE ERROR

6910 THE SIMULATED DISK IS SET UP FOR CYLINDER=0, TRACK=1
SECTOR=1, KEYS=1, 256 WORDS OF 177400
A WRITE DATA COMMAND IS GIVEN TO WRITE CYLINDER=1
TRACK=1, SECTOR=1, KEY1=1, KEY2=1
MRFROM BUFFER IS FILLED WITH 125252
REINTO BUFFER IS FILLED WITH 177400
AFTER THE WRITE COMMAND THE DISK IS EXPECTED TO
HAVE 177400

6919 *****

6968 *****
TEST 105 MAKE CURRENT CYLINDER = 0

6982 *****
TEST 106 ERROR REG.1 - BIT #7 - HEADER COMPARE ERROR

6985 THE SIMULATED DISK IS SET UP FOR CYLINDER=0, TRACK=1
SECTOR=1, KEYS=1, 256 WORDS OF 177400
A WRITE DATA COMMAND IS GIVEN TO WRITE CYLINDER=0
TRACK=0, SECTOR=1, KEY1=1, KEY2=1
MRFROM BUFFER IS FILLED WITH 125252
REINTO BUFFER IS FILLED WITH 177400
AFTER THE WRITE COMMAND THE DISK IS EXPECTED TO
HAVE 177400

6994 *****

7042 *****
TEST 107 RHER1 - BIT #8 - CRC ERROR (READING)

7045 THE SIMULATED DISK IS SET TO READ CYLINDER=0, TRACK=1
SECTOR=1, KEYS=1, 256 WORDS OF 177400
A READ HEADER AND DATA COMMAND IS GIVEN TO READ
CYLINDER=0, TRACK=1, SECTOR=1, KEY1=1, KEY2=1
REINTO BUFFER IS FILLED WITH 0
MRFROM IS FILLED WITH 10000,401,1,1,1, AND ALL 177400
AFTER THE READ THE REINTO BUFFER IS EXPECTED TO
HAVE WHAT IS IN MRFROM - 10000,401,1,1 AND ALL 177400

7054 *****

7110 *****
TEST 110 RHER1 - BIT 8 - CRC ERROR (WRITING)

7113 THE SIMULATED DISK IS SET UP FOR CYLINDER=0, TRACK=1
SECTOR=1, KEYS=1, 256 WORDS OF 177400
A WRITE DATA COMMAND IS GIVEN TO WRITE CYLINDER=0
TRACK=1, SECTOR=1, KEY1=1, KEY2=1
MRFROM BUFFER IS FILLED WITH 125252
REINTO BUFFER IS FILLED WITH 177400
AFTER THE WRITE COMMAND THE DISK IS EXPECTED TO
HAVE 177400

7122 *****

7171 *****
TEST 111 MAKE CURRENT CYLINDER = 410.

7186 *****
TEST 112 RHDS1 (BIT #10) - LAST SECTOR TRANSFERRED, 'LST'
WRITE CYLINDER 410. FORMAT 16 BITS PER WORD
TRACK 18., SECTOR 21., KEYS 0, NUMBER OF WORDS
256. OF 377
LST BIT # 10 RHDS1 SHOULD SET AFTER WRITE
IS COMPLETE.

7194 *****

7297 *****
TEST 113 ERROR REGISTER 1 - BIT #9 AOE

7300 A WRITE DATA COMMAND IS GIVEN TO CYLINDER 410
SECTOR 21 TRACK 18, KEYS 0, DATA 377
WORD COUNT REGISTER FOR 326 (256+66+4) WORDS

AFTER 256 WORDS HAVE BEEN WRITTEN
AOE SHOULD COME UP
RHMC WILL SHOW 4 BECAUSE THE SILO IS 66 WORDS AND

256 WORDS HAVE BEEN WRITTEN - TOTAL 322
THIS IS 4 SHORT OF 326

```

7310 *****
7437 *****
TEST 114 MAKE CURRENT CYLINDER = 0
*****
7451 *****
TEST 115 ERROR REGISTER 1 - BIT #10 IAE
7454 A READ HEADER AND DATA IS GIVEN TO TRACK 20, SECTOR 0
AN INDEX PULSE IS GIVEN TO GET RHLA TO 0
IAE BIT SHOULD SET
7460 *****
7554 *****
TEST 116 ERROR REGISTER 1- BIT #10 IAE
7557 A WRITE HEADER AND DATA IS GIVEN TO SECTOR 22
TRACK 0 CYLINDER 0
WORD COUNT IS SET TO 256.
AN INDEX PULSE IS GIVEN TO GET RHLA TO 0
IAE BIT SHOULD SET
7566 *****
7665 *****
TEST 117 ERROR REGISTER 1- BIT #10 IAE
7668 A WRITE DATA IS GIVEN TO SECTOR 0
TRACK 0 CYLINDER 411
WORD COUNT IS SET TO 256.
AN INDEX PULSE IS GIVEN TO GET RHLA TO 0
IAE BIT SHOULD SET
7677 *****
7768 *****
TEST 120 END OF DRIVE

```


7771 THIS IS THE END OF TEST FOR ONE DRIVE
IF THERE ARE MORE DRIVES THEN THE PROGRAM
JUMPS TO TEST 5 FOR NEXT DRIVE TEST
END PASS IS REACHED ONLY AFTER ALL DRIVES ARE COMPLETE

7776 *****

7813

7814

SUBROUTINES

7815

7818 *****

7820

END OF PASS ROUTINE

7822 INCREMENT THE PASS NUMBER (\$PASS)
TYPE "END PASS #XXXXX" (WHERE XXXXX IS A DECIMAL NUMBER)
IF THERES A MONITOR GO TO IT
IF THERE ISN'T JUMP TO TST1

7861 *****
HERE IS A DETAILED EXPLANATION OF HOW THE LOOP ON ERROR WORKS.
ON HITTING AN ERROR IF THE LOOP ON ERROR SWITCH IS SET, THE
PROGRAM GOES BACK - USUALLY BACK TO THE BEGINNING OF THE TEST.

7866 WHEN THIS OPERATOR SELECTABLE SCOPE LOOP IS USED THEN THE POINT
THE PROGRAM GOES BACK TO CAN BE CHANGED.
THE RESTRICTIONS TO THE POINT WHERE THE PROGRAM CAN GO ARE: -
1. IT MUST BE WITHIN THE TEST UNDER CONSIDERATION
2. LOOP ON ERROR SWITCH MUST BE SET
3. THE ERROR MUST OCCUR WITHIN THE TEST UNDER CONSIDERATION
IF THE ERROR DOES NOT OCCUR WITHIN THE TEST UNDER CONSIDERATION
THE PROGRAM WILL REVERT TO NORMAL OPERATION. HOWEVER, IF LOOP ON
TEST SWITCH IS SET AND THIS OPERATOR SELECTABLE SCOPE LOOP IS USED
THEN THE PROGRAM WILL LOOP BACK TO THE SELECTED POINT WHEN IT

7876 COMES TO THE END OF THE TEST UNDER CONSIDERATION.

AFTER LOOPING FOR SOME TIME IF THE LOOP SWITCH IS PUT DOWN THEN
NORMAL OPERATION WILL CONTINUE.

7930 *****
SAVE REGISTERS ROUTINE

7960 *****
FLOAT 1 AND 0

8019 *****
CLEAR MEMORY ROUTINE

8052 *****
LOCAL TRAPS

8069 *****
CLEAR DISK ROUTINE

8082 *****
CHECK DISK STATUS ROUTINES

8212 *****
SAVE ROUTINE

8240 *****
WRITE CHECK ROUTINE

8284 *****
COMPARE ROUTINE

8380 *****
CRC GENERATION ROUTINE

8698 *****
JAM CURRENT CYLINDER ROUTINE

8737 *****
ECC GENERATION AND COMPARISON ROUTINE

9077 *****
RH BASE ADDRESS CHANGE ROUTINE

9079 THIS ROUTINE WILL ALLOW THE CHANGE OF THE BASE
ADDRESS FROM 176700 TO ANY TYPED VALUE

9156 *****
DISK SIMULATION

9157 *****

9217 *****

9314 *****

9336 *****

9343 *****

9470 *****

9537 *****

9608 *****

9686 *****

9718 *****

9850 *****

10010 *****

10123 *****

10168 *****
SYSMAC LIBRARY ROUTINES

10170 *****

10172 *****
SCOPE HANDLER ROUTINE

10174 THIS ROUTINE CONTROLS THE LOOPING OF SUBTESTS. IT WILL INCREMENT
AND LOAD THE TEST NUMBER(\$TSTNM) INTO THE DISPLAY REG.(DISPLAY<7:0>)
AND LOAD THE ERROR FLAG (SERFLG) INTO DISPLAY<15:08>
THE SWITCH OPTIONS PROVIDED BY THIS ROUTINE ARE:
SM14=1 LOOP ON TEST
SM11=1 INHIBIT ITERATIONS
SM09=1 LOOP ON ERROR
SM08=1 LOOP ON TEST IN SMR<7:0>
CALL SCOPE ;;SCOPE=IOT

10244 *****

10246 *****
CONVERT BINARY TO DECIMAL AND TYPE ROUTINE

10248 THIS ROUTINE IS USED TO CHANGE A 16-BIT BINARY NUMBER TO A 5-DIGIT
SIGNED DECIMAL (ASCII) NUMBER AND TYPE IT. DEPENDING ON WHETHER THE
NUMBER IS POSITIVE OR NEGATIVE A SPACE OR A MINUS SIGN WILL BE TYPED
BEFORE THE FIRST DIGIT OF THE NUMBER. LEADING ZEROS WILL ALWAYS BE
REPLACED WITH SPACES.
CALL:
MOV NUM,-(SP) ;;PUT THE BINARY NUMBER ON THE STACK
TYPDS ;;GO TO THE ROUTINE

10312 *****

10314

```
*****
TYPE ROUTINE
*****
```

10316 ROUTINE TO TYPE ASCIZ MESSAGE. MESSAGE MUST TERMINATE WITH A 0 BYTE.
 THE ROUTINE WILL INSERT A NUMBER OF NULL CHARACTERS AFTER A LINE FEED.
 NOTE1: SNULL CONTAINS THE CHARACTER TO BE USED AS THE FILLER CHARACTER.
 NOTE2: SFILLS CONTAINS THE NUMBER OF FILLER CHARACTERS REQUIRED.
 NOTE3: SFILLC CONTAINS THE CHARACTER TO FILL AFTER.

CALL:

```
1) USING A TRAP INSTRUCTION
   TYPE      ,MESADR      ;;MESADR IS FIRST ADDRESS OF AN ASCIZ STRING
OR
```

```
   TYPE
   MESADR
```

2) USING A JSR INSTRUCTION

```
   MOV      PS,-(SP)      ;;PUSH PROCESSOR STATUS WORD ON THE STACK
   JSR      PC,STYPE      ;;CALL TYPE ROUTINE
   MESADDR      ;;FIRST ADDRESS OF MESSAGE
```

```
10359 *****
```

10361

```
*****
TTY INPUT ROUTINE
*****
```

10370 TK INITIALIZE ROUTINE
 THIS ROUTINE WILL INITIALIZE THE TTY KEYBOARD INPUT QUEUE
 SETUP THE INTERRUPT VECTOR AND TURN ON THE KEYBOARD INTERRUPT
 CALL

```
   JSR      PC,STKINT
   RETURN
```

10387 TK SERVICE ROUTINE
 THIS ROUTINE WILL SERVICE THE TTY KEYBOARD INTERRUPT

10409 *****
 THIS ROUTINE WILL INPUT A SINGLE CHARACTER FROM THE TTY

```
CALL:   RDCHR      ;;INPUT A SINGLE CHARACTER FROM THE TTY
```

10413 RETURN HERE ;;CHARACTER IS ON THE STACK

10429 *****
 THIS ROUTINE WILL INPUT A STRING FROM THE TTY

```
CALL:   RDLIN      ;;INPUT A STRING FROM THE TTY
        RETURN HERE ;;ADDRESS OF FIRST CHARACTER WILL BE ON THE STACK
        ;;TERMINATOR WILL BE A BYTE OF ALL 0'S
```

10462 *****

10464

READ AN OCTAL NUMBER FROM THE TTY

10466 THIS ROUTINE WILL READ AN OCTAL (ASCII) NUMBER FROM THE TTY AND
CHANGE IT TO BINARY.
THE INPUT CHARACTERS WILL BE CHECKED TO INSURED THEY ARE LEGAL
OCTAL DIGITS. IF AN ILLEGAL CHARACTER IS READ A "?" WILL BE TYPED
FOLLOWED BY A CARRIAGE RETURN-LINE FEED. THE COMPLETE NUMBER MUST
THEN BE RETYPED. THE INPUT IS TERMINATED BY TYPING A CARRIAGE RETURN.
CALL:

RDOCT ;:READ AN OCTAL NUMBER
RETURN HERE ;:LOW ORDER BITS ARE ON TOP OF THE STACK
 ;:HIGH ORDER BITS ARE IN SHIOCT

10516 *****

10518

ERROR HANDLER ROUTINE

10520 THIS ROUTINE WILL INCREMENT THE ERROR FLAG AND THE ERROR COUNT,
SAVE THE ERROR ITEM NUMBER AND THE ADDRESS OF THE ERROR CALL
AND GO TO SERRTYP ON ERROR
THE SWITCH OPTIONS PROVIDED BY THIS ROUTINE ARE:
SW15=1 HALT ON ERROR
SW13=1 INHIBIT ERROR TYPEOUTS
SW10=1 BELL ON ERROR
SW09=1 LOOP ON ERROR
CALL ERROR N ;:ERR AND N=ERROR ITEM NUMBER

10559 *****

10561

ERROR MESSAGE TYPEOUT ROUTINE

10563 THIS ROUTINE USES THE "ITEM CONTROL BYTE" (\$ITEMB) TO DETERMINE WHICH
ERROR IS TO BE REPORTED. IT THEN OBTAINS, FROM THE "ERROR TABLE" (\$ERRTB),
AND REPORTS THE APPROPRIATE INFORMATION CONCERNING THE ERROR.

10616 *****

10618 *****
 BINARY TO OCTAL (ASCII) AND TYPE

10620 THIS ROUTINE IS USED TO CHANGE A 16-BIT BINARY NUMBER TO A 6-DIGIT
 OCTAL (ASCII) NUMBER AND TYPE IT.
 STYPOS---ENTER HERE TO SETUP SUPPRESS ZEROS AND NUMBER OF DIGITS TO TYPE

```
CALL:
      MOV     NUM,-(SP)      ;;NUMBER TO BE TYPED
      TYPOS   ;;CALL FOR TYPEOUT
      .BYTE  N              ;;N=1 TO 6 FOR NUMBER OF DIGITS TO TYPE
      .BYTE  M              ;;M=1 OR 0
                          ;;1=TYPE LEADING ZEROS
                          ;;0=SUPPRESS LEADING ZEROS
```

STYPON---ENTER HERE TO TYPE OUT WITH THE SAME PARAMETERS AS THE LAST
 STYPOS OR STYPOC

```
CALL:
      MOV     NUM,-(SP)      ;;NUMBER TO BE TYPED
      TYPON   ;;CALL FOR TYPEOUT
```

STYPOC---ENTER HERE FOR TYPEOUT OF A 16 BIT NUMBER

```
CALL:
      MOV     NUM,-(SP)      ;;NUMBER TO BE TYPED
      TYPOC   ;;CALL FOR TYPEOUT
```

10694 *****

10696 *****
 TRAP DECODER

10698 THIS ROUTINE WILL PICKUP THE LOWER BYTE OF THE "TRAP" INSTRUCTION
 AND USE IT TO INDEX THROUGH THE TRAP TABLE FOR THE STARTING ADDRESS
 OF THE DESIRED ROUTINE. THEN USING THE ADDRESS OBTAINED IT WILL
 GO TO THAT ROUTINE.

10711 *****
 TRAP TABLE

10713 THIS TABLE CONTAINS THE STARTING ADDRESSES OF THE ROUTINES CALLED
 BY THE "TRAP" INSTRUCTION.

10730 *****

10732

POWER DOWN AND UP ROUTINES

10772

ERROR TABLE, BIT DEFINITIONS & STARTING ADDRESSES

42
43
44
47
62
168
175
188
228
281
965

OPERATIONAL SWITCH SETTINGS
BASIC DEFINITIONS
TRAP CATCHER
STARTING ADDRESS(ES)
MEMORY MANAGEMENT DEFINITIONS
COMMON TAGS
ERROR POINTER TABLE
REGISTER ADDRESSES

DIAGNOSTIC CODE

1123
1124
1125
1127

SETUP TESTS

1222 T1 REFERENCE EACH REGISTER
1267 T2 RHCS2-CONTROL AND STATUS 2
1293 T3 PARTIAL TEST FOR RHAS FOR UNIT NUMBERS PRESENT
1312 T4 TEST FOR DRIVES PRESENT USING RHAS AND RHCS2
1453 T5 TEST SERIAL NUMBER AND DRIVE TYPE1
1499 T6 CHECK MOL TO BE LOW

REGISTER TESTS

1532 T7 RHCS2 - CONTROL AND STATUS 2
1534 T10 RHCS1 - CONTROL AND STATUS 1 REGISTER
1560 T11 RHCS1 - BIT # 13 - MCPE
1583 T12 RHMC - WORD COUNT REGISTER
1638 T13 RHBA - UNIBUS ADDRESS REGISTER
1663 T14 RHER1 - ERROR REGISTER #1
1688 T15 RHRM - MAINTENANCE REGISTER
1712 T16 RHDST - DESIRED SECTOR/TRACK ADDRESS
1771 T17 RHER2 - ERROR REGISTER #2
1798 T20 RHOF - MARGIN/OFFSET REGISTER
1825 T21 RHCA - DESIRED CYLINDER REGISTER
1851 T22 RHER3 - ERROR REGISTER #3
1873 T23 CONTROL AND STATUS 2 (RHCS 2) - 'NED'
1908 T24 CONTROL AND STATUS 2 (RHCS2) - 'CLR'
2058 T25 PACK ACKNOWLEDGE COMMAND TEST
2310 T26 UNIBUS INIT TEST

SILO TESTS

2380 T27 SILO TST 1
2473 T30 SILO TEST 2
2476 T31 SILO TEST 3
2530 T32 SILO TEST4
2594 T33 SILO TEST 5

MORE REGISTER TESTS

2662 T34 TEST ODD BYTE INSTRUCTION ON RHCS1
2694 T35 TEST ODD BYTE INSTRUCTION ON RHCS2
2770 T36 ODD BYTE TEST ON RHMC
2773 T37 TEST ODD BYTE INSTRUCTION ON RHBA

DCL COMMAND TESTS

2809 T40 TEST ILF BIT #0 IN REG. RHER1
2854 T41 READ IN PRESET
2888 T42 NO OPERATION FUNCTION TEST
2921 T43 DRIVE CLEAR
2948
3045
3124
3260

TABLE OF CONTENTS

3538	T44	SEEK COMMAND TEST
3782	T45	UNLOAD COMMAND TEST
3913	T46	OFFSET COMMAND TEST
4057	T47	RETURN TO CENTER LINE COMMAND TEST
4189	T50	RECALIBRATE COMMAND TEST
4320	T51	RELEASE COMMAND TEST
4392	T52	MAKE CURRENT CYLINDER = 0
4407	T53	LOOK AHEAD REGISTER
4621	T54	MAKE CURRENT CYLINDER = 0
4637		READ/WRITE ADDRESSING VIA RHM
4640	T55	WRITE HEADER AND DATA 1
4752	T56	WRITE HEADER AND DATA 2
4874	T57	WRITE HEADER AND DATA 3
4999	T60	PROGRAM ERROR RHCS2 #10
5094	T61	READ HEADER AND DATA 1
5222	T62	READ HEADER AND DATA 2
5349	T63	READ HEADER AND DATA 3
5476	T64	WRITE DATA
5573	T65	READ DATA
5689	T66	WRITE CHECK HEADER AND DATA
5821	T67	WRITE CHECK DATA
5952		ERROR BIT FUNCTIONAL TESTS
5956	T70	ATTENTION WITH ERROR TEST
6091	T71	BUS ADDRESS INHIBIT
6206	T72	RHCS2 - BIT # 11 - NEM
6299	T73	WRITE CHECK ERROR
6447	T74	ERROR REGISTER #1-BIT 4 -FORMAT ERROR
6591	T75	ERROR REGISTER #1-BIT 4 -FORMAT ERROR
6692	T76	RHER1 - BIT #2 - REG. MODIFICATION REFUSED
6796	T77	MAKE CURRENT CYLINDER = 1
6811	T100	ERROR REG1 - BIT #7 - HEADER COMPARE ERROR
6878	T101	MAKE CURRENT CYLINDER = 0
6893	T102	ERROR REG1 - BIT #7 - HEADER COMPARE ERROR
6960	T103	MAKE CURRENT CYLINDER = 1
6975	T104	ERROR REG.1 - BIT #7 - HEADER COMPARE ERROR
7037	T105	MAKE CURRENT CYLINDER = 0
7052	T106	ERROR REG.1 - BIT #7 - HEADER COMPARE ERROR
7113	T107	RHER1 - BIT #8 - CRC ERROR (READING)
7182	T110	RHER1 - BIT 8 - CRC ERROR (WRITING)
7244	T111	MAKE CURRENT CYLINDER = 410.
7260	T112	RHDS1 (BIT #10) - LAST SECTOR TRANSFERRED, 'LST'
7372	T113	ERROR REGISTER 1 - BIT #9 AOE
7513	T114	MAKE CURRENT CYLINDER = 0
7528	T115	ERROR REGISTER 1 - BIT #10 IAE
7632	T116	ERROR REGISTER 1- BIT #10 IAE
7744	T117	ERROR REGISTER 1- BIT #10 IAE
7848	T120	END OF DRIVE
7894		
7895		***SUBROUTINES***
7896		
7901		END OF PASS ROUTINE
8011		SAVE REGISTERS ROUTINE
8041		FLOAT 1 AND 0
8100		CLEAR MEMORY ROUTINE

TABLE OF CONTENTS

8133	LOCAL TRAPS
8150	CLEAR DISK ROUTINE
8163	CHECK DISK STATUS ROUTINES
8293	SAVE ROUTINE
8321	WRITE CHECK ROUTINE
8365	COMPARE ROUTINE
8461	CRC GENERATION ROUTINE
8779	JAM CURRENT CYLINDER ROUTINE
8818	ECC GENERATION AND COMPARISON ROUTINE
9158	RH BASE ADDRESS CHANGE ROUTINE
9237	DISK SIMULATION
10249	SYSMAC LIBRARY ROUTINES
10253	SCOPE HANDLER ROUTINE
10328	CONVERT BINARY TO DECIMAL AND TYPE ROUTINE
10396	TYPE ROUTINE
10443	TTY INPUT ROUTINE
10546	READ AN OCTAL NUMBER FROM THE TTY
10600	ERROR HANDLER ROUTINE
10643	ERROR MESSAGE TYPEOUT ROUTINE
10700	BINARY TO OCTAL (ASCII) AND TYPE
10778	TRAP DECODER
10793	TRAP TABLE
10814	POWER DOWN AND UP ROUTINES

GO4

MAINDEC-11-DZRPS-D. RJPO4 DISKLESS RH11 TEST-PART 1
DZRPSD.P11 OPERATIONAL SWITCH SETTINGS

MACY11 27(663) 8-OCT-75 16:14 PAGE 2

SEQ 0044

55
56

;* 6

ECC TEST-COMPARE END RESULTS ONLY IF SMO8 IS LOW

57
58
59
60
61
62
63
64
65
66
67
68
69
70
71
72
73
74
75
76
77
78
79
80
81
82
83
84
85
86
87
88
89
90
91
92
93
94
95
96
97
98
99
100
101
102
103
104
105
106
107
108
109
110

.SBTTL BASIC DEFINITIONS

```

: *INITIAL ADDRESS OF THE STACK POINTER *** 1000 ***
STACK= 1000
.EQUIV EMT,ERROR          ;; BASIC DEFINITION OF ERROR CALL
.EQUIV IOT,SCOPE          ;; BASIC DEFINITION OF SCOPE CALL
PS= 177776                ;; PROCESSOR STATUS WORD
.EQUIV PS,PSM
STKLM= 177774              ;; STACK LIMIT REGISTER
PIRQ= 177772              ;; PROGRAM INTERRUPT REQUEST REGISTER
SMR= 177570               ;; SWITCH REGISTER
DISPLAY=SMR

```

: *GENERAL PURPOSE REGISTER DEFINITIONS

```

R0= X0                    ;; GENERAL REGISTER
R1= X1                    ;; GENERAL REGISTER
R2= X2                    ;; GENERAL REGISTER
R3= X3                    ;; GENERAL REGISTER
R4= X4                    ;; GENERAL REGISTER
R5= X5                    ;; GENERAL REGISTER
R6= X6                    ;; GENERAL REGISTER
R7= X7                    ;; GENERAL REGISTER
.EQUIV R6,SP              ;; STACK POINTER
.EQUIV R7,PC              ;; PROGRAM COUNTER

```

: *PRIORITY LEVEL DEFINITIONS

```

PR0= 0                    ;; PRIORITY LEVEL 0
PR1= 40                   ;; PRIORITY LEVEL 1
PR2= 100                  ;; PRIORITY LEVEL 2
PR3= 140                  ;; PRIORITY LEVEL 3
PR4= 200                  ;; PRIORITY LEVEL 4
PR5= 240                  ;; PRIORITY LEVEL 5
PR6= 300                  ;; PRIORITY LEVEL 6
PR7= 340                  ;; PRIORITY LEVEL 7

```

: *"SWITCH REGISTER" SWITCH DEFINITIONS

```

SW15= 100000
SW14= 40000
SW13= 20000
SW12= 10000
SW11= 4000
SW10= 2000
SW09= 1000
SW08= 400
SW07= 200
SW06= 100
SW05= 40
SW04= 20
SW03= 10
SW02= 4
SW01= 2
SW00= 1
.EQUIV SW09,SW9

```



```

111 .EQUIV SM08, SM8
112 .EQUIV SM07, SM7
113 .EQUIV SM06, SM6
114 .EQUIV SM05, SM5
115 .EQUIV SM04, SM4
116 .EQUIV SM03, SM3
117 .EQUIV SM02, SM2
118 .EQUIV SM01, SM1
119 .EQUIV SM00, SM0

```

```

120
121 ;#DATA BIT DEFINITIONS (BIT00 TO BIT15)
122 100000 BIT15= 100000
123 040000 BIT14= 40000
124 020000 BIT13= 20000
125 010000 BIT12= 10000
126 004000 BIT11= 4000
127 002000 BIT10= 2000
128 001000 BIT09= 1000
129 000400 BIT08= 400
130 000200 BIT07= 200
131 000100 BIT06= 100
132 000040 BIT05= 40
133 000020 BIT04= 20
134 000010 BIT03= 10
135 000004 BIT02= 4
136 000002 BIT01= 2
137 000001 BIT00= 1

```

```

138 .EQUIV BIT09, BIT9
139 .EQUIV BIT08, BIT8
140 .EQUIV BIT07, BIT7
141 .EQUIV BIT06, BIT6
142 .EQUIV BIT05, BIT5
143 .EQUIV BIT04, BIT4
144 .EQUIV BIT03, BIT3
145 .EQUIV BIT02, BIT2
146 .EQUIV BIT01, BIT1
147 .EQUIV BIT00, BIT0

```

```

148
149 ;#BASIC "CPU" TRAP VECTOR ADDRESSES
150 000004 ERRVEC= 4 ;; TIME OUT AND OTHER ERRORS
151 000010 RESVEC= 10 ;; RESERVED AND ILLEGAL INSTRUCTIONS
152 000014 TBITVEC= 14 ;; "T" BIT
153 000014 TRTVEC= 14 ;; TRACE TRAP
154 000014 BPTVEC= 14 ;; BREAKPOINT TRAP (BPT)
155 000020 IOTVEC= 20 ;; INPUT/OUTPUT TRAP (IOT) **SCOPE**
156 000024 PMRVEC= 24 ;; POWER FAIL
157 000030 EMTVEC= 30 ;; EMULATOR TRAP (EMT) **ERROR**
158 000034 TRAPVEC= 34 ;; "TRAP" TRAP
159 000060 TKVEC= 60 ;; TTY KEYBOARD VECTOR
160 000064 TPVEC= 64 ;; TTY PRINTER VECTOR
161 000240 PIRQVEC= 240 ;; PROGRAM INTERRUPT REQUEST VECTOR
162

```

163
 164
 165
 166
 167
 168
 169
 170
 171
 172
 173
 174
 175
 176
 177
 178
 179
 180
 181
 182

.SBTTL TRAP CATCHER

000000

.=0
 ;#ALL UNUSED LOCATIONS FROM 4 - 776 CONTAIN A ".+2,HALT"
 ;#SEQUENCE TO CATCH ILLEGAL TRAPS AND INTERRUPTS
 ;#LOCATION 0 CONTAINS 0 TO CATCH IMPROPERLY LOADED VECTORS

.SBTTL STARTING ADDRESS(ES)

000200

.=200

000200 000137 004220

JMP @#BEGIN

::JUMP TO STARTING ADDRESS OF PROGRAM

000210 000137 004210

.=210

JMP @#BEGIN2

;ADDRESSES

;JUMP SELECT TEST

;#STARTING ADDRESS 200 FOR NORMAL STARTS
 ;#THIS WILL TEST ALL RPO4'S ON THE SYSTEM A SINGLE DRIVE AT A TIME
 ;#
 ;#STARTING ADDRESS 210 WILL TEST ONLY ONE SPECIFIED DRIVE


```

183
184      .SBTTL MEMORY MANAGEMENT DEFINITIONS
185
186      ;#KT11 VECTOR ADDRESS
187
188      000250      MMVEC= 250
189
190      ;#KT11 STATUS REGISTER ADDRESSES
191
192      177572      SR0= 177572
193      177574      SR1= 177574
194      177576      SR2= 177576
195      172516      SR3= 172516
196
197      ;#KERNAL "I" PAGE DESCRIPTOR REGISTERS
198
199      172300      KIPDR0= 172300
200      172302      KIPDR1= 172302
201      172304      KIPDR2= 172304
202      172306      KIPDR3= 172306
203      172310      KIPDR4= 172310
204      172312      KIPDR5= 172312
205      172314      KIPDR6= 172314
206      172316      KIPDR7= 172316
207
208      ;#KERNAL "I" PAGE ADDRESS REGISTERS
209
210      172340      KIPAR0= 172340
211      172342      KIPAR1= 172342
212      172344      KIPAR2= 172344
213      172346      KIPAR3= 172346
214      172350      KIPAR4= 172350
215      172352      KIPAR5= 172352
216      172354      KIPAR6= 172354
217      172356      KIPAR7= 172356
218
219      ;*****
220      001110      . =1110 ; ?
221

```

```

222 ;*****
223
224 .SBTTL COMMON TAGS
225
226 ;*THIS TABLE CONTAINS VARIOUS COMMON STORAGE LOCATIONS
227 ;*USED IN THE PROGRAM.
228
229      000046      000046      .=46
230 000046 037310 SENDAD ;;LOGICAL END OF PROGRAM
231
232      001100      .=1100
233
234 SCHTAG:
235 001100 000000 SPASS: .WORD 0 ;; START OF COMMON TAGS
236 001102 000 STSTNM: .BYTE 0 ;; CONTAINS PASS COUNT
237 001103 000 SERFLG: .BYTE 0 ;; CONTAINS THE TEST NUMBER
238 001104 000000 SICNT: .WORD 0 ;; CONTAINS ERROR FLAG
239 001106 000000 SLPADR: .WORD 0 ;; CONTAINS SUBTEST ITERATION COUNT
240 001110 000000 SLPERR: .WORD 0 ;; CONTAINS SCOPE LOOP
241 001112 000000 SERTTL: .WORD 0 ;; CONTAINS SCOPE RETURN FOR ERRORS
242 001114 000 SITEMB: .BYTE 0 ;; CONTAINS TOTAL ERRORS DETECTED
243 001115 001 SERMAX: .BYTE 1 ;; CONTAINS ITEM CONTROL BYTE
244 001116 000000 SERRPC: .WORD 0 ;; CONTAINS MAX. ERRORS PER TEST
245 001120 000000 SGDADR: .WORD 0 ;; CONTAINS PC OF LAST ERROR INSTRUCTION
246 001122 000000 SBDADR: .WORD 0 ;; CONTAINS OF 'GOOD' DATA
247 001124 000000 SGDDAT: .WORD 0 ;; CONTAINS OF 'BAD' DATA
248 001126 000000 SBDAT: .WORD 0 ;; CONTAINS 'GOOD' DATA
249 001130 000000 000000 000000 SBDAT: .WORD 0,0,0 ;; CONTAINS 'BAD' DATA
250 001136 177560 STKS: .WORD 0 ;; RESERVED--NOT TO BE USED
251 001140 177562 STKB: .WORD 0 ;; TTY KBD STATUS
252 001142 177564 STPS: .WORD 0 ;; TTY KBD BUFFER
253 001144 177566 STPB: .WORD 0 ;; TTY PRINTER STATUS REG.
254 001146 000 SNULL: .BYTE 0 ;; TTY PRINTER BUFFER REG.
255 001147 002 SFILLS: .BYTE 2 ;; CONTAINS NULL CHARACTER FOR FILLS
256 001150 012 SFILLC: .BYTE 12 ;; CONTAINS # OF FILLER CHARACTERS REQUIRED
257 001151 000 STPFLG: .BYTE 0 ;; INSERT FILL CHARS. AFTER A "LINE FEED"
258 001152 000000 SREGAD: .WORD 0 ;; "TERMINAL AVAILABLE" FLAG (BIT<07>=0=YES)
259
260 001154 000000 SREG0: .WORD 0 ;; CONTAINS THE FROM
261 001156 000000 SREG1: .WORD 0 ;; WHICH ($REG0) WAS OBTAINED
262 001160 000000 SREG2: .WORD 0 ;; CONTAINS (($REGAD)+0)
263 001162 000000 SREG3: .WORD 0 ;; CONTAINS (($REGAD)+2)
264 001164 000000 SREG4: .WORD 0 ;; CONTAINS (($REGAD)+4)
265 001166 000000 SREG5: .WORD 0 ;; CONTAINS (($REGAD)+6)
266 001170 000000 STMP0: .WORD 0 ;; CONTAINS (($REGAD)+10)
267 001172 000000 STMP1: .WORD 0 ;; CONTAINS (($REGAD)+12)
268 001174 000000 STMP2: .WORD 0 ;; USER DEFINED
269 001176 000000 STMP3: .WORD 0 ;; USER DEFINED
270 001200 000000 STMP4: .WORD 0 ;; USER DEFINED
271 001202 000000 STMP5: .WORD 0 ;; USER DEFINED
272 001204 000000 STIMES: 0 ;; MAX. NUMBER OF ITERATIONS
273 001206 000000 SESCAPE: 0 ;; ESCAPE ON ERROR
274 001210 177607 000377 SBELL: .ASCIZ <207><377><377> ;; CODE FOR BELL
275 001214 077 SQUES: .ASCII !/? ;; QUESTION MARK

```


MO4

MAINDEC-11-DZRPS-D, RJPO4 DISKLESS RH11 TEST-PART 1
DZRPSD.P11 COMMON TAGS

MACY11 27(663) 8-OCT-75 16:14 PAGE 8

SEQ 0050

276 001215 015
277 001216 000012

SCRLF: .ASCII <15>
SLF: .ASCIZ <12>

:::CARRIAGE RETURN
:::LINE FEED

278
279
280
281
282
283
284
285
286
287
288
289
290
291
292
293
294
295
296
297
298
299
300
301
302
303
304
305
306
307
308
309
310
311
312
313
314
315
316
317
318
319
320
321
322
323
324
325
326
327
328
329
330
331

.SBTTL ERROR POINTER TABLE

;*THIS TABLE CONTAINS THE INFORMATION FOR EACH ERROR THAT CAN OCCUR.
;*THE INFORMATION IS OBTAINED BY USING THE INDEX NUMBER FOUND IN
;*LOCATION SITEMB. THIS NUMBER INDICATES WHICH ITEM IN THE TABLE IS PERTINENT.
;*NOTE1: IF SITEMB IS 0 THE ONLY PERTINENT DATA IS (SERRPC).
;*NOTE2: EACH ITEM IN THE TABLE CONTAINS 4 POINTERS EXPLAINED AS FOLLOWS:

;* EM ::POINTS TO THE ERROR MESSAGE
;* DH ::POINTS TO THE DATA HEADER
;* DT ::POINTS TO THE DATA
;* DF ::POINTS TO THE DATA FORMAT

SERRTB:

;ITEM1

001220 054216 EM1 ;WRONG DATA IN READING OR WRITING HARDWARE REGISTER
001222 057461 DH1 ;PC
;REG. ADDR.
;GOOD DATA
;RECEIVED DATA
;SERRPC,REGADR,SGDDAT,SBDDAT
;0,0,0,0,0

;ITEM2

001230 054301 EM2 ;ERROR ON DATA COMMAND
001232 062615 DH33 ;PC
;PC OF JSR
;TEST NO
;WORD NO.
;GOOD DATA
;CONTENTS OF RHCS1
;CONTENTS OF RHDS1
;CONTENTS OF RHER1
;SERRPC,PCJSR,STSTNM,ERNORD,SGDDAT,CS1,DS1,ERI
;0,0,0,1,0,0,0,0

;ITEM3

001240 054301 EM2 ;ERROR ON DATA COMMAND
001242 062372 DH32 ;PC
;PC OF JSR
;TEST NO

332					:WORD NO.
333					:GOOD DATA
334					:BAD DATA
335					:CONTENTS OF RHCS1
336					:CONTENTS OF RHDS1
337					:CONTENTS OF RHER1
338					
339	001244	064374		DT32	:SERRPC,PCJSR,STSTNM,ERNORD,SGDDAT,SBDDAT,CS1,DS1,ER1
340	001246	064715		DF32	:0,0,0,1,0,0,0,0,0,
341					
342					
343					
344	001250	054301		EM2	:ERROR ON DATA COMMAND
345					
346	001252	062170		DH31	:PC
347					:TEST NO
348					:WORD NO.
349					:GOOD DATA
350					:BAD DATA
351					:CONTENTS OF RHCS1
352					:CONTENTS OF RHDS1
353					:CONTENTS OF RHER1
354					
355	001254	064352		DT31	:SERRPC,STSTNM,ERNORD,SGDDAT,SBDDAT,CS1,DS1,ER1
356	001256	064705		DF31	:0,0,1,0,0,0,0,0,
357					
358					
359					
360					
361	001260	000000		0	:
362	001262	000000		0	:
363	001264	064352		DT31	:SERRPC,STSTNM,ERNORD,SGDDAT,SBDDAT,CS1,DS1,ER1
364	001266	064705		DF31	:0,0,1,0,0,0,0,0,
365					
366					
367					
368	001270	054330		EM6	:ERROR ON WRITE HEADER AND DATA
369					
370	001272	062372		DH32	:PC
371					:PC OF JSR
372					:TEST NO
373					:WORD NO.
374					:GOOD DATA
375					:BAD DATA
376					:CONTENTS OF RHCS1
377					:CONTENTS OF RHDS1
378					:CONTENTS OF RHER1
379					
380	001274	064374		DT32	:SERRPC,PCJSR,STSTNM,ERNORD,SGDDAT,SBDDAT,CS1,DS1,ER1
381	001276	064715		DF32	:0,0,0,1,0,0,0,0,0,
382					
383					
384					
385					

:ITEM7

386	001300	054330	EM6	: ERROR ON WRITE HEADER AND DATA
387	001302	057604	DH2	: PC
388				: TEST NO
389				: WORD NO.
390				: GOOD DATA
391				: BAD DATA
392	001304	064066	DT3	: SERRPC, STSTNM, ERWORD, SGDDAT, SBDDAT
393	001306	064567	DF3	: 0,0,1,0,0,
394				
395				
396				
397	001310	000000		: ;ITEM10
398	001312	000000	0	:
399	001314	064066	DT3	: SERRPC, STSTNM, ERWORD, SGDDAT, SBDDAT
400	001316	064567	DF3	: 0,0,1,0,0,
401				
402				
403				
404	001320	054367	EM11	: CONTROLLER OR DRIVE STATUS
405	001322	057727	DH11	: PC
406				: TEST NO
407				: FAILING REG. ADDR
408				: CONTENTS OF RHCS1
409				: CONTENTS OF RHCS2
410				: CONTENTS OF RHDS1
411				: CONTENTS OF RHER1
412	001324	064102	DT11	: SERRPC, STSTNM, SBDADR, CS1, CS2, DS1, ER1
413	001326	064574	DF11	: 0,0,0,0,0,0
414				
415				
416				
417	001330	054367	EM11	: ;ITEM12
418				: ;WRONG DATA FROM SILO
419	001332	057461	DH1	: PC
420				: REG. ADDR
421				: GOOD DATA
422				: RECEIVED DATA
423	001334	064040	DT1	: SERRPC, REGADR, SGDDAT, SBDDAT
424	001336	064556	DF1	: 0,0,0,0
425				
426				
427				
428	001340	000000		: ;ITEM13
429	001342	000000	0	:
430	001344	064040	DT1	: SERRPC, TSTNM, REGADR, SGDDAT, SBDDAT
431	001346	064556	DF1	: 0,0,0,0,0
432				
433				
434				
435	001350	054422	EM14	: ;ITEM14
436	001352	060107	DH14	: REGISTER FAILED
437				: PC
438				: FAILING REG. ADDR
439				: CONTENTS OF FAILING REG.
				: CONTENTS OF RHCS1

440				: CONTENTS OF RHCS2
441				: CONTENTS OF RHDS1
442				: CONTENTS OF RHER1
443	001354	064122	DT14	: SERRPC, SBADR, SBDDAT, CS1, CS2, DS1, ER1
444	001356	064603	DF14	: 0,0,0,0,0,0,0
445				
446				
447				
448	001360	054442		: SPECIFIED REG. NON EXISTANT SO ABORT
449			EM15	: PROGRAM
450	001362	060307	DH15	: PC
451				: ADDR. OF REG
452	001364	064144	DT15	: SERRPC, TEMP1
453	001366	064613	DF15	: 0,0
454				
455				
456				
457	001370	054513		: WAIT LOOP FAILED
458	001372	060340	EM16	: PC
459			DH16	: MAT PC
460				: BIT WANTED
461				: REG. ADR.
462				: REG. CONT.
463	001374	064154	DT16	: SERRPC, STMP3, STMP1, STMP0, SBDDAT
464	001376	064616	DF16	: 0,0,0,0
465				
466				
467				
468	001400	054534		: WRITE CHECK FAILING
469	001402	060503	EM17	: PC
470			DH17	: TEST NO
471				: CONTENTS OF RHBA
472				: CONTENTS OF RHDB
473				: CONTENTS OF RHMC
474				: CONTENTS OF RHCS1
475				: CONTENTS OF RHCS2
476	001404	064172	DT17	: SERRPC, STSTNM, SBA, DB, MC, CS1, CS2
477	001406	064623	DF17	: 0,0,0,0,0,0,0
478				
479				
480				
481	001410	054560		: REGISTER FAILING
482	001412	060666	EM20	: PC
483			DH20	: TST NO
484				: CONTENTS OF RHER1
485				: CONTENTS OF RHER2
486				: CONTENTS OF RHER3
487				: CONTENTS OF RHAS
488				: CONTENTS OF RHDS1
489	001414	064212	DT20	: SERRPC, TSTNM ER1, ER2, ER3, AS, DS1
490	001416	064632	DF20	: 0,0,0,0,0,0,0
491				
492				
493				

: ITEM21

494	001420	054601		EM21		: INTERRUPT FAILING
495	001422	061051		DH21		: PC
496						: TEST NO
497						: CONTENTS OF RHCS1
498						: CONTENTS OF RHAS
499						: CONTENTS OF RHDS1
500	001424	064232		DT21		: SERRPC, TSTNM, CS1, AS, DS1
501	001426	064641		DF21		: 0,0,0,0,0
502						
503						
504						
505	001430	054623	; ITEM22	EM22		: ERROR IN DRIVE PRESENT -
506						: LOOKING AT RHAS AND RHCS2-NED(BIT#12)
507						: DRIVES PRESENT DO NOT AGREE
508						: NOTE: ON DUAL PORT SYSTEM
509						: DRIVE ON OTHER PORT WILL NOT GIVE NED
510						: HENCE THERE WILL BE A MISMATCH
511	001432	061171		DH22		: PC
512						: TEST NO
513						: RHAS UNIT (RHER1 BITS SET)
514						: RHCS2 UNIT ('NED' BIT TEST)
515	001434	064246		DT22		: SERRPC, TSTNM
516	001436	064646		DF22		: 0,0
517						
518						
519			; ITEM23			
520	001440	000000		0		: NO LONGER USED DUE TO SPECIAL 'NED'
521	001442	000000		0		: TEST TABLE TYPE OUT ROUTINE
522	001444	000000		0		
523	001446	000000		0		
524						
525						
526			; ITEM 24			
527	001450	055272		EM24		: LOOK AHEAD REGISTER AT THE
528						: BEGINNING OF A SECTOR IS IN
529						: ERROR
530	001452	061274		DH24		: PC
531						: RHDST
532						: BAD RHLA
533						: GOOD RHLA
534						: SECTOR NO
535						: SECTOR CLOCK
536	001454	064254		DT24		: SERRPC, DST, SBDDAT, STMP1, STMP2, STMP3
537	001456	064652		DF24		: 0,0,0,0,0
538						
539			; ITEM 25			
540	001460	055365		EM25		: LOOK AHEAD REGISTER IS
541						: IN ERROR
542						
543	001462	061274		DH24		: PC
544						: RHDST
545						: BAD RHLA
546						: GOOD RHLA
547						: SECTOR NO

548					:SECTOR CLOCK
549	001464	064254		DT24	:SERRPC,DST,\$BDDAT,\$TMP1,\$TMP2,\$TMP3
550	001466	064652		DF24	:0,0,0,0,0
551			;ITEM26		
552	001470	054367		EM11	:CONTROLLER OR DRIVE STATUS
553					
554	001472	061457		DH26	:PC
555					:PC OF JSR
556					:FAILING REGISTER ADDRESS
557					:CONTENTS OF RHCS1
558					:CONTENTS OF RHCS2
559					:CONTENTS OF RHDS1
560					:CONTENTS OF RHER1
561					
562	001474	064274		DT26	:SERRPC,PCJSR,\$BDADR,CS1,CS2,DS1,ER1
563	001476	064661		DF26	:0,0,0,0,0,0,
564					
565					
566					
567			;ITEM27		
568	001500	054216		EM1	:ERROR IN READING OR WRITING HARDWARE REGISTER
569					
570	001502	061662		DH27	:PC
571					:PC OF JSR
572					:TEST NUMBER
573					:FAILING REGISTER
574					:GOOD DATA
575					:RECEIVED DATA
576					
577	001504	064316		DT27	:SERRPC,PCJSR,TSTNM,REGADR,\$GDDAT,\$BDDAT
578	001506	064671		DF27	:0,0,0,0,0,0
579					
580					
581					
582			;ITEM30		
583	001510	055425		EM30	:CURRENT CYLINDER DOES NOT REFLECT DESIRED CYLINDER REG.
584	001512	062025		DH30	:PC
585					:PC OF JSR
586					:REGISTER ADDRESS
587					:GOOD DATA
588					:BAD DATA
589					
590	001514	064334		DT30	:SERRPC,PCJSR,REGADR,\$GDDAT,\$BDDAT
591	001516	064677		DF30	:0,0,0,0,0
592					
593					
594					
595			;ITEM31		
596	001520	055547		EM31	:ECC GENERATED IS INCORRECT
597					:EVERY WORD IN THIS SECTOR IS GIVEN IN "DATA USED"
598					
599	001522	063020		DH34	:PC
600					:TEST NUMBER
601					:GOOD ECC1

602				:GOOD EC2C
603				:WRITTEN ECC1
604				:WRITTEN ECC2
605				:DATA USED
606				
607	001524	064442	DT34	:SERRPC,TSTNM,GECC1,GECC2,MECC1,MECC2,DISK
608				
609	001526	064736	DF34	:0,0,0,0,0,0,0
610				
611				
612			; ITEM32	
613	001530	055672	EM32	:ON READ COMMAND AFTER DATA AND ECC HAVE BEEN READ
614				:ECC REGISTER OR RHER1 IS IN ERROR
615				:ONLY LOWER 11 BITS OF PATTERN REGISTER
616				:CAN BE READ
617				:THIS SHUOLD MATCH LOWER 11 BITS OF ECC1
618				
619	001532	063203	DH35	:PC
620				:TEST NUMBER
621				:GOOD ECC1
622				:GOOD ECC2
623				:PATTERN REGISTER
624				:RHER1
625				
626	001534	064462	DT35	:SERRPC,TSTNM,GECC1,GECC2,EC2,ER1
627				
628	001536	064745	DF35	:0,0,0,0,0,0
629				
630				
631				
632			; ITEM33	
633	001540	056161	EM33	:HIGH COUNT BIT NOT HIGH AFTER 38859 CLOCKS
634	001542	063406	DH36	:PC
635				:PC OF JSR
636				:TEST NUMBER
637				:RHR
638				:POSITION REG.
639				:PATTERN REGISTER
640				
641	001544	064504	DT36	:SERRPC,PCJSR,TSTNM,MR,EC1,EC2
642				
643	001546	064755	DF36	:0,0,0,0,0,0
644				
645			; ITEM34	
646	001550	056233	EM34	:ZERO DETECT BIT NOT HIGH WHEN THE
647				:32 BIT ECC REGISTER HAS ITS 21 BITS
648				:OF ZEROS
649				:ERROR PRINTOUT WILL CONTINUE TILL
650				:ZERO DETECT BIT IS HIGH
651	001552	063406	DH36	:PC
652				:PC OF JSR
653				:TEST NUMBER
654				:RHR
655				:POSITION REG.

656					;PATTERN REGISTER
657					
658	001554	064504		DT36	;SERRPC,PCJSR,TSTNM,MR,EC1,EC2
659					
660	001556	064755		DF36	;0,0,0,0,0,0
661					
662					
663					
664					;ITEM35
665	001560	056326		EM35	;POSITION REGISTER OR 11 BITS OF ;PATTERN REGISTER INCORRECT ;LOWER 11 BITS OF PATTERN REGISTER ;SHOULD MATCH LOWER 11 BITS OF GOOD ECC1 ;DATA ENVELOPE AND N-CODE ZEROS ARE IN DECIMAL
666					
667					
668					
669					
670					
671	001562	063545		DH37	;PC ;TEST NUMBER ;ECC POSITION ;GOOD POSITION ;GOOD ECC1 ;GOOD ECC2 ;ECC PATTERN ;DATA ENVELOPE ;N-CODE ZEROS
672					
673					
674					
675					
676					
677					
678					
679					
680					
681	001564	064522		DT37	;SERRPC,TSTNM,EC1,POSITI,GECC1,GECC2,EC2,DATENV,ZCODE
682					
683	001566	064763		DF37	;0,0,0,0,0,0,0,0,0
684					
685					
686					
687					;ITEM36
688	001570	056625		EM36	;ON A READ COMMAND WITH NON CORRECTABLE ;ERROR INSERTED DCK AND ECH SHOULD BE SET
689					
690	001572	063203		DH35	;PC ;TEST NUMBER ;GOOD ECC1 ;GOOD ECC2 ;PATTERN REGISTER ;POSITION REGISTER ;RHER1
691					
692					
693					
694					
695					
696					
697					
698	001574	064462		DT35	;SERRPC,TSTNM,GECC1,GECC2,EC2,EC1,ER1
699					
700	001576	064745		DF35	;0,0,0,0,0,0,0
701					
702					
703	001600	056736		EM37	;PGE ERROR
704	001602	057727		DH11	;PC ;TEST NO ;FAILING REG. ADDR ;CONTENTS OF RHCS1 ;CONTENTS OF RHCS2 ;CONTENTS OF RHDS1
705					
706					
707					
708					
709					

710		
711	001604	064102
712	001606	064574
713		
714		
715	001610	057073
716		
717	001612	063763
718		
719		
720	001614	064546
721	001616	064774

;ITEM40

DT11
DF11
EM40
DH40
DT40
DF40

```

:CONTENTS OF RHER1
:SERRPC, $TSTNM, $BDADR, CS1, CS2, DS1, ER1
:0,0,0,0,0,0

:RHC DID NOT = 0 AFTER A READ OR
:WRITE HEADER AND DATA
:PC
:TEST NO
:CONTENTS OF RHC
:SERRPC, TSTNM, $BDAT
:0,0,0

```


722
723
724
725
726
727
728
729
730
731
732
733
734
735
736
737
738
739
740
741
742
743
744
745
746
747
748
749
750
751
752
753
754
755
756
757
758
759
760
761
762
763
764
765
766
767
768
769
770
771
772
773
774
775

:RH11 REGISTERS

:WORD COUNT REGISTER (RHWC)
:EACH BIT IS CALLED BY BIT NUMBER

:BUS ADDRESS REGISTER (RHBA)
:EACH BIT IS CALLED BY BIT NUMBER

:CONTROL AND STATUS REGISTER 2 (RHCS2)

000001
000002
000004
000010
000020
000040
000100
000200
000400
001000
002000
004000
010000
020000
040000
100000

US1= 1 ;UNIT SELECT (BIT #0)
US2= 2 ;UNIT SELECT (BIT #1)
US4= 4 ;UNIT SELECT (BIT #2)
BAI= 10 ;BUS ADDRESS INCREMENT INHIBIT (BIT #3)
PAT= 20 ;INVERT PARITY ON MASS BUS TO EVEN (BIT #4)
CLR= 40 ;CLEAR (BIT #5)
IR= 100 ;INPUT READY (BIT #6)
OR= 200 ;OUTPUT READY (BIT #7)
MPE= 400 ;MASS BUS PARITY ERROR (BIT #8)
MXF= 1000 ;MISSED TRANSFER ERROR (BIT #9)
PGE= 2000 ;PROGRAM ERROR (BIT #10)
NEM= 4000 ;NON EXISTANT MEMORY (BIT #11)
NED= 10000 ;NON EXISTANT DRIVE (BIT #12)
UPE= 20000 ;UNIBUS PARITY ERROR (BIT #13)
MCE= 40000 ;WRITE CHECK ERROR (BIT #14)
DLT= 100000 ;DATA LATE (BIT #15)

:DATA BUFFER REGISTER (RHDB)
:EACH BIT IS CALLED BY BIT NUMBER

:RPO4 REGISTERS

:CONTROL AND STATUS 1 REGISTER. (#00)

000001
000100
000200
000400
001000

GO= 1 ;GO (BIT #0)
IE= 100 ;INTERRUPT ENABLE (BIT #6)
RDY= 200 ;READY (BIT #7)
A16= 400 ;HIGH ORDER UNIBUS BITS (BIT #8)
A17= 1000 ;HIGH ORDER UNIBUS BITS (BIT #9)

776	002000	PSEL=	2000	;PORT SELECT (BIT #10)
777	004000	DVA=	4000	;DEVICE AVAILABLE (BIT #11)
778	020000	MCPE=	20000	;MASSBUSS PARITY ERROR (BIT #13)
779	040000	TRE=	40000	;TRANSFER ERROR (BIT #14)
780	100000	SC=	100000	;SPECIAL CONDITION (BIT #15)
781				
782		;STATUS REGISTER (RHDS1) (#01)		
783				
784	000001	DFS=	1	;DRIVE FORWARD 5"/SEC. (BIT #0)
785	000002	DF20=	2	;DRIVE FORWARD 20"/SEC. (BIT #1)
786	000004	DIG8=	4	;DRIVE TO INNER GAVRD BAND (BIT #2)
787	000010	GRV=	10	;GO REVERSE (BIT #3)
788	000020	DL64=	20	;DIFFERENCE LESS THAN 64 (BIT #4)
789	000040	DE1=	40	;DIFFERENCE EQUALS 1 (BIT #5)
790	000100	VV=	100	;VOLUME VALID (BIT #6)
791	000200	DRY=	200	;DRIVE READY (BIT #7)
792	000400	DPR=	400	;DRIVE PRESENT (BIT #8)
793	001000	PRG=	1000	;PROGRAMABLE (BIT #9)
794	002000	LST=	2000	;LAST SECTOR TRANSFERRED (BIT #10)
795	004000	WRL=	4000	;WRITE LOCK (BIT #11)
796	010000	MOL=	10000	;MEDIUM ON-LINE (BIT #12)
797	020000	PIP=	20000	;POSITIONING OPERATION IN PROGRESS (BIT #13)
798	040000	ERR=	40000	;COMPOSIT ERROR. (BIT #14)
799	100000	ATA=	100000	;ATTENTION ACTIVE (BIT #15)
800				
801		;ERROR REGISTER #01 (RHER1) (#02)		
802	000001	ILF=	1	;ILLEGAL FUNCTION (BIT #0)
803	000002	ILR=	2	;ILLEGAL REGISTER (BIT #1)
804	000004	RMR=	4	;REGISTER MODIFICATION REFUSED (BIT #2)
805	000010	PAR=	10	;PARITY ERROR (BIT #3)
806	000020	FER=	20	;FORMAT ERROR (BIT #4)
807	000040	MCF=	40	;WRITE CLOCK FAIL (BIT #5)
808	000100	ECH=	100	;ECC HARD ERROR (BIT #6)
809	000200	HCE=	200	;HEADER COMPARE ERROR (BIT #7)
810	000400	HCRC=	400	;HEADER CRC ERROR (BIT #8)
811	001000	AOE=	1000	;ADDRESS OVERFLOW ERROR (BIT #9)
812	002000	IAE=	2000	;INVALID ADDRESS ERROR (BIT #10)
813	004000	MLE=	4000	;WRITE LOCK ERROR (BIT #11)
814	010000	DTE=	10000	;DRIVE TIMING ERROR (BIT #12)
815	020000	OPI=	20000	;OPERATION INCOMPLETE (BIT #13)
816	040000	UNS=	40000	;DRIVE UNSAFE (BIT #14)
817	100000	DCK=	100000	;DATA CHECK ERROR (BIT 15)
818				
819		;MAINTAINABILITY REGISTER (RHMR) (#03)		
820				
821	000001	DMD=	1	;DIAGINOSTIC MODE (BIT #0)
822	000002	MCLK=	2	;MAINTAINABILITY CLOCK (BIT #1)
823	000004	MINX=	4	;MAINTAINABILITY INDEX (BIT #2)
824	000010	MSTCK=	10	;MAINTAINABILITY SECTOR CLOCK (BIT #3)
825	000020	MRD=	20	;MAINTAINABILITY READ (BIT #4)
826	000040	MWR=	40	;MAINTAINABILITY WRITE (BIT #5)
827	000200	DENVL=	200	;DATA ENVELOPE (BIT #7)
828	000400	ZER=	400	;ZERO DETECT (BIT #8)
829	001000	DTSY=	1000	;MAINTAINABILITY SYNC DETECTED (BIT #9)

830
831
832
833
834
835
836
837
838
839
840
841
842
843
844
845
846
847
848
849
850
851
852
853
854
855
856
857
858
859
860
861
862
863
864
865
866
867
868
869
870
871
872
873
874
875
876
877
878
879
880
881
882
883

;ATTENTION SUMMARY PSEUDO-REGISTER (RHAS) (#04)

000001	AT0=	1	;DEVICE 0 (BIT #0)
000002	AT1=	2	;DEVICE 1 (BIT #1)
000004	AT2=	4	;DEVICE 2 (BIT #2)
000010	AT3=	10	;DEVICE 3 (BIT #3)
000020	AT4=	20	;DEVICE 4 (BIT #4)
000040	AT5=	40	;DEVICE 5 (BIT #5)
000100	AT6=	100	;DEVICE 6 (BIT #6)
000200	AT7=	200	;DEVICE 7 (BIT #7)

;DESIRED SECTOR/TRACK ADDRESS REGISTER (RHDST) (#1)
 ;EACH BIT IS CALLED BY BIT NUMBER

;DRIVE TYPE REGISTER (RHDT) (#06)
 ;EACH BIT IS CALLED BY BIT NUMBER

;LOOK-AHEAD REGISTER (RHLA) (#07)

000001	EXT1=	1	;EXTENSION 1 (BIT #0)
000002	EXT2=	2	;EXTENSION 2 (BIT #1)
000004	EXT4=	4	;EXTENSION 3 (BIT #2)
000010	EXT10=	10	;EXTENSION 4 (BIT #3)
000020	EXT20=	20	;EXTENSION 5 (BIT #4)
000040	EXT40=	40	;EXTENSION 6 (BIT #5)
000100	SC1=	100	;SECTOR COUNT FIELD 0 (BIT #6)
000200	SC2=	200	;SECTOR COUNT FIELD 1 (BIT #7)
000400	SC4=	400	;SECTOR COUNT FIELD 2 (BIT #8)
001000	SC10=	1000	;SECTOR COUNT FIELD 3 (BIT #9)
002000	SC20=	2000	;SECTOR COUNT FIELD 4 (BIT #10)
004000	TRK1=	4000	;TRACK FIELD 1 (BIT #11)
010000	TRK2=	10000	;TRACK FIELD 2 (BIT #12)
020000	TRK4=	20000	;TRACK FIELD 3 (BIT #13)
040000	TRK10=	40000	;TRACK FIELD 4 (BIT #14)
100000	TRK20=	100000	;TRACK FIELD 5 (BIT #15)

;ERROR REGISTER #2 (RHER2) (#10)

000001	MCU=	1	;WRITE CURRENT UNSAFE (BIT #0)
000002	CSF=	2	;CURRENT SINK FAILURE (BIT #1)
000004	MSU=	4	;WRITE SELECT UNSAFE (BIT #2)

884	000010	CSU=	10	:CURRENT SWITCH UNSAFE (BIT #3)
885	000020	MSE=	20	:MOTOR SEQUENCE ERROR (BIT #4)
886	000040	TDF=	40	:TRANSITIONS DETECTOR FAILURE (BIT #5)
887	000100	TUF=	100	:TRANSITIONS UNSAFE (BIT #6)
888	000200	FEN=	200	:FAILSAFE ENABLED (BIT #7)
889	000400	MRU=	400	:WRITE READY UNSAFE (BIT #8)
890	001000	MHS=	1000	:MULTIPLE HEAD SELECT (BIT #9)
891	002000	NHS=	2000	:NO HEAD SELECTION (BIT #10)
892	004000	IXE=	4000	:INDEX ERROR (BIT #11)
893	010000	VU30=	10000	:30VOLT UNSAFE (BIT #12)
894	020000	PLU=	20000	:PLO UNSAFE (BIT #13)
895	100000	ACU=	100000	:ACUNSAFE (BIT #15)
896				
897				
898				
899	000001			
900	000002	OF25=	1	:OFFSET 25 MICRO INCHES (BIT #0)
901	000004	OF50=	2	:OFFSET 50 MICRO INCHES (BIT #1)
902	000010	OF100=	4	:OFFSET 100 MICRO INCHES (BIT #2)
903	000020	OF200=	10	:OFFSET 200 MICRO INCHES (BIT #3)
904	000040	OF400=	20	:OFFSET 400 MICRO INCHES (BIT #4)
905		OF800=	40	:OFFSET 800 MICRO INCHES (BIT #5)
906	000200	OFREV=	200	:OFFSET NEGATIVE (REVERSE) (BIT #7)
907	002000	HCI=	2000	:HEADER COMPARE INHIBIT (BIT #10)
908	004000	ECI=	4000	:ERROR CORRECTION CODE INHIBIT (BIT #11)
909	010000	FMT22=	10000	:FORMAT BIT (BIT #12)
910				
911				
912				
913				
914				
915				
916				
917				
918				
919				
920				
921				
922				
923				
924				
925				
926				
927				
928				
929				
930				
931				
932				
933				
934				
935				
936				
937				

;OFFSET REGISTER (RHOF) (#11)

;DESIRED CYLINDER ADDRESS (RHCA) (#12)
;EACH BIT IS CALLED BY BIT NUMBER.

;CURRENT CYLINDER ADDRESS (RHCC) (#13)
;EACH BIT IS CALLED BY BIT NUMBER

;SERIAL NUMBER REGISTER (RHSN) (#14)
;EACH IS CALLED BY BIT NUMBER

;ERROR REGISTER #03 (RHER3) (#15)

938
939
940
941
942
943
944
945
946
947
948
949
950
951
952
953
954
955
956
957
958
959
960
961
962

000001
000002
000010
000020
000040
000100
040000
100000

PSU= 1
VUF= 2
UMR= 10
PRE= 20
ACL= 40
DCL= 100
SKI= 40000
OCYL= 100000

;PACK SPEED UNSAFE (BIT #0)
;VELOCITY UNSAFE (BIT #1)
;ANY UNSAFE EXCEPT READ/WRITE (BIT #3)
;DISK PACK ROTATION ERROR (BIT #4)
;AC LOW (BIT #5)
;DC LOW (BIT #6)
;SEEK INCOMPLETE (BIT #14)
;OFF CYLINDER (BIT #15)

;ECC POSITION REGISTER (RHEC1) (#16)
;EACH BIT IS CALLED BY BIT NUMBER

;ECC PATTERN REGISTER (RHEC2) (#17)
;EACH BIT IS CALLED BY BIT NUMBER

963
964
965
966
967
968
969
970
971
972
973
974
975
976
977
978
979
980
981
982
983
984
985
986
987
988
989
990
991
992
993
994
995
996
997
998
999
1000
1001
1002
1003
1004
1005
1006
1007
1008

.SBTTL REGISTER ADDRESSES

;RPO4 VECTOR ADDRESS

001620 000254

RPVEC: 254

;RPO4 VECTOR ADDRESS

;RPO4 DISK I/O REGISTERS LOCATED IN THE RH11 CONTROLLER
;NOTE: THE CONTENTS OF THESE LOCATIONS WILL BE DIFFRENT
; IF THE "CHANGE BASE ADDRESS" ROUTINE IS USED.
; THIS ROUTINE STARTS AT LOCATION TAGED "BASECH"

001622 176722
001624 176702
001626 176704
001630 176710

RHDB: 176722
RHMC: 176702
RHBA: 176704
RHCS2: 176710

;DATA BUFFER SEE NOTE ABOVE
;WORD COUNT SEE NOTE ABOVE
;BUS ADDRESS SEE NOTE ABOVE
;CONTROL AND STATUS 2 SEE NOTE ABOVE

;RPO4 DISK I/O REGISTERS LOCATED IN THE RPO4 DEVICE LOGIC
;NOTE: THE CONTENTS OF THESE LOCATIONS WILL BE DIFFRENT
; IF THE "CHANGE BASE ADDRESS ROUTINE IS USED.
; THIS ROUTINE STARTS AT LOCATION TAGED "BASECH"

001632 176700
001634 176714
001636 176706
001640 176740
001642 176732
001644 176734
001646 176742
001650 176716
001652 176724
001654 176712
001656 176726
001660 176730
001662 176744
001664 176746
001666 176720
001670 176736

RHCS1: 176700
RHER1: 176714
RHDST: 176706
RHER2: 176740
RHOF: 176732
RHCA: 176734
RHER3: 176742
RHAS: 176716
RHMR: 176724
RHDS1: 176712
RHDT: 176726
RHSN: 176730
RHEC1: 176744
RHEC2: 176746
RHLA: 176720
RHCC: 176736

;CONTROL AND STATUS 1 SEE NOTE ABOVE
;ERROR #1 SEE NOTE ABOVE
;DESIRED SECTOR/TRACK ADDRESS SEE NOTE ABOVE
;ERROR #2 SEE NOTE ABOVE
;OFFSET SEE NOTE ABOVE
;DESIRED CYLINDER ADDRESS SEE NOTE ABOVE
;ERROR #3 SEE NOTE ABOVE
;ATTENTION SUMMARY SEE NOTE ABOVE
;MAINTAINABILITY SEE NOTE ABOVE
;DRIVE STATUS SEE NOTE ABOVE
;DRIVE TYPE SEE NOTE ABOVE
;SERIAL NUMBER SEE NOTE ABOVE
;ECC POSITION SEE NOTE ABOVE
;ECC PATTERN SEE NOTE ABOVE
;LOOK-AHEAD SEE NOTE ABOVE
;CURRENT CYLINDER ADDRESS SEE NOTE ABOVE


```

1009
1010
1011
1012
1013
1014
1015
1016 001672 000000
1017 001674 000000
1018 001676 000000
1019 001700 000000
1020
1021
1022 001702 000000
1023 001704 000000
1024 001706 000000
1025 001710 000000
1026 001712 000000
1027 001714 000000
1028 001716 000000
1029 001720 000000
1030 001722 000000
1031 001724 000000
1032 001726 000000
1033 001730 000000
1034 001732 000000
1035 001734 000000
1036 001736 000000
1037 001740 000000

```

```

;THE FOLLOWING LOCATIONS ARE RESERVED FOR REGISTER SAVES
;ANY TIME THERE IS AN ERROR ALL THESE WILL BE FILLED
;ONLY SOME MAY BE PRINTED BUT ALL WILL BE FILLED TRUE
;FOR THE TIME JUST AFTER THE "ERROR" ERROR COMMAND

```

```

DB:      0      ;DATA BUFFER
WC:      0      ;WORD COUNT
BA:      0      ;BUS ADDRESS
CS2:     0      ;CONTROL AND STATUS 2

CS1:     0      ;CONTROL AND STATUS 1
ER1:     0      ;ERROR #1
DST:     0      ;DESIRED SECTOR/TRACK ADDRESS
ER2:     0      ;ERROR #2
OF:      0      ;OFFSET
CA:      0      ;DESIRED CYLINDER ADDRESS
ER3:     0      ;ERROR #3
AS:      0      ;ATTENTION SUMMARY
MR:      0      ;MAINTAINABILITY
DS1:     0      ;DRIVE STATUS
DT:      0      ;DRIVE TYPE
SN:      0      ;SERIAL NUMBER
EC1:     0      ;ECC POSITION
EC2:     0      ;ECC PATTERN
LA:      0      ;LOOK-AHEAD
CC:      0      ;CURRENT CYLINDER ADDRESS

```

```

1038
1039 ;FLAGS & INTERNAL PROGRAM CONTROL WORDS
1040
1041 001742 000010 UNITS: .BLKM 8. ;TABLE OF DRIVES PRESENT TO TEST
1042 001762 000000 UNIT: .WORD 0 ;UNIT UNDER TEST
1043 001764 000000 NOUNIT: .WORD 0 ;NUMBER OF UNITS PRESENT
1044 ;USED TO KEEP TRACK OF UNIT UNDER TEST
1045 001766 000000 NUNIT: .WORD 0 ;USED TO DETERMINE IF THERE IS MORE
1046 ;THAN ONE UNIT
1047 001770 000000 SELECT: .WORD 0 ;ALL ONES INDICATE UNIT TO BE SELECTED
1048 001772 000000 UNITSL: .WORD 0 ;UNIT NO. SELECTED
1049
1050 001774 000000 ERFLGS: 0 ;ERROR FLAG
1051
1052 001776 000000 SAVDT: 0 ;SAVE DRIVE TYPE REGISTER
1053 ;FOR COMPARISON IN DRIVE CLEAR TEST
1054 ;AND RH INIT TEST
1055 002000 000000 SAVSN: 0 ;SAVE SERIAL NUMBER REGISTER
1056 ;FOR COMPARISON IN DRIVE CLEAR TEST
1057 ;AND RH INIT TEST
1058
1059 002002 000000 PCJSR: 0 ;SAVE PC OF JSR WHICH GAVE THE ERROR
1060
1061 002004 000000 ATTENT: 0 ;ATTENTION BIT FOR PRESENT UNIT
1062 002006 000000 TOTALAT: 0 ;TATAL ATTENTION BITS
1063
1064 002010 000000 TMPILL: 0 ;TEMPORARY ILLEGAL FUNCTION
1065
1066 002012 000000 TSECC: 0 ;FLAG TO SAY IF ECC TEST OR NOT
1067 ;WHEN =177777 IT IS AN ECC TEST
1068 ;WHEN =0IT IS NOT AN ECC TEST
1069
1070 002014 000000 TESDTE: 0 ;FLAG TO SAY IF DRIVE TIMING ERROR OR NOT
1071 ;WHEN = 177777 IT IS A DTE TEST
1072 ;WHEN = 0 IT IS NOT A DTE TEST
1073
1074 002016 000000 TAGDTE: 0 ;TEMPORARY TAG USED IN DRIVE TIMING
1075 ;ERROR TEST
1076
1077 002020 000000 TSTNM: 0 ;TEST NUMBER
1078
1079 002022 000000 FIRST: 0 ;IF ZERO WILL TYPE HEADER

```



```

1080
1081
1082           ;FUNCTION EQUATES
1083
1084           ;TABLE OF FUNCTIONS FOR RHCS1, THEN "GO" BIT HAS TO BE SET
1085
1086 002024      FUTABL:
1087 002024      NOPERA: 0           ;NO OPERATION
1088 002026      UNLOAD: 2          ;UNLOAD (STAND BY)
1089 002030      RECALI: 6          ;RECALIBRATE
1090 002032      DCLEAR: 10         ;DRIVE CLEAR
1091 002034      RELEAS: 12         ;RELEASE (DUAL-PORT OPERATION)
1092 002036      SERCH: 30          ;SEARCH COMMAND
1093 002040      WRCHK: 50          ;WRITE CHECK DATA
1094 002042      WRCHDT: 52         ;WRITE CHECK HEADER AND DATA
1095 002044      WRDAT: 60          ;WRITE DATA
1096 002046      WRIFOR: 62         ;WRITE HEADER AND DATA (FORMAT)
1097 002050      READAT: 70         ;READ DATA
1098 002052      REFOR: 72          ;READ HEADER AND DATA
1099 002054      SEECOM: 4          ;SEEK COMMAND
1100 002056      OFSETC: 14         ;OFFSET COMMAND
1101 002060      RETCL: 16          ;RETURN TO CENTERLINE
1102 002062      PKACK: 22          ;PACK ACKNOWLEDGE
1103 002064      READIN: 20         ;READ IN
1104 002066      ILLEGL: .WORD     ;COMPUTED ILLEGAL FUNCTION
1105
1106
1107           ;DATA BUFFER FOR READ WRITE
1108
1109 002070      000422      MRFROM: .BLKW 274.      ;WRITE FROM THIS BUFFER
1110 003134      000422      REINTO: .BLKW 274.     ;READ INTO THIS BUFFER
1111
1112
1113
1114           ;TABLE FOR ATTENTION BITS
1115           ;ATTENTION TABLE
1116
1117 004200      001      002      004      ATABLE: .BYTE 1,2,4,10,20,40,100,200
1118 004203      010
1119 004206      100      200      040
1120

```

```

1121
1122          .SBTTL
1123          .SBTTL   ***DIAGNOSTIC CODE***
1124          .SBTTL
1125
1126          .SBTTL   SETUP TESTS
1127
1128 004210 012737 177777 001770 BEGIN2: MOV   #-1, @#SELECT   ;SELECT UNIT
1129 004216 000402          BR     START
1130 004220 005037 001770 BEGIN:  CLR   @#SELECT   ;DO NOT SELECT UNIT
1131                                     ;NORMAL RUN
1132
1133          START:
1134 004224 012737 000340 177776 MOV   @340, @#PS           ;; LOCK OUT ALL INTERRUPTS
1135 004232 012706 001100 MOV   @#SCMTAG, R6        ;; FIRST LOCATION TO BE CLEARED
1136 004236 005026          CLR   (R6)+              ;; CLEAR MEMORY LOCATION
1137 004240 022706 001136 CMP   @#STKS, R6         ;; DONE?
1138 004244 001374          BNE   -6                ;; LOOP BACK IF NO
1139 004246 012706 001000 MOV   @#STACK, SP        ;; SETUP THE STACK POINTER
1140 004252 012737 051652 000020 MOV   @#SCOPE, @#IOTVEC  ;; IOT VECTOR FOR SCOPE ROUTINE
1141 004260 012737 000340 000022 MOV   @340, @#IOTVEC+2   ;; LEVEL 7
1142 004266 012737 053240 000030 MOV   @#ERROR, @#EMTVEC  ;; EMT VECTOR FOR ERROR ROUTINE
1143 004274 012737 000340 000032 MOV   @340, @#EMTVEC+2   ;; LEVEL 7
1144 004302 012737 054004 000034 MOV   @#STRAP, @#TRAPVEC ;; TRAP VECTOR FOR TRAP CALLS
1145 004310 012737 000340 000036 MOV   @340, @#TRAPVEC+2 ;; LEVEL 7
1146 004316 012737 054052 000024 MOV   @#SPMRON, @#PMRVEC ;; POWER FAILURE VECTOR
1147 004324 012737 000340 000026 MOV   @340, @#PMRVEC+2   ;; LEVEL 7
1148 004332 005067 174646 CLR   @#TIMES           ;; INITIALIZE NUMBER OF ITERATIONS
1149 004336 005067 174644 CLR   @#SESCAPE         ;; CLEAR THE ESCAPE ON ERROR ADDRESS
1150 004342 112767 000001 174545 MOV   #1, @#SERMAX      ;; ALLOW ONE ERROR PER TEST
1151 004350 012767 004350 174530 MOV   #., @#SLPADR      ;; INITIALIZE THE LOOP ADDRESS FOR SCOPE
1152 004356 012767 004356 174524 MOV   #., @#SLPERR      ;; SETUP THE ERROR LOOP ADDRESS
1153
1154
1155
1156 004364 012767 000000 173404 MOV   #0, @#PS          ;; SET PROCESSOR STATUS TO 0
1157 004372 012777 051570 175220 MOV   @#RPVECT, @#RPVEC ;; THIS IS FOR UNTIMELY RPO4 INTERRUPTS
1158 004400 004737 052512          JSR   PC, @#STKINT      ;; INITILIZE THE TTY KEYBOARD
1159 004404 005737 002022          TST   @#FIRST          ;; IS THIS FIRST TIME ROUND
1160 004410 001001          BNE   1$              ;; SKIP HEADER IF NOT
1161 004412 000402          BR     2$              ;; DO HEADER IF SO
1162 004414 000137 005240          JMP   @#SND1          ;; SKIP OVERALL PROGRAM HEADER
1163
1164          2$:
1165 004420 104400 004426          TYPE  @.,+4           ;; TYPE ASCIZ STRING
1166 004424 000435          BR     64$           ;; GET OVER THE ASCIZ
1167          ;;.ASCIZ <15><12>/RJP04 DISKLESS RH11 TEST - PART I (STATIC 1A)- DZRPS-D/
1168          64$:
1169 004520 104400 004526          TYPE  @.,+4           ;; TYPE ASCIZ STRING
1170 004524 000416          BR     65$           ;; GET OVER THE ASCIZ
1171          ;;.ASCIZ <15><12>/REVISION DATE: 21-JULY-75/
1172          65$:
1173 004562 104400 004570          TYPE  @.,+4           ;; TYPE ASCIZ STRING
1174 004566 000402          BR     66$           ;; GET OVER THE ASCIZ

```


1175					66\$:	::.ASCIZ	<15><12>/ /	
1176	004574					TYPE	+4	:::TYPE ASCIZ STRING
1177	004574	104400	004602			BR	67\$:::GET OVER THE ASCIZ
1178	004600	000432				::.ASCIZ	<15><12>/	ALL DCL UNDER TEST MUST BE LOCKED ON CORRECT PORT/
1179					67\$:			
1180	004666					TYPE	+4	:::TYPE ASCIZ STRING
1181	004666	104400	004674			BR	68\$:::GET OVER THE ASCIZ
1182	004672	000427				::.ASCIZ	<15><12>/	IF CHANGES ARE REQUIRED ON PORT SWITCH THEN/
1183					68\$:			
1184	004752					TYPE	+4	:::TYPE ASCIZ STRING
1185	004752	104400	004760			BR	69\$:::GET OVER THE ASCIZ
1186	004756	000430				::.ASCIZ	<15><12>/	A CYCLE UP SEQUENCE IS REQUIRED FOR STROBING/
1187					69\$:			
1188	005040					TYPE	+4	:::TYPE ASCIZ STRING
1189	005040	104400	005046			BR	70\$:::GET OVER THE ASCIZ
1190	005044	000414				::.ASCIZ	<15><12>/	THE PORT SELECT FLOP/
1191					70\$:			
1192	005076					TYPE	+4	:::TYPE ASCIZ STRING
1193	005076	104400	005104			BR	71\$:::GET OVER THE ASCIZ
1194	005102	000402				::.ASCIZ	<15><12>/ /	
1195					71\$:			
1196	005110					TYPE	+4	:::TYPE ASCIZ STRING
1197	005110	104400	005116			BR	72\$:::GET OVER THE ASCIZ
1198	005114	000427				::.ASCIZ	<15><12>/	ALL DCL NOT UNDER TEST MUST BE SWITCHED OFF/
1199					72\$:			
1200	005174					TYPE	+4	:::TYPE ASCIZ STRING
1201	005174	104400	005202			BR	73\$:::GET OVER THE ASCIZ
1202	005200	000417				::.ASCIZ	<15><12>/	OR LOCKED ON THE OTHER PORT/
1203					73\$:			
1204	005240							
1205								
1206	005240	012737	177777	002022	SND1:	MOV	#-1, 2#FIRST	:NEXT TIME DO NOT GIVE HEADER
1207	005246	005737	001770			TST	2#SELECT	:WAS IT A 200 START
1208	005252	001435				BEQ	TST1	:BRANCH IF STARTING FROM 200
1209								
1210	005254	104400	005262			TYPE	+4	:::TYPE ASCIZ STRING
1211	005260	000423				BR	64\$:::GET OVER THE ASCIZ
1212					64\$:	::.ASCIZ	<15><12>/	SELECT UNIT NUMBER TO BE TESTED ? /
1213	005330					RDOCT		
1214	005330	104416				BIC	#177770, (SP)	:ONLY KEEP LAST 3 BITS
1215	005332	042716	177770			MOV	(SP), 2#UNIT	:SAVE UNIT TO BE TESTED
1216	005336	011637	001762			MOV	(SP)+, 2#UNITSL	:SAVE UNIT TO BE TESTED
1217	005342	012637	001772					
1218								
1219								

```

1220
1221
1222
1223
1224
1225 005346 000004
1226 005350 012767 000001 173626
1227 005356 012706 001000
1228 005362 012737 000001 002020
1229 005370 012737 053246 000030
1230
1231 005376 012737 005424 000004
1232 005404 012700 000024
1233 005410 012701 C01622
1234 005414 013102
1235 005416 005300
1236 005420 001375
1237 005422 000471
1238 005424 012737 000006 000004
1239 005432 022626
1240 005434 016167 177776 173530
1241 005442 104015
1242 005444 032737 020000 177570
1243 005452 001053
1244
1245 005454 104400 005462
1246 005460 000431
1247
1248 005544
1249 005544 104400 005532
1250 005550 000411
1251
1252 005574
1253
1254 005574 012746 044146
1255
1256 005600 104402
1257 005602 000137 037204
1258 005606 012737 053240 000030
1259
1260 005614 012737 000006 000004
1261

*****
*TEST 1 REFERENCE EACH REGISTER
* REFERENCE EACH REGISTER BY A MOVE INSTRUCTION
*****
†ST1: SCOPE
MOV #1,STIMES ;;DO 1 ITERATION
MOV #STACK,SP ;;SET UP STACK POINTER
MOV #TTNO,#TSTNM ;;THIS SAVES TEST NUMBER
MOV #REGSA1,#EMTVEC;ERROR VECTOR SO THAT
NO REGISTERS ARE SAVED
MOV #25,#ERRVEC;SET UP FOR BUS TIMEOUT
MOV #24,R0;THERE ARE 24 REG TO TEST
MOV #RH08,R1;R1 NOW HAS ADDR OF ADDR OF FIRST REG.
15: MOV #2(R1)+,R2;READ HARDWARE REG.
DEC R0;COUNT DOWN
BNE 15;BRANCH IF 24 NOT DONE
BR 35;BRANCH IF 24 DONE
25: MOV #ERRVEC+2,#ERRVEC;RESTORE TRAP CATCHER
CMP (SP)+,(SP)+;CLEAN STACK
MOV -2(R1),STMP1;STORE FAILING REG ADDR
ERROR 15;REGISTER NON EXISTANT
BIT #SM13,#SMR;INHIBIT ERROR PRINTOUT ?
BNE 45;BRANCH IF YES
TYPE #"+4";;TYPE ASCIZ STRING
BR 64$;GET OVER THE ASCIZ
;;.ASCIZ <15><12>/IF BASE ADDRESS IS TO BE CHANGED HALT PROGRAM /
64$: TYPE #"+4";;TYPE ASCIZ STRING
BR 65$;GET OVER THE ASCIZ
;;.ASCIZ <15><12>/AND RESTART AT /
65$: MOV #BASECH,-(SP);GET READY TO TYPE STARTING ADDRESS
;OF "CHANGE OF BASE ADDRESS" ROUTINE
TYPOC
45: JMP #SEOP;GO TO END OF PROGRAM
35: MOV #ERROR,#EMTVEC;RESTORE ERROR VECTOR
;SO THAT REGISTERS ARE SAVED
MOV #ERRVEC+2,#ERRVEC;RESTORE TRAP CATCHER

```


1262
1263
1264
1265
1266
1267
1268
1269
1270
1271
1272
1273
1274
1275
1276
1277
1278
1279
1280
1281
1282
1283
1284
1285
1286
1287
1288
1289
1290
1291
1292
1293
1294
1295
1296
1297
1298
1299
1300
1301
1302
1303
1304
1305

;*TEST 2 RHCS2-CONTROL AND STATUS 2

;* THIS PARTIALLY TESTS RHCS2 TO ENABLE DETERMINATION
;* OF THE NUMBER OF DRIVES PRESENT

TST2: SCOPE
MOV #1,STIMES ;;DO 1 ITERATION
MOV #STACK,SP ;;RESET STACK
MOV #TTNO,#TSTNM ;THIS SAVES TEST NUMBER
UN: MOV #PRCS2+12,#UN
MOV #RHCS2,#UN+2
JSR R5,#BITST ;TEST BITS IN REGISTER
.WORD 0 ;ONLY THESE BITS ARE TEST READ/WRITE
.WORD 0 ;ADDRESS OF REG. BEING TESTED
ERROR 1 ;IN CORRECT DATA RECEIVED
RTS PC ;RETURN TO BLT3 ROUTINE

;*TEST 3 PARTIAL TEST FOR RHAS FOR UNIT NUMBERS PRESENT

TST3: SCOPE
MOV #1,STIMES ;;DO 1 ITERATION
MOV #TTNO,#TSTNM ;THIS SAVES TEST NUMBER
MOV #RHAS,R1 ;R1 HAS ADDRESS OF RHAS
MOV #-1,#R1 ;THIS CLEARS RHAS (SURPRISED!)
MOV #R1,#\$BDDAT ;TEST DATA
TSTB #BDDAT
BEQ TST4 ;BRANCH IF GOOD
CLR #SGDDAT ;GOOD DATA
MOV R1,#REGADR ;FAILING REG. RHAS
ERROR 1 ;RHAS DOES NOT CLEAR
;WITH ONES

```

1306
1307
1308
1309
1310
1311 005746 000004
1312 005750 012767 000001 173226
1313
1314 005756 000005
1315 005760 004737 052512
1316 005764 032737 020000 177570
1317 005772 001155
1318
1319 005774 104400 006002
1320 006000 000430
1321
1322 006062
1323 006062 104400 006070
1324 006066 000402
1325
1326 006074
1327 006074 104400 006102
1328 006100 000430
1329
1330 006162
1331 006162 104400 006170
1332 006166 000424
1333
1334 006240
1335 006240 104400 006246
1336 006244 000430
1337
1338 006326
1339
1340 006326 013701 001650
1341 006332 013702 001630
1342 006336 005012
1343 006340 012700 000010
1344 006344 013704 001634
1345 006350 012714 177777
1346 006354 005212
1347 006356 005300
1348 006360 001373
1349
1350 006362 111137 002006
1351
1352 006366 105037 002007
1353 006372 105711
1354 006374 001402
1355 006376 000167 000400
1356
1357 006402 032737 020000 177570 25:
1358 006410 001402
1359 006412 000167 000640

```

```

*****
: #TEST 4 TEST FOR DRIVES PRESENT USING RHAS AND RHCS2
*****
TST4: SCOPE
MOV #1,STIMES ;;DO 1 ITERATION

RESET ;;START WITH AN INIT
JSR PC,@#STKINT ;;INITILIZE TTY KEYBOARD
BIT #SW13,@#SMR ;;INHIBIT ERROR TYPEOUT?
BNE 45 ;;BRANCH IF YES

TYPE #4 ;;TYPE ASCIZ STRING
BR 64S ;;GET OVER THE ASCIZ
;;.ASCIZ <15><12>/LOOKING AT RHAS - RPO4 DRIVES ASSUMED PRESENT/

64S: TYPE #4 ;;TYPE ASCIZ STRING
BR 65S ;;GET OVER THE ASCIZ
;;.ASCIZ <15><12>/

65S: TYPE #4 ;;TYPE ASCIZ STRING
BR 66S ;;GET OVER THE ASCIZ
;;.ASCIZ <15><12>/*****

66S: TYPE #4 ;;TYPE ASCIZ STRING
BR 67S ;;GET OVER THE ASCIZ
;;.ASCIZ <15><12>/THIS MUST BE VERIFIED BY OPERATOR !!/

67S: TYPE #4 ;;TYPE ASCIZ STRING
BR 68S ;;GET OVER THE ASCIZ
;;.ASCIZ <15><12>/*****

45: MOV @#RHAS,R1 ;;LOAD R1 WITH ADDR. OF RHAS
MOV @#RHCS2,R2 ;;LOAD R2 WITH ADDR. OF RHCS2
CLR @R2 ;;CLEAR RHCS2 (ADDRESS UNIT #0)
MOV @8,R0 ;;INITIALIZE DRIVE COUNTER
MOV @#RHER1,R4 ;;LOAD R4 WITH ADDR. OF RHER1
15: MOV @-1,@R4 ;;MOVE ERRORS INTO RHER1 OF UNIT ADDRESSED
INC @R2 ;;INCREMENT UNIT NO. (RHCS2)
DEC R0 ;;COUNT DOWN DRIVE COUNTER
BNE 15 ;;TEST AND DO NEXT UNIT IF 0 NOT DONE

MOV @R1,@#TOTALAT ;;SAVE ALL RESULTING ATTENTION BITS
;;(USED IN DRIVE CLEAR TEST)
CLRB @#TOTALAT+1 ;;CLEAR UPPER BYTE
TSTB @R1 ;;TEST RHAS FOR ANY DRIVES PRESENT
BEQ 25 ;;NONE RESPONDING - TYPE THE MESSAGE
JMP XE2 ;;SOME THERE - GO FILL "UNITS" TABLE

25: BIT #SW13,@#SMR ;;INHIBIT ERROR TYPE OUT?
BEQ 35 ;;TYPE "NO DRIVES" MESSAGE IF NO
JMP SELTST ;;CHECK FOR SELECTED UNIT START AND LOAD

```


;"UNITS" TABLE WITH DESIRED DRIVE IF SO

```

1360
1361
1362 006416          35:      TYPE          +4          ;;TYPE ASCIZ STRING
1363 006416 104400 006424      BR          69S          ;;GET OVER THE ASCIZ
1364 006422 000412          ;;.ASCIZ          <15><12>/NO DRIVES-RHAS=0/
1365
1366 006450          69S:      TYPE          +4          ;;TYPE ASCIZ STRING
1367 006450 104400 006456      BR          70S          ;;GET OVER THE ASCIZ
1368 006454 000402          ;;.ASCIZ          <15><12>/
1369
1370 006462          70S:      TYPE          +4          ;;TYPE ASCIZ STRING
1371 006462 104400 006470      BR          71S          ;;GET OVER THE ASCIZ
1372 006466 000436          ;;.ASCIZ          <15><12>/WRITING ONES INTO ERROR REGISTER #1 FOR ALL UNIT NUMBERS/
1373
1374 006564          71S:      TYPE          +4          ;;TYPE ASCIZ STRING
1375 006564 104400 006572      BR          72S          ;;GET OVER THE ASCIZ
1376 006570 000441          ;;.ASCIZ          <15><12>/DOES NOT SET ANY BIT IN THE ATTENTION REGISTER SO ABORT P
1377
1378 006674          72S:      TYPE          +4          ;;TYPE ASCIZ STRING
1379 006674 104400 006702      BR          73S          ;;GET OVER THE ASCIZ
1380 006700 000436          ;;.ASCIZ          <15><12>/TO LOOP ON THIS TEST NO PRINTOUT, SET SWITCHES 13, 8 & 2/
1381
1382 006776          73S:
1383
1384 006776 000137 037204      JMP          @#SEOP          ;GO OUT----->
1385
1386 007002          XE2:
1387 007002 012700 000010      25:      MOV          #8, R0          ;LOAD "UNITS" TABLE COUNTER
1388 007006 012703 001742      MOV          #UNITS, R3      ;LOAD "UNITS" TABLE POINTER
1389 007012 012723 177777      35:      MOV          #-1, (R3)+      ;PRESET 1ST TABLE BLOCK TO ALL ONES
1390 007016 005300          DEC          R0              ;COUNT DOWN
1391 007020 001374          BNE          35              ;PRESET NEXT BLOCK IF 8 NOT DONE
1392
1393 007022 012703 001742          105:     MOV          #UNITS, R3      ;RELOAD THE TABLE POINTER
1394 007026 005005          CLR          R5              ;INITIALIZE UNIT NO. TO 0
1395 007030 005037 001764          CLR          @#NUNIT         ;NO. OF UNITS PRESENT
1396 007034 012700 000010          MOV          #8, R0          ;RELOAD THE TABLE COUNTER
1397 007040 011137 001170          MOV          @R1, @#STMPD     ;ADDR OF RHAS INTO TEMPORARY STORAGE
1398 007044 006037 001170          45:      ROR          @#STMPD         ;SET CARRY IF 0 BIT = 1 (UNIT ATTEN.)
1399 007050 103067          BCC          55              ;CHECK NEXT UNIT IF ONE NOT IN BIT 0
1400
1401 007052 010577 172552          115:     MOV          R5, @RHCS2       ;INSERT UNIT NO. INTO RHCS2 UNIT ADDR.
1402 007056 022777 024020 172572      CMP          #24020, @RHDT     ;READ RHDT - IS IT A DUAL PORT RPO4 ?
1403 007064 001452          BEQ          65              ;YES...TYPE THE UNIT NO.
1404 007066 022777 020020 172562      CMP          #20020, @RHDT     ;READ RHDT - IS IT A SINGLE PORT RPO4 ?
1405 007074 001446          BEQ          65              ;YES...TYPE THE UNIT NO.
1406
1407          ;NO...IT'S NOT AN RPO4 DEVICE SO TYPE
1408          ;OUT THE DEVICE TYPE
1409
1410 007076 104400 007104          TYPE          +4          ;;TYPE ASCIZ STRING
1411 007102 000410          BR          64S          ;;GET OVER THE ASCIZ
1412          ;;.ASCIZ          <15><12>/UNIT NUMBER /
1413 007124          64S:

```



```

1446
1447
1448
1449
1450
1451
1452
1453
1454
1455 007272 000004
1456 007274 012767 000001 171702
1457 007302 012767 007504 171576
1458
1459 007310 004737 040522
1460 007314 005037 002004
1461 007320 013700 001762
1462 007324 116037 004200 002004
1463
1464 007332 104400 007340
1465 007336 000415
1466
1467 007372
1468
1469 007372 013746 001762
1470 007376 104402
1471 007400 104400 001215
1472 007404 104400 007412
1473 007410 000410
1474
1475 007432
1476 007432 017746 172222
1477 007436 104402
1478 007440 104400 001215
1479 007444 104400 007452
1480 007450 000410
1481
1482 007472
1483 007472 017746 172160
1484 007476 104402
1485 007500 104400 001215
1486 007504 005777 172150
1487 007510 005777 172142
1488 007514 017737 172140 002000
1489 007522 017737 172130 001776
1490
1491

```

```

*****
*TEST 5      TEST SERIAL NUMBER AND DRIVE TYPE I
*            READ SERIAL NUMBER REGISTER AND DRIVE TYPE REGISTER
*            TYPE IT OUT AND PROCEED
*            TO LOOP HERE SET SWITCH 8 AND THIS TEST NO AND RESTART
*****
TST5:  SCOPE
      MOV    #1,STIMES      ;;DO 1 ITERATION
      MOV    #1$,SLPADR    ;;SET SCOPE LOOP ADDRESS
      JSR    PC,@CLDISK    ;FILL UNIT NO.
      CLR    @ATTENT      ;CLEAR
      MOV    @UNIT,RO      ;RO CONTAINS UNIT NO
      MOVB  ATABLE(RO),@ATTENT ;SET APPROPRIATE ATTENTION BIT
      TYPE   +4           ;;TYPE ASCIZ STRING
      BR    64$          ;;GET OVER THE ASCIZ
      ;;.ASCIZ  <15><12>/TESTING DRIVE NUMBER /
64$:
      MOV    @UNIT,-(SP)   ;UNIT NO. TO STACK
      TYPOC          ;TYPE DRIVE NO.
      TYPE   ,SCRLF
      TYPE   +4           ;;TYPE ASCIZ STRING
      BR    65$          ;;GET OVER THE ASCIZ
      ;;.ASCIZ  <15><12>/SERIAL NO. = /
65$:
      MOV    @RHSN,-(SP)  ;;SAVE @RHSN FOR TYPEOUT
      TYPOC          ;;GO TYPE--OCTAL ASCII(ALL DIGITS)
      TYPE   ,SCRLF
      TYPE   +4           ;;TYPE ASCIZ STRING
      BR    66$          ;;GET OVER THE ASCIZ
      ;;.ASCIZ  <15><12>/DRIVE TYPE = /
66$:
      MOV    @RHDT,-(SP)  ;;SAVE @RHDT FOR TYPEOUT
      TYPOC          ;;GO TYPE--OCTAL ASCII(ALL DIGITS)
      TYPE   ,SCRLF
1$:    TST    @RHSN      ;READ SERIAL NO. AND DRIVE TYPE
      TST    @RHDT      ;THESE TWO ARE TO HELP SCOPE LOOPS
      MOV    @RHSN,@SAVSN ;SAVE TO CHECK IF CLR RHCS2 BIT 5 CLEARS ANY BITS
      MOV    @RHDT,@SAVDT ;SAVE TO CHECK IF CLR RHCS2 BIT 5 CLEARS ANY BITS

```

1492
 1493
 1494
 1495
 1496
 1497
 1498
 1499
 1500
 1501 007530 000004
 1502 007532 012737 000006 002020
 1503 007540 004737 040522
 1504 007544 032713 010000
 1505 007550 001550
 1506 007552 104400 007560
 1507 007556 000421
 1508
 1509 007622
 1510 007622 104400 007630
 1511 007626 000424
 1512
 1513 007700
 1514 007700 104400 007706
 1515 007704 000430
 1516
 1517 007766
 1518 007766 032713 010000
 1519 007772 001375
 1520 007774 104400 010002
 1521 010000 000434
 1522
 1523 010072

```

*****
;#TEST 6      CHECK MOL TO BE LOW
;#          MAKE SURE THAT DRIVE IS OFF LINE BEFORE STARTING PROGRAM
;#          IF DRIVE IS ON LINE THEN AFTER TYPE OUT THE PROGRAM WILL
;#          HANG FOR EVER WAITING FOR DRIVE TO GO OFF LINE
*****
†ST6:  SCOPE
      MOV      #TTNO,@#TSTNM      ;THIS SAVES TEST NUMBER
      JSR      PC,@#CLDISK      ;GIVE INITILIZE
      BIT      #MOL,@R3          ;CHECK MOL IN RHDS1
      BEQ      TST7              ;BRANCH IF MOL LOW
      TYPE     TST7+4            ;TYPE ASCIZ STRING
      BR       64$              ;GET OVER THE ASCIZ
64$:  ;;.ASCIZ    <15><12>/DRIVE IS ON LINE - MOL IS HIGH/
      TYPE     TST7+4            ;TYPE ASCIZ STRING
      BR       65$              ;GET OVER THE ASCIZ
65$:  ;;.ASCIZ    <15><12>/HIT STOP ON DRIVE TO GET IT OFF LINE/
      TYPE     TST7+4            ;TYPE ASCIZ STRING
      BR       66$              ;GET OVER THE ASCIZ
66$:  ;;.ASCIZ    <15><12>/PROGRAM WILL HANG TESTING MOL TILL MOL IS LOW/
1$:   BIT      #MOL,@R3          ;CHECK MOL IN RHDS1
      BNE     1$                ;BRANCH IF MOL IS HIGH
      TYPE     TST7+4            ;TYPE ASCIZ STRING
      BR       67$              ;GET OVER THE ASCIZ
67$:  ;;.ASCIZ    <15><12>/GOOD - MOL IS NOW LOW . PROGRAM WILL NOW BE EXECUTED/

```


1524
1525
1526
1527
1528
1529
1530
1531
1532
1533
1534
1535
1536
1537
1538
1539
1540
1541
1542
1543
1544
1545
1546
1547
1548
1549
1550
1551
1552
1553
1554
1555
1556
1557
1558
1559
1560
1561
1562
1563
1564
1565
1566
1567
1568
1569
1570
1571

.SBTTL REGISTER TESTS

:TEST 7 RHCS2 - CONTROL AND STATUS 2
:TEST LOADING AND READING OF ALL POSSIBLE BITS IN THE HARDWARE
:REGISTERS. USE A PATTERN OF WALKING 0'S (-2,-3,-5 ETC.) AND
:WALKING 1'S (1,2,4,10 ETC)

TST7: SCOPE

010072 000004

MOV #STACK,SP ;RESET STACK
MOV #TTNO,#TSTNM ;THIS SAVES TEST NUMBER
PRCS2: MOV #RHCS2,#PRCS2+14 ;GET REGISTER ADDRESS
MOV #UNIT,#RHCS2 ;MOVE UNIT NO. UNDER TEST
JSR R5,BITST ;TEST BITS IN REGISTER
.WORD 20017 ;ONLY THESE BITS ARE TESTED FOR READ/WRITE
.WORD 176710 ;ADDRESS OF REG. BEING TESTED
ERROR 1 ;INCORRECT DATA RECEIVED
RTS PC ;RETURN TO BLT3 ROUTINE

010074 012706 001000
010100 012737 000007 002020
010106 013737 001630 010130
010114 013777 001762 171506
010122 004567 030046
010126 020017
010130 176710
010132 104001
010134 000207

:TEST 10 RHCS1 - CONTROL AND STATUS 1 REGISTER
:TEST LOADING AND READING OF ALL POSSIBLE BITS IN THE HARDWARE
:REGISTERS. USE A PATTERN OF WALKING 0'S (-2,-3,-5 ETC.) AND
:WALKING 1'S (1,2,4,10 ETC)

TST10: SCOPE

010136 000004

MOV #STACK,SP ;RESET STACK
MOV #TTNO,#TSTNM ;THIS SAVES TEST NUMBER
PRCS1: MOV #RHCS1,#PRCS1+14 ;GET REGISTER ADDRESS
MOV #UNIT,#RHCS2 ;MOVE UNIT NO. UNDER TEST
JSR R5,BITST ;TEST BITS IN REGISTER
.WORD 1476 ;ONLY THESE BITS ARE TESTED FOR READ/WRITE
.WORD 176700 ;ADDRESS OF REG. BEING TESTED
ERROR 1 ;INCORRECT DATA RECEIVED
RTS PC ;RETURN TO BLT3 ROUTINE

010140 012706 001000
010144 012737 000010 002020
010152 013737 001632 010174
010160 013777 001762 171442
010166 004567 030002
010172 001476
010174 176700
010176 104001
010200 000207

```

1572
1573
1574
1575
1576
1577
1578
1579
1580 010202 000004
1581
1582 010204 012706 001000      MOV      #STACK,SP      ;RESET STACK
1583 010210 012737 000011 002020  MOV      #TTNO,@#TSTNM ;THIS SAVES TEST NUMBER
1584 010216 004737 040522      JSR      PC,@#CLDISK   ;INIT AND SET UNIT NUMBER AND DEVICE -
1585                                     ;CPU REG. CORRESPONDENCE (R1-R4)
1586
1587                                     ;SET FORCED PARITY ERROR 'PAT'
1588 010222 052777 000020 171400  BIS      #PAT,@#RHCS2  ;SET 'PAT' TO INVERT PARITY
1589                                     ;GENERATED
1590 010230 005077 171400      CLR      @#RHER1      ;WRITE DCL REGISTER USING BAD CONTROL PARITY
1591
1592                                     ;WITH THIS PARITY ERROR NOTHING WILL BE READ TILL IT IS
1593                                     ;CLEARED
1594
1595 010234 011137 001126      MOV      @#R1,@#SBDDAT ;RHCSI ----> SBDDAT
1596 010240 022737 104200 001126  CMP      #SC!DVA!RDY,@#SBDDAT ;COMPARE RHCSI AFTER PARITY
1597                                     ;ERROR
1598 010246 001406      BEQ      1$           ;BRANCH IF SC!DVA!RDY=1
1599 010250 012737 104200 001124  MOV      #SC!DVA!RDY,@#SGDDAT ;GOOD DATA
1600 010256 010137 040172      MOV      R1,@#REGADR  ;REGISTER ADDRESS RHCSI
1601 010262 104001      ERROR      1         ;SETTING PAT AND
1602                                     ;WRITING DCL REGISTER RHCSI
1603                                     ;DID NOT SET SC!DVA!RDY
1604                                     ;WITH 'PAT' BIT HIGH
1605 010264 013746 001762      1$: MOV      @#UNIT,-(SP) ;GET UNIT NUMBER
1606 010270 052716 000120      BIS      #PAT!IR,(SP) ;INCLUDE PAT AND IR
1607 010274 012637 001124      MOV      (SP)+,@#SGDDAT ;PUT ON STACK
1608 010300 011237 001126      MOV      @#R2,@#SBDDAT ;RHCS2 ----> SBDDAT
1609 010304 023737 001124 001126  CMP      @#SGDDAT,@#SBDDAT ;COMPARE RHCS2
1610 010312 001403      BEQ      2$           ;OK - SC!DVA!RDY ARE HIGH
1611 010314 010237 040172      MOV      R2,@#REGADR  ;REGISTER ADDRESS
1612 010320 104001      ERROR      1         ;READING DCL REGISTER RHCS2 DID NOT
1613                                     ;SHOW UNIT#!PAT!IR BITS HIGH
1614
1615 010322 011437 001126      2$: MOV      @#R4,@#SBDDAT ;RHER1 ----> SBDDAT
1616 010326 022737 000010 001126  CMP      #PAR,@#SBDDAT ;ERROR REGISTER RHER1 SHOU'LD
1617                                     ;HAVE 'PAR' SET
1618 010334 001406      BEQ      3$           ;A - OK, IT DOES
1619 010336 012737 000010 001124  MOV      #PAR,@#SGDDAT ;GOOD DATA
1620 010344 010437 040172      MOV      R4,@#REGADR  ;FAILING REGISTER RHER1
1621 010350 104001      ERROR      1         ;PARITY ERROR DID NOT
1622                                     ;SET 'PAR'
1623
1624 010352      3$:

```

```

*****
*TEST 11      RHCSI - BIT # 13 - MCPE
*      THIS FORCES A MASS BUS CONTROL PARITY ERROR
*      BY SETTING 'PAT', LOOKING FOR 'PAR', WRITING RHCSI
*      AND READING RHER1
*****
TST11: SCOPE

```


1625
1626
1627
1628
1629
1630
1631
1632
1633
1634
1635
1636
1637
1638
1639
1640
1641
1642
1643
1644
1645
1646
1647
1648
1649
1650
1651
1652
1653
1654
1655
1656
1657
1658
1659
1660
1661
1662
1663
1664
1665
1666
1667
1668
1669
1670
1671
1672

```
*****
:TEST 12      RHMC - WORD COUNT REGISTER
:             TEST LOADING AND READING OF ALL POSSIBLE BITS IN THE HARDWARE
:             REGISTERS. USE A PATTERN OF WALKING 0'S (-2,-3,-5 ETC.) AND
:             WALKING 1'S (1,2,4,10 ETC)
*****
```

010352 000Q04

TST12: SCOPE

010354 012706 001000

```
MOV      #STACK,SP      ;RESET STACK
MOV      #TTNO,#TSTNM   ;THIS SAVES TEST NUMBER
```

010360 012737 000012 002020

010366 013737 001624 010410

```
PRMC:    MOV      @RHMC,@PRMC+14      ;GET REGISTER ADDRESS
```

010374 013777 001762 171226

```
MOV      @UNIT,@RHCS2      ;MOVE UNIT NO. UNDER TEST
```

010402 004567 027566

```
JSR      R5,BITST          ;TEST BITS IN REGISTER
```

010406 177777

```
.WORD    177777            ;ONLY THESE BITS ARE TESTED FOR READ/WRITE
```

010410 176702

```
.WORD    176702            ;ADDRESS OF REG. BEING TESTED
```

010412 104001

```
ERROR    1                  ;INCORRECT DATA RECEIVED
```

010414 000207

```
RTS      PC                  ;RETURN TO BLT3 ROUTINE
```

```
*****
:TEST 13      RHBA - UNIBUS ADDRESS REGISTER
:             TEST LOADING AND READING OF ALL POSSIBLE BITS IN THE HARDWARE
:             REGISTERS. USE A PATTERN OF WALKING 0'S (-2,-3,-5 ETC.) AND
:             WALKING 1'S (1,2,4,10 ETC)
*****
```

010416 000004

TST13: SCOPE

010420 012706 001000

```
MOV      #STACK,SP      ;RESET STACK
MOV      #TTNO,#TSTNM   ;THIS SAVES TEST NUMBER
```

010424 012737 000013 002020

010432 013737 001626 010454

```
PRBA:    MOV      @RHBA,@PRBA+14      ;GET REGISTER ADDRESS
```

010440 013777 001762 171162

```
MOV      @UNIT,@RHCS2      ;MOVE UNIT NO. UNDER TEST
```

010446 004567 027522

```
JSR      R5,BITST          ;TEST BITS IN REGISTER
```

010452 177776

```
.WORD    177776            ;ONLY THESE BITS ARE TESTED FOR READ/WRITE
```

010454 176704

```
.WORD    176704            ;ADDRESS OF REG. BEING TESTED
```

010456 104001

```
ERROR    1                  ;INCORRECT DATA RECEIVED
```

010460 000207

```
RTS      PC                  ;RETURN TO BLT3 ROUTINE
```

1673
1674
1675
1676
1677
1678
1679
1680
1681
1682
1683
1684
1685
1686
1687
1688
1689
1690
1691
1692
1693
1694
1695

010462 000004
010464 012706 001000
010470 012737 000014 002020
010476 013737 001634 010520
010504 013777 001762 171116
010512 004567 027456
010516 177777
010520 176714
010522 104001
010524 000207

```

*****
*TEST 14      RHER1 - ERROR REGISTER #1
*            TEST LOADING AND READING OF ALL POSSIBLE BITS IN THE HARDWARE
*            REGISTERS.  USE A PATTERN OF WALKING 0'S (-2,-3,-5 ETC.) AND
*            WALKING 1'S (1,2,4,10 ETC)
*****
TST14: SCOPE
MOV #STACK,SP ;RESET STACK
MOV #TTNO,@TSTNM ;THIS SAVES TEST NUMBER
PRER1: MOV @RHER1,@PRER1+14 ;GET REGISTER ADDRESS
MOV @UNIT,@RHCS2 ;MOVE UNIT NO. UNDER TEST
JSR RS,BITST ;TEST BITS IN REGISTER
.WORD 177777 ;ONLY THESE BITS ARE TESTED FOR READ/WRITE
.WORD 176714 ;ADDRESS OF REG. BEING TESTED
ERROR 1 ;INCORRECT DATA RECEIVED
RTS PC ;RETURN TO BLT3 ROUTINE

```


1696
1697
1698
1699
1700
1701
1702
1703
1704
1705
1706
1707
1708
1709
1710
1711
1712
1713
1714
1715
1716
1717
1718
1719
1720
1721
1722
1723
1724
1725
1726
1727
1728
1729
1730
1731
1732
1733
1734
1735
1736
1737
1738
1739
1740
1741
1742
1743
1744
1745
1746
1747
1748
1749

010526 000004
010530 012737 000015 002020
010536 004737 040522
010542 013700 001652
010546 012701 000001
010552 012702 000005
010556 012710 000001
010562 050110
010564 010146
010566 052716 000401
010572 011637 001124
010576 022610
010600 001405
010602 011067 170320
010606 010037 040172
010612 104001
010614 000241
010616 006101
010620 052701 000400
010624 042701 001000
010630 005302
010632 001351
010634 012701 000435
010640 012702 000005
010644 012710 000001
010650 050110
010652 020110
010654 001407
010656 010137 001124
010662 011037 001126
010666 010037 040172
010672 104001
010674 000261
010676 006101
010700 042701 001340
010704 052701 000400

```
*****
*TEST 15      RHR - MAINTENANCE REGISTER
*      BIT 0 (DMD) MUST BE SET BEFORE THE OTHER BITS
*      ARE READ WRITE
*      ONLY 5 LOW ORDER BITS ARE TESTED (R2 HAS 5)
*****
TST15: SCOPE
                MOV      #TTNO, @TSTNM      ; THIS SAVES TEST NUMBER
                JSR      PC, @CLDISK        ; SET UNIT NUMBER AND INIT
                MOV      @RHR, R0           ; R0 HAS MAINTENANCE REG. ADR.
                MOV      #1, R1            ; R1 HAS DATA
                MOV      #5, R2            ; R2 HAS COUNT OF NUMBER OF BITS
15:             MOV      #DMD, @R0         ; SET DIAGNOSTIC MODE BIT
                BIS      R1, @R0           ; SET DATA IN RHR
                MOV      R1, -(SP)         ; SAVE DATA FOR COMPARES
                BIS      #DMD!400, (SP)    ; INCLUDE BIT 0
                MOV      (SP), @SGDDAT    ; SAVE FOR ERROR PRINTOUT
                CMP      (SP)+, @R0        ; COMPARE DATA
                BEQ      25                ; BRANCH IF GOOD
                MOV      @R0, @BDDAT      ; BAD DATA
                MOV      R0, @REGADR      ; FAILING REG. ADR.
                ERROR    1                 ; MAINTENANCE REGISTER
                ; FAILED TO SET INDICATED
                ; BITS
25:             CLC                        ; CLEAR CARRY
                ROL      R1                ; GET NEXT DATA
                BIS      #400, R1          ; SET UNUSED BITS
                BIC      #BIT09, R1       ; CLEAR READ ONLY BIT
                DEC      R2                ; COUNT
                BNE     15                 ; BRANCH IF 5 BITS NOT DONE

                ; NOW FLOAT A 0
                MOV      #435, R1          ; R1 HAS DATA
                MOV      #5, R2            ; R2 HAS COUNT BITS
35:             MOV      #DMD, @R0         ; SET DIAGNOSTIC MODE BITS
                BIS      R1, @R0           ; SET DATA IN RHR
                CMP      R1, @R0          ; COMPARE DATA
                BEQ      45                ; BRANCH IF GOOD
                MOV      R1, @SGDDAT      ; GOOD DATA
                MOV      @R0, @BDDAT      ; BAD DATA
                MOV      R0, @REGADR      ; FAILING REG. ADR. RHR
                ERROR    1                 ; MAINTENANCE REGISTER
                ; DOES NOT ALLOW WRITING
                ; ZEROS
45:             SEC                        ; SET CARRY
                ROL      R1                ; GET NEXT DATA
                BIC      #BIT05!BIT06!BIT07!BIT09, R1 ; CLEAR READ ONLY BIT
                BIS      #BIT08, R1       ; SET BIT ZEROED BY ROL
```

87

GO7

MAINDEC-11-DZRPS-D, RJPO4 DISKLESS RH11 TEST-PART 1
DZRPSD.P11 T15 RMR - MAINTENANCE REGISTER

MACY11 27(663) 8-OCT-75 16:14 PAGE 41

SEQ 0083

1750 010710 005302
1751 010712 001354
1752
1753

DEC
BNE

R2
35

;COUNT IF 5 BITS DONE
;BRANCH IF INCOMPLETE

1754
1755
1756
1757
1758
1759
1760
1761
1762
1763
1764
1765
1766
1767
1768
1769
1770
1771
1772
1773
1774
1775
1776
1777
1778
1779
1780
1781
1782
1783
1784
1785
1786
1787
1788
1789
1790
1791
1792
1793
1794
1795
1796
1797
1798
1799
1800
1801
1802
1803
1804
1805

```
*****
; *TEST 16      RHDST - DESIRED SECTOR/TRACK ADDRESS
; *            TEST LOADING AND READING OF ALL POSSIBLE BITS IN THE HARDWARE
; *            REGISTERS. USE A PATTERN OF WALKING 0'S (-2,-3,-5 ETC.) AND
; *            WALKING 1'S (1,2,4,10 ETC)
*****
†ST16: SCOPE
```

010714 000004

010716 012706 001000
010722 012737 000016 002020
010730 013737 001636 010752
010736 013777 001762 170664
010744 004567 027224
010750 017437
010752 176706
010754 104001
010756 000207

```
MOV #STACK, SP ;RESET STACK
MOV #TTNO, #TSTNM ;THIS SAVES TEST NUMBER
PRDST: MOV #RHDST, #PRDST+14 ;GET REGISTER ADDRESS
        MOV #UNIT, #RHCS2 ;MOVE UNIT NO. UNDER TEST
        JSR RS, BITST ;TEST BITS IN REGISTER
        .WORD 17437 ;ONLY THESE BITS ARE TESTED FOR READ/WRITE
        .WORD 176706 ;ADDRESS OF REG. BEING TESTED
        ERROR 1 ;INCORRECT DATA RECEIVED
        RTS PC ;RETURN TO BLT3 ROUTINE
```

```
*****
; *TEST 17      RHER2 - ERROR REGISTER #2
; *            TEST LOADING AND READING OF ALL POSSIBLE BITS IN THE HARDWARE
; *            REGISTERS. USE A PATTERN OF WALKING 0'S (-2,-3,-5 ETC.) AND
; *            WALKING 1'S (1,2,4,10 ETC)
*****
†ST17: SCOPE
```

010760 000004

010762 012706 001000
010766 012737 000017 002020
010774 013737 001640 011016
011002 013777 001762 170620
011010 004567 027160
011014 177777
011016 176740
011020 104001
011022 000207

```
MOV #STACK, SP ;RESET STACK
MOV #TTNO, #TSTNM ;THIS SAVES TEST NUMBER
PRER2: MOV #RHER2, #PRER2+14 ;GET REGISTER ADDRESS
        MOV #UNIT, #RHCS2 ;MOVE UNIT NO. UNDER TEST
        JSR RS, BITST ;TEST BITS IN REGISTER
        .WORD 177777 ;ONLY THESE BITS ARE TESTED FOR READ/WRITE
        .WORD 176740 ;ADDRESS OF REG. BEING TESTED
        ERROR 1 ;INCORRECT DATA RECEIVED
        RTS PC ;RETURN TO BLT3 ROUTINE
```

1806
1807
1808
1809
1810
1811
1812
1813
1814
1815
1816
1817
1818
1819
1820
1821
1822
1823
1824
1825
1826
1827
1828
1829
1830
1831
1832
1833
1834
1835
1836
1837
1838
1839
1840
1841
1842
1843
1844
1845
1846
1847
1848
1849
1850
1851
1852

011024 000004
011026 012706 001000
011032 012737 000020 002020
011040 013737 001642 011062
011046 013777 001762 170554
011054 004567 027114
011060 016277
011062 176732
011064 104001
011066 000207

```

*****
*TEST 20      RHOF - MARGIN/OFFSET REGISTER
*            TEST LOADING AND READING OF ALL POSSIBLE BITS IN THE HARDWARE
*            REGISTERS.  USE A PATTERN OF WALKING 0'S (-2,-3,-5 ETC.) AND
*            WALKING 1'S (1,2,4,10 ETC)
*****
TST20: SCOPE
          MOV      #STACK,SP      ;RESET STACK
          MOV      #TTNO,#TSTNM   ;THIS SAVES TEST NUMBER
          MOV      @RHOF,@PROF+14 ;GET REGISTER ADDRESS
          MOV      @UNIT,@RHCS2   ;MOVE UNIT NO. UNDER TEST
          JSR      R5,BITST       ;TEST BITS IN REGISTER
          .WORD    16277          ;ONLY THESE BITS ARE TESTED FOR READ/WRITE
          .WORD    176732        ;ADDRESS OF REG. BEING TESTED
          ERROR    1              ;INCORRECT DATA RECEIVED
          RTS      PC             ;RETURN TO BLT3 ROUTINE

```

```

*****
*TEST 21      RHCA - DESIRED CYLINDER REGISTER
*            TEST LOADING AND READING OF ALL POSSIBLE BITS IN THE HARDWARE
*            REGISTERS.  USE A PATTERN OF WALKING 0'S (-2,-3,-5 ETC.) AND
*            WALKING 1'S (1,2,4,10 ETC)
*****
TST21: SCOPE
          MOV      #STACK,SP      ;RESET STACK
          MOV      #TTNO,@TSTNM   ;THIS SAVES TEST NUMBER
          MOV      @RHCA,@PRCA+14 ;GET REGISTER ADDRESS
          MOV      @UNIT,@RHCS2   ;MOVE UNIT NO. UNDER TEST
          JSR      R5,BITST       ;TEST BITS IN REGISTER
          .WORD    1777           ;ONLY THESE BITS ARE TESTED FOR READ/WRITE
          .WORD    176734        ;ADDRESS OF REG. BEING TESTED
          ERROR    1              ;INCORRECT DATA RECEIVED
          RTS      PC             ;RETURN TO BLT3 ROUTINE

```

Handwritten mark

1853
1854
1855
1856
1857
1858
1859
1860
1861
1862
1863
1864
1865
1866
1867
1868
1869
1870
1871
1872
1873
1874
1875
1876
1877
1878
1879
1880
1881
1882
1883
1884
1885
1886

011134 000004
011136 012706 001000
011142 012737 000022 002020
011150 013737 001646 011172
011156 013777 001762 170444
011164 004567 027004
011170 177777
011172 176742
011174 104001
011176 000207

```
*****  
:TEST 22 RHER3 - ERROR REGISTER #3  
:TEST LOADING AND READING OF ALL POSSIBLE BITS IN THE HARDWARE  
:REGISTERS. USE A PATTERN OF WALKING 0'S (-2,-3,-5 ETC.) AND  
:WALKING 1'S (1,2,4,10 ETC)  
*****  
TST22: SCOPE  
MOV #STACK,SP ;RESET STACK  
MOV #TTNO,#TSTNM ;THIS SAVES TEST NUMBER  
PRER3: MOV #RHER3,#PRER3+14 ;GET REGISTER ADDRESS  
MOV #UNIT,#RHCS2 ;MOVE UNIT NO. UNDER TEST  
JSR R5,BITST ;TEST BITS IN REGISTER  
.WORD 177777 ;ONLY THESE BITS ARE TESTED FOR READ/WRITE  
.WORD 176742 ;ADDRESS OF REG. BEING TESTED  
ERROR 1 ;INCORRECT DATA RECEIVED  
RTS PC ;RETURN TO BLT3 ROUTINE
```

```
*****  
:OF THE TWENTY REGISTERS (4 IN RH11, 16 IN RPO4) ONLY 12 ARE  
:CHECKED IN THE ABOVE TESTS  
:TWO ARE ALREADY TESTED (SERIAL NO. AND DRIVE TYPE)  
:THE OTHER 7 WHICH ARE RHDS1, RHLA, RHCC, RHEC1, RHEC1, RHEC2  
:ARE READ ONLY REGISTERS. ONE OR ZERO CANNOT BE WRITTEN  
*****
```

1887
1888
1889
1890
1891
1892
1893
1894
1895
1896
1897
1898
1899
1900
1901
1902
1903
1904
1905
1906
1907
1908
1909
1910
1911
1912
1913
1914
1915
1916
1917
1918
1919
1920
1921
1922
1923
1924
1925
1926
1927
1928
1929
1930
1931
1932
1933
1934
1935
1936
1937
1938
1939
1940

011200 000004
011202 012706 001000
011206 012737 000023 002020
011214 004737 040522
011220 005037 001774
011224 012701 047070
011230 012700 000010
011234 012721 177777
011240 005300
011242 001374
011244 005012
011246 012700 000010
011252 012701 047070
011256 005714
011260 032712 010000
011264 001415
011266 005300
011270 001434
011272 011246
011274 042716 177770
011300 005216
011302 013703 001632
011306 005203
011310 112713 000100
011314 012612
011316 000757
011320 022777 024020 170330
011326 001405
011330 022777 020020 170320
011336 001401

```
*****
;TEST 23 CONTROL AND STATUS 2 (RHCS 2) - 'NED'
; THIS TESTS THE UNIT SELECT BITS #0-2 (US1-4)
; AND NON-EXISTENT DRIVE BIT #12 (NED)
;
; THE OTHER RHCS2 BITS ARE NOT TESTED HERE
*****
TST23: SCOPE
MOV #STACK, SP ;RESET STACK
MOV #TTNO, @TSTNM ;THIS SAVES TEST NUMBER
JSR PC, @CLDISK ;HERE IT IS USED TO SETUP HARDWARE/
;CPU REGISTER CORRESPONDENCE
;R1=RHCS1
;R2=RHCS2
;R3=RHDS1
;R4=RHER1
CLR @ERFLGS ;CLEAR ERROR FLAG
;SIMULATED DISK AREA WILL BE USED AS A TEMPORARY
;STORAGE TABLE FOR DRIVES PRESENT
MOV #DISK, R1 ;LOAD TABLE POINTER
MOV #8, R0 ;LOAD TABLE LOCATION COUNTER
1$: MOV #-1, (R1)+ ;FILL 8 LOCATIONS WITH -1
DEC R0 ;COUNT DOWN ONE LOCATION
BNE 1$ ;BRANCH IF 8 NOT DONE
CLR @R2 ;SELECT UNIT NO.0 (U2!U1!U0=0)
MOV #8, R0 ;RELOAD TABLE LOCATION COUNTER
MOV #DISK, R1 ;RELOAD THE TABLE POINTER
2$: TST @R4 ;READ A DRIVE REGISTER (RHER1)
BIT #NED, @R2 ;NON EXISTENT DRIVE BIT = 0 ?
BEQ 3$ ;YES... DRIVE PRESENT, CHECK THE TYPE
7$: DEC R0 ;NO...DECREMENT DRIVE COUNT
BEQ 4$ ;CHECK RESULTS IF 8 DRIVES DONE
10$: MOV @R2, -(SP) ;PUT 'RHCS2' ON THE STACK
BIC #1C7, (SP) ;MASK ALL BUT THE UNIT NUMBER
INC (SP) ;INCREMENT THE UNIT NUMBER
MOV @RHCS1, R3 ;GET RHCS1 ADDRESS
INC R3 ;ADDRESS UPPER BYTE OF RHCS1
MOVB #100, @R3 ;SET TIE IN RHCS1
;WITHOUT ADDRESSING DRIVE
MOV (SP)+, @R2 ;RHCS2 HAS INCREMENTED UNIT WITH NED CLEARED
BR 2$ ;TEST FOR NEXT DRIVE
3$: CMP #24020, @RHDT ;IS THIS A DUAL PORT RPO4 ?
BEQ 8$ ;TYPE IT OUT IF SO
CMP #20020, @RHDT ;IS THIS A SINGLE PORT RPO4 ?
BEJ 8$ ;TYPE IT OUT IF SO
```



```

1941 011340 000752          BR      7S          ;NO RPO4 FOUND SO CHECK NEXT UNIT
1942
1943 011342 012746 000010    8S:    MOV      #8, -(SP)      ;LOAD MAX NO. OF DRIVES
1944 011346 160016          SUB      RO, (SP)        ;(SP) NOW HAS THE PRESENT DRIVE NO.
1945 011350 012621          MOV      (SP)+, (R1)+    ;LOAD TABLE, INCR TABLE LOCATION &
1946                                ;RESTORE THE STACK TO WHERE IT WAS
1947 011352 005300          DEC      RO              ;DECREMENT THE DRIVE COUNT
1948 011354 001402          BEQ     4S              ;CHECK RESULTS IF 8 UNITS CHECKED
1949 011356 005212          INC     2R2             ;SELECT NEXT UNIT
1950 011360 000736          BR      2S              ;GO TEST IT
1951
1952 011362 004037 041416    4S:    JSR     RO, 2#COMPAR    ;COMPARE RESULTS
1953 011366 001742          UNITS   ;RHER1/RHAS DERIVED DATA
1954 011370 047070          DISK    ;'NED' TEST DATA
1955 011372 000010          8.     ;NO. OF WORDS TO COMPARE
1956 011374 011402          5S     ;RETURN FOR ERROR HEADER
1957 011376 011430          6S     ;RETURN FOR ERROR DATA
1958 011400 011546          13S    ;RETURN FOR GOOD COMPARISON (NEXT TEST)
1959
1960                                ;SPECIAL 'NED'/'RHAS' TABLE TYPE OUT ROUTINE
1961
1962 011402 104022          5S:    ERROR   22
1963 011404 012703 000010    MOV     #8, R3          ;LENGTH OF BOTH UNIT TABLES
1964 011410 012701 001742    MOV     #UNITS, R1      ;ADDRESS OF RHAS/RHER1 UNITS TABLE
1965 011414 012702 047070    MOV     #DISK, R2       ;ADDRESS OF 'NED' RHCS2 UNITS TABLE
1966 011420 012137 001124    14S:   MOV     (R1)+, 2#SGDDAT ;LOAD RHAS UNIT NO. INTO "SGDDAT" AND
1967                                ;INCREMENT THE TABLE LOCATION
1968 011424 012237 001126    MOV     (R2)+, 2#SBDDAT ;LOAD 'NED' UNIT NO. INTO "SBDDAT"
1969                                ;& INCR TABLE LOCATION
1970
1971 011430 032737 020000 177570 6S:    BIT     #SM13, 2#SMR    ;INHIBIT ERROR TYPE OUTS ?
1972 011436 001043          BNE     13S            ;YES...EXIT
1973 011440 022737 177777 001124    CMP     #-1, 2#SGDDAT   ;DOES RHAS UNIT TABLE LOCATION = -1 ?
1974 011446 001413          BEQ     11S            ;YES...DON'T TYPE IT - CHECK 'NED' TABLE
1975 011450 104400 057450    TYPE   ,SPACE8         ;NO...TAB OVER PC COLUMN
1976 011454 104400 057450    TYPE   ,SPACE8         ;TAB OVER THE TEST NO. COLUMN
1977 011460 013746 001124    MOV     2#SGDDAT, -(SP) ;SAVE 2#SGDDAT FOR TYPEOUT
1978 011464 104404          TYPOS   ;GO TYPE--OCTAL ASCII
1979 011466 006           .BYTE   6              ;TYPE 6 DIGITS
1980 011467 000           .BYTE   0              ;SUPPRESS LEADING ZEROS
1981 011470 104400 057456    TYPE   ,SPACE2         ;SPACE OVER TO THE NEXT COLUMN
1982 011474 000406          BR      12S           ;CHECK THE 'NED' UNIT TABLE
1983
1984 011476 104400 057450    11S:   TYPE   ,SPACE8         ;TAB OVER THE PC COLUMN
1985 011502 104400 057450    TYPE   ,SPACE8         ;TAB OVER THE TEST NO. COLUMN
1986 011506 104400 057450    TYPE   ,SPACE8         ;TAB OVER THE RHAS UNIT COLUMN
1987
1988 011512 022737 177777 001126 12S:   CMP     #-1, 2#SBDDAT   ;DOES 'NED' UNIT TABLE LOCATION = - 1 ?
1989 011520 001404          BEQ     9S              ;YES...DON'T TYPE IT
1990 011522 013746 001126    MOV     2#SBDDAT, -(SP) ;SAVE 2#SBDDAT FOR TYPEOUT
1991 011526 104404          TYPOS   ;GO TYPE--OCTAL ASCII
1992 011530 006           .BYTE   6              ;TYPE 6 DIGITS
1993 011531 000           .BYTE   0              ;SUPPRESS LEADING ZEROS
1994
    
```


2034
2035
2036
2037
2038
2039
2040
2041
2042
2043
2044
2045
2046
2047
2048
2049
2050
2051
2052
2053
2054
2055
2056
2057
2058
2059
2060
2061
2062
2063
2064
2065
2066
2067
2068
2069
2070
2071
2072
2073
2074
2075
2076
2077
2078
2079
2080
2081
2082
2083
2084
2085
2086
2087

011546 000004
011550 004737 040522
011554 005037 001774

011560 012777 177777 170034
011566 012777 177777 170030
011574 012777 177777 170024
011602 052777 157010 170020
011610 012777 001476 170014
011616 012777 177777 170010
011624 012777 017437 170004
011632 012777 177777 170000
011640 012777 016277 167774
011646 012777 177777 167770
011654 012777 177777 167764
011662 012777 000001 167762
011670 012777 177777 167754

011676 052712 000040
011702 013712 001762
011706 012700 001622

011712 012737 177777 001124
011720 011037 040172
011724 013037 001126
011730 023737 001124 001126
011736 001401
011740 104001

011742 052712 000040
011746 013712 001762

: #TEST 24 CONTROL AND STATUS 2 (RHCS2) - 'CLR'
: THIS TESTS THE UNIT SELECT BITS (US1-4) AND CLEAR BIT #5 (CLR)

: ALL REGISTERS ARE LOADED WITH ALL ONES EXCEPT BIT #0 AND #6
: WHICH ARE "GO" AND "INTERRUPT ENABLE", THEN 'CLR' IS GIVEN
: (RHDB IS READ FIRST AS THIS WILL SET DTL IN RHCS2 AND
: SC AND TRE IN RHCS1.)

: ANOTHER CLR IS GIVEN THEN ALL OTHER REGISTERS ARE READ

†ST24: SCOPE

JSR PC, @CLDISK ;SET REGISTERS AND CLEAR
CLR @ERFLGS ;CLEAR ANY ERRORS

;FILL ALL POSSIBLE BITS WITH ONES

MOV #177777, @RHDB ;BUS ADDRESS REGISTER GETS 177777
MOV #177777, @RHMC ;WORD COUNT REGISTER GETS 177777
MOV #177777, @RHBA ;BUS ADDRESS REGISTER GETS 177777
BIS #157010, @RHCS2 ;CONTROL AND STATUS 2 GETS 157010
MOV #1476, @RHCS1 ;CONTROL AND STATUS REGISTER GETS 1476
MOV #177777, @RHER1 ;ERROR REGISTER1 GETS 177777
MOV #17437, @RHDS1 ;DESIRED SECTOR TRACK
MOV #177777, @RHER2 ;ERROR REGISTER 2
MOV #16277, @RHOF ;OFFSET REGISTER
MOV #177777, @RHCA ;DESIRED CYLINDER
MOV #177777, @RHER3 ;ERROR REGISTER 3
MOV @DMD, @RHMR ;MAINTENANCE REGISTER
MOV #177777, @RHMR ;MAINTENANCE REGISTER

BIS #CLR, @R2 ;CLEAR ALL POSSIBLE BITS
MOV @UNIT, @R2 ;REINSTATE UNIT NO.
MOV @RHDB, @R0 ;R0 CONTAINS ADDR. OF ADDR. OF REG.

;DATA BUFFER REGISTER
MOV #177777, @SGDDAT ;GOOD DATA FOR ERROR
MOV @R0, @REGADR ;REGISTER ADDRESS
MOV @R0+, @SBDDAT ;TEST DATA
CMP @SGDDAT, @SBDDAT ;COMPARE GOOD WITH TEST DATA
BEQ 25 ;BRANCH IF GOOD
ERROR 1 ;RHDB DID NOT HAVE ALL ONES
;AFTER A CLR IN RHCS2

25: BIS #CLR, @R2 ;SET CLEAR AGAIN BECAUSE
;READING RHDB AFTER CLEARING WILL
;SET DLT SC AND TRE
MOV @UNIT, @R2 ;REINSTATE UNIT NO.

```

2088
2089 ;WORD COUNT REGISTER
2090
2091
2092 011752 012737 177777 001124 3$: MOV #177777, @#SGDDAT :GOOD DATA FOR ERROR TYPEOUT
2093 011760 013037 001126 :MOV @ (RO)+, @#SBDDAT :TEST DATA
2094 011764 022737 177777 001126 :CMP #177777, @#SBDDAT :COMPARE DATA
2095 011772 001402 :BEQ 4$ :BRANCH IF GOOD
2096 011774 004737 012560 :JSR PC, @#ERCS2C :JUMP TO ERROR FOR CLR (BIT 5)
2097 : :IN RHCS2
2098
    
```

```

2099 ;BUS ADDRESS REGISTER
2100
2101
2102 012000 012737 000000 001124 4$: MOV #0, @#SGDDAT :GOOD DATA FOR ERROR TYPEOUT
2103 012006 013037 001126 :MOV @ (RO)+, @#SBDDAT :TEST DATA
2104 012012 022737 000000 001126 :CMP #0, @#SBDDAT :COMPARE DATA
2105 012020 001402 :BEQ 5$ :BRANCH IF GOOD
2106 012022 004737 012560 :JSR PC, @#ERCS2C :JUMP TO ERROR FOR CLR (BIT 5)
2107 : :IN RHCS2
2108
    
```

```

2109 ;CONTROL AND STATUS 2 REGISTER
2110
2111
2112 012026 012746 000100 5$: MOV #100, -(SP) :INCLUDE IR
2113 012032 053716 001762 :BIS @#UNIT, (SP) :SET UNIT NO.
2114 012036 012637 001124 :MOV (SP)+, @#SGDDAT :GOOD DATA FOR TYPE OUT
2115 012042 013037 001126 :MOV @ (RO)+, @#SBDDAT :TEST DATA
2116 012046 023737 001124 001126 :CMP @#SGDDAT, @#SBDDAT :COMPARE DATA
2117 012054 001402 :BEQ 6$ :BRANCH IF GOOD
2118 012056 004737 012560 :JSR PC, @#ERCS2C :JUMP TO ERROR FOR CLR (BIT 5)
2119 : :IN RHCS2
2120
    
```

```

2121 ;CONTROL AND STATUS 1 REGISTER
2122
2123
2124
2125 012062 012737 004276 001124 6$: MOV #4276, @#SGDDAT :GOOD DATA FOR ERROR TYPEOUT
2126 012070 013037 001126 :MOV @ (RO)+, @#SBDDAT :TEST DATA
2127 012074 022737 004276 001126 :CMP #4276, @#SBDDAT :COMPARE DATA
2128 012102 001402 :BEQ 7$ :BRANCH IF GOOD
2129 012104 004737 012560 :JSR PC, @#ERCS2C :JUMP TO ERROR FOR CLR (BIT 5)
2130 : :IN RHCS2
2131
    
```

```

2132 ;ERROR 1 REGISTER
2133
2134
2135 012110 012737 000000 001124 7$: MOV #0, @#SGDDAT :GOOD DATA FOR ERROR TYPEOUT
2136 012116 013037 001126 :MOV @ (RO)+, @#SBDDAT :TEST DATA
2137 012122 022737 000000 001126 :CMP #0, @#SBDDAT :COMPARE DATA
2138 012130 001402 :BEQ 10$ :BRANCH IF GOOD
2139 012132 004737 012560 :JSR PC, @#ERCS2C :JUMP TO ERROR FOR CLR (BIT 5)
2140 : :IN RHCS2
2141
    
```


2142 ;DESIRED SECTOR/TRACK REGISTER
 2143
 2144
 2145 012136 012737 017437 001124 10\$: MOV #17437, @#SGDDAT ;GOOD DATA FOR ERROR TYPEOUT
 2146 012144 013037 001126 MOV @ (RO)+, @#SBDDAT ;TEST DATA
 2147 012150 022737 017437 001126 CMP #17437, @#SBDDAT ;COMPARE DATA
 2148 012156 001402 BEQ 11\$;BRANCH IF GOOD
 2149 012160 004737 012560 JSR PC, @#ERCS2C ;JUMP TO ERROR FOR CLR (BIT 5)
 2150 ;IN RHCS2

2151 ;ERROR 2 REGISTER
 2152
 2153
 2154
 2155 012164 012737 000000 001124 11\$: MOV #0, @#SGDDAT ;GOOD DATA FOR ERROR TYPEOUT
 2156 012172 013037 001126 MOV @ (RO)+, @#SBDDAT ;TEST DATA
 2157 012176 022737 000000 001126 CMP #0, @#SBDDAT ;COMPARE DATA
 2158 012204 001402 BEQ 12\$;BRANCH IF GOOD
 2159 012206 004737 012560 JSR PC, @#ERCS2C ;JUMP TO ERROR FOR CLR (BIT 5)
 2160 ;IN RHCS2

2161 ;OFFSET REGISTER
 2162
 2163
 2164
 2165 012212 012737 116000 001124 12\$: MOV #116000, @#SGDDAT ;GOOD DATA FOR ERROR TYPEOUT
 2166 012220 013037 001126 MOV @ (RO)+, @#SBDDAT ;TEST DATA
 2167 012224 022737 116000 001126 CMP #116000, @#SBDDAT ;COMPARE DATA
 2168 012232 001402 BEQ 13\$;BRANCH IF GOOD
 2169 012234 004737 012560 JSR PC, @#ERCS2C ;JUMP TO ERROR FOR CLR (BIT 5)
 2170 ;IN RHCS2

2171 ;DESIRED CYLINDER ADDRESS REGISTER
 2172
 2173
 2174
 2175 012240 012737 001777 001124 13\$: MOV #1777, @#SGDDAT ;GOOD DATA FOR ERROR TYPEOUT
 2176 012246 013037 001126 MOV @ (RO)+, @#SBDDAT ;TEST DATA
 2177 012252 022737 001777 001126 CMP #1777, @#SBDDAT ;COMPARE DATA
 2178 012260 001402 BEQ 14\$;BRANCH IF GOOD
 2179 012262 004737 012560 JSR PC, @#ERCS2C ;JUMP TO ERROR FOR CLR (BIT 5)
 2180 ;IN RHCS2

2181 ;ERROR 3 REGISTER
 2182
 2183
 2184
 2185 012266 012737 000000 001124 14\$: MOV #0, @#SGDDAT ;GOOD DATA FOR ERROR TYPEOUT
 2186 012274 013037 001126 MOV @ (RO)+, @#SBDDAT ;TEST DATA
 2187 012300 022737 000000 001126 CMP #0, @#SBDDAT ;COMPARE DATA
 2188 012306 001402 BEQ 15\$;BRANCH IF GOOD
 2189 012310 004737 012560 JSR PC, @#ERCS2C ;JUMP TO ERROR FOR CLR (BIT 5)
 2190 ;IN RHCS2

2191 ;ATTENTION SUMMARY REGISTER
 2192
 2193
 2194 012314 013037 001126 15\$: MOV @ (RO)+, @#SBDDAT ;GET RHAS CONTENTS
 2195 012320 012737 000000 001124 MOV #0, @#SGDDAT ;GOOD DATA FOR ERROR TYPE OUT

```

2196 012326 123737 001124 001126      CMPB  @#SGDDAT,@#SBDDAT      ;COMPARE FOR RHAS
2197 012334 001402                      BEQ   16$                    ;BRANCH IF GOOD
2198 012336 004737 012560      JSR   PC,@#ERCS2C           ;JUMP TO ERROR FOR CLR (BIT 5)
2199                                     ;IN RHCS2
2200
2201                                     ;MAINTAINABILITY REGISTER
2202
2203
2204 012342 012737 000400 001124 16$:  MOV   #400, @#SGDDAT      ;GOOD DATA FOR ERROR TYPEOUT
2205 012350 013037 001126                      MOV   @ (RO)+,@#SBDDAT    ;TEST DATA
2206 012354 022737 000400 001126      CMP   #400, @#SBDDAT      ;COMPARE DATA
2207 012362 001402                      BEQ   17$                    ;BRANCH IF GOOD
2208 012364 004737 012560      JSR   PC,@#ERCS2C           ;JUMP TO ERROR FOR CLR (BIT 5)
2209                                     ;IN RHCS2
2210
2211                                     ;DRIVE STATUS REGISTER
2212
2213 012370 012737 000600 001124 17$:  MOV   #600,@#SGDDAT      ;GOOD DATA FOR ERROR TYPEOUT
2214 012376 013046                      MOV   @ (RO)+,-(SP)        ;GET RHDS1
2215 012400 011637 001126                      MOV   (SP),@#SBDDAT       ;TEST DATA
2216 012404 042716 001100                      BIC   #VV!PROG,(SF)       ;CLEAR VV AND PROG
2217 012410 022726 000600                      CMP   #600,(SP)+          ;COMPARE DATA
2218 012414 001402                      BEQ   20$                    ;BRANCH IF GOOD
2219 012416 004737 012560      JSR   PC,@#ERCS2C           ;JUMP TO ERROR FOR CLR (BIT 5)
2220                                     ;IN RHCS2
2221
2222                                     ;DRIVE TYPE
2223
2224
2225 012422 013737 001776 001124 20$:  MOV   @#SAVDT, @#SGDDAT   ;GOOD DATA FOR ERROR TYPEOUT
2226 012430 013037 001126                      MOV   @ (RO)+,@#SBDDAT    ;TEST DATA
2227 012434 023737 001776 001126      CMP   @#SAVDT, @#SBDDAT   ;COMPARE DATA
2228 012442 001402                      BEQ   21$                    ;BRANCH IF GOOD
2229 012444 004737 012560      JSR   PC,@#ERCS2C           ;JUMP TO ERROR FOR CLR (BIT 5)
2230                                     ;IN RHCS2
2231
2232
2233                                     ;SERIAL NUMBER REGISTER
2234
2235
2236
2237 012450 013737 002000 001124 21$:  MOV   @#SAVSN, @#SGDDAT   ;GOOD DATA FOR ERROR TYPEOUT
2238 012456 013037 001126                      MOV   @ (RO)+,@#SBDDAT    ;TEST DATA
2239 012462 023737 002000 001126      CMP   @#SAVSN, @#SBDDAT   ;COMPARE DATA
2240 012470 001402                      BEQ   22$                    ;BRANCH IF GOOD
2241 012472 004737 012560      JSR   PC,@#ERCS2C           ;JUMP TO ERROR FOR CLR (BIT 5)
2242                                     ;IN RHCS2
2243
2244
2245                                     ;ECC1 POSITION
2246
2247
2248 012476 012737 000000 001124 22$:  MOV   #0, @#SGDDAT        ;GOOD DATA FOR ERROR TYPEOUT
2249 012504 013037 001126                      MOV   @ (RO)+,@#SBDDAT    ;TEST DATA
    
```


E08

MAINDEC-11-DZRP5-D, RJPO4 DISKLESS RH11 TEST-PART 1 MACY11 27(663) 8-OCT-75 16:14 PAGE 52
 DZRP5D.P11 T24 CONTROL AND STATUS 2 (RHCS2) - 'CLR'

SEQ 0094

```

2250 012510 022737 000000 001126      CMP      #0,      @#SBDDAT ;COMPARE DATA
2251 012516 001402                    BEQ      23$      ;BRANCH IF GOOD
2252 012520 004737 012560      JSR      PC,@#ERCS2C ;JUMP TO ERROR FOR CLR (BIT 5)
2253                                     ;IN RHCS2
2254
2255
2256                                     ;ECC2 PATTERN
2257
2258
2259 012524 012737 000000 001124 23$:  MOV      #0,      @#SGDDAT ;GOOD DATA FOR ERROR TYPEOUT
2260 012532 013037 001126                    MOV      @#(RO)+,@#SBDDAT ;TEST DATA
2261 012536 022737 000000 001126      CMP      #0,      @#SBDDAT ;COMPARE DATA
2262 012544 001402                    BEQ      24$      ;BRANCH IF GOOD
2263 012546 004737 012560      JSR      PC,@#ERCS2C ;JUMP TO ERROR FOR CLR (BIT 5)
2264                                     ;IN RHCS2
2265
2266
2267                                     ;LOOK-AHEAD REGISTER
2268
2269 012552 005720                    24$:  TST      (RO)+      ;AS THE LOOK-AHEAD REG. CANNOT BE PREDICTED
2270                                     ;AFTER AN INIT IT IS NOT CHECKED
2271
2272                                     ;CURRENT CYLINDER ADDRESS REGISTER
2273
2274 012554 005720                    25$:  TST      (RO)+      ;AS THE CURRENT REG. CANNOT BE PREDICTED
2275                                     ;AFTER A INIT IT IS NOT CHECKED
2276
2277
2278 012556 000405                    26$:  BR       TST25      ;BRANCH OVER JSR
2279
2280
2281 012560 014037 040172      ERCS2C: MOV      -(RO), @#REGADR ;FAILING REGISTER ADDRESS
2282 012564 104001                    ERROR  1      ;CLR (BIT 5) IN RHCS2 DID
2283                                     ;NOT CLEAR APPROPRIATE BITS
2284                                     ;OR CLEARED EXTRA BITS
2285 012566 005720                    TST      (RO)+      ;UNDO -(RO) FOR BAD DATA
2286 012570 000207                    RTS      PC      ;RETURN TO TEST ABOVE

```

```

2287
2288
2289
2290
2291
2292
2293
2294
2295
2296
2297
2298 012572 000004
2299 012574 012706 001000
2300 012600 012737 000025 002020
2301
2302 012606 004737 040522
2303
2304 012612 012777 000001 167032
2305 012620 013777 002062 167004
2306
2307
2308 012626 004037 041214
2309 012632 001624
2310 012634 003134
2311 012636 000023
2312
2313
2314 012640 052777 000001 166764
2315
2316
2317 012646 052737 000100 003164
2318
2319
2320
2321
2322
2323 012654 004037 041214
2324 012660 001624
2325 012662 002070
2326 012664 000023
2327
2328
2329
2330
2331 012666 113737 003161 002115
2332
2333
2334
2335
2336
2337 012674 004037 041416
2338 012700 003134
2339 012702 002070
2340 012704 000023

```

```

:*****
:TEST 25      PACK ACKNOWLEDGE COMMAND TEST
:
:*      THE PACK ACKNOWLEDGE COMMAND WILL BE LOADED INTO RHCS1 WITH GO
:*      THEN ALL REGISTERS WILL BE CHECKED
:*      RH CLEAR WILL BE GIVEN
:*      THEN ALL REGISTERS WILL BE CHECKED
:*****
TST25: SCOPE
MOV      #STACK,SP      ;RESET STACK
MOV      #TTNO,#TSTNM   ;THIS SAVES TEST NUMBER
JSR      PC,#CLDISK     ;INIT AND SET UP GENERAL CPU/DEVICE
                        ;REGISTER CORRESPONDENCE AND UNIT NO.
MOV      #DMD,#RHM      ;SET DIAGNOSTIC MODE
MOV      #PKACK,#RHCS1  ;LOAD "PACK ACKNOWLEDGE COMMAND" INTO RHCS1
                        ;SAVE REGISTERS FOR COMPARISON AFTER 'GO' IS ISSUED
JSR      RO,#SAVER      ;SAVE
RMC      ;FROM
REINTO   ;TO
19.      ;NUMBER OF REGISTERS SAVED
BIS      #GO,#RHCS1    ;ISSUE 'GO' TO PACK ACKNOWLEDGE COMMAND
                        ;CHANGE SAVED REGISTERS TO EXPECTED VALUES
BIS      #VV,#REINTO+30 ;SAVED RHCS1
                        ;AFTER GO HAS BEEN GIVEN TO PACK ACKNOWLEDGE COMMAND
                        ;SAVE REGISTERS AGAIN SO THAT COMPARISONS CAN
                        ;BE DONE
JSR      RO,#SAVER      ;SAVE
RMC      ;FROM
WRFROM   ;NUMBER OF REGISTERS SAVED
19.
                        ;AS UPPER BYTE OF RHAS CAN BE CHANGING IN A DUAL PORT
                        ;OPERATION THE UPPER BYTE OF RHAS WILL BE SAVED AS IS
                        ;SO THAT THE COMPARES ARE ONLY VALID FOR THE LOWER BYTE
MOV      #REINTO+25,#WRFROM+25;SAVE UPPER RHAS
                        ;COMPARE REGISTERS BEFORE PACK ACKNOWLEDGE COMMAND
                        ;WITH AFTER GO
JSR      RO,#COMPAR     ;COMPARE
REINTO   ;GOOD BUFFER
WRFROM   ;TEST BUFFER
19.      ;NUMBER

```



```

2355
2356
2357
2358
2359
2360
2361
2362
2363
2364
2365
2366 012734 000004
2367 012736 012706 001000
2368 012742 012737 000026 002020
2369 012750 004737 040522
2370
2371
2372
2373
2374 012754 012777 177777 166642
2375 012762 012777 177777 166636
2376 012770 052777 157010 166632
2377 012776 012777 001476 166626
2378 013004 012777 177777 166622
2379 013012 012777 017437 166616
2380 013020 012777 177777 166612
2381 013026 012777 016277 166606
2382 013034 012777 000777 166602
2383 013042 012777 177777 166576
2384 013050 012777 000001 166574
2385 013056 012777 177777 166566
2386
2387
2388 013064 004037 041214
2389 013070 001624
2390 013072 003134
2391 013074 000021
2392
2393
2394 013076 000005
2395 013100 004737 052512
2396 013104 053777 001762 166516
2397
2398
2399 013112 005037 003136
2400 013116 013746 001762
2401 013122 052716 000100
2402 013126 012637 003140
2403 013132 012737 004276 003142
2404 013140 005037 003144
2405 013144 005037 003150
2406 013150 012737 116000 003152
2407 013156 005037 003156
2408 013162 105037 003160

```

```

*****
;#TEST 26 UNIBUS INIT TEST

```

```

;* ALL POSSIBLE REGISTER BITS ARE FILLED WITH ONES
;* A RESET COMMAND IS GIVEN
;* ALL REGISTERS ARE CHECKED

```

```

*****

```

```

TST26: SCOPE
MOV #STACK, SP ;RESET STACK
MOV #TTNO, #TSTNM ;THIS SAVES TEST NUMBER
JSR PC, #CLDISK ;INIT AND SET UP GENERAL CPU/DEVICE
;REGISTER CORRESPONDENCE

```

```

;FILL ALL POSSIBLE REGISTER BITS WITH ONES

```

```

MOV #177777, #RHMC ;WORD COUNT REGISTER GETS 177777
MOV #177777, #RHBA ;BUS ADDRESS REGISTER GETS 177777
BIS #157010, #RHCS2 ;CONTROL AND STATUS 2 GETS 177430
MOV #1476, #RHCS1 ;CONTROL AND STATUS REGISTER 1 GETS 21476
MOV #177777, #RHER1 ;ERROR REGISTER1 GETS 177777
MOV #17437, #RHDS1 ;DESIRED SECTOR TRACK
MOV #177777, #RHER2 ;ERROR REGISTER 2
MOV #16277, #RHOF ;OFFSET REGISTER
MOV #777, #RHCA ;DESIRED CYLINDER
MOV #177777, #RHER3 ;ERROR REGISTER 3
MOV #DND, #RHMR ;MAINTENANCE REGISTER
MOV #177777, #RHMR ;MAINTENANCE REGISTER

```

```

;BEFORE RESET SAVE REGISTERS IN READ INTO BUFFER
JSR RO, #SAVER ;SAVE
RHMC ;FROM
REINTO ;TO
17. ;NUMBER

```

```

;GIVE RESET AND REINSTATE UNIT NUMBER

```

```

RESET
JSR PC, #STKINT ;INITIALIZE TK
BIS #UNIT, #RHCS2

```

```

;CHANGE ORIGINAL SAVED REGISTERS TO EXPECTED VALUES AFTER RESET

```

```

CLR #REINTO+2 ;CLEAR SAVED RHBA
MOV #UNIT, -(SP) ;GET UNIT NUMBER FRO SAVED RHCS2
BIS #IR, (SP) ;INCLUDE IR
MOV (SP)+, #REINTO+4 ;SAVED RHCS2
MOV #DVA!RDY!76, #REINTO+6 ;SAVED RHCS1
CLR #REINTO+10 ;SAVED RHER1
CLR #REINTO+14 ;SAVED RHER2
MOV #116000, #REINTO+16 ;SAVED RHOF
CLR #REINTO+22 ;SAVED RHER3
CLRB #REINTO+24 ;SAVED RHAS

```



```

2409 013166 012737 000400 003162      MOV      #400, @#REINT0+26; SAVED RHM1
2410
2411      ;CHANGE RHDS1 WITHOUT CHANGING PROG BIT
2412 013174 013746 003164      MOV      @#REINT0+30, -(SP) ; GET RHDS1
2413 013200 042716 176777      BIC      #+CPR0G, (SP) ; CLEAR EVERYTHING EXCEPT PROG
2414 013204 052716 000700      BIS      #700, (SP) ; SET EXPECTED BITS - 'DPR', 'DRY' & 'W'
2415
2416 013210 012637 003164      4S:     MOV      (SP)+, @#REINT0+30; SAVED RHDS1
2417 013214 005037 003172      CLR      @#REINT0+36 ; SAVED RHEC1
2418 013220 005037 003174      CLR      @#REINT0+40 ; SAVED RHEC2
2419
2420      ;AFTER RESET, SAVE REGISTERS FOR COMPARISONS TO BE DONE
2421 013224 004037 041214      JSR      RD, @#SAVER ; SAVE
2422 013230 001E24 ; FROM
2423 013232 002070 ; TO
2424 013234 000021 ; NUMBER
2425
2426      ;AS UPPER BYTE OF RHAS CAN BE CHANGING IN A DUAL PORT
2427      ;OPERATION THE UPPER BYTE OF RHAS WILL BE SAVED AS IS
2428      ;SO THAT THE COMPARES ARE ONLY VALID FOR THE LOWER BYTE
2429 013236 113737 003161 002115      MOV8     @#REINT0+25, @#MRFROM+25; SAVE UPPER RHAS
2430
2431      ;COMPARE REGISTERS BEFORE RESET WITH REGISTERS AFTER RESET
2432 013244 004037 041416      JSR      RD, @#COMPAR ; COMPARE
2433 013250 003134 ; GOOD BUFFER
2434 013252 002070 ; TEST BUFFER
2435 013254 000021 ; NUMBER
2436 013256 013264 ; RETURN FOR ERROR
2437 013260 013264 ; SAME
2438 013262 013304 ; RETURN FOR GOOD COMPARISON
2439
2440 013264 013705 045272      1S:     MOV      @#ERMWORD, R5 ; GETTING READY TO INDEX
2441 013270 060505 ; DOUBLE ERROR WORD
2442 013272 016537 001622 040172      MOV      RHC-2(R5), @#REGADR ; FAILING REGISTER ADDRESS
2443 013300 104001 ; REGISTER CONTENTS AFTER
2444 ; A RESET THAT IS AN
2445 ; UNIBUS INITIALIZE CAUSED
2446 ; AN IMPROPER REGISTER CHANGE
2447 013302 000207      2S:     RTS      PC ; RETURN TO COMPARISON
2448 013304 ; RETURN TO POINT ON GOOD COMPARISON

```

2449
2450
2451
2452
2453
2454
2455
2456
2457
2458
2459
2460
2461
2462
2463
2464
2465
2466
2467
2468
2469
2470
2471
2472
2473
2474
2475
2476
2477
2478
2479
2480
2481
2482
2483
2484
2485
2486
2487
2488
2489
2490
2491
2492
2493
2494
2495
2496
2497
2498
2499
2500
2501
2502

.SBTTL SILO TESTS

*TEST 27 SILO TST 1

* THIS TESTS THE SILO BUFFER IN THE RH11 CONTROLLER
* A READ IS ATTEMPTED FROM AN EMPTY SILO
* DATA LATE (DLT) (RHCS2), TRANSFER ERROR (TRE) (RHCS1),
* SPECIAL CONDITION (SC) (RHCS1) SHOULD SET
* THEN LOADING "1" INTO TRE SHOULD CLEAR DLT, TRE AND SC

TST27: SCOPE
013304 000004
013306 012706 001000
013312 012737 000027 002020
013320 004767 025176
013324 017700 166272
013330 013746 001762
013334 052716 100100
013340 004737 040126
013344 022637 001700
013350 001403
013352 010237 001122
013356 104011

013360 022737 144200 001702 15:
013366 001403
013370 010137 001122
013374 104011

013376 012711 040000 25:
013402 004737 040126
013406 022737 004200 001702

013414 001403
013416 010137 001122
013422 104011

013424 013746 001762 35:
013430 052716 000100
013434 022637 001700
013440 001403
013442 010237 001122

MOV #STACK, SP ; RESET STACK
MOV #TTNO, #TSTNM ; THIS SAVES TEST NUMBER
JSR PC, CLDISK ; CLEAR DISK AND LOAD R'S
MOV #RHDB, R0 ; READ FROM EMPTY SILO
MOV #UNIT, -(SP) ; GET UNIT NO. IN
BIS #DLT!IR, (SP) ; GET DATA LATE BIT AND IR
JSR PC, #PUTREG ; SAVE REGISTERS
CMP (SP)+, #CS2 ; IS DATA LATE BIT UP?
BEQ 15 ; IF YES BRANCH
MOV R2, #SBDADR ; IF NOT STORE FAILING REG.
ERROR 11 ; RHCS2 DID NOT HAVE DLT
; RHCS2 SHOULD HAVE ONLY
; DLT AND UNIT NUMBER (BIT 0-2)
; ALL OTHER BITS SHOULD
; BE 0
CMP #SC!TRE!RDY!DVA, #CS1 ; IS SPECIAL CONDITION, TRANSFER ERROR
; AND READY UP?
BEQ 25 ; IF YES BRANCH
MOV R1, #SBDADR ; IF NOT STORE FAILING REG.
ERROR 11 ; RHCS1 DID NOT HAVE SC, DVA
; TRE AND RDY. AFTER A
; READ FROM EMPTY SILO ONLY
; THESE BITS SHOULD BE UP
; ALL OTHERS SHOULD BE 0
MOV #TRE, R1 ; CLEAR ERROR BITS BY MOVING
; ONE INTO TRE IN RHCS1
JSR PC, #PUTREG ; SAVE REGISTERS
CMP #RDY!DVA, #CS1 ; ALL BITS BUT RDY AND DVA SHOULD
; BE 0
BEQ 35 ; BRANCH IF YES
MOV R1, #SBDADR ; STORE FAILING ADDRESS
ERROR 11 ; AFTER A ONE IN TRE ONLY
; READY AND DVA SHOULD BE SET IN
; RHCS1

MOV #UNIT, -(SP) ; RHCS2 SHOULD HAVE IR AND UNIT ONLY
BIS #IR, (SP)
CMP (SP)+, #CS2 ; BRANCH IF YES
BEQ TST30 ; STORE FAILING ADDR
MOV R2, #SBDADR

K08

MAINDEC-11-DZRPS-D, RJPO4 DISKLESS RH11 TEST-PART 1
DZRPSO.P11 T27 SILO TST 1

MACY11 27(663) 8-OCT-75 16:14 PAGE 58

SEQ 0100

2503 013446 104011

ERROR 11

;AFTER A ONE IN TRE ONLY

2504
2505
2506
2507
2508
2509
2510
2511
2512
2513
2514
2515
2516
2517
2518
2519
2520
2521
2522
2523
2524
2525
2526
2527
2528
2529
2530
2531
2532
2533
2534
2535
2536
2537
2538
2539
2540
2541
2542
2543
2544
2545
2546
2547
2548
2549
2550
2551
2552
2553
2554
2555
2556
2557

:TEST 30 SILO TEST 2

:* THIS TESTS THE IR AND "OR" BITS OF RHCS2
:* AT THE BEGINNING IR SHOULD BE SET AND "OR" RESET
:* LOADING 0 IN SILO RESETS IR FOR ONLY 2 MICRO SECONDS
:* THIS TIME CANNOT BE CHECKED BUT IT IS CHECKED TO SEE IF
:* IT DOES GO DOWN OR NOT
:* THEN ALL 1 IS LOADED IN SILO "OR" SHOULD BECOME SET
:* IN 30 MICRO SECONDS AGAIN TIME IS NOT CHECKED
:* "OR" SHOULD BE SET
:* THE OUTPUT FROM THE SILO SHOULD BE 0 AND ALL ONES

TST30: SCOPE
013450 000004
013452 012706 001000
013456 012737 000030 002020
013464 004767 025032
013470 013746 001762
013474 052716 000100
013500 004737 040126
013504 022637 001700
013510 001403
013512 010237 001122
013516 104011

013520 005077 166076 1S:
013524 012777 177777 166070
013532 013737 001630 013542
013540 104424
013542 000000 2S:
013544 000200
013546 013746 001762 3S:
013552 052716 000300
013556 004737 040126
013562 022637 001700
013566 001403
013570 010237 001122
013574 104011

013576 017700 166020 4S:
013602 017705 166014
013606 022700 000000
013612 001410
013614 005037 001124

013620 010037 001126
013624 013737 001622 040172
013632 104001

MOV #STACK, SP ;RESET STACK
MOV #TTNO, @TSTNM ;THIS SAVES TEST NUMBER
JSR PC, CLDISK ;CLEAR REGISTERS LOAD R'S
MOV @#UNIT, -(SP)
BIS #IR, (SP)
JSR PC, @#PUTREG ;SAVE REGISTERS
CMP (SP)+, @#CS2 ;IR SHOULD BE SET "OR" RESET
BEQ 1S
MOV R2, @#SBDADR ;FAILING REGISTER RHCS2
ERROR 11 ;RHCS2 DOES NOT HAVE IR
;SET, UNIT NO. SET AND
;ALL OTHER BITS 0
1S: CLR @RHDB ;LOAD DATA BUFFER (SILO) WITH 0
MOV #-1, @RHDB ;LOAD SILO WITH ALL ONES
MOV @#RHCS2, @#2S ;ADDRESS OF RHCS2
MAT ;WAIT TRAP
2S: .WORD ;ADDRESS OF RHCS2
OR
3S: MOV @#UNIT, -(SP) ;
BIS #OR!IR, (SP) ;IR AND "OR"
JSR PC, @#PUTREG ;SAVE REGISTERS
CMP (SP)+, @#CS2 ;IR AND "OR" SHOULD BE SET
BEQ 4S
MOV R2, @#SBDADR ;SAVE RHCS2 ADDR. FAILING REG.
ERROR 11 ;"OR" IN RHCS2 SHOULD BE
;SET TOGETHER WITH IR AND
;UNIT NO.
4S: MOV @RHDB, R0 ;SAVE SILO DATA SHOULD BE 0
MOV @RHDB, R5 ;SAVE SILO DATA SHOULD BE ALL 1
CMP #0, R0 ;FIRST WORD 0? XYZ DO MORE TEST
BEQ 5S ;BRANCH IF YES
CLR @#SGDDAT ;GOOD DATA

MOV R0, @#SBDAT ;BAD DATA
MOV @#RHDB, @#REGADR ;SAVE RHDB FAILING REG.
ERROR 1 ;SILO DID NOT HAVE THE FIRST WORD
;"0" WHEN "OR" WAS SET

2558	013634	022705	177777	
2559	013640	001411		
2560	013642	012737	177777	001124
2561	013650	010537	001126	
2562	013654	013737	001622	040172
2563	013662	104001		
2564				
2565				
2566				

5S:

CMP	#-1,RS
BEQ	TST31
MOV	#-1, @#SGDDAT
MOV	RS, @#SBDDAT
MOV	@#RHDB, @#REGADR
ERROR	1

```

:SECOND WORD ALL ONES?
:BRANCH IF YES
:GOOD DATA
:BAD DATA
:SAVE RHDB FAILING REG.
:SILO DID NOT HAVE THE SECOND
:WORD OF ALL ONES WHEN "OR"
:WAS SET

```

2567
2568
2569
2570
2571
2572
2573
2574
2575
2576
2577
2578
2579
2580
2581
2582
2583
2584
2585
2586
2587
2588
2589
2590
2591
2592
2593
2594
2595
2596
2597
2598
2599
2600
2601
2602
2603
2604
2605
2606
2607
2608
2609
2610
2611
2612
2613
2614
2615
2616
2617
2618
2619
2620

;#TEST 31 SILO TEST 3

;* THIS TESTS SILO BUFFER BY FILLING IT WITH A COUNT FROM
;* 0 TO 65 AND THEN CHECKING IF IR IS DOWN AND "OR"
;* IS HIGH AND COMPARING THE SILO OUTPUT.

```

†ST31: SCOPE
MOV      #TTNO, @#TSTNM      ; THIS SAVES TEST NUMBER
MOV      #SILOTB, RO        ; TABLE POINTER
MOV      #67, R5            ; COUNTER
1$: CLR   (RO)↓             ; CLEAR TOTAL TABLE
        DEC   R5            ; COUNT
        BNE  1$            ; BRANCH IF NOT COMPLETELY CLEAR
        JSR  PC, CLDISK     ; CLEAR ALL REG.
        CLR  RO
        MOV  #66, R5        ; COUNT
2$: MOV  RO, @RHDB          ; LOAD SILO WITH COUNT FROM 0 TO 65
        INC  RO            ; NEXT COUNT
        DEC  R5            ; IS 66 LOADS DONE?
        BNE  2$            ; BRANCH IF NOT.
        MOV  @#UNIT, -(SP)
        BIS  #OR, (SP)
        JSR  PC, @#PUTREG    ; SAVE REGISTERS
        CMP  (SP)+, @#CS2    ; "OR" SHOULD BE SET IR RESET
        BEQ  3$            ; BRANCH IF YES
        MOV  R2, @#SBDADR    ; SAVE RHCS2 ADR. FAILING REG.
        ERROR 11           ; "OR" WAS NOT SET, IR WAS NOT
        CLR  @#ERFLGS        ; RESET AFTER SILO WAS FULL
3$: MOV  #SILOTB, RO        ; POINTER
        MOV  #66, R5        ; COUNTER
4$: MOV  @RHDB, (RO)+       ; READ SILO
        DEC  R5            ; COUNT
        BNE  4$            ; BRANCH IF 66 NOT DONE
        MOV  #SILOTB, RO    ; POINTER
        MOV  #66, R5
        CLR  -(SP)
5$: CMP  (SP), (RO)+
        BEQ  7$            ; BRANCH IF GOOD
        MOV  -(RO), @#SBDAT  ; BAD DATA
        MOV  (SP), @#SGDAT  ; GOOD DATA
        MOV  @#RHDB, @#REGADR ; FAILING REG. RHDB
        TST  @#ERFLGS        ; IS THIS FIRST ERROR?
        BNE  6$            ; IF NOT BRANCH
        ERROR 12           ; THESE TWO ERROR CALLS ARE FOR
        BR   64$           ; BRANCH TO AVOID PRINTING NEXT ERROR
6$: ERROR 13              ; THE SAME TYPEOUT. SILO
                          ; HAD A COUNT WRITTEN IN.
                          ; ON READ OUT AN ERROR WAS
                          ; DETECTED. THE TOTAL SILO
                          ; READOUT IS IN LOCATION

```


2633
 2634
 2635
 2636
 2637
 2638
 2639
 2640
 2641
 2642 014112 000004
 2643 014114 012737 000032 002020
 2644 014122 004767 024374
 2645 014126 005000
 2646 014130 005200
 2647 014132 010077 165464
 2648 014136 022700 000103
 2649 014142 001401
 2650 014144 000771
 2651 014146 004737 040126
 2652
 2653 014152 032737 100000 001700
 2654 014160 001003
 2655 014162 010237 001122
 2656 014166 104011
 2657 014170 017737 165426 001126
 2658 014176 012737 000001 001124
 2659 014204 023737 001124 001126
 2660 014212 001404
 2661 014214 013737 001622 040172
 2662 014222 104012
 2663

```

*****
:TEST 32      SILO TEST4
:           NOW PUT 67 WORDS INTO SILO AND CHECK FOR DLT
:           EVEN AFTER THE 67TH. WORD INPUT THE FIRST WORD SHOULD NOT CHANGE
*****

```

```

*****
TST32: SCOPE
MOV      #TTNO, @#TSTNM      ; THIS SAVES TEST NUMBER
JSR      PC, CLDISK         ; CLEAR DISK REG.
CLR      RO                  ; CLEAR RO
1$:      INC      RO          ; ADD 1
MOV      RO, @RHDB          ; LOAD SILO
CMP      #67., RO           ; 67 DONE?
BEQ      2$                 ; BRANCH IF YES
BR       1$                  ; NO SO BRANCH
2$:      JSR      PC, @PUTREG ; SAVE REGISTERS

BIT      #DLT, @#CS2        ; DLT SET?
BNE      3$                 ; BRANCH IF YES
MOV      R2, @#SBDADR       ; FAILING ADDRESS RHCS2
ERROR    11                  ; DATA LATE DID NOT SET AT 67TH.
3$:      MOV      @RHDB, @#SBDAT ; INPUT TO SILO
MOV      #1, @#SGDDAT       ; GOOD DATA
CMP      @#SGDDAT, @#SBDAT  ; COMPARE
BEQ      TST33              ; BRANCH IF GOOD
MOV      @#RHDB, @#REGADR   ; FAILING REG. RHDB
ERROR    12                  ; WORD IN RHDB CHANGED
                          ; AFTER THE 67TH INPUT.

```


2664
2665
2666
2667
2668
2669
2670
2671
2672
2673
2674
2675
2676
2677
2678
2679
2680
2681
2682
2683
2684
2685
2686
2687
2688
2689
2690
2691
2692
2693
2694
2695
2696
2697
2698
2699
2700
2701
2702
2703
2704
2705
2706
2707
2708
2709
2710
2711
2712
2713
2714
2715
2716
2717

014224 000004
014226 012737 000033 002020
014234 004737 040522
014240 013746 001762
014244 052716 000100
014250 004737 040126
014254 022637 001700
014260 001403
014262 010237 001122
014266 104011
014270 013700 001622
014274 005001
014276 010110
014300 005201
014302 022701 000004
014306 103373
014310 013737 001630 014320
014316 104424
014320 000000
014322 000200
014324 004737 040522
014330 013746 001762
014334 052716 000100
014340 004737 040126
014344 022637 001700
014350 001403
014352 010237 001122
014356 104011
014360 013700 001622
014364 012710 000004
014370 011201
014372 011005
014374 011003
014376 011204
014400 032701 000200
014404 001424
014406 022705 000004
014412 001410
014414 010037 040172

```

*****
:TEST 33      SILO TEST 5
:*           THE SILO IS LOADED WITH 0,1,2,3 THEN AFTER
:*           'OR' IS UP A CLR IN RHCS2 IS DONE THEN 4,
:*           IS LOADED. AFTER 'OR' IS UP 2 READS FROM
:*           SILO ARE DONE, ON THE LAST, 'DTL' IN RHCS2 SHOULD BE SET.
*****
†TST33: SCOPE
MOV          #TTNO, @TSTNM      ; THIS SAVES TEST NUMBER
JSR          PC, @CLDISK       ; CLEAR DISK
MOV          @#UNIT, -(SP)      ; GET UNIT NO.
BIS          #IR, (SP)         ; SET INPUT READY
JSR          PC, @PUTREG        ; SAVE REGISTERS
CMP          (SP)+, @#CS2       ; IR SHOULD BE SET "OR" CLEARED
BEQ          1$                ; BRANCH IF GOOD
MOV          R2, @#SBDADR       ; FAILING REGISTER RHCS2
ERROR        11                ; RHCS2 DOES NOT HAVE IR SET
                                ; AND ALL OTHER BITS 0
1$: MOV       @#RHDB, R0        ; R0 HAS RHDB ADDRESS
CLR          R1                 ; DATA
2$: MOV       R1, @R0          ; 0, THEN 1 THEN 2 THEN 3
                                ; IN RHDB
INC          R1                 ; INCREMENT DATA
CMP          #4, R1             ; IS 4 DONE
BHS         2$                 ; BRANCH IF NOT
MOV          @#RHCS2, @#3$      ;
MAT         .WORD 0            ; WAIT FOR "OR"
3$: OR        @#RHCS2, @#3$     ; RHCS2 ADDRESS
                                ; WAIT ON OR.
JSR          PC, @CLDISK       ; CLR IN RHCS2
MOV          @#UNIT, -(SP)      ; UNIT NO.
BIS          #IR, (SP)         ;
JSR          PC, @PUTREG        ; SAVE REGISTERS
CMP          (SP)+, @#CS2       ; IR SHOULD BE SET "0"=0
BEQ          4$                ; BRANCH IF GOOD
MOV          R2, @#SBDADR       ; FAILING REGISTER RHCS2
ERROR        11                ; RHCS2 DOES NOT HAVE IR SET
                                ; AND ALL OTHER BITS 0
4$: MOV       @#RHDB, R0        ; R0 HAS RHDB ADDRESS
MOV          #4, @R0           ; LOAD 4 IN SILO
MOV          @R2, R1           ; SAVE RHCS2
MOV          @R0, R5           ; READ THE 4 IN SILO
MOV          @R0, R3           ; READ SILO TO GET DLT
MOV          @R2, R4           ; SAVE RHCS2
BIT          #OR, R1           ; TEST FOR OR IN RHCS2
BEQ          6$                ; IF OR IS NOT SET BRANCH
CMP          #4, R5            ; SILO 4 IS NOW COMPARED
BEQ          5$                ;
MOV          R0, @#REGADR       ; SILO ADDRESS

```

2718	014420	012737	000004	001124		MOV	#4, 2#SGDDAT	:GOOD DATA
2719	014426	010537	001126			MOV	R5, 2#SBDDAT	:BAD DATA
2720	014432	104001				ERROR	1	:SILO DID NOT CONTAIN WORD
2721								:PUT IN AFTER "OR" WAS UP
2722	014434	005703			5S:	TST	R3	:IS IT ZERO BECAUSE SILO
2723								:IS DESTRUCTIVE READ
2724	014436	001407				BEQ	6S	:BRANCH IF GOOD
2725	014440	010037	040172			MOV	R0, 2#REGADR	:SILO ADDRESS
2726	014444	005037	001124			CLR	2#SGDDAT	:GOOD DATA
2727	014450	010337	001126			MOV	R3, 2#SBDDAT	:BAD DATA
2728	014454	104001				ERROR	1	:SILO SHOULD BE ZERO
2729								:AFTER THE ONE WORD PUT IN
2730								:HAS BEEN TAKEN OUT AS
2731								:SILO IS A DESTRUCTIVE READ
2732	014456	032704	100000		6S:	BIT	#DLT, R4	:
2733	014462	001013				BNE	TST34	:BRANCH IF DLT SET
2734	014464	013746	001762			MOV	2#UNIT, -(SP)	:GET UNIT NO
2735	014470	052716	100300			BIS	#DLT!OR!IR, (SP)	:
2736	014474	012637	001124			MOV	(SP)+, 2#SGDDAT	:GOOD DATA
2737	014500	010437	001126			MOV	R4, 2#SBDDAT	:BAD DATA
2738	014504	010237	040172			MOV	R2, 2#REGADR	:RHCS2 ADDRESS
2739	014510	104001				ERROR	1	:DATA LATE ERROR

2740
2741
2742
2743
2744
2745
2746
2747
2748
2749
2750
2751
2752
2753
2754
2755
2756
2757
2758
2759
2760
2761
2762
2763
2764
2765
2766
2767
2768
2769
2770
2771
2772
2773
2774
2775
2776

.SBTTL MORE REGISTER TESTS

;*TEST 34 TEST ODD BYTE INSTRUCTION ON RHCS1

;* RDY (BIT 07) AND DVA (BIT 11) SHOULD ALWAYS BE SET

TST34: SCOPE

014512 000004

014514 012737 000034 002020

014522 012706 001000

014526 004767 023770

014532 012711 003566

014536 010146

014540 005216

014542 112736 000005

014546 011137 001126

014552 022737 006766 001126

014560 001406

014562 012737 006766 001124

014570 010137 040172

014574 104001

014576 112711 000032

014602 011137 001126

014606 022737 006632 001126

014614 001406

014616 012737 006632 001124

014624 010137 040172

014630 104001

MOV #TTNO, @TSTNM ; THIS SAVES TEST NUMBER
MOV #STACK, SP ; RESET STACK
JSR PC, CLDISK ; CLEAR DISK REG.
MOV #3566, @R1 ; LOAD RHCS1 WITH ANY NUMBER
MOV R1, -(SP) ; GETTING READY TO FORM ODD BYTE
INC (SP) ; SP NOW HAS ODD BYTE FOR RHCS1
MOVB #5, @ (SP)+ ; MOVE 5 INTO ODD BYTE FOR RHCS1
MOV @R1, @#SBDDAT ; TEST DATA
CMP #2566!DVA!RDY, @#SBDDAT ; RHCS1 SHOULD HAVE 6766
BEQ IS ; BRANCH IF GOOD
MOV #2566!DVA!RDY, @#SGDDAT ; GOOD DATA
MOV R1, @#REGADR ; FAILING REGISTER RHCS1
ERROR 1 ; MOVING A NUMBER INTO
; ODD BYTE OF RHCS1 GAVE
; WRONG RESULTS
IS: MOVB #32, @R1 ; MOVE INTO EVEN BYTE
MOV @R1, @#SBDDAT ; TEST DATA
CMP #2432!DVA!RDY, @#SBDDAT ; RHCS1 SHOULD HAVE 6632
BEQ TST35 ; BRANCH IF GOOD
MOV #2432!DVA!RDY, @#SGDDAT ; GOOD DATA
MOV R1, @#REGADR ; FAILING REGISTER RHCS1
ERROR 1 ; MOVING A NUMBER INTO EVEN
; BYTE OF RHCS1 GAVE WRONG
; RESULTS

2777
2778
2779
2780
2781
2782
2783
2784
2785
2786
2787
2788
2789
2790
2791
2792
2793
2794
2795
2796
2797
2798
2799
2800
2801
2802
2803
2804
2805
2806
2807
2808
2809
2810
2811
2812
2813
2814
2815
2816
2817
2818
2819
2820
2821

```

;*****
;*TEST 35      TEST ODD BYTE INSTRUCTION ON RHCS2
;*      IR (BIT 06) AND THE UNIT SELECT (BIT 0-2) WILL BE SET
;*****
TST35:  SCOPE
        MOV     @TTNO,@TSTNM      ;THIS SAVES TEST NUMBER
        JSR     PC,CLDISK
        BIS     #177000,(R2)      ;LOAD RHCS2
        MOV     R2,-(SP)          ;GETTING READY FOR ODD BYTE
        INC     (SP)              ;SP NOW HAS ODD BYTE FOR RHCS2
        CLRB   @SP+              ;CLERR RHCS2 ODD BYTE
        MOV     @#UNIT,-(SP)      ;GET UNIT NO.
        BIS     #IR,(SP)          ;INPJT READY AS IT IS SET
        MOV     @R2,@#SBDDAT      ;TEST DATA
        CMP     (SP)+,@#SBDDAT    ;COMPARE TO SEE THAT
        ;"CLRB" DID CLEAR
        BEQ     1$
        MOV     @#UNIT,@#SGDDAT
        BIS     #IR,@#SGDDAT      ;GOOD DATA
        MOV     R2,@#REGADR      ;FAILING REGISTER RHCS2
        ERROR   1                ;CLEARING ODD BYTE OF RHCS2
        ;GAVE WRONG RESULTS
1$:     MOV     @#UNIT,-(SP)
        BIS     #BAI,(SP)
        BIS     #UPE,@R2          ;HAVE UPE AND MPE IN RHCS2
        ;BESIDES UNIT SELECT
        MOV     (SP)+,@R2         ;MOVE INTO EVEN BYTE OF RHCS2
        MOV     @#UNIT,-(SP)
        BIS     #UPE!IR!BAI,(SP)
        MOV     (SP),@#SGDDAT    ;GOOD DATA
        MOV     @R2,@#SBDDAT      ;TEST DATA
        CMP     (SP)+,@#SBDDAT    ;COMPARE TO SEE THAT MOV B DID
        ;MOVE EVEN BYTE ONLY
        BEQ     TST36             ;BRANCH IF GOOD
        MOV     R2,@#REGADR      ;FAILING REGISTER RHCS2
        ERROR   1                ;MOVING A NUMBER INTO EVEN
        ;BYTE OF RHCS2 GAVE WRONG
        ;RESULTS

```



```

2822
2823
2824 ;*****
2825 ;*TEST 36 ODD BYTE TEST ON RHMC
2826
2827 ;* IN THIS REGISTER NO BITS SHOULD BE PERMANENTLY SET
2828
2829 ;*****
2830 TST36: SCOPE
2831
2832 014776 000004
2833 015000 012737 000036 002020 MOV #TTNO, @TSTNM ; THIS SAVES TEST NUMBER
2834 015006 012706 001000 MOV #STACK, SP ; RESET STACK
2835 015012 004767 023504 JSR PC, CLDISK ; CLEAR DISK REGISTERS
2836 015016 013704 001624 MOV @RHMC, R4 ; R4 NOW IS WORD COUNT REGISTER
2837 015022 012714 025252 MOV #25252, @R4 ; LOAD RHMC
2838 015026 010446 MOV R4, -(SP) ; GETTING READY TO FORM ODD BYTE
2839 015030 005216 INC (SP) ; SP NOW HAS ODD BYTE FOR RHMC
2840 015032 112736 000377 MOV #377, @R4 ; MOVE 377 INTO ODD BYTE OF RHMC
2841 015036 011437 001126 MOV @R4, @SBDDAT ; TEST DATA
2842 015042 022737 177652 001126 CMP #177652, @SBDDAT ; COMPARE TO SEE IF MOV# DID OK
2843 015050 001406 BEQ IS ; BRANCH IF GOOD
2844 015052 012737 177652 001124 MOV #177652, @SGDDAT ; GOOD DATA
2845 015060 010437 040172 MOV R4, @REGADR ; REGISTER FAILING RHMC
2846 015064 104001 ERROR 1 ; MOVING INTO ODD BYTE OF RHMC
2847 015066 112714 000123 1S: MOV #123, @R4 ; GAVE WRONG RESULTS
2848 015072 011437 001126 MOV @R4, @SBDDAT ; MOVE INTO EVEN BYTE OF RHMC
2849 015076 022737 177523 001126 CMP #177523, @SBDDAT ; TEST DATA
2850 015104 001406 BEQ TST37 ; BRANCH IF GOOD
2851 015106 012737 177523 001124 MOV #177523, @SGDDAT ; GOOD DATA
2852 015114 010437 040172 MOV R4, @REGADR ; REGISTER FAILING RHMC
2853 015120 104001 ERROR 1

```

2854
2855
2856
2857
2858
2859
2860
2861
2862
2863
2864
2865
2866
2867
2868
2869
2870
2871
2872
2873
2874
2875
2876
2877
2878
2879
2880
2881
2882
2883
2884
2885
2886
2887

015122 000004

015124 012706 001000
015130 012737 000037 002020
015136 004767 023360
015142 013704 001626
015146 012714 025253
015152 010446
015154 005216
015156 112736 000377
015162 011437 001126
015166 022737 177652 001126
015174 001406
015176 012737 177652 001124
015204 010437 040172
015210 104001
015212 112714 000125
015216 011437 001126
015222 022737 177524 001126
015230 001406
015232 012737 177524 001124
015240 010437 040172
015244 104001

;TEST 37 TEST ODD BYTE INSTRUCTION ON RHBA

;* BIT 0 SHOULD ALWAYS BE 0

TST37: SCOPE

MOV #STACK, SP ;RESET STACK
MOV #TTNO, @#TSTNM ;THIS SAVES TEST NUMBER
JSR PC, CLDISK
MOV @#RHBA, R4 ;R4 HAS ADDRESS OF RHBA
MOV #25253, @R4 ;LOAD RHBA
MOV R4, -(SP) ;GETTING READY FOR ODD BYTE
INC (SP) ;SP HAS ODD BYTE ADR. OF RHBA
MOVB #377, @#(SP)+ ;LOAD ODD BYTE OF RHBA
MOV @R4, @#SBDDAT ;TEST DATA
CMP #177652, @#SBDDAT ;COMPARE MOVB RESULTS
BEQ 15 ;BRANCH IF GOOD
MOV #177652, @#SGDDAT ;GOOD DATA
MOV R4, @#REGADR ;FAILING REGISTER RHBA
ERROR 1 ;MOVING INTO ODD BYTE OF
;RHBA GAVE WRONG RESULTS
15: MOVB #125, @R4
MOV @R4, @#SBDDAT ;TEST DATA
CMP #177524, @#SBDDAT
BEQ TST40 ;BRANCH IF GOOD
MOV #177524, @#SGDDAT ;GOOD DATA
MOV R4, @#REGADR ;FAILING REGISTER RHBA
ERROR 1 ;MOVING INTO EVEN BYTE OF
;RHBA GAVE WRONG RESULTS

2888
2889
2890
2891
2892
2893
2894
2895
2896
2897
2898
2899
2900
2901
2902
2903
2904
2905
2906
2907
2908
2909
2910
2911
2912
2913
2914
2915
2916
2917
2918
2919
2920
2921
2922
2923
2924
2925
2926
2927
2928
2929
2930
2931
2932
2933
2934
2935
2936
2937
2938
2939
2940
2941

.SBTTL DCL COMMAND TESTS

: FOUR GENERAL REGISTERS WILL BE RESERVED FOR HARDWARE
: R1=RHCS1 CONTROL AND STATUS1
: R2=RHCS2 CONTROL AND STATUS2
: R3=RHDS1 DRIVE STATUS 1
: R4=RHER1 ERROR REGISTER1
:
: WHENEVER ANY OTHER USE IS MADE OF THESE REGISTERS
: APPROPRIATE SAVING MUST BE DONE
:*****

: ERROR REGISTER #01 (RHER1) TEST
: BIT #1 (ILLEGAL REGISTER) CANNOT BE TESTED ON PDP11 THIS BIT
: IS FOR PDP10 USE ONLY
:*****

: *TEST 40 TEST ILF BIT #0 IN REG. RHER1
:
: ALL 3 ILLEGAL FUNCTION CODES SHOULD SET - ATA,ERR,ILF - AND ARE TESTED
: A GO WITHOUT CLEARING ILF ERR SHOULD SET - MXF,DLT,TRE - BITS AND THEY ARE ALSO

TST40: SCOPE

015246 000004
015250 012706 001000
015254 012737 000040 002020
015262 004737 040522
015266 012777 000001 164356
015274 005037 002010
015300 012700 002024 1\$:
015304 012705 000021
015310 023720 002010 2\$:
015314 001004
015316 062737 000002 002010
015324 000765
015326 005305 3\$:
015330 001367
015332 032737 000100 002010
015340 001077
015342 013737 002010 002066

MOV #STACK,SP ;RESET STACK
MOV #TTNO,@TSTNM ;THIS SAVES TEST NUMBER
JSR PC,@CLDISK ;CLEAR REGISTERS
MOV #DND,@RHMR ;SET DIAGNOSTIC MODE
CLR @TMPILL ;GET READY TO MAKE ILLEGAL FUNCTION
MOV #FUTABL,RO ;LOAD FUNCTION CODE TABLE START
MOV #17,R5 ;COUNTER (16 GOOD FUNCTIONS)
CMP @TMPILL,(RO)+ ;IS THIS A LEGAL FUNCTION CODE?
BNE 3\$;NO - DECR. FUNCT. CODE CTR
ADD #2,@TMPILL ;YES MAKE NEXT FUNCTION CODE
BR 1\$;TEST NEXT FUNCTION CODE
DEC R5 ;MAKE NEXT CODE IF 1ST 16
 ;LEGAL FUNCTIONS NOT DONE
BNE 2\$;BRANCH IF 16 NOT COMPLETE
BIT #100,@TMPILL ;ALL BITS UP TO BIT #5 COMPARED?
BNE 12\$;YES - EXIT ----->
MOV @TMPILL,@ILLEG ;NO - TEST THE ILLEGAL FUNCTION

```

2942 015350 062737 000002 002010      ADD      #2, @TMPILL      ;TEST NEW FUNCTION CODE NEXT TIME
2943
2944 015356 004737 040522      4S:     JSR      PC, @CLDISK
2945 015362 012777 000001 164262      MOV      #DMD, @RHMR      ;SET DIAGNOSTIC MODE
2946 015370 013711 002066      MOV      @ILLEGL, @R1     ;ILLEGAL FUNCTION ----> RHCS1
2947 015374 012737 015356 001110      MOV      #4S, @SLPERR     ;ERROR RETURN POINT
2948 015402 004737 040126      JSR      PC, @PUTREG      ;SAVE REGISTERS
2949 015406 005737 001704      TST      @ER1            ;THERE SHOULD NOT BE ANY ERROR YET
2950 015412 001403      BEQ      5S              ;CONTINUE IF RHER1 STILL = 0
2951 015414 010437 001122      MOV      R4, @SBDADR     ;FAILING REGISTER ADDRESS RHER1
2952 015420 104011      ERROR      11           ;ALTHOUGH AN ILLEGAL FUNCTION
2953                                     ;HAS BEEN MOVED INTO RHCS1
2954                                     ;NO ERRORS SHOULD SHOW TILL
2955                                     ;GO IS SET RHER1 SHOULD BE
2956                                     ;ALL ZEROS
2957
2958 015422 052711 000001      5S:     BIS      #GO, @R1        ;GO IN RHCS1
2959 015426 004737 040126      JSR      PC, @PUTREG      ;SAVE REGISTERS
2960 015432 022737 000001 001704      CMP      #ILF, @ER1     ;ILLEGAL FUNCTION BIT SHOULD BE SET
2961 015440 001403      BEQ      6S              ;IT IS - CONTINUE
2962 015442 010437 001122      MOV      R4, @SBDADR     ;FAILING REGISTER ADDRESS RHER1
2963 015446 104011      ERROR      11           ;ILLEGAL FUNCTION DID NOT
2964                                     ;SET ON AN ILLEGAL FUNCTION
2965                                     ;EXECUTION, THE ILLEGAL FUNCTION
2966                                     ;BEING EXECUTED IS IN RHCS1
2967
2968 015450 013746 001724      6S:     MOV      @DS1, -(SP)     ;GET RHDS1
2969 015454 042716 001000      BIC      #PROG, (SP)     ;MASK PROG
2970 015460 022726 140700      CMP      #ATA!ERR!VV!DPR:DRY, (SP)+
2971                                     ;ATTENTION (BIT 15)
2972                                     ;VOLUME VALID (BIT 6)
2973                                     ;COMPOSIT ERROR (BIT 14)
2974                                     ;DEVICE READY (BIT 7) SHOULD
2975                                     ;BE SET ON RHDS1
2976 015464 001404      BEQ      7S              ;THEY ARE - CONTINUE
2977 015466 013737 001654 001122      MOV      @RHDS1, @SBDADR ;FAILING REGISTER ADDRESS RHDS1
2978 015474 104011      ERROR      11           ;FOLLOWING BITS SHOULD BE SET
2979                                     ;WITH AN ILLEGAL FUNCTION
2980                                     ;ATTENTION (BIT 15)
2981                                     ;COMPOSIT ERROR (BIT 14)
2982                                     ;MEDIUM ON LINE (BIT 12)
2983                                     ;DEVICE READY (BIT 7)
2984
2985 015476 004737 042762      7S:     JSR      PC, @MIDDLE     ;GIVE A WRITE HEADER AND
2986                                     ;DATA COMMAND WITHOUT
2987                                     ;CLEARING THE ERRORS
2988                                     ;USING "MIDDLE" SO THAT
2989                                     ;IT WILL COME BACK BEFORE
2990                                     ;THE END TO FIND OUT ITS
2991                                     ;STATE
2992 015502 010237 015510      MOV      R2, @10S       ;MOVE RHCS2 ADDRESS
2993 015506 104424      NAT
2994 015510 000000      10S:   .WORD      0       ;WAIT FOR 'MXF' BIT
2995 015512 001000      MXF      ;ADDRESS OF RHCS2

```



```

2996 015514 004737 040126      11$: JSR PC,@#PUTREG      ;SAVE REGISTERS
2997
2998 015520 032737 040000 001702      BIT @TRE,@#CS1      ;TRANSFER ERROR (BIT 14) RHCS1 - 'TRE'
2999                                     ;SHOULD SET DUE TO 'MXF'
3000 015526 001003                                     BNE 13$             ;IT IS - CONTINUE
3001 015530 010137 001122      MOV R1,@#SBDADR     ;FAILING REGISTER RHCS1
3002 015534 104011      ERROR 11           ;TRANSFER ERROR (BIT 14) RHCS1 - 'TRE'
3003                                     ;SHOULD BE SET DUE TO 'MXF'
3004                                     ;LOCAL SCOPE RETURN POINT
3005
3006 015536 000660      13$: BR 1$         ;GO BACK & TEST NEXT FUNCTION CODE
3007
3008 015540 000240      12$: NOP
3009
    
```

3010
3011
3012
3013
3014
3015
3016
3017
3018
3019
3020
3021
3022
3023
3024
3025
3026
3027
3028
3029
3030
3031
3032
3033
3034
3035
3036
3037
3038
3039
3040
3041
3042
3043
3044
3045
3046
3047
3048
3049
3050
3051
3052
3053
3054
3055
3056
3057
3058
3059
3060
3061
3062
3063

;TEST 41 READ IN PRESET

;* ALL POSSIBLE REGISTERS WILL BE FILLED WITH ONES
;* THE REGISTER CONTENTS WILL BE SAVED IN REINTO BUFFER
;* THE READ IN PRESET COMMAND WILL BE GIVEN
;* ALL REGISTERS WILL BE CHECKED

!ST41: SCOPE
MOV #STACK, SP ;RESET STACK
MOV #TTNO, @#TSTNM ;THIS SAVES TEST NUMBER
JSR PC, @#CLDISK ;INIT AND SET GENERAL REGISTERS
;FILL ALL POSSIBLE BITS WITH ONES
MOV #177777, @RHMC ;WORD COUNT REGISTER GETS 177777
MOV #177777, @RHBA ;BUS ADDRESS REGISTER GETS 177777
MOV #17437, @RHDST ;DESIRED SECTOR TRACK GETS 17437
MOV #16377, @RHOF ;OFFSET REGISTER GETS 16277
MOV #777, @RHCA ;DESIRED CYLINDER GETS 777
MOV #A16!A17, -(SP) ;GET BIT 9 AND 8
BIS @#READIN, (SP)
MOV (SP)+, @RHCS1 ;FILL READ IN PRESET IN RHCS1
MOV @DMD, @RHMR ;SET DIAGNOSTIC MODE

;THE REGISTERS WILL BE SAVED IN REINTO BUFFER

JSR RD, @#SAVER ;SAVE
RHMC ;FROM
REINTO ;TO
17. ;NUMBER SAVED

;GIVE READ IN PRESET COMMAND

BIS #GO, @RHCS1 ;INCLUDE GO TO READ IN PRESET

;NOW SAVED REGISTERS WILL BE CHANGED TO EXPECTED VALUE

CLR @#REINTO+12 ;CLEAR SAVED RHDST
BIC #FMT22!HCI!ECI, @#REINTO+16 ;CLEAR FMT22, HCI, ECI IN
;SAVED RHOF
BIS #VV, @#REINTO+16 ;SET VV IN SAVED RHOF
CLR @#REINTO+20 ;CLEAR SAVED RHCA

;AFTER A READ IN PRESET COMMAND

;SAVE REGISTERS AGAIN SO THAT COMPARES CAN BE DONE

JSR RD, @#SAVER ;SAVE
RHMC ;FROM
WRFROM ;TO
17. ;NUMBER OF REGISTERS SAVED

;AS UPPER BYTE OF RHAS CAN BE CHANGING IN A DUAL PORT


```

3088
3089
3090
3091          ;*****
3092          ;#TEST 42      NO OPERATION FUNCTION TEST
3093          ;#      ALL POSSIBLE REGISTERS ARE CLEARED THEN A"NOP"=0
3094          ;#      IS GIVEN NO CHANGE SHOULD HAPPEN
3095          ;#      ALL POSSIBLE REGISTERS ARE FILLED WITH ONES THEN A "NOP"
3096          ;#      IS GIVEN NO CHANGE SHOULD HAPPEN
3097          ;*****
3098 015766 000004          †ST42: SCOPE
3099 015770 012737 000042 002020      MOV      #TTNO,#TSTNM      ;THIS SAVES TEST NUMBER
3100
3101          ;START WITH CLR IN RHCS2 (BITS)
3102 015776 004737 040522      JSR      PC,#CLDISK      ;CLEAR ALL POSSIBLE BITS
3103 016002 012777 000001 163642      MOV      #DMD,#RHM      ;SET DIAGNOSTIC MODE
3104 016010 013711 002024      MOV      #NOPERA,#R1      ;PUT NOP OPERATION=0 IN RHCS1
3105 016014 012700 001624      MOV      #RMC,R0          ;STARTING ADDRESS OF REG
3106 016020 012703 001674      MOV      #MC,R3          ;STARTING ADDRESS OF WHERE SAVED
3107 016024 012702 000021      MOV      #RMC2-RMC+2/2,R2 ;NUMBER OF REGISTERS
3108 016030 013023          15:  MOV      @(R0)+,(R3)+      ;SAVE HARDWARE REG
3109 016032 005302          DEC      R2              ;COUNT
3110 016034 001375          BNE     15              ;BRANCH IF NOT COMPLETE
3111 016036 013737 001654 016056      MOV      #RHDS1,#25      ;GET ADDRESS OF DRIVE STATUS
3112 016044 010137 016064          MOV      R1,#35          ;GET ADDRESS OF RHCS1
3113 016050 052711 000001          BIS      #GO,#R1        ;GO TO RHCS1
3114 016054 104424          MAT          ;WAIT FOR DRY IN RHDS1
3115 016056 000000          25:  .WORD 0              ;ADDRESS OF DRIVE STATUS RHDS1
3116 016060 000200          DRY          ;DRY WILL BE WAITED ON
3117 016062 104424          MAT          ;WAIT FOR RDY IN RHCS1
3118 016064 000000          35:  .WORD 0              ;ADDRESS OF RHCS1 PUT HERE BY AN
3119          ;EARLIER MOV
3120 016066 000200          RDY          ;RDY WILL BE WAITED ON
3121
3122          ;AFTER A NO OP COMMAND
3123          ;SAVE REGISTERS AGAIN SO THAT COMPARES CAN BE DONE
3124
3125 016070 004037 041214      JSR      R0,#SAVER      ;SAVE
3126 016074 001624          RMC          ;FROM
3127 016076 002070          MRFROM      ;TO
3128 016100 000021          17.         ;NUMBER OF REGISTERS SAVED
3129
3130          ;AS UPPER BYTE OF RHAS CAN BE CHANGING IN A DUAL PORT
3131          ;OPERATION THE UPPER BYTE OF RHAS WILL BE SAVED AS IS
3132          ;SO THAT THE COMPARES ARE ONLY VALID FOR THE LOWER BYTE
3133 016102 113737 001721 002115      MOV     #AS+1,#MRFROM+25;SAVE UPPER RHAS
3134
3135
3136          ;COMPARE REGISTERS BEFORE NO OP COMMAND
3137          ;WITH AFTER COMMAND
3138
3139 016110 004037 041416      JSR      R0,#COMPAR     ;COMPARE
3140 016114 001674          MC          ;GOOD BUFFER
3141 016116 002070          MRFROM      ;TEST BUFFER

```



```

3142 016120 000021          17.          ;NUMBER OF REGISTERS
3143 016122 016130          4$          ;RETURN FOR ERROR
3144 016124 016130          4$          ;SAME
3145 016126 016150          5$          ;RETURN FOR GOOD COMPARISON
3146
3147 016130 013705 045272    4$:  MOV    @#ERMORD,R5    ;GETTING READY TO INDEX
3148 016134 060505          ADD    R5,R5          ;DOUBLE ERROR WORD
3149 016136 016537 001622 040172  MOV    RHMC-2(R5),@#REGADR ;FADING REG. ADDRESS
3150 016144 104001          ERROR  1              ;NO OP COMMAND CAUSED IMPROPER
3151                                     ;REGISTER CHANGE
3152 016146 000207          RTS    PC              ;RETURN FOR FURTHER COMPARISONS
3153
3154 016150          5$:          ;NO ERRORS
3155
3156
3157
3158 016150 012737 016156 001110  MOV    #14$,@#SLPERR    ;SET SCOPE LOOP TO 14$
3159 016156 004737 040522          JSR    PC,@#CLDISK     ;INIT LAST ALL ZERO TEST
3160 016162 012777 000001 163462 14$:  MOV    @#DMD,@#RHMR     ;SET DIAGNOSTIC MODE
3161
3162
3163                                     ;NOW START WITH ALL ONES IN ALL POSSIBLE REGISTERS
3164
3165 016170 012700 001624          MOV    @#RHMC,RO        ;ADDRESS OF FIRST REGISTER
3166 016174 012705 000021          MOV    @#RHEC2-RHMC+2/2,R5 ;NO. OF REGISTERS
3167 016200 012730 177676          MOV    @#177676,@(RO)+  ;FILL WITH ALL ONES
3168 016204 013777 001762 163416 6$:  MOV    @#UNIT,@#RHCS2   ;REINSTATE UNIT NUMBER UNDER TEST
3169                                     ;KEEP INTERRUPT DISABLED
3170 016212 005305          DEC    R5              ;COUNT
3171 016214 001371          BNE    6$              ;BRANCH IF INCOMPLETE
3172 016216 013711 002024          MOV    @#NOPERA,@R1     ;PUT NOP OPERATION =0 IN RHCS1
3173 016222 012700 001624          MOV    @#RHMC,RO        ;STARTING ADDRESS OF REG
3174 016226 012703 001674          MOV    @#MC,R3          ;STARTING ADDRESS OF WHERE SAVED
3175 016232 012702 000021          MOV    @#RHEC2-RHMC+2/2,R2 ;NUMBER OF REGISTERS
3176 016236 013023          7$:  MOV    @(RO)+,(R3)+     ;SAVE HARDWARE REG
3177 016240 005302          DEC    R2              ;COUNT
3178 016242 001375          BNE    7$              ;BRANCH IF NOT COMPLETE
3179 016244 013737 001654 016264  MOV    @#RHDS1,@#10$    ;GET ADDRESS OF DRIVE STATUS
3180 016252 010137 016272          MOV    R1,@#11$        ;GET ADDRESS OF RHCS1
3181 016256 052711 000001          BIS    @#GO,@R1        ;GO TO RHCS1
3182 016262 104424          MAT          ;WAIT FOR DRY IN RHDS1
3183 016264 000000          10$: .WORD 0            ;ADDRESS OF DRIVE STATUS RHDS1
3184 016266 000200          DRY          ;DRY WILL BE WAITED ON
3185 016270 104424          MAT          ;WAIT FOR RDY IN RHCS1
3186 016272 000000          11$: .WORD 0            ;ADDRESS OF RHCS1 PUT HERE BY AN
3187                                     ;EARLIER MOV.
3188 016274 000200          RDY          ;RDY WILL BE WAITED ON
3189
3190                                     ;AFTER A NO OP COMMAND
3191                                     ;SAVE REGISTERS AGAIN SO THAT COMPARES CAN BE DONE
3192
3193 016276 004037 041214          JSR    RO,@#SAVER      ;SAVE
3194 016302 001624          RHMC          ;FROM
3195 016304 002070          MRFROM        ;TO

```

```

3196 016306 000021          17.          ;NUMBER OF REGISTERS SAVED
3197
3198          ;AS UPPER BYTE OF RHAS CAN BE CHANGING IN A DUAL PORT
3199          ;OPERATION THE UPPER BYTE OF RHAS WILL BE SAVED AS IS
3200          ;SO THAT THE COMPARES ARE ONLY VALID FOR THE LOWER BYTE
3201 016310 113737 001721 002115  MOVB  @#AS+1,@#MRFROM+25;SAVE UPPER RHAS
3202
3203
3204          ;COMPARE REGISTERS BEFORE NO OP COMMAND
3205          ;WITH AFTER COMMAND
3206
3207 016316 004037 041416  JSR   RO,@#COMPAR      ;COMPARE
3208 016322 001674          MC          ;GOOD BUFFER
3209 016324 002070          MRFROM      ;TEST BUFFER
3210 016326 000021          17.          ;NUMBER OF REGISTERS
3211 016330 016336          12$         ;RETURN FOR ERROR
3212 016332 016336          12$         ;SAME
3213 016334 016355          13$         ;RETURN FOR GOOD COMPARISON
3214
3215 016336 013705 045272 12$:  MOV   @#ERMORD,RS      ;GETTING READY TO INDEX
3216 016342 060505          ADD   RS,RS          ;DOUBLE ERROR WORD
3217 016344 016537 001622 040172  MOV   RMC-2(R5),@#REGADR ;FAILING REG. ADDRESS
3218 016352 104001          ERROR  1          ;NO OP COMMAND CAUSED IMPROPER
3219
3220 016354 000207          RTS   PC          ;REGISTER CHANGE
3221
3222 016356          13$:          ;RETURN FOR FURTHER COMPARISONS
          ;NO ERRORS

```



```

3223
3224
3225
3226
3227
3228
3229
3230
3231
3232
3233
3234
3235 016356 000004
3236 016360 012706 001000
3237 016364 012737 000043 002020
3238 016372 004737 040522
3239
3240
3241
3242 016376 012777 177777 163216
3243 016404 012777 177777 163212
3244 016412 012777 177777 163206
3245 016420 052777 157010 163202
3246 016426 012777 001476 163176
3247 016434 012777 177777 163172
3248 016442 012777 017437 163166
3249 016450 012777 177777 163162
3250 016456 012777 016277 163156
3251 016464 012777 177777 163152
3252 016472 012777 177777 163146
3253 016500 012777 000001 163144
3254 016506 012777 177777 163136
3255
3256
3257
3258
3259 016514 013700 002006
3260 016520 005012
3261 016522 012705 000010
3262 016526 006000 30S:
3263 016530 103002
3264 016532 012714 177777
3265 016536 005212 31S:
3266 016540 005305
3267 016542 001401
3268 016544 000770
3269 016546 013746 001762 27S:
3270 016552 052716 157010
3271 016556 012612
3272
3273
3274 016560 012777 000001 163064
3275 016566 013711 002032
3276 016572 052711 000001

```

```

;*****
;*TEST 43 DRIVE CLEAR

```

```

;* ALL WRITE BITS OF ALL REGISTERS EXCEPT RH0B ARE FILLED WITH
;* ONES EXCEPT FOR BIT #0 AND BIT #6 WHICH ARE "GO" AND
;* "ENABLE INTERRUPT" BITS
;* THEN A DRIVE CLEAR IS PERFORMED
;* THEN ALL REGISTERS EXCEPT RH0B ARE CHECKED

```

```

;*****

```

```

TST43: SCOPE
MOV #STACK,SP ;RESET STACK
MOV #TTNO,@TSTNM ;THIS SAVES TEST NUMBER
JSR PC,@CLDISK ;SET REGISTERS AND CLEAR

```

```

;FILL ALL POSSIBLE BITS WITH ONES

```

```

MOV #177777,@RH0B ;BUS ADDRESS REGISTER GETS 177777
MOV #177777,@RH0C ;WORD COUNT REGISTER GETS 177777
MOV #177777,@RH0A ;BUS ADDRESS REGISTER GETS 177777
BIS #157010,@RHCS2 ;CONTROL AND STATUS 2 GETS 157010
MOV #1476,@RHCS1 ;CONTROL AND STATUS REGISTER GETS 1476
MOV #177777,@RHER1 ;ERROR REGISTER1 GETS 177777
MOV #17437,@RHDS1 ;DESIRED SECTOR TRACK
MOV #177777,@RHER2 ;ERROR REGISTER 2
MOV #16277,@RHOF ;OFFSET REGISTER
MOV #177777,@RHCA ;DESIRED CYLINDER
MOV #177777,@RHER3 ;ERROR REGISTER 3
MOV #DMD,@RHMR ;MAINTENANCE REGISTER
MOV #177777,@RHMR ;MAINTENANCE REGISTER

```

```

;THIS SETS BITS FOR ALL PRESENT DRIVES

```

```

MOV @TOTALAT,R0 ;GET DRIVE PRESENT
CLR @R2 ;CLEAR RHCS2 AND CARRY BIT
MOV #8,@RS ;COUNTER
ROR R0 ;GET BIT INTO CARRY
BCC 31S ;BRANCH IF NO UNIT ON THIS BIT
MOV #-1,@R4 ;MOVE INTO ERROR REGISTER TO SET ATA
INC @R2 ;INCREMENT RHCS2 - UNIT NO.
DEC RS ;COUNT
BEQ 27S ;BRANCH IF 8 DONE
BR 30S ;CONTINUE THIS ROUTINE
MOV @UNIT,-(SP)
BIS #157010,(SP) ;REINSTATE SET BITS
MOV (SP)+,@R2

```

```

MOV #DMD,@RHMR ;SET DMD
MOV @DCLEAR,@R1 ;DRIVE CLEAR = 10 INTO RHCS1
BIS #GO,@R1 ;GO

```

```

3277 016576 012700 001622      MOV      #RHDB, RO      ;RO CONTAINS ADDR. OF ADDR. OF REG.
3278
3279
3280                                ;DATA BUFFER REGISTER
3281
3282
3283 016602 012737 177777 001124 28S:  MOV      #177777, @#SGDDAT ;GOOD DATA FOR ERROR TYPEOUT
3284 016610 013037 001126      MOV      @ (RO)+, @#SBDDAT ;TEST DATA
3285 016614 022737 177777 001126      CMP      #177777, @#SBDDAT ;COMPARE DATA
3286 016622 001402      BEQ      3S ;BRANCH IF GOOD
3287 016624 004737 017462      JSR      PC, @#ERCLFC ;JUMP TO ERROR FOR CLR (BIT 5)
3288                                ;IN RHCS2
3289
3290
3291                                ;WORD COUNT REGISTER
3292
3293
3294 016630 012737 177777 001124 3S:  MOV      #177777, @#SGDDAT ;GOOD DATA FOR ERROR TYPEOUT
3295 016636 013037 001126      MOV      @ (RO)+, @#SBDDAT ;TEST DATA
3296 016642 022737 177777 001126      CMP      #177777, @#SBDDAT ;COMPARE DATA
3297 016650 001402      BEQ      4S ;BRANCH IF GOOD
3298 016652 004737 017462      JSR      PC, @#ERCLFC ;JUMP TO ERROR FOR CLR (BIT 5)
3299                                ;IN RHCS2
3300
3301
3302                                ;BUS ADDRESS REGISTER
3303
3304
3305 016656 012737 177776 001124 4S:  MOV      #177776, @#SGDDAT ;GOOD DATA FOR ERROR TYPEOUT
3306 016664 013037 001126      MOV      @ (RO)+, @#SBDDAT ;TEST DATA
3307 016670 022737 177776 001126      CMP      #177776, @#SBDDAT ;COMPARE DATA
3308 016676 001402      BEQ      5S ;BRANCH IF GOOD
3309 016700 004737 017462      JSR      PC, @#ERCLFC ;JUMP TO ERROR FOR CLR (BIT 5)
3310                                ;IN RHCS2
3311
3312
3313                                ;CONTROL AND STATUS 2 REGISTER
3314
3315
3316
3317 016704 012746 000110      5S:  MOV      #110, -(SP) ;INCLUDE IR
3318 016710 053716 001762      BIS      @#UNIT, (SP) ;SET UNIT NO.
3319 016714 012637 001124      MOV      (SP)+, @#SGDDAT ;GOOD DATA FOR TYPE OUT
3320 016720 013037 001126      MOV      @ (RO)+, @#SBDDAT ;TEST DATA
3321 016724 023737 001124 001126      CMP      @#SGDDAT, @#SBDDAT ;COMPARE DATA
3322 016732 001402      BEQ      6S ;BRANCH IF GOOD
3323 016734 004737 017462      JSR      PC, @#ERCLFC ;JUMP TO ERROR FOR CLR (BIT 5)
3324                                ;IN RHCS2
3325
3326
3327
3328                                ;CONTROL AND STATUS 1 REGISTER
3329
3330 016740 005737 001766      6S:  TST      @#NUNIT ;ARE THERE MORE THAN ONE UNIT

```



```

3331 016744 001404          BEQ      32$          ;BRANCH IF ONLY ONE UNIT
3332 016746 012737 104210 001124  MOV      #104210, @#SGDDAT ;GOOD DATA
3333 016754 000403          BR       33$
3334 016756 012737 004210 001124 32$:  MOV      #4210, @#SGDDAT ;GOOD DATA
3335 016764 013037 001126 33$:  MOV      @ (RO)+, @#SBDDAT ;TEST DATA
3336
3337 016770 023737 001124 001126  CMP      @#SGDDAT, @#SBDDAT ;COMPARE DATA
3338 016776 001402          BEQ      7$          ;BRANCH IF GOOD
3339 017000 004737 017462          JSR      PC, @#ERCLFC ;JUMP TO ERROR FOR CLR BIT 5
3340                               ;IN RHCS2
3341
3342                               ;ERROR 1 REGISTER
3343
3344
3345 017004 012737 000000 001124 7$:  MOV      #0, @#SGDDAT ;GOOD DATA FOR ERROR TYPEOUT
3346 017012 013037 001126          MOV      @ (RO)+, @#SBDDAT ;TEST DATA
3347 017016 022737 000000 001126  CMP      #0, @#SBDDAT ;COMPARE DATA
3348 017024 001402          BEQ      10$         ;BRANCH IF GOOD
3349 017026 004737 017462          JSR      PC, @#ERCLFC ;JUMP TO ERROR FOR CLR (BIT 5)
3350                               ;IN RHCS2
3351
3352                               ;DESIRED SECTOR/TRACK REGISTER
3353
3354
3355 017032 012737 017437 001124 10$: MOV      #17437, @#SGDDAT ;GOOD DATA FOR ERROR TYPEOUT
3356 017040 013037 001126          MOV      @ (RO)+, @#SBDDAT ;TEST DATA
3357 017044 022737 017437 001126  CMP      #17437, @#SBDDAT ;COMPARE DATA
3358 017052 001402          BEQ      11$         ;BRANCH IF GOOD
3359 017054 004737 017462          JSR      PC, @#ERCLFC ;JUMP TO ERROR FOR CLR (BIT 5)
3360                               ;IN RHCS2
3361
3362                               ;ERROR 2 REGISTER
3363
3364
3365 017060 012737 000000 001124 11$: MOV      #0, @#SGDDAT ;GOOD DATA FOR ERROR TYPEOUT
3366 017066 013037 001126          MOV      @ (RO)+, @#SBDDAT ;TEST DATA
3367 017072 022737 000000 001126  CMP      #0, @#SBDDAT ;COMPARE DATA
3368 017100 001402          BEQ      12$         ;BRANCH IF GOOD
3369 017102 004737 017462          JSR      PC, @#ERCLFC ;JUMP TO ERROR FOR CLR (BIT 5)
3370                               ;IN RHCS2
3371
3372                               ;OFFSET REGISTER
3373
3374
3375 017106 012737 116000 001124 12$: MOV      #116000, @#SGDDAT ;GOOD DATA FOR ERROR TYPEOUT
3376 017114 013037 001126          MOV      @ (RO)+, @#SBDDAT ;TEST DATA
3377 017120 022737 116000 001126  CMP      #116000, @#SBDDAT ;COMPARE DATA
3378 017126 001402          BEQ      13$         ;BRANCH IF GOOD
3379 017130 004737 017462          JSR      PC, @#ERCLFC ;JUMP TO ERROR FOR CLR (BIT 5)
3380                               ;IN RHCS2
3381
3382                               ;DESIRED CYLINDER ADDRESS REGISTER
3383
3384

```

H10

MAINDEC-11-DZAPS-D RJPO4 DISKLESS RH11 TEST-PART 1
DZAPSD.P11 T43 DRIVE CLEAR

MACY11 27(663) 8-OCT-75 16:14 PAGE 81

SEQ 0123

```

3385 017134 012737 001777 001124 13S:  MOV    #1777, @#SGDDAT ;GOOD DATA FOR ERROR TYPEOUT
3386 017142 013037 001126             MOV    @ (RO)+, @#SBDDAT ;TEST DATA
3387 017146 022737 001777 001126     CMP    #1777, @#SBDDAT ;COMPARE DATA
3388 017154 001402             BEQ    14S ;BRANCH IF GOOD
3389 017156 004737 017462             JSR    PC, @#ERCLFC ;JUMP TO ERROR FOR CLR (BIT 5)
3390                                     ;IN RHCS2
3391
3392                                     ;ERROR 3 REGISTER
3393
3394
3395 017162 012737 000000 001124 14S:  MOV    #0, @#SGDDAT ;GOOD DATA FOR ERROR TYPEOUT
3396 017170 013037 001126             MOV    @ (RO)+, @#SBDDAT ;TEST DATA
3397 017174 022737 000000 001126     CMP    #0, @#SBDDAT ;COMPARE DATA
3398 017202 001402             BEQ    15S ;BRANCH IF GOOD
3399 017204 004737 017462             JSR    PC, @#ERCLFC ;JUMP TO ERROR FOR CLR (BIT 5)
3400                                     ;IN RHCS2
3401
3402                                     ;ATTENTION SUMMARY REGISTER
3403
3404 017210 013737 002006 001124 15S:  MOV    @#TOTALAT, @#SGDDAT ;SET ALL BITS OF DRIVE PRESENT IN RHAS
3405 017216 043737 002004 001124     BIC    @#ATTENT, @#SGDDAT ;CLEAR ONLY WORKING DRIVE BIT
3406 017224 013037 001126             MOV    @ (RO)+, @#SBDDAT ;GET RHAS
3407 017230 123737 001124 001126     CMPB  @#SGDDAT, @#SBDDAT ;COMPARE DATA
3408 017236 001402             BEQ    16S ;BRANCH IF GOOD
3409 017240 004737 017462             JSR    PC, @#ERCLFC ;JUMP TO ERROR FOR CLR (BIT 5) IN RHCS2
3410
3411                                     ;MAINTAINABILITY REGISTER
3412
3413
3414 017244 012737 000400 001124 16S:  MOV    #400, @#SGDDAT ;GOOD DATA FOR ERROR TYPEOUT
3415 017252 013037 001126             MOV    @ (RO)+, @#SBDDAT ;TEST DATA
3416 017256 022737 000400 001126     CMP    #400, @#SBDDAT ;COMPARE DATA
3417 017264 001402             BEQ    17S ;BRANCH IF GOOD
3418 017266 004737 017462             JSR    PC, @#ERCLFC ;JUMP TO ERROR FOR CLR (BIT 5)
3419                                     ;IN RHCS2
3420
3421                                     ;DRIVE STATUS REGISTER
3422
3423 017272 012737 000700 001124 17S:  MOV    #700, @#SGDDAT ;GOOD DATA FOR PRINTOUT
3424 017300 013046             MOV    @ (RO)+, -(SP) ;GET RHDS1
3425 017302 011637 001126             MOV    (SP), @#SBDDAT ;TEST DATA
3426 017306 042716 001000             BIC    #PROG, (SP) ;CLEAR PROG BIT
3427 017312 022726 000700             CMP    #700, (SP)+ ;COMPARE DATA
3428 017316 001402             BEQ    20S ;BRANCH IF GOOD
3429 017320 004737 017462             JSR    PC, @#ERCLFC ;JUMP TO ERROR FOR DRIVE CLEAR
3430
3431                                     ;DRIVE TYPE
3432
3433
3434 017324 013737 001776 001124 20S:  MOV    @#SAVDT, @#SGDDAT ;GOOD DATA FOR ERROR TYPEOUT
3435 017332 013037 001126             MOV    @ (RO)+, @#SBDDAT ;TEST DATA
3436 017336 023737 001776 001126     CMP    @#SAVDT, @#SBDDAT ;COMPARE DATA
3437 017344 001402             BEQ    21S ;BRANCH IF GOOD
3438 017346 004737 017462             JSR    PC, @#ERCLFC ;JUMP TO ERROR FOR CLR (BIT 5)

```


J10

MAINDEC-11-DZRPS-D, RJPO4 DISKLESS RH11 TEST-PART 1
DZRPSD.P11 T43 DRIVE CLEAR

MACY11 27(663) 8-OCT-75 16:14 PAGE 83

SEQ 0125

3493 017474 005720
3494 017476 004737 040556
3495 017502 104400 057176
3496
3497 017506 000000
3498

TST (RD)+
JSR PC.#CHECKT
TYPE ,CPHALT

HALT

:UNDO -(RD) FOR BAD DATA
:CHECK THAT DVA, RDY, DPR, DRY = 1
:AND THAT NO OTHERS = 1. CANNOT CON-
:TINUE TESTING IF BOTH AREN'T TRUE
:STOP THE TEST

3499
3500
3501
3502
3503
3504
3505
3506
3507
3508
3509
3510
3511
3512
3513
3514
3515
3516
3517
3518
3519
3520
3521
3522
3523
3524
3525
3526
3527
3528
3529
3530
3531
3532
3533
3534
3535
3536
3537
3538
3539
3540
3541
3542
3543
3544
3545
3546
3547
3548
3549
3550
3551
3552

; *TEST 44 SEEK COMMAND TEST

; * THE SEEK COMMAND WILL BE LOADED INTO RHCS1 WITH GO
; * THEN ALL REGISTERS WILL BE CHECKED
; * RH CLEAR WILL BE GIVEN
; * THEN ALL REGISTERS WILL BE CHECKED

; *TEST 44: SCOPE

MOV #STACK, SP ; RESET STACK
MOV #TTNO, @#TSTNM ; THIS SAVES TEST NUMBER
JSR PC, @#CLDISK ; INIT AND SET UP GENERAL REG.
; AND UNIT NUMBER
MOV #DMD, @RHMR ; SET DIAGNOSTIC MODE BIT
; THIS ENABLES COMMANDS WITHOUT MOL
; AND HOLDS RHLA FROM MOVING

CLR @RHDS1 ; MAKE DESIRED SECTOR TRACK LEGAL
MOV @#SEECOM, @RHCS1 ; LOAD SEEK COMMAND INTO RH
MOV @RHCC, -(SP) ; GET RHCC
CMP #410., (SP)+ ; IS CURRENT CYLINDER SAME AS 410.
BEQ 95 ; BRANCH IF YES TO MAKE RHCA =409.
MOV #410., @#STMP5 ; GET READY TO MAKE RHCA =410
BR 105 ; BRANCH TO FILL RHCA
MOV #409., @#STMP5 ; GET READY TO MAKE RHCA =409.
MOV @#STMP5, @RHCA ; MAKE DESIRED CYLINDER 401. OR 409.

; SAVE REGISTERS FOR COMPARISON AFTER GO

JSR RO, @#SAVER ; SAVE
RMC ; FROM
REINTO ; TO
19. ; NUMBER OF REGISTERS SAVED

; GIVE GO TO COMMAND
BIS #GO, @RHCS1 ; GO TO COMMAND

; CHANGE SAVED REGISTERS TO EXPECTED VALUES

BIS #GO, @#REINTO+6 ; SAVED RHCS1
BIS #PIP, @#REINTO+30 ; SAVED RHDS1
BIC #DRY, @#REINTO+30 ; SAVED RHDS1

; AFTER GO HAS BEEN GIVEN TO SEEK COMMAND
; SAVE REGISTERS AGAIN SO THAT COMPARISONS CAN
; BE DONE

```

3553
3554 017650 004037 041214 JSR RO,@#SAVER ;SAVE
3555 017654 001624 RHC ;FROM
3556 017656 002070 WRFROM ;TO
3557 017660 000023 19. ;NUMBER OF REGISTERS SAVED
3558
3559 ;AS UPPER BYTE OF RHAS CAN BE CHANGING IN A DUAL PORT
3560 ;OPERATION THE UPPER BYTE OF RHAS WILL BE SAVED AS IS
3561 ;SO THAT THE COMPARES ARE ONLY VALID FOR THE LOWER BYTE
3562 017662 113737 003161 002115 MOVB @#REINTO+25,@#WRFROM+25;SAVE UPPER RHAS
3563
3564 ;COMPARE REGISTERS BEFORE SEEK COMMAND
3565 ;WITH AFTER GO
3566
3567 017670 004037 041416 JSR RO,@#COMPAR ;COMPARE
3568 017674 003134 REINTO ;GOOD BUFFER
3569 017676 002070 WRFROM ;TEST BUFFER
3570 017700 000023 19. ;NUMBER
3571 017702 017710 1$ ;RETURN FOR ERROR
3572 017704 017710 1$ ;SAME
3573 017706 017730 2$ ;RETURN FOR GOOD COMPARISON
3574
3575 017710 013705 045272 1$: MOV @#ERWORD,R5 ;GETTING READY TO INDEX
3576 017714 060505 ADD R5,R5 ;DOUBLE ERROR WORD
3577 017716 016537 001622 040172 MOV RHC-2(R5),@#REGADR ;FAILING REGISTER ADDRESS
3578
3579 017724 104001 ERROR 1 ;IMPROPER REGISTER CHANGE
3580 ;AFTER SEEK COMMAND
3581 ;WITH GO IS GIVEN
3582 017726 000207 RTS PC ;RETURN TO COMPARISON
3583
3584
3585 ;NOW GIVE INIT AND GET GO AND PIP DOWN
3586
3587 017730 052712 000040 2$: BIS #CLR,@R2 ;RH INITILIZE
3588 017734 013712 001762 MOV @#UNIT,@R2 ;REINSTATE UNIT NUMBER
3589 017740 012777 000001 161704 MOV #DMD,@RHR ;SET DIAGNOSTIC MODE BIT
3590 ;THIS ENABLES COMMANDS WITHOUT MOL
3591 ;AND HOLDS RHLA FROM MOVING
3592
3593 ;CHANGE REGISTERS TO EXPECTED VALUE
3594 017746 042737 000001 003142 BIC #GO,@#REINTO+6 ;SAVED RHCS1
3595 017754 042737 020000 003164 BIC #PIP,@#REINTO+30 ;SAVED RHDS1
3596 017762 052737 000200 003164 BIS #DRY,@#REINTO+30 ;SAVED RHDS1
3597 017770 017737 161672 003176 MOV @RHLA,@#REINTO+42;SAVED RHLA
3598 017776 013737 001202 003200 MOV @#STMP5,@#REINTO+44 ;SAVED RHCC
3599
3600
3601 ;AFTER INITILIZE SAVE REGISTERS SO THAT
3602 ;COMPARES CAN BE DONE
3603
3604 020004 004037 041214 JSR RO,@#SAVER ;SAVE
3605 020010 001624 RHC ;FROM
3606 020012 002070 WRFROM ;TO

```



```

3607 020014 000023          19.          ;NUMBER OF REGISTERS SAVED
3608
3609
3610
3611
3612
3613 020016 113737 003161 002115      ;AS UPPER BYTE OF RHAS CAN BE CHANGING IN A DUAL PORT
3614
3615
3616
3617 020024 004037 041416      ;OPERATION THE UPPER BYTE OF RHAS WILL BE SAVED AS IS
3618 020030 003134
3619 020032 002070
3620 020034 000023      ;SO THAT THE COMPARES ARE ONLY VALID FOR THE LOWER BYTE
3621
3622
3623
3624
3625
3626 020044 013705 045272      ;COMPARE REGISTERS AFTER INITILIZE
3627 020050 060505      JSR      RO,2#COMPAR      ;COMPARE
3628 020052 016537 001622 040172      REINTO   ;GOOD BUFFER
3629 020060 104001      WRFROM   ;TEST BUFFER
3630
3631
3632
3633 020062 000207      19.      ;NUMBER OF REGISTERS TO BE
3634
3635
3636
3637 020044 013705 045272      3$      ;COMPARED
3638 020050 060505      3$      ;RETURN POINT FOR ERROR
3639 020052 016537 001622 040172      4$      ;SAME
3640 020060 104001      4$      ;RETURN POINT FOR GOOD COMPARISON
3641
3642
3643 020044 013705 045272      3$:      MOV      2#ERWORD,R5      ;GETTING READY TO INDEX
3644 020050 060505      ADD      R5,R5            ;DOUBLE ERROR WORD
3645 020052 016537 001622 040172      MOV      RHMC-2(R5),2#REGADR ;FAILING REGISTER ADDRESS
3646 020060 104001      ERROR      1            ;IMPROPER REGISTER
3647
3648
3649
3650
3651
3652 020062 000207      RTS      PC            ;CONTENTS AFTER GIVING AN
3653
3654
3655
3656 020064
3657
3658
3659
3660
3661
3662
3663
3664
3665
3666
3667
3668
3669
3670
3671
3672
3673
3674
3675
3676
3677
3678
3679
3680
3681
3682
3683
3684
3685
3686
3687
3688
3689
3690
3691
3692
3693
3694
3695
3696
3697
3698
3699
3700
3701
3702
3703
3704
3705
3706
3707
3708
3709
3710
3711
3712
3713
3714
3715
3716
3717
3718
3719
3720
3721
3722
3723
3724
3725
3726
3727
3728
3729
3730
3731
3732
3733
3734
3735
3736
3737
3738
3739
3740
3741
3742
3743
3744
3745
3746
3747
3748
3749
3750
3751
3752
3753
3754
3755
3756
3757
3758
3759
3760
3761
3762
3763
3764
3765
3766
3767
3768
3769
3770
3771
3772
3773
3774
3775
3776
3777
3778
3779
3780
3781
3782
3783
3784
3785
3786
3787
3788
3789
3790
3791
3792
3793
3794
3795
3796
3797
3798
3799
3800
3801
3802
3803
3804
3805
3806
3807
3808
3809
3810
3811
3812
3813
3814
3815
3816
3817
3818
3819
3820
3821
3822
3823
3824
3825
3826
3827
3828
3829
3830
3831
3832
3833
3834
3835
3836
3837
3838
3839
3840
3841
3842
3843
3844
3845
3846
3847
3848
3849
3850
3851
3852
3853
3854
3855
3856
3857
3858
3859
3860
3861
3862
3863
3864
3865
3866
3867
3868
3869
3870
3871
3872
3873
3874
3875
3876
3877
3878
3879
3880
3881
3882
3883
3884
3885
3886
3887
3888
3889
3890
3891
3892
3893
3894
3895
3896
3897
3898
3899
3900
3901
3902
3903
3904
3905
3906
3907
3908
3909
3910
3911
3912
3913
3914
3915
3916
3917
3918
3919
3920
3921
3922
3923
3924
3925
3926
3927
3928
3929
3930
3931
3932
3933
3934
3935
3936
3937
3938
3939
3940
3941
3942
3943
3944
3945
3946
3947
3948
3949
3950
3951
3952
3953
3954
3955
3956
3957
3958
3959
3960
3961
3962
3963
3964
3965
3966
3967
3968
3969
3970
3971
3972
3973
3974
3975
3976
3977
3978
3979
3980
3981
3982
3983
3984
3985
3986
3987
3988
3989
3990
3991
3992
3993
3994
3995
3996
3997
3998
3999
4000

```

```

3661                                     ; AFTER GO HAS BEEN GIVEN TO SEEK COMMAND
3662                                     ; SAVE REGISTERS AGAIN SO THAT COMPARISONS CAN
3663                                     ; BE DONE
3664 020152 004037 041214                JSR    RO,2#SAVER          ; SAVE
3665 020156 001624                        RHMC                        ; FROM
3666 020160 002070                        MRFROM                       ; TO
3667 020162 000023                        19.                          ; NUMBER OF REGISTERS SAVED
3668
3669
3670                                     ; AS UPPER BYTE OF RHAS CAN BE CHANGING IN A DUAL PORT
3671                                     ; OPERATION THE UPPER BYTE OF RHAS WILL BE SAVED AS IS
3672                                     ; SO THAT THE COMPARES ARE ONLY VALID FOR THE LOWER BYTE
3673 020164 113737 003161 002115          MOVB   2#REINTO+25,2#MRFROM+25;SAVE UPPER RHAS
3674
3675
3676                                     ; COMPARE REGISTERS BEFORE COMMAND
3677                                     ; WITH AFTER GO
3678
3679 020172 004037 041416                JSR    RO,2#COMPAR        ; COMPARE
3680 020176 003134                        REINTO                       ; GOOD BUFFER
3681 020200 002070                        MRFROM                       ; TEST BUFFER
3682 020202 000023                        19.                          ; NUMBER
3683 020204 020212                        5$                            ; RETURN FOR ERROR
3684 020206 020212                        5$                            ; SAME
3685 020210 020232                        6$                            ; RETURN FOR GOOD COMPARISON
3686 020212 013705 045272                SS:  MOV   2#ERMORD,RS        ; GETTING READY TO INDEX
3687 020216 060505                        ADD    RS,RS                 ; DOUBLG ERROR MORD
3688 020220 016537 001622 040172          MOV    RHMC-2(RS),2#REGADR ; FAILING REGISTER ADDRESS
3689 020226 104001                        ERROR 1                      ; IMPROPER REGISTER CHANGE
3690
3691                                     ; AFTER COMMAND
3692 020230 000207                        RTS    PC                    ; WITH GO IS GIVEN
3693                                     ; RETURN TO COMPARISON
3694
3695                                     ; NOW GIVE INIT AND GET GO AND PIP DOWN
3696 020232 052712 000040                6$: BIS   #CLR,2R2          ; RH INITILIZE
3697 020236 013712 001762                MOV   2#UNIT,2R2           ; REINSTATE UNIT NUMBER
3698 020242 012777 000001 161402          MOV   #DMD,2RHMR          ; SET DIAGNOSTIC MODE BIT
3699                                     ; THIS ENABLES COMMANDS WITHOUT MOL
3700                                     ; AND HOLDS RHLA FROM MOVING
3701
3702                                     ; CHANGE REGISTERS TO EXPECTED VALUE
3703
3704 020250 042737 000001 003142          BIC   #GO,2#REINTO+6      ; SAVED RHCS1
3705 020256 042737 020000 003164          BIC   #PIP,2#REINTO+30   ; SAVED RHDS1
3706 020264 052737 000200 003164          BIS   #DRY,2#REINTO+30   ; SAVED RHDS1
3707 020272 017737 161370 003176          MOV   2RHLA,2#REINTO+42 ; SAVED RHLA
3708 020300 005037 003200                        CLR   2#REINTO+44        ; SAVED RHCC
3709
3710
3711                                     ; AFTER INITIALIZE SAVE REGISTERS SO THAT
3712                                     ; COMPARES CAN BE DONE
3713
3714 020304 004037 041214                JSR    RO,2#SAVER          ; SAVE

```



```

3743
3744
3745 ;*****
3746 ;*TEST 45 UNLOAD COMMAND TEST
3747
3748 ;* THE UNLOAD COMMAND WILL BE LOADED INTO RHCS1 WITH GO
3749 ;* THEN ALL REGISTERS WILL BE CHECKED
3750 ;* RH CLEAR WILL BE GIVEN
3751
3752 ;*****
3753 020364 000004          ST45: SCOPE
3754 020366 012706 001000  MOV #STACK,SP ;RESET STACK
3755 020372 012737 000045 002020  MOV #TTNO,#TSTNM ;THIS SAVES TEST NUMBER
3756 020400 004737 040522  JSR PC,#CLDISK ;INIT AND SET UP GENERAL REG.
3757 ;AND UNIT NUMBER
3758 020404 012777 000001 161240  MOV #DMD,#RHMR ;SET DIAGNOSTIC MODE BIT
3759 ;THIS ENABLES COMMANDS WITHOUT MOL
3760 ;AND HOLDS RHLA FROM MOVING
3761
3762 020412 013777 002026 161212  MOV #UNLOAD,#RHCS1 ;LOAD UNLOAD COMMAND INTO RH
3763 ;SAVE REGISTERS FOR COMPARISON AFTER GO
3764
3765 020420 004037 041214  JSR RO,#SAVER ;SAVE
3766 020424 001624  RHMC ;FROM
3767 020426 003134  REINTO ;TO
3768 020430 000023  19. ;NUMBER OF REGISTERS SAVED
3769
3770
3771 ;GIVE GO TO UNLOAD COMMAND
3772 020432 052777 000001 161172  BIS #GO,#RHCS1 ;GO TO UNLOAD COMMAND
3773
3774 ;CHANGE SAVED REGISTERS TO EXPECTED VALUES
3775 020440 052737 000001 003142  BIS #GO,#REINTO+6 ;SAVED RHCS1
3776 020446 052737 020000 003164  BIS #PIP,#REINTO+30 ;SAVED RHDS1
3777 020454 042737 000200 003164  BIC #DRY,#REINTO+30 ;SAVED RHDS1
3778
3779 020462 005737 001100  TST #SPASS ;IS THIS FIRST PASS
3780 020466 001053  BNE 55 ;BRANCH IF NOT FIRST PASS
3781 020470 032737 020000 177570  BIT #SM13,#SMR ;INHIBIT ERROR PRINT HIGH?
3782 020476 001047  BNE 55 ;BRANCH IF SM13 HIGH
3783
3784 020500 104400 020506  TYPE #+4 ;TYPE ASCIZ STRING
3785 020504 000441  BR 645 ;GET OVER THE ASCIZ
3786 ;;.ASCIZ <15><12>/IF DRIVE CONNECTED "STAND BY" LAMP SHOULD BE LIT ON DRIVE
3787 020610 645:
3788
3789 020610 013746 001762  MOV #UNIT,-(SP) ;UNIT UNDER TEST
3790 020614 104410  TYPDS
3791
3792 ;AFTER GO HAS BEEN GIVEN TO UNLOAD COMMAND
3793 ;SAVED REGISTERS AGAIN SO THAT COMPARISONS CAN
3794 ;BE DONE
3795
3796 020616 004037 041214  55: JSR RO,#SAVER ;SAVE

```



```

3797 020622 001624          RHMC          ;FROM
3798 020624 002070          MRFROM         ;TO
3799 020626 000023          19.           ;NUMBER OF REGISTERS SAVED
3800
3801                          ;AS UPPER BYTE OF RHAS CAN BE CHANGING IN A DUAL PORT
3802                          ;OPERATION THE UPPER BYTE OF RHAS WILL BE SAVED AS IS
3803                          ;SO THAT THE COMPARES ARE ONLY VALID FOR THE LOWER BYTE
3804 020630 113737 003161 002115  MOVB    @#REINT0+25,@#MRFROM+25;SAVE UPPER RHAS
3805
3806
3807                          ;COMPARE REGISTERS BEFORE UNLOAD COMMAND
3808                          ;WITH AFTER GO
3809
3810 020636 004037 041416      JSR     RD,@#COMPAR      ;COMPARE
3811 020642 003134            REINT0      ;GOOD BUFFER
3812 020644 002070          MRFROM         ;TEST BUFFER
3813 020646 000023          19.           ;NUMBER
3814 020650 020656          15             ;RETURN FOR ERROR
3815 020652 020656          15             ;SAME
3816 020654 020676          25             ;RETURN FOR GOOD COMPARISON
3817 020656 013705 045272      15:      MOV     @#ERWORD,RS      ;GETTING READY TO INDEX
3818 020662 060505          ADD     RS,RS           ;DOUBLE ERROR WORD
3819 020664 016537 001622 040172  MOV     RHMC-2(R5),@#REGADR ;FAILING REGISTER ADDRESS
3820
3821 020672 104001          ERROR    1           ;IMPROPER REGISTER CHANGE
3822                          ;AFTER UNLOAD COMMAND
3823                          ;WITH GO IS GIVEN
3824 020674 000207          RTS     PC           ;RETURN TO COMPARISON
3825
3826                          ;NOW GIVE INIT AND GET ALL GO AND PIP DOWN
3827 020676 052712 000040      25:      BIS     #CLR,@R2        ;RH INITILIZE
3828 020702 013712 001762      MOV     @#UNIT,@R2      ;REINSTATE UNIT NUMBER
3829 020706 012777 000001 160736  MOV     #DMD,@RHMR      ;SET DIAGNOSTIC MODE BIT
3830                          ;THIS ENABLES COMMANDS WITHOUT MOL
3831                          ;AND HOLDS RHLA FROM MOVING
3832
3833                          ;CHANGE REGISTERS TO EXPECTED VALUE
3834 020714 042737 000001 003142  BIC     #GO,@#REINT0+6 ;SAVED RHCSI
3835 020722 042737 020000 003164  BIC     #PIP,@#REINT0+30 ;SAVED RHDS1
3836 020730 052737 000200 003164  BIS     #DRY,@#REINT0+30 ;SAVED RHDS1
3837 020736 017737 160724 003176  MOV     @RHLA,@#REINT0+42;SAVED RHLA
3838
3839                          ;AFTER INITIALIZE SAVE REGISTERS SO THAT
3840                          ;COMPARES CAN BE DONE
3841
3842 020744 004037 041214      JSR     RD,@#SAVER      ;SAVE
3843 020750 001624          RHMC          ;FROM
3844 020752 002070          MRFROM         ;TO
3845 020754 000023          19.           ;NUMBER OF REGISTERS SAVED
3846
3847                          ;COMPARE REGISTERS AFTER INITIALIZE
3848                          ;AS UPPER BYTE OF RHAS CAN BE CHANGING IN A DUAL PORT
3849                          ;OPERATION THE UPPER BYTE OF RHAS WILL BE SAVED AS IS
3850                          ;SO THAT THE COMPARES ARE ONLY VALID FOR THE LOWER BYTE

```


3872
3873
3874
3875
3876
3877
3878
3879
3880
3881
3882
3883
3884
3885
3886
3887
3888
3889
3890
3891
3892
3893
3894
3895
3896
3897
3898
3899
3900
3901
3902
3903
3904
3905
3906
3907
3908
3909
3910
3911
3912
3913
3914
3915
3916
3917
3918
3919
3920
3921
3922
3923
3924
3925

;TEST 46 OFFSET COMMAND TEST

;# THE OFFSET COMMAND WILL BE LOADED INTO RHCSI WITH GO
;# THEN ALL REGISTERS WILL BE CHECKED
;# RH CLEAR WILL BE GIVEN
;# THEN ALL REGISTERS WILL BE CHECKED

TST46: SCOPE

MOV #STACK, SP ;RESET STACK
MOV #TTNO, #TSTNM ;THIS SAVES TEST NUMBER
JSR PC, #CLDISK ;INIT AND SET UP GENERAL REG.
;AND UNIT NUMBER
MOV #DMD, #RHMR ;SET DIAGNOSTIC MODE BIT
;THIS ENABLES COMMANDS WITHOUT MOL
;AND HOLDS RHLA FROM MOVING

;GIVE ONE INDEX PULSE TO CLEAR RHLA BEFORE THE START OF THIS TEST

BIS #MINX, #RHMR ;SET INDEX PULSE
BIC #MINX, #RHMR ;CLEAR INDEX

;TO ENABLE LOOP ON THIS TEST THE POSITIONER HAS TO
;BE BROUGHT TO CENTER LINE

MOV #RHCC, #RHCA ;SET DESIRED CYLINDER TO RHCC
MOV #SEECON, #RI ;SEEK COMMAND TO RHCSI
INC #RI ;GO TO SEEK COMMAND

;FOUR SECTOR CLOCKS ARE GIVEN TO TAKE POSITIONER OFF OFFSET POSITION

MOV #4, RO ;COUNTER
MOV #MSTCK!DMD, #RHMR ;SET SECTOR CLOCK
MOV #DMD, #RHMR ;RESET SECTOR CLOCK
DEC RO ;COUNT
BNE SS ;BRANCH IF NOT COMPLETE

JSR PC, #CLDISK ;INIT AND SET UP GENERAL REG.
;AND UNIT NUMBER

MOV #DMD, #RHMR ;SET DIAGNOSTIC MODE BIT
;THIS ENABLES COMMANDS WITHOUT MOL
;AND HOLDS RHLA FROM MOVING

MOV #OFSETC, #RHCSI ;LOAD AN OFFSET BIT
MOV #OF25, #RHOF ;SET AN OFFSET BIT

;SAVE REGISTERS FOR COMPARISON AFTER GO

JSR RO, #SAVER ;SAVE
RHC ;FROM
REINTO ;TO
19. ;NUMBER OF REGISTERS SAVED

```

3926                                     :GIVE GO TO OFFSET COMMAND
3927 021166 052777 000001 160436      BIS      #GO, @RHCS1      ;GO TO OFFSET COMMAND
3928
3929                                     :CHANGE SAVED REGISTERS TO EXPECTED VALUES
3930 021174 052737 000001 003142      BIS      #GO, @REINTO+6  ;SAVED RHCS1
3931 021202 052737 020000 003164      BIS      #PIP, @REINTO+30 ;SAVED RHDS1
3932 021210 042737 000200 003164      BIC      #DRY, @REINTO+30 ;SAVED RHDS1
3933
3934                                     :AFTER GO HAS BEEN GIVEN TO OFFSET COMMAND
3935                                     :SAVE REGISTERS AGAIN SO THAT COMPARISONS CAN
3936                                     :BE DONE
3937
3938 021216 004037 041214                JSR      R0, @SAVER      ;SAVE
3939 021222 001624                        RHMIC    ;FROM
3940 021224 002070                        WRFROM   ;TO
3941 021226 000023                        19.      ;NUMBER OF REGISTERS SAVED
3942
3943                                     :AS UPPER BYTE OF RHAS CAN BE CHANGING IN A DUAL PORT
3944                                     :OPERATION THE UPPER BYTE OF RHAS WILL BE SAVED AS IS
3945                                     :SO THAT THE COMPARES ARE ONLY VALID FOR THE LOWER BYTE
3946 021230 113737 003161 002115      MOVB     @REINTO+25, @WRFROM+25;SAVE UPPER RHAS
3947
3948
3949                                     :COMPARE REGISTERS BEFORE OFFSET COMMAND
3950                                     :WITH AFTER GO
3951
3952 021236 004037 041416                JSR      R0, @COMPAR    ;COMPARE
3953 021242 003134                        REINTO   ;GOOD BUFFER
3954 021244 002070                        WRFROM   ;TEST BUFFER
3955 021246 000023                        19.      ;NUMBER
3956 021250 021256                        1$      ;RETURN FOR ERROR
3957 021252 021256                        1$      ;SAME
3958 021254 021276                        2$      ;RETURN FOR GOOD COMPARISON
3959
3960 021256 013705 045272                1$:     MOV     @ERNORD, R5    ;GETTING READY TO INDEX
3961 021262 060505                        ADD     R5, R5         ;DOUBLE ERROR WORD
3962 021264 016537 001622 040172      MOV     RHMIC-2(R5), @REGADR ;FAILING REGISTER ADDRESS
3963 021272 104001                        ERROR   1              ;IMPROPER REGISTER CHANGE
3964                                     :AFTER OFFSET COMMAND
3965                                     :WITH GO IS GIVEN
3966 021274 000207                        RTS     PC              ;RETURN TO COMPARISON
3967
3968                                     :NOW GIVE INIT AND GET ALL GO AND PIP DOWN
3969
3970 021276 052712 000040                2$:     BIS     #CLR, @R2    ;RH INITILIZE
3971 021302 013712 001762                MOV     @UNIT, @R2     ;REINSTATE UNIT NUMBER
3972 021306 012777 000001 160336      MOV     #DMD, @RMR     ;SET DIAGNOSTIC MODE BIT
3973                                     :THIS ENABLES COMMANDS WITHOUT MOL
3974                                     :AND HOLDS RHLA FROM MOVING
3975
3976                                     :CHANGE REGISTERS TO EXPECTED VALUE
3977 021314 042737 000001 003142      BIC     #GO, @REINTO+6  ;SAVED RHCS1
3978 021322 042737 000001 003152      BIC     #OF25, @REINTO+16;SAVED RHOF
3979 021330 042737 020000 003164      BIC     #PIP, @REINTO+30 ;SAVED RHDS1

```


H11

```

3980 021336 052737 000200 003164      BIS      @DRY,@#REINTO+30 ;SAVED RHDS1
3981 021344 017737 160316 003176      MOV      @RHLA,@#REINTO+42;SAVED RHLA
3982
3983                                     ;AFTER INITIALIZE SAVE REGISTERS SO THAT
3984                                     ;COMPARES CAN BE DONE
3985
3986 021352 004037 041214      JSR      RO,@#SAVER      ;SAVE
3987 021356 001624              RHMC                    ;FROM
3988 021360 002070              WRFROM                 ;TO
3989 021362 000023              19.                   ;NUMBER OF REGISTERS SAVED
3990
3991                                     ;AS UPPER BYTE OF RHAS CAN BE CHANGING IN A DUAL PORT
3992                                     ;OPERATION THE UPPER BYTE OF RHAS WILL BE SAVED AS IS
3993                                     ;SO THAT THE COMPARES ARE ONLY VALID FOR THE LOWER BYTE
3994 021364 113737 003161 002115      MOVB     @#REINTO+25,@#WRFROM+25;SAVE UPPER RHAS
3995
3996                                     ;COMPARE REGISTERS AFTER INITIALIZE
3997 021372 004037 041416      JSR      RO,@#COMPAR    ;COMPARE
3998 021376 003134              REINTO                 ;GOOD BUFFER
3999 021400 002070              WRFROM                 ;TEST BUFFER
4000 021402 000023              19.                   ;NUMBER OF REGISTERS TO BE
4001                                     ;COMPARED
4002 021404 021412              3$                     ;RETURN POINT FOR ERROR
4003 021406 021412              3$                     ;SAME
4004 021410 021432              4$                     ;RETURN POINT FOR GOOD COMPARISON
4005
4006 021412 013705 045272      3$:  MOV     @#ERNORD,RS   ;GETTING READY TO INDEX
4007 021416 060505              ADD     RS,RS           ;DOUBLE ERROR WORD
4008 021420 016537 001622 040172      MOV     RHMC-2(RS),@#REGADR ;FAILING REGISTER ADDRESS
4009 021426 104001              ERROR  1               ;IMPROPER REGISTER
4010                                     ;CONTENTS AFTER GIVING AN
4011                                     ;INITIALIZE FOLLOWING A
4012                                     ;OFFSET COMMAND
4013 021430 000207              RTS     PC              ;RETURN TO COMPARISON
4014
4015 021432      4$:                   ;GOOD COMPARISON

```

4016
4017
4018
4019
4020
4021
4022
4023
4024
4025
4026
4027
4028
4029
4030
4031
4032
4033
4034
4035
4036
4037
4038
4039
4040
4041
4042
4043
4044
4045
4046
4047
4048
4049
4050
4051
4052
4053
4054
4055
4056
4057
4058
4059
4060
4061
4062
4063
4064
4065
4066
4067
4068
4069

: #TEST 47 RETURN TO CENTER LINE COMMAND TEST

: # THE RETURN TO CENTER LINE COMMAND WILL BE LOADED INTO RHCS1 WITH GO
: # THEN ALL REGISTERS WILL BE CHECKED
: # RH CLEAR WILL BE GIVEN
: # THEN ALL REGISTERS WILL BE CHECKED

TST47: SCOPE
MOV #STACK, SP ; RESET STACK
MOV #TTNO, @TSTNM ; THIS SAVES TEST NUMBER
JSR PC, @CLDISK ; INIT AND SET UP GENERAL REG.
; AND UNIT NUMBER
MOV #DMD, @RHMR ; SET DIAGNOSTIC MODE BIT
; THIS ENABLES COMMANDS WITHOUT MOL
; AND HOLDS RHLA FROM MOVING

: GIVE ONE INDEX PULSE TO CLEAR RHLA BEFORE THE START OF THIS TEST
BIS #MINX, @RHMR ; SET INDEX PULSE
BIC #MINX, @RHMR ; CLEAR INDEX

MOV @RETCL, @RHCS1 ; LOAD RETURN TO CENTER LINE COMMAND INTO RHCS1

: SAVE REGISTERS FOR COMPARISON AFTER GO
JSR RO, @SAVER ; SAVE
RHC ; FROM
REINTO ; TO
19. ; NUMBER OF REGISTERS SAVED

: GIVE GO TO RETURN TO CENTER LINE COMMAND
BIS #GO, @RHCS1 ; GO TO RETURN TO CENTER COMMAND

: FOUR SECTOR CLOCKS ARE GIVEN TO TAKE POSITIONER TO CENTER LINE
MOV #4, RO ; COUNTER
MOV #MSTCK!DMD, @RHMR ; SET SECTOR CLOCK
MOV #DMD, @RHMR ; RESET SECTOR CLOCK
DEC RO ; COUNT
BNE SS ; BRANCH IF NOT COMPLETE

: CHANGE SAVED REGISTERS TO EXPECTED VALUES
BIS #GO, @REINTO+6 ; SAVED RHCS1
BIS #PIP, @REINTO+30 ; SAVED RHDS1
BIC #DRY, @REINTO+30 ; SAVED RHDS1

: AFTER GO HAS BEEN GIVEN TO RETURN TO CENTER LINE COMMAND
: SAVE REGISTERS AGAIN SO THAT COMPARISONS CAN
: BE DONE
JSR RO, @SAVER ; SAVE


```

4070 021574 001624          RHC          :FROM
4071 021576 002070          MRFROM       :TO
4072 021600 000023          19.          :NUMBER OF REGISTERS SAVED
4073
4074
4075          :AS UPPER BYTE OF RHAS CAN BE CHANGING IN A DUAL PORT
4076          :OPERATION THE UPPER BYTE OF RHAS WILL BE SAVED AS IS
4077 021602 113737 003161 002115  MOVB 2#REINTO+25,2#MRFROM+25;SAVE UPPER RHAS
4078
4079
4080          :COMPARE REGISTERS BEFORE RETURN TO CENTER LINE COMMAND
4081          :WITH AFTER GO
4082 021610 004037 041416  JSR  RD,2#COMPAR  :COMPARE
4083 021614 003134          REINTO         :GOOD BUFFER
4084 021616 002070          MRFROM       :TEST BUFFER
4085 021620 000023          19.          :NUMBER
4086 021622 021630          1$          :RETURN FOR ERROR
4087 021624 021630          1$          :SAME
4088 021626 021650          2$          :RETURN FOR GOOD COMPARISON
4089
4090 021630 013705 045272 1$:  MOV  2#ERWORD,R5  :GETTING READY TO INDEX
4091 021634 060505          ADD  R5,R5       :DOUBLE ERROR WORD
4092 021636 016537 001622 040172  MOV  RHC-2(R5),2#REGADR :FAILING REGISTER ADDRESS
4093 021644 104001          ERROR 1        :IMPROPER REGISTER CHANGE
4094
4095          :AFTER RETURN TO CENTER LINE COMMAND
4096 021646 000207          RTS  PC         :WITH GO IS GIVEN
4097
4098          :RETURN TO COMPARISON
4099          :NOW GIVE INIT AND GET ALL GO AND PIP DOWN
4100 021650 052712 000040 2$:  BIS  #CLR,2R2    :RH INITILIZE
4101 021654 013712 001762          MOV  2#UNIT,2R2  :REINSTATE UNIT NUMBER
4102 021660 012777 000001 157764  MOV  #DMD,2RHR   :SET DIAGNOSTIC MODE BIT
4103
4104          :THIS ENABLES COMMANDS WITHOUT MOL
4105          :AND HOLDS RHLA FROM MOVING
4106
4107          :CHANGE REGISTERS TO EXPECTED VALUE
4108 021666 042737 000001 003142  BIC  #GO,2#REINTO+6 ;SAVED RHCS1
4109 021674 042737 020000 003164  BIC  #PIP,2#REINTO+30 ;SAVED RHDS1
4110 021702 052737 000200 003164  BIS  #DRY,2#REINTO+30 ;SAVED RHDS1
4111 021710 017737 157752 003176  MOV  2#RHLA,2#REINTO+42;SAVED RHLA
4112
4113          :AFTER INITIALIZE SAVE REGISTERS SO THAT
4114          :COMPARES CAN BE DONE
4115 021716 004037 041214  JSR  RD,2#SAVER  :SAVE
4116 021722 001624          RHC          :FROM
4117 021724 002070          MRFROM       :TO
4118 021726 000023          19.          :NUMBER OF REGISTERS SAVED
4119
4120          :AS UPPER BYTE OF RHAS CAN BE CHANGING IN A DUAL PORT
4121          :OPERATION THE UPPER BYTE OF RHAS WILL BE SAVED AS IS
4122 021730 113737 003161 002115  MOVB 2#REINTO+25,2#MRFROM+25;SAVE UPPER RHAS
4123
    
```

K11

MAINDEC-11-DZRP5-D, RJPO4 DISKLESS RH11 TEST-PART 1 MACY11 27(663) 8-OCT-75 16:14 PAGE 97
DZRP5D.P11 T47 RETURN TO CENTER LINE COMMAND TEST

SEQ 0139

```
4124  
4125  
4126 021736 004037 041416  
4127 021742 003134  
4128 021744 002070  
4129 021746 000023  
4130  
4131 021750 021756  
4132 021752 021756  
4133 021754 021776  
4134  
4135  
4136 021756 013705 045272 3S: MOV 3#ERWORD,R5 ;GETTING READY TO INDEX  
4137 021762 060505 ADD R5,R5 ;DOUBLE ERROR WORD  
4138 021764 016537 001622 040172 MOV RHC-2(R5),3#REGADR ;FAILING REGISTER ADDRESS  
4139 021772 104001 ERROR 1 ;IMPROPER REGISTER  
4140 ;CONTENTS AFTER GIVING AN  
4141 ;INITIALIZE FOLLOWING A RETURN TO  
4142 ;CENTER LINE COMMAND  
4143 021774 000207 RTS PC ;RETURN TO COMPARISON  
4144  
4145 021776 4S: ;GOOD COMPARISON  
4146
```

```
;COMPARE REGISTERS AFTER INITIALIZE  
JSR RO,3#COMPAR ;COMPARE  
REINTO ;GOOD BUFFER  
MRFROM ;TEST BUFFER  
19. ;NUMBER OF REGISTERS TO BE  
;COMPARED  
3S ;RETURN POINT FOR ERROR  
3S ;SAME  
4S ;RETURN POINT FOR GOOD COMPARISON
```

```
;GETTING READY TO INDEX  
;DOUBLE ERROR WORD  
;FAILING REGISTER ADDRESS  
;IMPROPER REGISTER  
;CONTENTS AFTER GIVING AN  
;INITIALIZE FOLLOWING A RETURN TO  
;CENTER LINE COMMAND  
;RETURN TO COMPARISON
```

```
;GOOD COMPARISON
```


4147
4148
4149
4150
4151
4152
4153
4154
4155
4156
4157
4158 021776 000004
4159 022000 012706 001000
4160 022004 012737 000050 002020
4161 022012 004737 040522
4162
4163 022016 012777 000001 157626
4164
4165
4166
4167
4168
4169
4170 022024 052777 000004 157620
4171 022032 042777 000004 157612
4172
4173 022040 013777 002030 157564
4174
4175
4176 022046 004037 041214
4177 022052 001624
4178 022054 003134
4179 022056 000023
4180
4181
4182 022060 052777 000001 157544
4183
4184
4185
4186 022066 012700 000004
4187 022072 012777 000011 157552 SS:
4188 022100 012777 000001 157544
4189 022106 005300
4190 022110 001370
4191
4192
4193
4194 022112 052737 000001 003142
4195 022120 052737 020000 003164
4196 022126 042737 000200 003164
4197
4198
4199
4200

;#TEST 50 RECALIBRATE COMMAND TEST

;* THE RECALIBRATE COMMAND WILL BE LOADED INTO RHCS1 WITH GO
;* THEN ALL REGISTERS WILL BE CHECKED
;* RH CLEAR WILL BE GIVEN
;* THEN ALL REGISTERS WILL BE CHECKED

;ST50: SCOPE

MOV #STACK, SP ;RESET STACK
MOV #TTNO, #TSTNM ;THIS SAVES TEST NUMBER
JSR PC, #CLDISK ;INIT AND SET UP GENERAL REG.
AND UNIT NUMBER
MOV #DMD, #RHMR ;SET DIAGNOSTIC MODE BIT
;THIS ENABLES COMMANDS WITHOUT MOL
;AND HOLDS RHLA FROM MOVING

;GIVE ONE INDEX PULSE TO CLEAR RHLA BEFORE THE START OF THIS TEST

BIS #MINK, #RHMR ;SET INDEX PULSE
BIC #MINK, #RHMR ;CLEAR INDEX

MOV #RECALI, #RHCS1 ;LOAD RECALIBRATE COMMAND INTO RHCS1

;SAVE REGISTERS FOR COMPARISON AFTER GO

JSR RO, #SAVER ;SAVE
RHMIC ;FROM
REINTO ;TO
19. ;NUMBER OF REGISTERS SAVED

;GIVE GO TO RECALIBRATE COMMAND

BIS #GO, #RHCS1 ;GO TO RECALIBRATE COMMAND

;FOUR SECTOR CLOCKS ARE GIVEN TO TAKE POSITIONER TO CYLINDER 0

MOV #4, RO ;COUNTER
MOV #MSTCK!DMD, #RHMR ;SET SECTOR CLOCK
MOV #DMD, #RHMR ;RESET SECTOR CLOCK
DEC RO ;COUNT
BNE SS ;BRANCH IF NOT COMPLETE

;CHANGE SAVED REGISTERS TO EXPECTED VALUES

BIS #GO, #REINTO+6 ;SAVED RHCS1
BIS #PIP, #REINTO+30 ;SAVED RHDS1
BIC #DRY, #REINTO+30 ;SAVED RHDS1

;AFTER GO HAS BEEN GIVEN TO RECALIBRATE COMMAND
;SAVE REGISTERS AGAIN SO THAT COMPARISONS CAN
;BE DONE

```

4201 022134 004037 041214 JSR RO, @SAVER ;SAVE
4202 022140 001624 RHC FROM ;FROM
4203 022142 002070 MRFROM ;TO
4204 022144 000023 19. ;NUMBER OF REGISTERS SAVED
4205
4206 ;AS UPPER BYTE OF RHAS CAN BE CHANGING IN A DUAL PORT
4207 ;OPERATION THE UPPER BYTE OF RHAS WILL BE SAVED AS IS
4208 ;SO THAT THE COMPARES ARE ONLY VALID FOR THE LOWER BYTE
4209 022146 113737 003161 002115 MOVB @REINTO+25, @MRFROM+25; SAVE UPPER RHAS
4210
4211
4212 ;COMPARE REGISTERS BEFORE RECALIBRATE COMMAND
4213 ;WITH AFTER GO
4214 022154 004037 041416 JSR RO, @COMPAR ;COMPARE
4215 022160 003134 REINTO ;GOOD BUFFER
4216 022162 002070 MRFROM ;TEST BUFFER
4217 022164 000023 19. ;NUMBER
4218 022166 022174 1$ ;RETURN FOR ERROR
4219 022170 022174 1$ ;SAME
4220 022172 022214 2$ ;RETURN FOR GOOD COMPARISON
4221
4222 022174 013705 045272 1$: MOV @ERNORD, R5 ;GETTING READY TO INDEX
4223 022200 060505 ADD R5, R5 ;DOUBLE ERROR WORD
4224 022202 016537 001622 040172 MOV RHC-2(R5), @REGADR ;FAILING REGISTER ADDRESS
4225 022210 104001 ERROR 1 ;IMPROPER REGISTER CHANGE
4226 ;AFTER RECALIBRATE COMMAND
4227 ;WITH GO IS GIVEN
4228 022212 000207 RTS PC ;RETURN TO COMPARISON
4229
4230 ;NOW GIVE INIT AND GET ALL GO AND PIP DOWN
4231
4232 022214 052712 000040 2$: BIS #CLR, @R2 ;RH INITILIZE
4233 022220 013712 001762 MOV @UNIT, @R2 ;REINSTATE UNIT NUMBER
4234 022224 012777 000001 157420 MOV #DMD, @RHR ;SET DIAGNOSTIC MODE BIT
4235 ;THIS ENABLES COMMANDS WITHOUT MOL
4236 ;AND HOLDS RHLA FROM MOVING
4237
4238 ;CHANGE REGISTERS TO EXPECTED VALUE
4239 022232 042737 000001 003142 BIC #GO, @REINTO+6 ;SAVED RHCS1
4240 022240 042737 020000 003164 BIC #PIP, @REINTO+30 ;SAVED RHDS1
4241 022246 052737 000200 003164 BIS #DRY, @REINTO+30 ;SAVED RHDS1
4242 022254 017737 157406 003176 MOV @RHLA, @REINTO+42; SAVED RHLA
4243
4244 ;AFTER INITIALIZE SAVE REGISTERS SO THAT
4245 ;COMPARES CAN BE DONE
4246 022262 004037 041214 JSR RO, @SAVER ;SAVE
4247 022266 001624 RHC FROM ;FROM
4248 022270 002070 MRFROM ;TO
4249 022272 000023 19. ;NUMBER OF REGISTERS SAVED
4250
4251 ;AS UPPER BYTE OF RHAS CAN BE CHANGING IN A DUAL PORT
4252 ;OPERATION THE UPPER BYTE OF RHAS WILL BE SAVED AS IS
4253 ;SO THAT THE COMPARES ARE ONLY VALID FOR THE LOWER BYTE
4254 022274 113737 003161 002115 MOVB @REINTO+25, @MRFROM+25; SAVE UPPER RHAS

```



```

4255
4256
4257
4258 022302 004037 041416
4259 022306 003134
4260 022310 002070
4261 022312 000023
4262
4263 022314 022322
4264 022316 022322
4265 022320 022342
4266
4267 022322 013705 045272 3S:
4268 022326 060505
4269 022330 016537 001622 040172
4270 022336 104001
4271
4272
4273
4274 022340 000207
4275
4276 022342 4S:

```

```

:COMPARE REGISTERS AFTER INITIALIZE
JSR RO,2#COMPAR :COMPARE
REINTO :GOOD BUFFER
MRFROM :TEST BUFFER
19. :NUMBER OF REGISTERS TO BE
:COMPARED
3S :RETURN POINT FOR ERROR
3S :SAME
4S :RETURN POINT FOR GOOD COMPARISON

MOV 2#ERWORD,R5 :GETTING READY TO INDEX
ADD R5,R5 :DOUBLE ERROR WORD
MOV RHC-2(R5),2#REGADR :FAILING REGISTER ADDRESS
ERROR 1 :IMPROPER REGISTER
:CONTENTS AFTER GIVING AN
:INITIALIZE FOLLOWING A
:RECALIBRATE COMMAND
RTS PC :RETURN TO COMPARISON

4S: :GOOD COMPARISON

```

```

4277
4278
4279
4280
4281
4282
4283
4284
4285
4286
4287
4288 022342 000004
4289 022344 012706 001000
4290 022350 012737 000051 002020
4291 022356 004737 040522
4292
4293 022362 012777 000001 157262
4294
4295
4296
4297 022370 013777 002034 157234
4298
4299
4300 022376 004037 041214
4301 022402 001624
4302 022404 003134
4303 022406 000023
4304
4305
4306 022410 052777 000001 157214
4307 022416 052777 000001 157226
4308
4309
4310
4311 022424 052737 000001 003162
4312 022432 017737 157230 003176
4313
4314
4315
4316
4317 022440 004037 041214
4318 022444 001624
4319 022446 002070
4320 022450 000023
4321
4322
4323
4324
4325 022452 113737 003161 002115
4326
4327
4328
4329
4330 022460 004037 041416

```

```

*****
;TEST 51      RELEASE COMMAND TEST
*****
;#      THE RELEASE COMMAND WILL BE LOADED INTO RHCS1 WITH GO
;#      THEN ALL REGISTERS WILL BE CHECKED
;#      RH CLEAR WILL BE GIVEN
;#      THEN ALL REGISTERS WILL BE CHECKED
*****
TST51:  SCOPE
        MOV      #STACK,SP      ;RESET STACK
        MOV      #TTNO,#TSTNM  ;THIS SAVES TEST NUMBER
        JSR      PC,#CLDISK    ;INIT AND SET UP GENERAL REG.
                                   ;AND UNIT NUMBER
        MOV      #DMD,#RHMR    ;SET DIAGNOSTIC MODE BIT
                                   ;THIS ENABLES COMMANDS WITHOUT MOL
                                   ;AND HOLDS RHLA FROM MOVING
        MOV      #RELEASE,#RHCS1 ;LOAD RELEASE COMMAND INTO RHCS1

        ;SAVE REGISTERS FOR COMPARISON AFTER GO
        JSR      RO,#SAVER      ;SAVE
        RHMIC    ;FROM
        REINTO   ;TO
        19.                ;NUMBER OF REGISTERS SAVED

        ;GIVE GO TO RELEASE COMMAND
        BIS      #GO,#RHCS1    ;GO TO RELEASE COMMAND
        BIS      #DMD,#RHMR    ;SET DMD TO HOLD RHLA

        ;CHANGE SAVED REGISTERS TO EXPECTED VALUES
        ;AFTER GO HAS BEEN GIVEN TO RELEASE COMMAND
        BIS      #DMD,#REINTO+26;SAVED RHMR
        MOV      #RHLA,#REINTO+42;SAVED RHLA

        ;SAVE REGISTERS AGAIN SO THAT COMPARISONS CAN
        ;BE DONE
        JSR      RO,#SAVER      ;SAVE
        RHMIC    ;FROM
        MRFROM   ;TO
        19.                ;NUMBER OF REGISTERS SAVED

        ;AS UPPER BYTE OF RHAS CAN BE CHANGING IN A DUAL PORT
        ;OPERATION THE UPPER BYTE OF RHAS WILL BE SAVED AS IS
        ;SO THAT THE COMPARES ARE ONLY VALID FOR THE LOWER BYTE
        MOV      #REINTO+25,#MRFROM+25;SAVE UPPER RHAS

        ;COMPARE REGISTERS BEFORE RELEASE COMMAND
        ;WITH AFTER GO
        JSR      RO,#COMPAR     ;COMPARE

```


4331	022464	003134			REINTO			:GOOD BUFFER
4332	022466	002070			MRFROM			:TEST BUFFER
4333	022470	000023			19.			:NUMBER
4334	022472	022500			1\$:RETURN FOR ERROR
4335	022474	022500			1\$:SAME
4336	022476	022520			2\$:RETURN FOR GOOD COMPARISON
4337								
4338	022500	013705	045272	1\$:	MOV	2#ERWORD,RS		:GETTING REHYD TO INDEX
4339	022504	060505			ADD	RS,RS		:DOUBLE ERROR WORD
4340	022506	016537	001622 040172		MOV	RHMC-2(RS),2#REGADR		:FAILING REGISTER ADDRESS
4341	022514	104001			ERROR	1		:IMPROPER REGISTER CHANGE
4342								:AFTER RELEASE COMMAND
4343								:WITH GO IS GIVEN
4344	022516	000207			RTS	PC		:RETURN TO COMPARISON
4345	022520			2\$:				
4346								

4347
4348
4349
4350
4351
4352
4353 022520 000004
4354 022522 012706 001000
4355 022526 004737 040522
4356 022532 012777 000001 157112
4357 022540 004037 043074
4358
4359
4360 022544 000000
4361
4362
4363
4364
4365
4366
4367
4368
4369
4370
4371
4372
4373
4374
4375 022546 000004
4376 022550 012706 001000
4377 022554 012737 000053 002020
4378 022562 004737 040522
4379
4380
4381 022566 012777 000025 157042
4382
4383 022574 005077 157044
4384 022600 012777 010000 157034
4385 022606 013711 002036
4386
4387
4388 022612 004037 041214
4389
4390 022616 001624
4391 022620 003134
4392 022622 000023
4393
4394 022624 004737 040556
4395 022630 104400 057176
4396
4397 022634 000000
4398
4399
4400

```

*****
;*TEST 52 MAKE CURRENT CYLINDER = 0
*****

```

```

TST52: SCOPE
MOV #STACK, SP ;RESET STACK
JSR PC, @#CLDISK ;INIT DRIVE
MOV #DMD, @RHMR ;SET DIAGNOSTIC MODE
JSR RO, @#MAKECYL ;SUBROUTINE TO GIVE A SEEK
;COMMAND FOLLOMED BY A INIT
;THIS SHUOLD CHANGE RHCC
;CHANGE RHCC TO 0

```

```

*****
;*TEST 53 LOOK AHEAD REGISTER

```

```

;* A SEARCH COMMAND IS GIVEN FOR CYLINDER 0, TRACK 0, SECTOR 21.
;* THE LOOK AHEAD REGISTER IS CHECKED AFTER INDEX PULSE
;* THE EXTENSION FIELD IS CHECKED IN EACH SECTOR AFTER
;* 128 BYTES THEN AGAIN AFTER 128 MORE BYTES THEN AGAIN AFTER 256 MORE BYTES
;* THE SECTOR COUNT FIELD IS CHECKED AFTER EACH SECTOR
;* AT THE END ALL REGISTERS ARE CHECKED

```

```

*****
TST53: SCOPE

```

```

MOV #STACK, SP ;RESET STACK
MOV #TTNO, @#TSTNM ;THIS SAVES TEST NUMBER
JSR PC, @#CLDISK ;INIT AND SET UP GENERAL REGISTERS

;THESE ARE REGULAR SET UPS FOR SEARCH COMMAND
MOV #21., @RHST ;DESIRED SECTOR/TRACK REGISTER
;TRACK 0 SECTOR 21
CLR @RHCA ;DESIRED CYLINDER =0
MOV #FMT22, @RHOF ;FORMAT BIT=1 (16 BITS PER WORD)
MOV @#SERCH, @R1 ;FILL SEARCH COMMAND IN RHCS1

;NOW SAVE REGISTERS STARTING FROM RHMC IN WRITE FROM BUFFER
JSR RO, @#SAVER ;SAVE REGISTERS FOR COMPARISON
;AT THE END OF THE SEARCH
RHMC ;START SAVING FROM RHMC
REINTO ;SAVE INTO REINTO
19. ;NUMBER OF REGISTERS SAVED

JSR PC, @#CHECKT ;CHECK THAT DVA, RDY, DPR, DRY = 1
TYPE ,CPHALT ;AND THAT NO OTHERS = 1. CANNOT CON-
;TINUE TESTING IF BOTH AREN'T TRUE
HALT ;STOP THE TEST

```

```

;NOW THE DIAGNOSTIC MODE BIT WILL BE SET
;AND THE SEARCH OPERATION STARTED

```



```

4401
4402 022636 005037 001172 CLR 2#STMP1 ;THIS WILL HAVE THE EXPECTED
4403 ;VALUE OF RHLA REGISTER
4404
4405 022642 013700 001652 MOV 2#RHMR,R0 ;NOW R0 HAS MAINTENANCE REG. ADDR.
4406 022646 017703 156764 MOV 2#RHST,R3 ;GET DESIRED SECTOR/TRACK REG.
4407 022652 042703 177400 BIC 2#177400,R3 ;GET SECTOR ONLY
4408 022656 010337 051202 MOV R3,2#SECTR ;DUPLICATE SECTOR
4409 022662 012710 000001 MOV 2#DMD,2#R0 ;S
4410 022666 052777 000001 156736 BIS 2#GO,2#RHCS1 ;GO
4411 022674 052710 000010 BIS 2#MSTCK,2#R0 ;SET SECTOR CLOCK
4412 022700 042710 000010 BIC 2#MSTCK,2#R0 ;CLEAR SECTOR CLOCK
4413 022704 000240 NOP ;ALLOW TIME BETWEEN SECTOR CLOCKS
4414 022706 052710 000010 BIS 2#MSTCK,2#R0 ;SET SECTOR CLOCK
4415 022712 042710 000010 BIC 2#MSTCK,2#R0 ;CLEAR SECTOR CLOCK
4416 022716 000240 NOP ;ALLOW TIME BETWEEN SECTOR CLOCKS
4417 022720 052710 000014 BIS 2#MINX!MSTCK,2#R0 ;SET INDEX AND SECTOR CLOCK
4418 022724 012710 000001 MOV 2#DMD,2#R0 ;RESET INDEX AND SECTOR CLOCK
4419 022730 005703 TST R3 ;IF SECTOR REQUIRED JUMP OUT
4420 022732 001555 BEQ 115 ;BRANCH OF SECTOR ZERO REQUIRED
4421
4422 ;AFTER THE INDEX PULSE RHLA WILL BE CHECKED TO BE ZERO
4423 ;AND STMP4 WILL BE SET UP TO COUNT BYTES
4424 022734 012737 001140 001200 15: MOV 2#608,2#STMP4 ;THERE ARE 608 BYTES PER SECTOR
4425 022742 017737 156720 001126 MOV 2#RHLA,2#SBODAT ;SAVE RHLA
4426 022750 017737 156662 001706 MOV 2#RHST,2#DST ;SAVE DESIRED SECTOR TRACK
4427 022756 023737 001172 001126 CMP 2#STMP1,2#SBODAT ;RHLA SHOULD BE HAVE EXTENSION
4428 ;FIELD EQUAL TO ZERO
4429 022764 001414 BEQ 25 ;BRANCH IF GOOD
4430 022766 013737 051202 001174 MOV 2#SECTR,2#STMP2 ;GET SECTOR SOUGHT
4431 022774 160337 001174 SUB R3,2#STMP2 ;STMP2 NOW HAS PRESENT SECTOR
4432 023000 012746 001140 MOV 2#608,-(SP) ;NUMBER OF BYTES PER SECTOR
4433 023004 163716 001200 SUB 2#STMP4,(SP) ;(SP)HAS PRESENT BYTE NUMBER
4434 023010 012637 001176 MOV (SP)+,2#STMP3 ;PRESENT BYTE NUMBER
4435 023014 104024 ERROR 24 ;LOOK AHEAD REGISTER AT THE BEGINING OF A
4436 ;SECTOR IS IN ERROR
4437
4438 ;NOW THE 304 WORDS WILL START
4439 ;FOR FIRST BYTE CLOCK WILL BE INDEPENDENT OF
4440 ;SECTOR CLOCK THEN IT WILL COINCIDE FOREVER TILL
4441 ;THE BEGINNING OF NEXT SECTOR
4442
4443 ;ONE WORD ONLY THAT IS TWO BYTES
4444
4445
4446 023016 012702 000010 25: MOV 2#8,R2 ;BYTE
4447 023022 012705 000002 MOV 2#2,R5 ;BYTES PER WORD
4448 023026 000404 BR 45
4449 023030 052710 000012 35: BIS 2#MSTCK!MCLK,2#R0 ;SET SECTOR AND CLOCK
4450 023034 042710 000012 BIC 2#MSTCK!MCLK,2#R0 ;CLEAR SECTOR AND CLOCK
4451 023040 052710 000002 45: BIS 2#MCLK,2#R0 ;SET CLOCK
4452 023044 042710 000002 BIC 2#MCLK,2#R0 ;CLEAR CLOCK
4453 023050 005302 DEC R2 ;BYTE COUNTER
4454 023052 001372 BNE 45 ;BRANCH IF BYTE NOT COMPLETE

```

```

4455 023054 005337 001200      DEC      @#STMP4      ;BYTE COUNT DOWN
4456 023060 012702 000007      MOV      #7,R2      ;SETUP FOR SECOND BYTE
4457 023064 005305      DEC      R5          ;IS WORD COMPLETE?
4458 023066 001360      BNE     35          ;BRANCH IF NOT COMPLETE
4459                                ;TO GIVE SECTOR CLOCK AND CLOCK
4460
4461                                ;NOW 303 WORDS ARE LEFT ALL ARE IDENTICAL
4462                                ;THAT IS 606 IDENTICAL BYTES WILL BE GIVEN
4463                                ;RHLA WILL BE CHECKED STAR TO COUNT AFTER
4464                                ;BEGINNING OF SECTOR PULSE
4465                                ;AFTER 128 BYTES (2 BYTES ARE ALREADY GIVEN)
4466                                ;SO 127 MORE
4467                                ;THEN RHLA WILL BE CHECKED AFTER 128 MORE BYTES
4468                                ;THEN RHLA WILL BE CHECKED AFTER 256 MORE BYTES
4469                                ;THEN THE TOTAL OF 608 BYTES WILL BE COMPLETED
4470                                ;AND RHLA WILL BE MADE READY FOR NEXT SECTOR
4471                                ;AND RHLA WILL BE CHECKED
4472
4473 023070 012705 000100      MOV      #64.,R5     ;R5 WILL KEEP TRACK WHEN
4474                                ;EXTENSION FIELD IS TO BE CHECKED
4475 023074 012701 000177      MOV      #127.,R1    ;FIRST TIME CHECK EXTENSION FIELD
4476                                ;AFTER 127 MORE BYTES
4477 023100 012702 000007      5S:    MOV      #7,R2     ;CLOCKS PER BYTE COUNTER
4478 023104 052710 000012      BIS     #MSTCK!MCLK,@R0 ;SET SECTOR CLOCK AND CLOCK
4479 023110 042710 000012      BIC     #MSTCK!MCLK,@R0 ;CLEAR SECTOR CLOCK AND CLOCK
4480 023114 052710 000002      6S:    BIS     #MCLK,@R0   ;SET CLOCK
4481 023120 042710 000002      BIC     #MCLK,@R0   ;RESET CLOCK
4482 023124 005302      DEC     R2          ;COUNT DOWN CLOCKS PER BYTE
4483 023126 001372      BNE     65          ;BRANCH IF BYTE NOT COMPLETE
4484 023130 005337 001200      DEC     @#STMP4     ;COUNT DOWN BYTES
4485 023134 001436      BEQ     105         ;BRANCHOUT IF 608 BYTES DONE
4486 023136 005301      DEC     R1          ;COUNT DOWN NUMBER OF BYTES
4487                                ;TO CHECK EXTENSION FIELD
4488 023140 001357      BNE     55          ;BRANCH IF EXTENSION FIELD NOT
4489                                ;TO BE CHECKED YET
4490
4491                                ;NOW THE EXTENSION FIELD OF THE LOOK AHEAD REGISTER
4492                                ;WILL BE CHECKED
4493
4494 023142 062737 000020 001172      ADD     #20,@#STMP1  ;GET TO THE NEXT EXTENSION
4495 023150 017737 156512 001126      MOV     @RHLA,@#SBDDAT ;GET RHLA FOR COMPARISON
4496
4497 023156 017737 156454 001706      MOV     @RHDST,@#DST ;SAVE DESIRED SECTOR TRACK
4498 023164 023737 001172 001126      CMP     @#STMP1,@#SBDDAT ;CHECK VALUE OF RHLA
4499 023172 001414      BEQ     75          ;BRANCH IF GOOD
4500 023174 013737 051202 001174      MOV     @#SECTR,@#STMP2 ;GET SECTOR SOUGHT
4501 023202 160337 001174      SUB     R3,@#STMP2  ;STMP2 NOW HAS PRESENT SECTOR
4502 023206 012746 001140      MOV     #608.,-(SP) ;NUMBER OF BYTES PER SECTOR
4503 023212 163716 001200      SUB     @#STMP4,(SP) ;(SP) HAS PRESENT BYTE NUMBER
4504 023216 012637 001176      MOV     (SP)+,@#STMP3 ;PRESENT BYTE NUMBER
4505 023222 104025      ERROR   25          ;LOOK AHEAD ERROR IN THE MIDDLE
4506                                ;OF A SECTOR IS IN ERROR
4507
4508 023224 060505      7S:    ADD     R5,R5      ;GET NEXT STEP TO CHECK EXTENSION FIELD

```



```

4509 023226 010501          MOV    R5,R1          ;PUT IN COUNTER
4510 023230 000723          BR     55             ;BRANCH BACK SECTOR
4511                                     ;IS NOT COMPLETE
4512 023232 062737 000020 001172 10S:  ADD    #20,2#STMP1
4513 023240 052710 000010          BIS    #MSTCK,2RO    ;THESE TWO INSTRUCTIONS GIVE
4514 023244 042710 000010          BIC    #MSTCK,2RO    ;ONE SECTOR CLOCK EXTRA
4515 023250 000240          NOP
4516 023252 052710 000010          BIS    #MSTCK,2RO    ;ALLOW TIME BETWEEN SECTOR CLOCK
4517 023256 042710 000010          BIC    #MSTCK,2RO    ;THESE TWO INSTRUCTIONS GIVE
4518                                     ;ONE SECTOR CLOCK EARLY
4519 023262 005303          DEC    R3            ;BEFORE THE NEXT SECTOR
4520 023264 001223          BNE    15            ;IS REQUIRED NO OF SECTORS COMPLETE
4521                                     ;BRANCH IF NOT
4522                                     ;NOW THE REQUIRED SECTOR IS REACHED
4523                                     ;ONE SECTOR CLOCK WILL BE GIVEN TO GET SECTOR PULSE
4524                                     ;DOWN AND HENCE ATA UP
4525
4526 023266 012702 000010          11S:  MOV    #8,,R2        ;8 CLOCKS
4527 023272 052710 000002          12S:  BIS    #MCLK,2RO    ;SET CLOCK
4528 023276 042710 000002          BIC    #MCLK,2RO    ;CLEAR CLOCKS
4529 023302 005302          DEC    R2            ;COUNT DOWN
4530 023304 001372          BNE    12S          ;BRANCH IF 8 NOT DONE
4531 023306 052710 000012          BIS    #MSTCK!MCLK,2RO ;SET SECTOR AND CLOCK
4532 023312 042710 000012          BIC    #MSTCK!MCLK,2RO ;CLEAR SECTOR AND CLOCK
4533
4534                                     ;NOW ALL REGISTERS WILL BE COMPARED
4535                                     ;SO FILL EXPECTED VALUE INTO SAVED LOCATIONS
4536
4537 023316 052737 100000 003142          BIS    #SC,2#REINTO+6 ;INCLUDE SC IN SAVED RHCS1
4538 023324 053737 002004 003160          BIS    2#ATTENT,2#REINTO+24 ;FILL APPROPRIATE ATTENTION
4539                                     ;IN SAVED RHAS
4540 023332 052737 000001 003162          BIS    #DMD,2#REINTO+26 ;SET DMD IN RHMR SAVED
4541 023340 052737 100000 003164          BIS    #ATA,2#REINTO+30 ;SET ATA IN RHDS1 SAVED
4542 023346 013737 001172 003176          MOV    2#STMP1,2#REINTO+42 ;MOVE EXPECTED VALUE
4543                                     ;INTO RHLA SAVED
4544
4545                                     ;AFTER SEARCH COMMAND
4546                                     ;SAVE REGISTERS AGAIN SO THAT COMPARES CAN BE DONE
4547
4548 023354 004037 041214          JSR    RO,2#SAVER    ;SAVE
4549 023360 001624          RHM   ;FROM
4550 023362 002070          MRFROM ;TO
4551 023364 000023          19.    ;NUMBER
4552
4553                                     ;AS UPPER BYTE OF RHAS CAN BE CHANGING IN A DUAL PORT
4554                                     ;OPERATION THE UPPER BYTE OF RHAS WILL BE SAVED AS IS
4555                                     ;SO THAT THE COMPARES ARE ONLY VALID FOR THE LOWER BYTE
4556 023366 113737 003161 002115          MOVB  2#REINTO+25,2#MRFROM+25;SAVE UPPER RHAS
4557
4558
4559                                     ;COMPARE REGISTERS BEFORE SEARCH WITH AFTER
4560
4561 023374 004037 041416          JSR    RO,2#COMPAR   ;COMPARE
4562 023400 003134          REINTO ;GO BUFFER

```



```

4576
4577
4578
4579
4580 023434 000004
4581 023436 012706 001000
4582 023442 004737 040522
4583 023446 012777 000001 156176
4584 023454 004037 043074
4585
4586
4587 023460 000000
4588

```

```

;*****
;#TEST 54 MAKE CURRENT CYLINDER = 0
;*****
TST54: SCOPE
MOV #STACK,SP ;RESET STACK
JSR PC, @#CLDISK ;INIT DRIVE
MOV #DMD, @RHMR ;SET DIAGNOSTIC MODE
JSR RO, @#MAKECYL ;SUBROUTINE TO GIVE A SEEK
;COMMAND FOLLOED BY A INIT
;THIS SHUOLD CHANGE RHCC
;CHANGE RHCC TO 0

```

4589
4590
4591
4592
4593
4594
4595
4596
4597
4598
4599
4600
4601
4602
4603
4604
4605
4606
4607
4608
4609
4610
4611
4612
4613
4614
4615
4616
4617
4618
4619
4620
4621
4622
4623
4624
4625
4626
4627
4628
4629
4630
4631
4632
4633
4634
4635
4636
4637
4638
4639
4640
4641
4642

.SBTTL READ/WRITE ADDRESSING VIA RMR

;TEST 55 WRITE HEADER AND DATA 1

;* WRITE CYLINDER 0, FORMAT 16 BIT PER WORD
;* TRACK 0, SECTOR 0, KEYS 0, NUMBER OF WORDS 256 OF 0'S
;* AS EVERYTHING IS ZERO THIS PROVES VERY LITTLE
;* ANY ERROR LOGIC INDICATIONS ARE NOT CONCLUSIVE ON FIRST PASS
;* BECAUSE ERROR LOGIC HAS NOT BEEN CHECKED YET (ON FIRST PASS)

1ST55: SCOPE
MOV #STACK, SP ;RESET STACK
MOV #TTNO, #TSTNM ;THIS SAVES TEST NUMBER
15: MOV #SECGAP, R0 ; POINTER
MOV #304, R1 ; COUNTER
MOV #-1, (R0)+ ; CLEAR "DISK" AREA TO ALL ONES
DEC R1
BNE 15
JSR PC, CLDISK ; THIS IS USED TO SET UP GENERAL
; REGISTER CORRESPONDENCE

;THESE ARE TO SET UP FOR DISKLESS USE ONLY

MOV #FMT22, #MNCYL ;FORMAT 22=16 BIT WORDS AND
;CYLINDER 0
CLR #MSECTR ;TRACK=0, SECTOR=0
CLR #MKEY1 ;KEY1=0
CLR #MKEY2 ;KEY2=0
MOV #256, #FNWORD ;256 DATAWORDS
JSR R5, #CRC ;GO TO CALCULATE CRC
MNCYL
GCRC

;THESE ARE REGULAR SETUPS FOR RH11 & "WRFROM" OUTPUT BUFFER

MOV #-260, #RHMC ;256 DATA WORDS 4 HEADER WORDS
MOV #WRFROM, R0 ;BUS ADDRESS TO BE
MOV R0, #RHB# ;BUFFER "WRFROM"
MOV #259, R5 ;COUNTER
MOV #FMT22, (R0)+ ;FORMAT =16 BIT WORD
25: CLR (R0)+ ;SECTOR=0, TRACK=0, KEYS=0, ALL DATA=0
DEC R5 ;& CYLINDER=0...SO CLEAR ALL "WRFROM"
BNE 25 ;CONTINUE IF ALL 259 NOT COMPLETE
CLR #RHDS# ;TRACK=0, SECTOR=0


```

4643 023622 004737 040556      JSR    PC, @#CHECKT      ;CHECK THAT DVA,ROY,DPR,DRY = 1
4644 023626 104400 057176      TYPE   ,CPHALT          ;AND THAT NO OTHERS = 1. CANNOT CON-
4645                                     ;TINUE TESTING IF BOTH AREN'T TRUE
4646 023632 000000                HALT                    ;STOP THE TEST
4647
4648 023634 013711 002046      MOV    @#WRIFOR, @R1    ;GET READY FOR WRITE HEADER
4649                                     ;AND DATA WITH 62 IN RHCS1
4650 023640 005037 001774      CLR    @#ERFLGS        ;CLEAR ERROR FLAG
4651 023644 012777 010000 155770  MOV    @#FMT22, @RHOF   ;FORMAT BIT=1 16 BIT WORDS
4652 023652 005077 155766      CLR    @#RHCA          ;CYLINDER 0
4653 023656 004737 050134      JSR    PC, @#COMMD     ;WRITE HEADER AND DATA FROM "WRFROM"
4654                                     ;INTO THE RHM REGISTER AND BACK INTO
4655                                     ;CORE "DISK" AREA
4656
4657                                     ;IF THE PROGRAM COMES BACK HERE WITHOUT ERROR
4658                                     ;PRINT OUTS FROM THE "COMMD" ROUTINE THAT MEANS
4659                                     ;ALL HEADER ON DISK IS GOOD IE. ONLY DATA IS
4660                                     ;TO BE CHECKED TO SEE IF IT IS ZERO
4661
4662                                     ;AND WRITE DATA GAP AND TOLERANCE GAP TO SEE IF THEY
4663                                     ;ARE ALL ZEROS (ECC1 AND ECC2 MAY NOT BE 0) AS WELL AS
4664                                     ;CHECKING RHMC FOR ZERO
4665
4666 023662 017767 155736 155236  MOV    @#RHMC, @BDDAT   ;MOVE WORD COUNTER INTO BAD DATA
4667 023670 001401                BEQ    55              ;SHOULD HAVE COUNTED UP TO ZERO
4668 023672 104040                ERROR  40              ;RHMC DID NOT = 0 AFTER A WRITE
4669                                     ;HEADER AND DATA
4670
4671 023674 005737 001774      55:   TST    @#ERFLGS     ;HAVE ANY ERRORS OCCURED?
4672 023700 001034                BNE    TST56          ;: BRANCH IF YES
4673 023702 004737 040746      JSR    PC, @#CHECKE    ;CHECK THAT BITS = 1
4674 023706 104400 057176      TYPE   ,CPHALT          ;CANNOT CONTINUE TESTING IF THEY DON'T
4675 023712 000000                HALT                    ;STOP THE TEST
4676 023714 005037 050070      CLR    @#MECC1        ;CLEAR ECC
4677 023720 005037 050072      CLR    @#MECC2
4678
4679                                     ;REINTO BUFFER IS FILLED WITH EXPECTED DATA OF ALL 0'S
4680
4681 023724 004037 040440      JSR    RO, @#CLAREA    ;CLEAR "REINTO"
4682 023730 003134                REINTO                  ;FROM
4683 023732 004174                REINTO+(272.*2)        ;TO
4684 023734 000000                .WORD 0                ;FILL WITH ZEROS
4685 023736 005037 001774      CLR    @#ERFLGS        ;CLEAR ERROR FLAG
4686
4687
4688                                     ;COMPARE "REINTO" BUFFER WITH "DISK" BUFFER
4689 023742 004037 041416      JSR    RO, @#COMPAR    ;CHECK
4690 023746 003134                REINTO                  ;GOOD BUFFER
4691 023750 047070                DISK                    ;TEST BUFFER
4692 023752 000421                273.                   ;NUMBER OF WORDS CHECKED
4693 023754 023762                35                     ;RETURN POINT FOR ERROR HEADER
4694 023756 023766                45                     ;RETURN POINT FOR ERROR DATA
4695 023760 023772                TST56                  ;RETURN FOR GOOD COMPARISON
4696 023762 104007      35:   ERROR  7          ;READ ERROR10 NEXT

```

4697 023764 000207
4698 023766 104010
4699
4700
4701
4702
4703 023770 000207
4704

4S: RTS PC
ERROR 10

RTS PC

:RETURN TO "COMPAR"
:WORD NOS 1 TO 256 ARE
:DATA WORDS
:257 AND 258 ARE ECC
:ZEROED OUT
:259 TO 273 TOLERANCE GAP


```

4705
4706
4707
4708
4709
4710
4711
4712
4713
4714
4715
4716 023772 000004
4717 023774 012706 001000
4718 024000 012737 000056 002020
4719
4720 024006 012700 046772
4721 024012 012701 000460
4722 024016 005020
4723 024020 005301
4724 024022 001375
4725 024024 004737 040522
4726
4727
4728
4729 024030 012737 010000 050274
4730
4731 024036 012737 000001 050276
4732 024044 005037 050300
4733 024050 005037 050302
4734 024054 012737 000400 050334
4735 024062 004537 041730
4736 024066 050274
4737 024070 050304
4738
4739
4740
4741 024072 012777 177374 155524
4742 024100 012700 002070
4743 024104 010077 155516
4744
4745 024110 012720 010000
4746
4747 024114 012720 000001
4748 024120 005020
4749 024122 005020
4750 024124 012705 000400
4751 024130 012720 177777
4752 024134 005305
4753 024136 001374
4754 024140 012777 000001 155470
4755
4756 024146 004737 040556
4757 024152 104400 057176
4758

```

```

;*****
;*TEST 56      WRITE HEADER AND DATA 2
;
;*      WRITE CYLINDER0, FORMAT 16 BITS PER WORD
;*      TRACK 0, SECTOR 1, KEYS 0, NUMBER OF WORDS 256
;*      OF ALL ONES.
;*      ANY ERROR LOGIC INDICATIONS ARE NOT CONCLUSIVE ON FIRST PASS
;*      BECAUSE ERROR LOGIC HAS NOT BEEN CHECKED YET (ON FIRST PASS)
;*****
TST56: SCOPE
MOV      #STACK,SP      ;RESET STACK
MOV      #TTNO,#TSTNM   ;THIS SAVES TEST NUMBER
;
MOV      #SECGAP,RO     ;POINTER
MOV      #304.,R1       ;COUNTER
1$: CLR   (RO)+          ;CLEAR SIMULATED "DISK" AREA IN CORE
DEC      R1
BNE     1$
JSR     PC,#CLDISK     ;THIS IS USED TO SET GENERAL REGISTERS
;
;THESE ARE TO BE SETUP FOR DISKLESS USE ONLY
MOV      #FMT22,#MAYL   ;FORMAT 22 = 16 BIT WORDS AND
;CYLINDER 0
MOV      #1,#MSECTR     ;TRACK=0, SECTOR=1
CLR      #KEY1          ;KEY1=0
CLR      #KEY2          ;KEY2=0
MOV      #256.,#FNWORD  ;256 DATA WORDS
JSR      R5,#CRC        ;GO TO CALCULATE CRC
MAYL
GCRC
;
;THESE ARE REGULAR SETUPS FOR THE RH11 AND "MRFROM" BUFFER
MOV      #-260.,#RHMC   ;256 DATA WORDS 4 HEADER WORDS
MOV      #MRFROM,RO     ;THESE TWO INSTRUCTIONS GETS
MOV      RO,#RHB#A     ;ADDR. OF MRFROM INTO RO AND
;BUS ADDRESS REGISTER
MOV      #FMT22,(RO)+  ;FORMAT=16 BIT WORDS
;CYLINDER=0
2$: MOV   #1,(RO)+     ;TRACK=0, SECTOR=1, KEYS=0
CLR   (RO)+          ;KEY1=0
CLR   (RO)+          ;KEY2=0
MOV   #256.,R5       ;COUNTER
3$: MOV   #-1,(RO)+   ;MOVE ALL ONES FOR DATA
DEC   R5
BNE   3$             ;BRANCH IF DATA NOT COMPLETE
MOV   #1,#RHDST     ;TRACK=0 SECTOR=1
;
JSR   PC,#CHECKT   ;CHECK THAT DVA,RDY,DPR,DRY = 1
TYPE ,CPHALT      ;AND THAT NO OTHERS = 1. CANNOT CON-
;TINUE TESTING IF BOTH AREN'T TRUE

```

```

4759 024156 000000 HALT ;STOP THE TEST
4760
4761 024160 013711 002046 MOV @#WRIFOR,@R1 ;GET READY FOR WRITE HEADER AND
4762 ;DATA WITH 62 IN RHCSI
4763 024164 005037 001774 CLR @#ERFLGS ;CLEAR ERROR FLAG
4764 024170 012777 010000 155444 MOV @#FMT22,@RHOF ;FORMAT BIT=1 (16 BIT WORDS)
4765 024176 005077 155442 CLR @RHCA ;CYLINDER =0
4766 024202 004737 050134 JSR PC,@#COMHND ;WRITE HEADER AND DATA INTO "DISK" AREA
4767
4768 ;IF THE PROGRAM COMES BACK HERE WITHOUT ERROR PRINTOUTS
4769 ;FROM THE "COMHND" ROUTINE THAT MEANS ALL THE HEADER ON "DISK"
4770 ;IS GOOD IE. ONLY THE DATA IS TO BE CHECKED TO SEE IF IT IS
4771 ;ALL ONES AND WRITE DATA GAP AND TOLERANCE GAP TO SEE IF
4772 ;THEY ARE ALL ZEROS, - ECC1 AND ECC2 ARE NOT CHECKED.
4773
4774 ;RHMC IS CHECKED TO BE = 0
4775
4776 024206 017767 155412 154712 MOV @RHMC,@BDDAT ;LOAD WORD COUNTER JUST IN CASE
4777 024214 001401 BEQ 6$ ;SHOULD BE = 0
4778 024216 104040 ERROR 40 ;RHMC DOES NOT = 0 AFTER A WRITE
4779 ;HEADER AND DATA IS COMPLETED
4780
4781 024220 005737 001774 6$: TST @#ERFLGS ;HAVE ANY ERRORS OCCURRED?
4782 024224 001041 BNE TST57 ;BRANCH IF YES
4783 024226 004737 040746 JSR PC,@#CHECKE ;CHECK THAT BITS = 1
4784 024232 104400 057176 TYPE ,CPHALT ;CANNOT CONTINUE TESTING IF THEY DON'T
4785 024236 000000 HALT ;STOP THE TEST
4786 024240 005037 050070 CLR @#ECC1 ;CLEAR ECC
4787 024244 005037 050072 CLR @#ECC2 ;CLEAR ECC
4788
4789 ;FILL "REINTO" BUFFER WITH EXPECTED DATA OF ALL 1'S
4790
4791 024250 004037 040440 JSR RO,@#CLAREA ;FILL REINTO BUFFER
4792 024254 003134 REINTO ;FROM
4793 024256 004132 REINTO+(255.*2) ;TO
4794 024260 177777 .WORD -1 ;DATA
4795 024262 004037 040440 JSR RO,@#CLAREA ;FILL REST
4796 024266 004134 REINTO+(256.*2) ;FROM
4797 024270 004174 REINTO+(272.*2) ;TO
4798 024272 000000 0 ;DATA
4799
4800 024274 005037 001774 CLR @#ERFLGS ;CLEAR ERROR FLAG
4801
4802 ;NOW COMPARE "DISK" BUFFER WITH "REINTO" BUFFER IN CORE
4803
4804 024300 004037 041416 JSR RO,@#COMPAR ;CHECK
4805 024304 003134 REINTO ;GOOD BUFFER
4806 024306 047070 DISK ;TEST BUFFER
4807 024310 000421 273. ;NUMBER OF WORDS CHECKED
4808 024312 024320 4$ ;RETURN POINT FOR ERROR HEADER
4809 024314 024324 5$ ;RETURN POINT FOR ERROR DATA
4810 024316 024330 TST57 ;RETURN FOR GOOD COMPARISON
4811 024320 104007 4$: ERROR 7 ;READ ERROR IO NEXT
4812 024322 000207 RTS PC ;RETURN TO COMPARE

```


B13

MAINDEC-11-DZRPS-D, RJPO4 DISKLESS RH11 TEST-PART 1
DZRPSD.P11 T56 WRITE HEADER AND DATA 2

MACY11 27(663) 8-OCT-75 16:14 PAGE 114

SEQ 0156

4813 024324 104010
4814
4815
4816
4817
4818
4819
4820
4821
4822 024326 000207
4823
4824
4825

55: ERROR 10

RTS PC

;WORD NOS 1 TO 256 ARE
;DATA WORDS
;WORD NOS 257 AND 258
;ARE ECC WHICH HAVE BEEN
;ZEROED
;WORD NOS 259
;IS DATA GAP
;WORD NOS 260 TO 273
;ARE TOLERANCE GAP
;RETURN TO COMPARE

```

4826
4827
4828 *****
4829 *TEST 57 WRITE HEADER AND DATA 3
4830 * WRITE CYLINDER 0 FORMAT 16 BITS PER WORD
4831 * TRACK 1, SECTOR 1, KEY 0, NUMBER OF WORDS 256
4832 * ALTERNATE ONES AND ZEROS (052525)
4833 * ANY ERROR LOGIC INDICATIONS ARE NOT CONCLUSIVE ON FIRST PASS
4834 * BECAUSE ERROR LOGIC HAS NOT BEEN CHECKED YET (ON FIRST PASS)
4835 *****
4836 024330 000004
4837 024332 012706 001000
4838 024336 012737 000057 002020
4839
4840 024344 012700 046772
4841 024350 012701 000460
4842 024354 012720 000377
4843 024360 005301
4844 024362 001374
4845 024364 004767 014132
4846
4847
4848
4849
4850 024370 012737 010000 050274
4851
4852 024376 012737 000401 050276
4853 024404 005037 050300
4854 024410 005037 050302
4855 024414 012737 000400 050334
4856 024422 004537 041730
4857 024426 050274
4858 024430 050304
4859
4860
4861
4862 024432 012777 177374 155164
4863 024440 012700 002070
4864
4865 024444 010077 155156
4866
4867 024450 012720 010000
4868
4869 024454 012720 000401
4870 024460 005020
4871 024462 005020
4872 024464 012705 000400
4873 024470 012720 052525
4874 024474 005305
4875 024476 001374
4876 024500 012777 000401 155130
4877
4878 024506 004737 040556
4879 024512 104400 057176

```

```

*****
*TEST 57 WRITE HEADER AND DATA 3
* WRITE CYLINDER 0 FORMAT 16 BITS PER WORD
* TRACK 1, SECTOR 1, KEY 0, NUMBER OF WORDS 256
* ALTERNATE ONES AND ZEROS (052525)
* ANY ERROR LOGIC INDICATIONS ARE NOT CONCLUSIVE ON FIRST PASS
* BECAUSE ERROR LOGIC HAS NOT BEEN CHECKED YET (ON FIRST PASS)
*****
†ST57: SCOPE
MOV #STACK,SP ;RESET STACK
MOV #ATTNO,†TSTNM ;THIS SAVES TEST NUMBER
MOV #SECCAP,RO ;POINTER
MOV #304,R1 ;COUNTER
15: MOV #377,(RO)+ ;LOAD SIMULATED "DISK" AREA WITH 377
DEC R1
BNE 15
JSR PC,CLDISK ;THIS IS USED TO SET UP GENERAL
;REGISTER CORRESPONDENCE
;THESE ARE TO BE SETUP FOR DISKLESS USE ONLY
MOV #FMT22,†MCYL ;FORMAT 22=16 BIT WORDS AND
;CYLINDER 0
MOV #401,†MSECTR ;TRACK=1, SECTOR=1
CLR †MKEY1 ;KEY1=0
CLR †MKEY2 ;KEY2=0
MOV #256,†FMWORD ;256 DATA WORDS
JSR R5,†CRC ;GO TO CALCULATE CRC
MCYL
GCRC
;THESE ARE REGULAR SETUPS FOR RH11 AND "MRFROM" BUFFER
MOV #-260,†RHMC ;256 DATA WORDS 4 HEADER WORDS
MOV #MRFROM,RO ;THESE TWO INSTRUCTIONS GET
;ADDR. OF MRFROM INTO RO
;AND BUS ADDRESS REGISTER
MOV RO,†RHBA
MOV #FMT22,(RO)+ ;FORMAT=16 BIT WORDS
;CYLINDER=0
25: MOV #401,(RO)+ ;TRACK=1, SECTOR=1, KEYS=0
CLR (RO)+ ;KEY1=0
CLR (RO)+ ;KEY2=0
MOV #256,R5 ;COUNTER
35: MOV #052525,(RO)+ ;MOVE ALTERNATE ONES FOR DATA
DEC R5 ;COUNT
BNE 35 ;BRANCH IF DATA NOT COMPLETE
MOV #401,†RHDS1 ;TRACK=1 SECTOR=1
JSR PC,†CHECKT ;CHECK THAT DVA,RDY,DPR,DRY = 1
TYPE ,CPHALT ;AND THAT NO OTHERS = 1. CANNOT CON-

```



```

4880                                     ;TINUE TESTING IF BOTH AREN'T TRUE
4881 024516 000000 HALT ;STOP THE TEST
4882
4883 024520 013711 002046 MOV @#RIFOR,@R1 ;GET READY FOR WRITE HEADER
4884 ;AND DATA WITH 62 IN RHCSI
4885 024524 005037 001774 CLR @#ERFLGS ;CLEAR ERROR FLAG
4886 024530 012777 010000 155104 MOV #FMT22,@RHOF ;FORMAT BIT=1(16 BIT WORDS)
4887 024536 005077 155102 CLR @RHCA ;CYLINDER=0
4888 024542 004737 050134 JSR PC,@#COMMHD ;WRITE HEADER AND DATA INTO "DISK" CORE
4889
4890 ;IF THE PROGRAM COMES BACK HERE WITHOUT ERROR PRINTOUTS
4891 ;FROM THE "COMMHD" ROUTINE THAT MEANS ALL HEADER IN
4892 ;"DISK" IS GOOD. DATA IS TO BE CHECKED TO SEE
4893 ;IF IT IS ALL 052525 AND WRITE DATA GAP AND
4894 ;TOLERANCE GAP TO SEE IF THEY ARE ALL ZEROS.
4895
4896 ;RHMC IS CHECKED TO BE = 0
4897
4898 024546 017767 155052 154352 MOV @RHMC,@SDDAT ;LOAD AND TEST FOR ZERO
4899 024554 001401 BEQ 65 ;RHMC SHOULD = 0
4900 024556 104040 ERROR 40 ;RHMC DID NOT = 0 AFTER A WRITE
4901 ;HEADER AND DATA WAS COMPLETED
4902
4903 ;ONLY ECC1 AND ECC2 ARE NOT CHECKED
4904
4905 024560 005737 001774 65: TST @#ERFLGS ;HAVE ANY ERRORS OCCURED?
4906 024564 001041 BNE TST60 ;BRANCH IF YES
4907 024566 004737 040746 JSR PC,@#CHECKE ;CHECK THAT BITS = 1
4908 024572 104400 057176 TYPE ,CPHALT ;CANNOT CONTINUE TESTING IF THEY DON'T
4909 024576 000000 HALT ;STOP THE TEST
4910 024600 005037 050070 CLR @#MECC1 ;CLEAR ECC
4911 024604 005037 050072 CLR @#MECC2 ;CLEAR ECC
4912
4913 ;FILL "REINTO" BUFFER WITH EXPECTED DATA
4914
4915 024610 004037 040440 JSR RD,@#CLAREA ;FILL REINTO BUFFER
4916 024614 003134 REINTO ;FROM
4917 024616 004132 REINTO+(255.*2) ;TO
4918 024620 052525 .WORD 52525 ;DATA
4919 024622 004037 040440 JSR RD,@#CLAREA ;FILL REST
4920 024626 004134 REINTO+(256.*2) ;FROM
4921 024630 004174 REINTO+(272.*2) ;TO
4922 024632 000000 .WORD 0 ;DATA
4923 024634 005037 001774 CLR @#ERFLGS ;CLEAR ERROR FLAG
4924
4925 ;NOW COMPARE "DISK" BUFFER WITH "REINTO" BUFFER IN CORE
4926
4927 024640 004037 041416 JSR RD,@#COMPAR ;CHECK
4928 024644 003134 REINTO ;GOOD BUFFER
4929 024646 047070 DISK ;TEST BUFFER
4930 024650 000421 273. ;NUMBER OF WORDS CHECKED
4931 024652 024660 45 ;RETURN POINT FOR ERROR HEADER
4932 024654 024664 55 ;RETURN POINT FOR ERROR DATA
4933 024656 024670 TST60 ;RETURN FOR GOOD COMPARISON

```

4934	024660	104007	4S:	ERROR	7
4935	024662	000207		RTS	PC
4936	024664	104010	5S:	ERROR	10
4937					
4938					
4939					
4940					
4941					
4942					
4943					
4944					
4945	024666	000207		RTS	PC
4946					
4947					
4948					
4949					

```

;READ ERROR 10 NEXT
;RETURN TO COMPARE
;WORD NOS 1 TO 256 ARE
;DATA WORDS
;WORD NOS 257 AND 258
;ARE ECC WHICH HAVE BEEN
;ZEROED
;WORD NOS 259
;IS DATA GAP
;WORD NOS 260 TO 273
;ARE TOLERANCE GAP
;RETURN TO COMPARE

```



```

4950
4951 ;*****
4952 ;*TEST 60 PROGRAM ERROR RHCS2 #10
4953
4954 ;* WRITE CYLINDER 0, FORMAT 16 BIT PER WORD
4955 ;* TRACK 0, SECTOR 0, KEYS 0, NUMBER OF WORDS 256 OF 0'S
4956 ;* WHILE GO BIT IS SET ANOTHER GO IS GIVEN THIS SHOULD SET
4957 ;* PROGRAM ERROR
4958
4959 ;*****
4960 024670 000004 TST60: SCOPE
4961 024672 012706 001000 MOV #STACK,SP ;RESET STACK
4962 024676 012737 000060 002020 MOV #TTNO,#TSTNM ;THIS SAVES TEST NUMBER
4963
4964 024704 012700 046772 MOV #SECGAP,R0 ;POINTER
4965 024710 012701 000460 MOV #304,R1 ;COUNTER
4966 024714 012720 177777 15: MOV #-1,(R0)+ ;CLEAR DISK AREA TO ALL ONES.
4967 024720 005301 DEC R1 ;
4968 024722 001374 BNE 15 ;
4969 024724 004767 013572 JSR PC,CLDISK ;THIS IS USED TO SET GENERAL
4970 ;REGISTERS
4971
4972 ;THESE ARE TO SET UP FOR DISKLESS USE ONLY
4973
4974 024730 012737 010000 050274 MOV #FMT22,#MCYL ;FORMAT 22=16 BITWORDS AND
4975 ;CYLINDER 0
4976 024736 005037 050276 CLR #MSECTR ;TRACK=0, SECTOR=0
4977 024742 005037 050300 CLR #MKEY1 ;KEY1=0
4978 024746 005037 050302 CLR #MKEY2 ;KEY2=0
4979 024752 012737 000400 050334 MOV #256,#FNWORD ;256 DATAWORDS
4980 024760 004537 041730 JSR RS,#CRC ;GO TO CALCULATE CRC
4981 024764 050274 MCYL
4982 024766 050304 GCRC
4983
4984 ;THESE ARE REGULAR SETUPS
4985
4986 024770 012777 177374 154626 MOV #-260,#RHMC ;256 DATA WORDS 4 HEADER WORDS
4987 024776 012700 002070 MOV #MRFROM,R0 ;FROM BUFFER "MRFROM"
4988 025002 010077 154620 MOV R0,RHBA ;IN BUS ADDRESS
4989 025006 012705 000403 MOV #259,R5 ;COUNTER
4990 025012 012720 010000 MOV #FMT22,(R0)+ ;FORMAT =16 BIT WORD
4991 ;CYLINDER=0
4992 025016 005020 25: CLR (R0)+ ;SECTOR=0, TRACK=0,KEYS=0, ALL DATA=0
4993 025020 005305 DEC R5 ;COUNT
4994 025022 001375 BNE 25 ;BRANCH IF ALL 259 NOT COMPLETE
4995 025024 005077 154606 CLR #RHDSST ;TRACK=0, SECTOR=0
4996
4997 025030 004737 040556 JSR PC,#CHECKT ;CHECK THAT DVA, RDY, DPR, DRY = 1
4998 025034 104400 057176 TYPE ,CPHALT ;AND THAT NO OTHERS = 1. CANNOT CON-
4999 ;TINUE TESTING IF BOTH AREN'T TRUE
5000 HALT ;STOP THE TEST
5001
5002 025042 013711 002046 MOV #MRFOR,R1 ;GET READY FOR WRITE HEADER
5003 ;AND DATA WITH 62 IN RHCSI

```

5004	025046	005037	001774		CLR	2#ERFLG\$;CLEAR ERROR FLAG
5005	025052	012777	010000	154562	MOV	#FMT22,2RHOF	;FORMAT BIT=1 16 BIT WORDS
5006	025060	005077	154560		CLR	2RHCA	;CYLINDER 0
5007	025064	012777	000001	154560	MOV	#DMD,2RHMR	;SET DIAGNOSTIC MODE
5008	025072	052777	000001	154532	BIS	#GO,2RHCS1	;GO
5009	025100	000240			NOP		
5010	025102	052777	000001	154522	BIS	#GO,2RHCS1	;THIS GO SHOULD SET PGE
5011							
5012	025110	004737	040126		JSR	PC,2#PUTREG	;SAVE REGISTERS
5013	025114	032737	002000	001702	BIT	#PGE,2#CS1	;IS PGE SET
5014	025122	001404			BEQ	35	;BRANCH IF GOOD
5015	025124	013737	001630	001122	MOV	2#RHCS2,2#SBDADR	
5016	025132	104037			ERROR	37	;PGE DID NOT SET WHEN A WRITE ;WAS ATTEMPTED WITH ONE IN PROGRESS
5017							
5018	025134						

35:

H13

5019
5020
5021
5022
5023
5024
5025
5026
5027
5028
5029
5030
5031
5032
5033
5034
5035
5036
5037
5038
5039
5040

```
*****  
:* THESE TESTS ARE THROUGH THE MAINTAINABILITY REGISTER - RHMR  
:*  
:* THE SECTOR GAP AND SYNC BYTE ARE ALWAYS READ AS  
:* ZEROS AND 144000 NO MATTER WHAT IS IN THE SIMULATED DISK AREA  
:* TAGGED SECGAP: AND MSSYNC:  
:*  
:* THE HEADER CONSISTING OF CYLINDER ADDRESS, SECTOR,  
:* TRACK AND THE KEYS ARE READ FROM LOCATION  
:* CYL: SECTOR: KEY1: AND KEY2 AND NOT FROM  
:* HEADER: ON SIMULATED DISK  
:*  
:* CRC IS READ FROM SIMULATED DISK LOCATION WCRC:  
:* HEADER GAP IS ALWAYS READ AS ZEROS NO MATTER  
:* WHAT IS ON THE SIMULATED DISK AREA  
:*  
:* THE DATA SYNC IS READ FROM HDMSYN:  
:* ON SIMULATED DISK  
:*  
:* ALL DATA IS READ FROM SIMULATED DISK DISK:  
*****
```

5041
5042
5043
5044
5045
5046
5047
5048
5049
5050
5051
5052
5053
5054
5055
5056
5057
5058
5059
5060
5061
5062
5063
5064
5065
5066
5067
5068
5069
5070
5071
5072
5073
5074
5075
5076
5077
5078
5079
5080
5081
5082
5083
5084
5085
5086
5087
5088
5089
5090
5091
5092
5093
5094

```
*****
*TEST 61      READ HEADER AND DATA 1
*            READ CYLINDER 0 FORMAT 16 BITS PER WORD
*            TRACK 0, SECTOR 0, KEYS 0, 256 WORDS OF 0
*            ANY ERROR LOGIC INDICATIONS ARE NOT CONCLUSIVE ON FIRST PASS
*            BECAUSE ERROR LOGIC HAS NOT BEEN CHECKED YET (ON FIRST PASS)
*****
```

```
†ST61:  SCOPE
        MOV      #STACK,SP      ;RESET STACK
        MOV      #ATTNO,‡#TSTNM ;THIS SAVES TEST NUMBER
```

```
;;      SETUP FOR WHAT IS TO BE READ
;;      HEADER CRC IS RESTORED FROM A SUBROUTINE
```

```
MOV      #0,-(SP)      ;DATA TO BE READ
MOV      #256.,R5      ;COUNTER
MOV      #DISK,R0      ;START OF SIMULATED DISK DATA
1$:      MOV      (SP),(R0)+  ;MOVE IN DATA ON TO SIMULATED DISK
        DEC      R5      ;COUNT
        BNE     1$      ;BRANCH IF 256 NOT COMPLETE
        TST     (SP)+   ;UNDO -(SP)
        MOV     #17.,R5 ;2 ECC WORDS
                        ;1 DATA GAP
                        ;14 TOLERANCE GAP
2$:      CLR      (R0)+   ;CLEAR ECC, DATA GAP, AND
        DEC     R5      ;TOLERANCE GAP
        BNE     2$      ;BRANCH IF NOT COMPLETE
```

```
;THESE ARE TO SETUP FOR DISKLESS USE ONLY
```

```
MOV      #0!FMT22,‡#CYL ;16 BITS PER WORD
                        ;CYLINDER 0, FORMAT 16 BITS
MOV      #0,‡#SECOTR+1 ;TRACK 0
MOV      #0,‡#SECOTR   ;SECTOR 0
MOV      #0,‡#KEY1     ;KEY1=0
MOV      #0,‡#KEY2     ;KEY2=0
MOV      #256.,‡#DAMWORD ;NO. OF DATA WORDS
CLR      ‡#X           ;THIS IS A READ COMMAND
JSR      R5,‡#CRC      ;GO TO CALCULATE CRC
CYL
MCRC
```

```
;THESE ARE REGULAR SETUPS FOR RH11 AND "REINTO"
```

```
JSR      PC,‡#CLDISK  ;SETUP GENERAL REGISTERS
MOV      #8-256.-4.,‡#RHMC ;256. DATA 4 HEADER WORDS
MOV      #REINTO,‡#RMB ;STARTING ADDRESS OF READ BUFFER
```



```

5095 025306 112746 000000          MOVB  #0, -(SP) ; IN LOWER BYTE GET SECTOR
5096 025312 112766 000000 000001  MOVB  #0, 1(SP) ; GET TRACK IN HIGHER BYTE
5097 025320 012677 154312          MOV   (SP)+, #RHDST ; TRACK/SECTOR IN RHDST
5098 025324 012777 014000 154310  MOV   #FMT22:ECI, #RHOF ; 16 BITS PER WORD
5099                                     ; ECC CORRECTION INHIBIT
5100                                     ; BECAUSE ECC IS NOT GOING
5101                                     ; TO BE CHECKED
5102 025332 005077 154306          CLR   #RHCA ; CYLINDER 0
5103
5104 025336 004737 040556          JSR   PC, #CHECKT ; CHECK THAT DVA, RDY, DPR, DRY = 1
5105 025342 104400 057176          TYPE ,CPHALT ; AND THAT NO OTHERS = 1. CANNOT CON-
5106                                     ; TINUE TESTING IF BOTH AREN'T TRUE
5107 025346 000000          HALT ; STOP THE TEST
5108
5109 025350 013711 002052          MOV   #REFOR, #R1 ; READ HEADER AND DATA=72
5110 025354 005037 001774          CLR   #ERFLGS ; CLEAR ERROR FLAG
5111 025360 004737 045026          JSR   PC, #COMHD ; READ HEADER AND DATA
5112
5113                                     ; IF THE PROGRAM COMES BACK HERE WITHOUT ERROR PRINTOUTS
5114                                     ; FROM THE "COMHD" ROUTINE THAT MEANS SECTOR GAP,
5115                                     ; FIRST SYNC, HEADER, HEADER CRC, HEADER GAP AND
5116                                     ; SYNC BYTE HAVE GONE BY AND SYNC'S WERE CORRECTLY
5117                                     ; DETECTED.
5118
5119
5120
5121                                     ; RHMC IS CHECKED TO BE = 0 AFTER THE READ OPERATION
5122
5123 025364 017767 154234 153534  MOV   #RHMC, #BDDAT ; LOAD AND TEST RHMC
5124 025372 001401          BEQ   20$ ; SHOULD = 0
5125 025374 104040          ERROR 40 ; RHMC DOES NOT = 0 AFTER A READ
5126                                     ; HEADER AND DATA
5127
5128                                     ; HEADER AND DATA ARE TO BE CHECKED.
5129                                     ; IN CHECKING READ DATA THE WRITE FROM BUFFER
5130                                     ; "MRFROM" IS FILLED WITH EXPECTED DATA AND
5131                                     ; COMPARISONS ARE MADE.
5132
5133 025376 005737 001774          20$: TST   #ERFLGS ; ANY ERRORS ALREADY THERE
5134 025402 001046          BNE   TST62 ; BRANCH IF YES
5135 025404 004737 040746          JSR   PC, #CHECKE ; CHECK THAT BITS = 1
5136 025410 104400 057176          TYPE ,CPHALT ; CANNOT CONTINUE TESTING IF THEY DON'T
5137 025414 000000          HALT ; STOP THE TEST
5138 025416 012700 002070          MOV   #MRFROM, #R0 ; GETTING READY TO FILL EXPECTED DATA
5139 025422 012720 010000          MOV   #0!FMT22, (R0)+ ; CYLINDER 0
5140 025426 112746 000000          MOVB  #0, -(SP) ; IN LOWER BYTE GET SECTOR
5141 025432 112766 000000 000001  MOVB  #0, 1(SP) ; GET TRACK IN HIGHER BYTE
5142 025440 012620          MOV   (SP)+, (R0)+ ; GET TRACK/SECTOR IN BUFFER
5143 025442 012720 000000          MOV   #0, (R0)+ ; KEY1 IN BUFFER
5144 025446 012720 000000          MOV   #0, (R0)+ ; KEY2 IN BUFFER
5145 025452 012701 000400          MOV   #256, #R1 ; DATA WORD COUNTER
5146 025456 012702 000000          MOV   #0, #R2 ; DATA
5147 025462 010220          3$: MOV   #R2, (R0)+ ; DATA INTO BUFFER
5148 025464 005301          DEC   #R1 ; COUNT

```

```

5149 025466 001375      BNE      3$          ;BRANCH IF 256 NOT DONE
5150
5151                      ;NOW READ DATA BUFFER WILL BE CHECKED
5152
5153 025470 004037 041416 JSR      RO,2#COMPAR ;CHECK
5154 025474 002070      MRFROM      ;GOOD BUFFER
5155 025476 003134      REINTO      ;TEST BUFFER
5156 025500 000404      4+256.     ;NUMBER OF WORDS CHECKED
5157 025502 025510      4$         ;RETURN POINT FOR ERROR HEADER
5158 025504 025514      5$         ;RETURN POINT FOR ERROR DATA
5159 025506 025520      TST62      ;RETURN FOR GOOD COMPARISON
5160 025510 104004      4$:      ERROR 4    ;READ NEXT ERROR
5161 025512 000207      RTS      PC    ;RETURN TO "COMPAR"
5162 025514 104005      5$:      ERROR 5    ;WORD NOS 1 TO 4 ARE
5163                      ;HEADER WORDS
5164                      ;5 TO 260 ARE DATA WORDS
5165 025516 000207      RTS      PC    ;RETURN TO "COMPAR"
5166
5167
5168
5169
5170

```



```

5171
5172 ;*****
5173 ;*TEST 62 READ HEADER AND DATA 2
5174 ;* READ CYLINDER 0 FORMAT 16 BITS PER WORD
5175 ;* TRACK 0, SECTOR 1, KEYS 0, 256 WORDS OF 177777
5176 ;* ANY ERROR LOGIC INDICATIONS ARE NOT CONCLUSIVE ON FIRST PASS
5177 ;* BECAUSE ERROR LOGIC HAS NOT BEEN CHECKED YET (ON FIRST PASS)
5178 ;*****
5179
5180 025520 000004
5181 025522 012706 001000
5182 025526 012737 000062 002020
5183
5184
5185 ;
5186 ; SETUP FOR WHAT IS TO BE READ
5187 ; HEADER CRC IS RESTORED FROM A SUBROUTINE
5188 025534 012746 177777
5189 025540 012705 000400
5190 025544 012700 047070
5191 025550 011620
5192 025552 005305
5193 025554 001375
5194 025556 005726
5195 025560 012705 000021
5196
5197
5198 025564 005020
5199 025566 005305
5200 025570 001375
5201
5202
5203 ;
5204 ; THESE ARE TO SETUP FOR DISKLESS USE ONLY
5205 025572 012737 010000 045152
5206
5207 025600 112737 000000 045155
5208 025606 112737 000001 045154
5209 025614 012737 000000 045156
5210 025622 012737 000000 045160
5211 025630 012737 000400 045232
5212 025636 005037 045162
5213 025642 004537 041730
5214 025646 045152
5215 025650 047052
5216
5217 ;
5218 ; THESE ARE REGULAR SETUPS FOR RH11 AND "REINTO"
5219 025652 004737 040522
5220 025656 012777 177374 153740
5221 025664 012777 003134 153734
5222 025672 112746 000001
5223 025676 112766 000000 000001
5224 025704 012677 153726

```

```

;*****
;*TEST 62 READ HEADER AND DATA 2
;* READ CYLINDER 0 FORMAT 16 BITS PER WORD
;* TRACK 0, SECTOR 1, KEYS 0, 256 WORDS OF 177777
;* ANY ERROR LOGIC INDICATIONS ARE NOT CONCLUSIVE ON FIRST PASS
;* BECAUSE ERROR LOGIC HAS NOT BEEN CHECKED YET (ON FIRST PASS)
;*****

†ST62: SCOPE
MOV #STACK,SP ;RESET STACK
MOV #TTNO,†TSTNM ;THIS SAVES TEST NUMBER

;
; SETUP FOR WHAT IS TO BE READ
; HEADER CRC IS RESTORED FROM A SUBROUTINE

MOV #-1,-(SP) ;DATA TO BE READ
MOV #256.,R5 ;COUNTER
MOV #DISK,RO ;START OF SIMULATED DISK DATA
1$: MOV (SP),(RO)+ ;MOVE IN DATA ON TO SIMULATED DISK
DEC R5 ;COUNT
BNE 1$ ;BRANCH IF 256 NOT COMPLETE
TST (SP)+ ;UNDO -(SP)
MOV #17.,R5 ;2 ECC WORDS
;1 DATA GAP
;14 TOLERANCE GAP
2$: CLR (RO)+ ;CLEAR ECC, DATA GAP, AND
DEC R5 ;TOLERANCE GAP
BNE 2$ ;BRANCH IF NOT COMPLETE

; THESE ARE TO SETUP FOR DISKLESS USE ONLY
MOV #0:FMT22,†CYL ;16 BITS PER WORD
;CYLINDER 0, FORMAT 16 BITS
MOV #0,†SECOTR+1 ;TRACK 0
MOV #1,†SECOTR ;SECTOR 1
MOV #0,†KEY1 ;KEY1=0
MOV #0,†KEY2 ;KEY2=0
MOV #256.,†DAMWORD ;NO. OF DATA WORDS
CLR †X ;THIS IS A READ COMMAND
JSR R5,†CRC ;GO TO CALCULATE CRC
CYL
MCRC

; THESE ARE REGULAR SETUPS FOR RH11 AND "REINTO"
JSR PC,†CLDISK ;SETUP GENERAL REGISTERS
MOV #-256.-4,†RHMC ;256. DATA 4 HEADER WORDS
MOV #REINTO,†RHBA ;STARTING ADDRESS OF READ BUFFER
MOV #1,-(SP) ;IN LOWER BYTE GET SECTOR
MOV #0,1(SP) ;GET TRACK IN HIGHER BYTE
MOV (SP)+,†RHDST ;TRACK/SECTOR IN RHDST

```

```

5225 025710 012777 014000 153724      MOV      #FMT22!ECI,ARHOF      ;16 BITS PER WORD
5226                                     ;ECC CORRECTION INHIBIT
5227                                     ;BECAUSE ECC IS NOT GOING
5228                                     ;TO BE CHECKED
5229 025716 005077 153722      CLR      ARHCA                ;CYLINDER 0
5230
5231 025722 004737 040556      JSR      PC,AR#CHECKT        ;CHECK THAT DVA,RDY,DPR,DRY = 1
5232 025726 104400 057176      TYPE    ,CPHALT              ;AND THAT NO OTHERS = 1. CANNOT CON-
5233                                     ;TINUE TESTING IF BOTH AREN'T TRUE
5234 025732 000000                HALT                          ;STOP THE TEST
5235
5236 025734 013711 002052      MOV      AR#REFOR,AR1        ;READ HEADER AND DATA=72
5237 025740 005037 001774      CLR      AR#ERFLG$           ;CLEAR ERROR FLAG
5238 025744 004737 045026      JSR      PC,AR#COMHD         ;READ HEADER AND DATA
5239
5240
5241                                     ;IF THE PROGRAM COMES BACK HERE WITHOUT ERROR PRINTOUTS
5242                                     ;FROM THE "COMHD" ROUTINE THAT MEANS SECTOR GAP,
5243                                     ;FIRST SYNC, HEADER, HEADER CRC, HEADER GAP AND
5244                                     ;SYNC BYTE HAVE GONE BY AND SYNC'S WERE CORRECTLY
5245                                     ;DETECTED.
5246
5247
5248                                     ;RHMC IS CHECKED TO BE = 0 AFTER THE READ OPERATION
5249
5250 025750 017767 153650 153150    MOV      AR#RHMC,SBDDAT      ;LOAD AND TEST RHMC
5251 025756 001401                BEQ      20$                  ;SHOULD = 0
5252 025760 104040                ERROR   40                    ;RHMC DOES NOT = 0 AFTER A READ
5253                                     ;HEADER AND DATA
5254
5255                                     ;HEADER AND DATA ARE TO BE CHECKED.
5256                                     ;IN CHECKING READ DATA THE WRITE FROM BUFFER
5257                                     ;"WRFROM" IS FILLED WITH EXPECTED DATA AND
5258                                     ;COMPARISONS ARE MADE.
5259
5260 025762 005737 001774          20$:  TST      AR#ERFLG$           ;ANY ERRORS ALREADY THERE
5261 025766 001046                BNE     TST63                 ;BRANCH IF YES
5262 025770 004737 040746      JSR      PC,AR#CHECKE        ;CHECK THAT BITS = 1
5263 025774 104400 057176      TYPE    ,CPHALT              ;CANNOT CONTINUE TESTING IF THEY DON'T
5264 026000 000000                HALT                          ;STOP THE TEST
5265 026002 012700 002070      MOV      #WRFROM,RO          ;GETTING READY TO FILL EXPECTED DATA
5266 026006 012720 010000      MOV      #0!FMT22,(RO)+      ;CYLINDER 0
5267 026012 112746 000001      MOVB    #1,-(SP)              ;IN LOWER BYTE GET SECTOR
5268 026016 112766 000000 000001  MOVB    #0,1(SP)              ;GET TRACK IN HIGHER BYTE
5269 026024 012620                MOV     (SP)+,(RO)+           ;GET TRACK/SECTOR IN BUFFER
5270 026026 012720 000000      MOV     #0,(RO)+              ;KEY1 IN BUFFER
5271 026032 012720 000000      MOV     #0,(RO)+              ;KEY2 IN BUFFER
5272 026036 012701 000400      MOV     #256,R1               ;DATA WORD COUNTER
5273 026042 012702 177777      MOV     #-1,R2                ;DATA
5274 026046 010220          3$:  MOV     R2,(RO)+              ;DATA INTO BUFFER
5275 026050 005301                DEC     R1                     ;COUNT
5276 026052 001375                BNE     3$                    ;BRANCH IF 256 NOT DONE
5277
5278                                     ;NOW READ DATA BUFFER WILL BE CHECKED

```



```

5297
5298
5299
5300
5301
5302
5303
5304
5305
5306 026104 000004
5307 026106 012706 001000
5308 026112 012737 000063 002020
5309
5310
5311
5312
5313
5314 026120 012746 052525
5315 026124 012705 000400
5316 026130 012700 047070
5317 026134 011620
5318 026136 005305
5319 026140 001375
5320 026142 005726
5321 026144 012705 000021
5322
5323
5324 026150 005020
5325 026152 005305
5326 026154 001375
5327
5328
5329
5330
5331 026156 012737 010000 045152
5332
5333 026164 112737 000001 045155
5334 026172 112737 000001 045154
5335 026200 012737 000000 045156
5336 026206 012737 000000 045160
5337 026214 012737 000400 045232
5338 026222 005037 045162
5339 026226 004537 041730
5340 026232 045152
5341 026234 047052
5342
5343
5344
5345 026236 004737 040522
5346 026242 012777 177374 153354
5347 026250 012777 003134 153350
5348 026256 112746 000001
5349 026262 112766 000001 000001
5350 026270 012677 153342

```

```

*****
;TEST 63 READ HEADER AND DATA 3
; READ CYLINDER 0 FORMAT 16 BITS PER WORD
; TRACK 1, SECTOR 1, KEYS 0, 256 WORDS OF 052525
; ANY ERROR LOGIC INDICATIONS ARE NOT CONCLUSIVE ON FIRST PASS
; BECAUSE ERROR LOGIC HAS NOT BEEN CHECKED YET (ON FIRST PASS)
*****
†ST63: SCOPE
MOV #STACK,SP ;RESET STACK
MOV #TTNO,@#TSTNM ;THIS SAVES TEST NUMBER

;
; SETUP FOR WHAT IS TO BE READ
; HEADER CRC IS RESTORED FROM A SUBROUTINE

MOV #052525,-(SP) ;DATA TO BE READ
MOV #256.,R5 ;COUNTER
MOV #DISK,R0 ;START OF SIMULATED DISK DATA
1$: MOV (SP),(R0)+ ;MOVE IN DATA ON TO SIMULATED DISK
DEC R5 ;COUNT
BNE 1$ ;BRANCH IF 256 NOT COMPLETE
TST (SP)+ ;UNDO -(SP)
MOV #17.,R5 ;2 ECC WORDS
;1 DATA GAP
;14 TOLERANCE GAP
2$: CLR (R0)+ ;CLEAR ECC, DATA GAP, AND
DEC R5 ;TOLERANCE GAP
BNE 2$ ;BRANCH IF NOT COMPLETE

;THESE ARE TO SETUP FOR DISKLESS USE ONLY
MOV #0!FMT22,@#CYL ;16 BITS PER WORD
;CYLINDER 0, FORMAT 16 BITS
MOVB #1,@#SECOTR+1 ;TRACK 1
MOVB #1,@#SECOTR ;SECTOR 1
MOV #0,@#KEY1 ;KEY1=0
MOV #0,@#KEY2 ;KEY2=0
MOV #256.,@#DANORD ;NO. OF DATA WORDS
CLR @#X ;THIS IS A READ COMMAND
JSR R5,@#CRC ;GO TO CALCULATE CRC
CYL
MCRC

;THESE ARE REGULAR SETUPS FOR RH11 AND "REINTO"
JSR PC,@#CLDISK ;SETUP GENERAL REGISTERS
MOV #-256.-4.,@#RHMC ;256. DATA 4 HEADER WORDS
MOV #REINTO,@#RABA ;STARTING ADDRESS OF READ BUFFER
MOVB #1,-(SP) ;IN LOWER BYTE GET SECTOR
MOVB #1,1(SP) ;GET TRACK IN HIGHER BYTE
MOV (SP)+,@#RHDST ;TRACK/SECTOR IN RHDST

```



```

5351 026274 012777 014000 153340      MOV      #FMT22!ECI, @RHOF      ; 16 BITS PER WORD
5352                                     ; ECC CORRECTION INHIBIT
5353                                     ; BECAUSE ECC IS NOT GOING
5354                                     ; TO BE CHECKED
5355 026302 005077 153336      CLR      @RHCA                  ; CYLINDER 0
5356
5357 026306 004737 040556      JSR      PC, @#CHECKT          ; CHECK THAT DVA, RDY, DPR, DRY = 1
5358 026312 104400 057176      TYPE    ,CPHALT                ; AND THAT NO OTHERS = 1. CANNOT CON-
5359                                     ; TINUE TESTING IF BOTH AREN'T TRUE
5360 026316 000000      HALT
5361                                     ; STOP THE TEST
5362 026320 013711 002052      MOV      @#REFOR, @R1          ; READ HEADER AND DATA=72
5363 026324 005037 001774      CLR      @#ERFLG$              ; CLEAR ERROR FLAG
5364 026330 004737 045026      JSR      PC, @#COMHD           ; READ HEADER AND DATA
5365
5366
5367                                     ; IF THE PROGRAM COMES BACK HERE WITHOUT ERROR PRINTOUTS
5368                                     ; FROM THE "COMHD" ROUTINE THAT MEANS SECTOR GAP,
5369                                     ; FIRST SYNC, HEADER, HEADER CRC, HEADER GAP AND
5370                                     ; SYNC BYTE HAVE GONE BY AND SYNC'S WERE CORRECTLY
5371                                     ; DETECTED.
5372
5373
5374                                     ; RHMC IS CHECKED TO BE = 0 AFTER THE READ OPERATION
5375
5376 026334 017767 153264 152564      MOV      @RHMC, @BDDAT         ; LOAD AND TEST RHMC
5377 026342 001401      BEQ      20$                    ; SHOULD = 0
5378 026344 104040      ERROR   40                      ; RHMC DOES NOT = 0 AFTER A READ
5379                                     ; HEADER AND DATA
5380
5381                                     ; HEADER AND DATA ARE TO BE CHECKED.
5382                                     ; IN CHECKING READ DATA THE WRITE FROM BUFFER
5383                                     ; "WRFROM" IS FILLED WITH EXPECTED DATA AND
5384                                     ; COMPARISONS ARE MADE.
5385
5386 026346 005737 001774      20$:  TST      @#ERFLG$             ; ANY ERRORS ALREADY THERE
5387 026352 001046      BNE      TST64                   ; BRANCH IF YES
5388 026354 004737 040746      JSR      PC, @#CHECKE          ; CHECK THAT BITS = 1
5389 026360 104400 057176      TYPE    ,CPHALT                ; CANNOT CONTINUE TESTING IF THEY DON'T
5390 026364 000000      HALT                            ; STOP THE TEST
5391 026366 012700 002070      MOV      @WRFROM, @R0          ; GETTING READY TO FILL EXPECTED DATA
5392 026372 012720 010000      MOV      @0!FMT22, (@R0)+      ; CYLINDER 0
5393 026376 112746 000001      MOV      @1, -(SP)              ; IN LOWER BYTE GET SECTOR
5394 026402 112766 000001 000001      MOV      @1, 1(SP)              ; GET TRACK IN HIGHER BYTE
5395 026410 012620      MOV      (SP)+, (@R0)+          ; GET TRACK/SECTOR IN BUFFER
5396 026412 012720 000000      MOV      @0, (@R0)+            ; KEY1 IN BUFFER
5397 026416 012720 000000      MOV      @0, (@R0)+            ; KEY2 IN BUFFER
5398 026422 012701 000400      MOV      @256, @R1              ; DATA WORD COUNTER
5399 026426 012702 052525      MOV      @052525, @R2          ; DATA
5400 026432 010220      3$:  MOV      @R2, (@R0)+            ; DATA INTO BUFFER
5401 026434 005301      DEC      @R1                    ; COUNT
5402 026436 001375      BNE      3$                      ; BRANCH IF 256 NOT DONE
5403
5404                                     ; NOW READ DATA BUFFER WILL BE CHECKED

```



```

5423
5424 ;*****
5425 ;*TEST 64 WRITE DATA
5426
5427 ;* WRITE CYLINDER 0, FORMAT 16 BITS PER WORD
5428 ;* TRACK 0, SECTOR 0, KEYS 0, NUMBER OF WORDS 256 OF 377
5429 ;* ANY ERROR LOGIC INDICATION IS NOT CONCLUSIVE ON FIRST PASS
5430 ;* BECAUSE ERROR LOGIC HAS NOT BEEN CHECKED
5431
5432 ;*****
5433 026470 000004 TST64: SCOPE
5434
5435 026472 012737 000064 002020 MOV #TTNO,@#TSTNM ;THIS SAVES TEST NUMBER
5436
5437
5438 026500 012706 001000 MOV #STACK,SP ;RESET STACK
5439 026504 012737 000064 002020 MOV #TTNO,@#TSTNM ;THIS SAVES TEST NUMBER
5440
5441 026512 004037 040440 JSR RD,@#CLAREA ;CLEAR SIMULATED DISK
5442 026516 047070 .WORD DISK ;FROM
5443 026520 050114 .WORD TOLGAP+16 ;TO
5444 026522 000000 .WORD 0 ;DATA
5445
5446 ;THESE ARE SETUP FOR DISKLESS USE ONLY
5447
5448 026524 012737 010000 045152 MOV #0!FMT22,@#CYL;CYLINDER 0
5449 ;16 BITS PER WORD
5450 026532 112737 000000 045155 MOVB #0,@#SECOTR+1;TRACK 0
5451 026540 112737 000000 045154 MOVB #0,@#SECOTR ;SECTOR 0
5452 026546 005037 045156 CLR @#KEY1 ;KEY1 0
5453 026552 005037 045160 CLR @#KEY2 ;KEY2 0
5454 026556 012737 000400 045220 MOV #256,@#NOWORD ;NO OF DATA WORDS
5455 026564 012737 000001 045162 MOV #1,@#X ;WRITE DATA
5456 026572 004537 041730 JSR RS,@#CRC ;GO TO CALCULATE CRC
5457 026576 045152 CYL
5458 026600 047052 MCRC
5459
5460 ;THESE ARE REGULAR SETUPS
5461
5462
5463 026602 004037 040440 JSR RD,@#CLAREA ;FILL WRITE BUFFER WITH 377
5464 026606 002070 MRFROM ;FROM LOCATION
5465 026610 003070 MRFROM+(256.*2) ;TO LOCATION
5466 026612 000377 377 ;DATA
5467 026614 004737 040522 JSR PC,@#CLDISK ;SETUP GENERAL REGISTERS
5468 026620 012777 177400 152776 MOV #-256,@#RMC ;256. DATA WORDS
5469 026626 012777 002070 152772 MOV #MRFROM,@#RBA ;STARTING ADDRESS OF WRITE BUFFER
5470 026634 012746 000000 MOV #0,-(SP) ;SECTOR 0
5471 026640 112766 000000 000001 MOVB #0,1(SP) ;TRACK 0
5472 026646 012677 152764 MOV (SP)+,@#RDST ;SECTOR 0 TRACK 0
5473 026652 012777 010000 152762 MOV #FMT22,@#RHF ;16 BITS PER WORD FORMAT
5474 026660 012777 000000 152756 MOV #0,@#RHC ;CYLINDER 0
5475 026666 004737 040556 JSR PC,@#CHECKT ;CHECK THAT DVA,RDY,DPR,DRY = 1
5476 026672 104400 057176 TYPE ,CPHALT ;AND THAT NO OTHERS = 1. CANNOT CON-

```

```

5477                                     ;TINUE TESTING IF BOTH AREN'T TRUE
5478 026676 000000                       HALT                                     ;STOP THE TEST
5479 026700 013711 002044                MOV      @#MRIDAT,@R1                    ;WRITE DATA=60
5480 026704 005037 001774                CLR      @#ERFLGS                       ;CLEAR ERROR FLAG
5481 026710 004737 045026                JSR      PC,@#COMHD                     ;WRITE DATA
5482
5483
5484                                     ;IF THE PROGRAM COMES BACK HERE WITHOUT ERROR PRINTOUTS
5485                                     ;FROM THE "COMHD" ROUTINE IT MEANS SECTOR GAP, SYNC BYTE
5486                                     ;HEADER, HEADER CRC, HEADER GAP AND SYNC BYTE HAVE GONE BY
5487                                     ;AND SYNC'S WERE CORRECTLY DETECTED, DATA IS TO BE CHECKED.
5488
5489 026714 004737 040126                   JSR      PC,@#PUTREG                    ;SAVE REGISTERS
5490 026720 005737 001774                   TST      @#ERFLGS                      ;HAVE ANY ERRORS OCCURED?
5491 026724 001041                               BNE      TST65                          ;BRANCH IF YES
5492 026726 012700 000377                   MOV      #377,R0                       ;GOOD DATA
5493 026732 012701 047070                   MOV      @#DISK,R1                     ;DATA WRITTEN INTO "DISK"
5494 026736 012702 000400                   MOV      #256.,R2                      ;COUNTER
5495
5496 026742 012737 000401 045272 1$:        MOV      #256.+1,@#ERMWORD             ;FOR ERROR WORD
5497 026750 020021                               CMP      R0,(R1)+                      ;COMPARE GOOD DATA WITH DATA ON DISK
5498 026752 001424                               BEQ      3$                             ;BRANCH IF GOOD
5499 026754 010037 001124                   MOV      R0,@#SGDDAT                   ;GOOD DATA
5500 026760 014137 001126                   MOV      -(R1),@#SBODAT                ;BAD DATA
5501 026764 160237 045272                   SUB      R2,@#ERMWORD                   ;ERROR WORD NO
5502 026770 005737 001774                   TST      @#ERFLGS                      ;ANY ERRORS ALREADY THERE?
5503 026774 001002                               BNE      2$                             ;BRANCH IF YES
5504 026776 104004                               ERROR   4                               ;ERROR ON WRITE DATA COMMAND
5505 027000 000401                               BR      64$                             ;BRANCH TO AVOID PRINTING NEXT ERROR
5506
5507 027002 104005                               2$:   ERROR   5                               ;WORD NO GIVES WORD IN ERROR
5508 027004 005721                               64$:  TST      (R1)+                      ;UNDO -(R1) FOR BAD DATA
5509 027006 013746 177570                   MOV      @#SWR,-(SP)                   ;GET SWITCH SETTING
5510 027012 042716 177177                   BIC      #177177,(SP)                   ;KEEP ONLY SWITCH 7 AND 8
5511 027016 022726 000200                   CMP      #SM07,(SP)+                   ;IS 7 SET AND 8 RESET
5512 027022 001402                               BEQ      TST65                          ;BRANCH OUT IF YES
5513 027024 005302                               3$:   DEC      R2                       ;IF NOT COUNT 256 WORDS
5514 027026 001345                               BNE      1$                             ;BRANCH IF 256. NOT DONE
5515
5516
5517
5518

```



```

5519
5520 ;*****
5521 ;*TEST 65      READ DATA
5522
5523 ;*      READ CYLINDER0, FORMAT 16 BITS PER WORD
5524 ;*      TRACK0, SECTOR 1, KEYS 0, 10 WORDS OF 177400
5525 ;*      ANY ERROR LOGIC INDICATION IS NOT CONCLUSIVE
5526 ;*      BECAUSE ERROR LOGIC HAS NOT BEEN CHECKED YET (ON FIRST PASS)
5527
5528 ;*****
5529 027030 000004      †ST65: SCOPE
5530 027032 012706 001000      MOV      #STACK,SP      ;RESET STACK
5531
5532 027036 012737 000065 002020      MOV      #TTNO,#TSTNM   ;THIS SAVES TEST NUMBER
5533 027044 004037 040440      JSR      RD,#CLAREA     ;CLEAR SIMULATED DISK
5534 027050 047070      .WORD   DISK            ;FROM
5535 027052 050066      .WORD   DISK+776       ;TO
5536 027054 177400      .WORD   177400         ;DATA
5537
5538 027056 004037 040440      JSR      RD,#CLAREA     ;CLEAR READ INTO BUFFER
5539 027062 003134      .WORD   REINTO         ;FROM
5540 027064 004132      .WORD   REINTO+776     ;TO
5541 027066 000000      .WORD   0              ;DATA
5542
5543 ;THESE ARE TO SETUP FOR DISKLESS USE ONLY
5544
5545 027070 012737 010000 045152      MOV      #FMT22,#CYL    ;CYLINDER 0 16 BITS PER WORD FORMAT
5546 027076 105037 045155      CLR      #SECTOR+1     ;TRACK 0
5547 027102 112737 000001 045154      MOV      #1,#SECTOR    ;SECTOR 1
5548 027110 005037 045156      CLR      #KEY1         ;KEY1=0
5549 027114 005037 045160      CLR      #KEY2         ;KEY2=0
5550 027120 012737 000012 045232      MOV      #10,#DAMWORD  ;NO. OF DATA WORDS
5551 027126 005037 045162      CLR      #X            ;THIS IS A READ COMMAND
5552 027132 004537 041730      JSR      RS,#CRC        ;GO TO CALCULATE CRC
5553 027136 045152      CYL
5554 027140 047052      MCRC
5555
5556 ;THESE ARE REGULAR SETUPS
5557
5558 027142 004737 040522      JSR      PC,#CLDISK    ;SETUP GENERAL REGISTERS
5559 027146 013711 002050      MOV      #READAT,#R1   ;READ DATA INTO RHCS1=70
5560 027152 012777 177766 152444      MOV      #-10,#RMC     ;10 DATA WORDS
5561 027160 012777 003134 152440      MOV      #REINTO,#RHA  ;STARTING ADDRESS OF READ BUFFER
5562 027166 112746 000001      MOV      #1,-(SP)      ;IN LOWER BYTE GET SECTOR 1
5563 027172 112766 000000 000001      MOV      #0,1(SP)     ;GET TRACK0 IN UPPER BYTE
5564 027200 012677 152432      MOV      (SP)+,#RHDST  ;TRACK/SECTOR IN RHDST
5565 027204 012777 014000 152430      MOV      #FMT22!ECI,#RHOF ;16 BITS PER WORD
5566 ;ECC CORRECTION INHIBIT BECAUSE
5567 ;ECC IS NOT CHECKED HERE
5568 027212 005077 152426      CLR      #RHA         ;CYLINDER 0
5569 027216 004737 040556      JSR      PC,#CHECKT    ;CHECK THAT DVA,RDY,DPR,DRY = 1
5570 027222 104400 057176      TYPE    ,CPHALT       ;AND THAT NO OTHERS = 1. CANNOT CON-
5571 ;TINUE TESTING IF BOTH AREN'T TRUE
5572 027226 000000      HALT                  ;STOP THE TEST

```

822

```

5573 027230 005037 001774 CLR @#ERFLGS ;CLEAR ERROR FLAG
5574 027234 004737 045026 JSR PC,@#COMHD ;READ DATA
5575
5576 ;IF THE PROGRAM COMES BACK HERE WITHOUT ERROR PRINTOUT
5577 ;FROM "COMHD" ROUTINE IN MEANS DATA IS TO BE CHECKED
5578
5579 ;NOW THE DATA READ INTO "REINTO" BUFFER WILL
5580 ;BE CHECKED, ONLY 10 WORDS SHOULD BE CHANGED
5581 ;ALL OTHER WORDS SHOULD REMAIN UNCHANGED
5582 ;THE "MRFROM" BUFFER IS FILLED WITH EXPECTED DATA AND CHECKED
5583
5584 027240 005737 001774 TST @#ERFLGS ;HAVE ANY ERRORS OCCURED?
5585 027244 001053 BNE TST66 ;BRANCH IF YES
5586 027246 004037 040440 JSR RO,@#CLAREA ;CLEAR BUFFER
5587 027252 002070 MRFROM ;FROM
5588 027254 003066 MRFROM+776 ;TO
5589 027256 000000 0 ;DATA
5590
5591 027260 004037 040440 JSR RO,@#CLAREA ;FILL EXPECTED DATA
5592 027264 002070 MRFROM ;FROM
5593 027266 002112 MRFROM+22 ;TO
5594 027270 177400 177400 ;DATA
5595
5596 ;NOW READ DATA BUFFER IS CHECKED
5597
5598 027272 012700 002070 MOV #MRFROM,RO ;GOOD DATA
5599 027276 012701 003134 MOV #REINTO,R1 ;DATA READ
5600 027302 012702 000400 MOV #256,R2 ;COUNTER
5601 027306 012737 000401 045272 15: MOV #257,@#ERNORD ;FOR ERROR WORD NO
5602 027314 022021 CMP (RO)+,(R1)+ ;COMPARE GOOD WITH READ BUFFER
5603 027316 001424 BEQ 25 ;BRANCH IF GOOD
5604 027320 014037 001124 MOV -(RO),@#SGDDAT ;GOOD DATA
5605 027324 014137 001126 MOV -(R1),@#SBDDAT ;BAD DATA
5606 027330 160237 045272 SUB R2,@#ERNORD ;ERROR WORD NO
5607 027334 005737 001774 TST @#ERFLGS ;ANY ERRORS ALREADY THERE
5608 027340 001002 BNE 35 ;IF YES BRANCH DO NOT TYPE HEADER
5609 027342 104004 ERROR 4 ;ERROR ON READ DATA
5610 027344 000401 BR 45 ;BRANCH TO AVOID PRINTING NEXT ERROR
5611 027346 104005 35: ERROR 5 ;WORD NO 1-10 ARE DATA
5612 ;WORDS
5613 ;WORD NOS 11-256 HAVE NOT BEEN
5614 ;READ AND BUFFER SHOULD BE
5615 ;ZERO IF OTHER THAN ZERO
5616 ;WRONG NUMBER OF WORDS HAVE
5617 ;BEEN READ IN THE DISK NOW
5618 ;CONTAINS 177400 ALL 256
5619 ;WORDS BUT ONLY 10 WORDS
5620 ;SHOULD BE READ IN
5621
5622 027350 022021 45: CMP (RO)+,(R1)+ ;UNDO -(RO) AND -(R1) FOR ERROR
5623 027352 013746 177570 MOV @#SMR,-(SP) ;GET SWITCH SETTING
5624 027356 042716 177177 BIC #177177,(SP) ;KEEP ONLY SWITCH 7 AND 8
5625 027362 022726 000200 CMP #SM07,(SP)+ ;IS 7 SET AND 8 RESET
5626 027366 001402 BEQ TST66 ;BRANCH OUT IF YES

```


I14

MAINDEC-11-DZRPS-D RJPO4 DISKLESS RH11 TEST-PART 1
DZRPSD.P11 T65 READ DATA

MACY11 27(663) 8-OCT-75 16:14 PAGE 134

SEQ 0176

5627 027370 005302
5628 027372 001345
5629
5630
5631

25: DEC R2
BNE 15

:COUNT
:BRANCH IF NOT COMPLETE

```

5632
5633 ;
5634 ;
5635 ;*****
5636 ;*TEST 66 WRITE CHECK HEADER AND DATA
5637
5638 ;* WRITE CHECK CYLINDER 0, FORMAT 16 BITS PER WORD
5639 ;* TRACK 1, SECTOR 1, KEYS 0, 36 WORDS AS SHOWN BELOW
5640 ;* ANY DEVICE LOGIC ERROR INDICATION IS NOT CONCLUSIVE ON FIRST PASS
5641 ;* BECAUSE ERROR LOGIC HAS NOT BEEN CHECKED YET (ON FIRST PASS)
5642 ;* ONLY RH WRITE CHECK ERROR (RHCS2 BIT 14) IS TESTED HERE
5643 ;*****
5644 027374 000004 ;*ST66: SCOPE
5645
5646 ; DATA TABLE
5647 ; 20 WORDS OF 070707
5648 ; THEN 16 WORDS WITH ZERO FLOATING FROM RIGHT
5649 ; TO LEFT (EG. 177776,177775,177773 ETC)
5650
5651 027376 012706 001000 MOV #STACK,SP ;RESET STACK
5652 027402 012737 000066 002020 MOV #TTNO,#STNM ;THIS SAVES TEST NUMBER
5653
5654 ;SET UP "REINTO" FOR WHAT IS TO BE READ
5655 027410 012701 003134 MOV #REINTO,R1 ;STARTING ADDRESS
5656 027414 012721 010000 MOV #FMT22,(R1)+ ;CYLINDER 0 FORMAT 16 BIT WORDS
5657 027420 012721 000401 MOV #401,(R1)+ ;TRACK=1, SECTOR=1
5658 027424 005021 CLR (R1)+ ;KEY1=0
5659 027426 005021 CLR (R1)+ ;KEY2=0
5660 027430 004037 040440 JSR RO,#CLAREA ;FILL "REINTO" BUFFER
5661 027434 003144 .WORD REINTO+(4*2) ;FROM
5662 027436 003212 .WORD REINTO+(23*2) ;TO
5663 027440 070707 .WORD 070707 ;DATA
5664
5665 027442 012700 177776 MOV #177776,RO ;GETTING READY TO FLOAT 0
5666 027446 012701 003214 MOV #REINTO+(24*2),R1 ;STARTING ADDRESS WHERE 177776 GOES
5667 027452 010021 15: MOV RO,(R1)+ ;MOVE IN FLOATING 0
5668 027454 000261 SEC ;SET CARRY
5669 027456 006100 ROL RO ;GET 0 ONE BIT LEFT
5670 027460 103774 BCS 15 ;BRANCH IF 16 NOT DONE
5671
5672 027462 004037 040440 JSR RO,#CLAREA ;FILL THE REST OF BUFFER WITH 0
5673 027466 003254 .WORD REINTO+(40*2) ;FROM
5674 027470 004132 .WORD REINTO+776 ;TO
5675 027472 000000 .WORD 0 ;DATA
5676
5677 ;SET UP SIMULATED DISK WITH WHAT IS TO BE READ
5678
5679 027474 004037 040440 JSR RO,#CLAREA ;FILL "DISK" BUFFER
5680 027500 047070 .WORD DISK ;FROM
5681 027502 047136 .WORD DISK+(19*2) ;TO
5682 027504 070707 .WORD 070707 ;DATA
5683
5684 027506 012700 177776 MOV #177776,RO ;GETTING READY TO FLOAT ZEROS
5685 027512 012701 047140 MOV #DISK+(20*2),R1 ;STARTING ADDRESS WHERE 177776 GOES

```



```

5686 027516 010021      2S:  MOV    RO,(R1)+      ;MOVE IN FLOATING 0
5687 027520 000261      SEC                      ;SET CARRY
5688 027522 006100      ROL    RO              ;GET 0 ONE BIT LEFT
5689 027524 103774      BCS    2S              ;BRANCH IF 16 NOT DONE
5690
5691 027526 004037 040440 JSR    RO,@#CLAREA     ;FILL THE REST OF BUFFER WITH 177777
5692 027532 047200      .WORD  DISK+(36.*2)    ;FROM
5693 027534 050066      .WORD  DISK+776       ;TO
5694 027536 177777      .WORD  177777        ;DATA
5695
5696 027540 004737 041246 JSR    PC,@#MRCHHD     ;WRITE CHECK HEADER AND DATA
5697                                ;CYLINDER 0, TRACK 1, SECTOR 1
5698
5699                                ;IF THE PROGRAM COMES BACK HERE THEN WRITE CHECK
5700                                ;HAS BEEN COMPLETED NOW WRITE CHECK ERROR BIT IS TO BE TESTED
5701
5702 027544 013746 001762  MOV    @#UNIT, -(SP)   ;GET UNIT NUMBER
5703 027550 052716 000100  BIS    #IR, (SP)      ;ONLY BIT 6 SHOULD BE SET
5704 027554 004737 040126  JSR    PC,@#PUTREG    ;SAVE REGISTERS
5705 027560 022637 001700  CMP    (SP)+, @#CS2   ;COMPARE RHCS2
5706 027564 001406      BEQ    4S              ;BRANCH IF GOOD
5707 027566 032712 040000  BIT    #MCE, @R2      ;WRITE CHECK ERROR HIGH?
5708 027572 001402      BEQ    3S              ;BRANCH IF ERROR NOT DUE TO "MCE"
5709 027574 104017      ERROR  17             ;RHD8 CONTAINS FAILING WORD
5710 027576 000401      BR     4S              ;RHD8 CONTAINS ADDRESS+2
5711                                ;OF THE WORD IN MEMORY FROM
5712                                ;THE DISK THAT DID NOT COMPARE
5713                                ;TRE AND SC WILL BE SET DUE TO
5714                                ;MCE
5715 027600 104017      3S:  ERROR  17             ;MCE CORRECTLY WAS NOT SET BUT SOME
5716                                ;BITS OTHER THAN IR
5717                                ;AND UNIT NO. WAS SET
5718
5719                                ;NOW CHECK MEMORY TO SEE IF NOTHING GOT DESTROYED
5720                                ;FILL "MRFROM" WITH WHAT SHOULD BE IN "REINTO" THEN CHECK
5721
5722 027602 012700 002070  4S:  MOV    #MRFROM, RO    ;STARTING ADDRESS
5723 027606 012720 010000  MOV    #FMT22, (RO)+  ;CYLINDER
5724 027612 012720 000401  MOV    #401, (RO)+   ;TRACK=1, SECTOR=1
5725 027616 005020      CLR    (RO)+         ;KEY1=0
5726 027620 005020      CLR    (RO)+         ;KEY2=0
5727
5728 027622 004037 040440 JSR    RO,@#CLAREA     ;FILL "MRFROM" BUFFER
5729 027626 002100      .WORD  MRFROM+(4*2)   ;FROM
5730 027630 002146      .WORD  MRFROM+(23.*2); TO
5731 027632 070707      .WORD  070707        ;DATA
5732
5733 027634 012700 177776  MOV    #177776, RO    ;GETTING READY TO FLOAT 0
5734 027640 012701 002150  MOV    #MRFROM+(24.*2), R1 ;STARTING ADDRESS WHERE 177776 GOES
5735 027644 010021      5S:  MOV    RO,(R1)+      ;MOVE IN FLOATING 0
5736 027646 000261      SEC                      ;SET CARRY
5737 027650 006100      ROL    RO              ;GET 0 ONE BIT LEFT
5738 027652 103774      BCS    5S              ;BRANCH IF 16 NOT DONE
5739

```

5740	027654	004037	040440	JSR	RO,2#CLAREA	:FILL THE REST OF BUFFER WITH 0
5741	027660	002210		.WORD	MRFROM+(<40.*2>	:FROM
5742	027662	003066		.WORD	MRFROM+776	:TO
5743	027664	000000		.WORD	0	:DATA
5744						
5745						:NOW THE READ BUFFER WILL BE CHECKED
5746	027666	005037	001774	CLR	2#ERFLGS	:CLEAR ERROR FLAG
5747						
5748	027672	004037	041416	JSR	RO,2#COMPAR	:CHECK
5749	027676	002070		MRFROM		:GOOD BUFFER
5750	027700	003134		REINTO		:TEST BUFFER
5751	027702	000400		256.		:NUMBER OF WORDS CHECKED
5752	027704	027712		6\$:RETURN POINT FOR ERROR HEADER
5753	027706	027716		7\$:RETURN POINT FOR ERROR DATA
5754	027710	027724		TST67		:RETURN FOR GOOD COMPARISON
5755	027712	104004	6\$:	ERROR	4	:READ NEXT ERROR 5
5756	027714	000207	7\$:	RTS	PC	:RETURN TO COMPARISON SUBROUTINE
5757	027716	104005		ERROR	5	:DATA IN REINTO BUFFER GOT
5758						:CHANGED AFTER A WRITE
5759						:CHECK HEADER AND DATA COMMAND
5760						:WORD NO CONTAINS THE WORD
5761						:NUMBER THAT GOT CHANGED
5762	027720	000207		RTS	PC	:RETURN TO COMPARISON SUBROUTINE
5763						
5764	027722	000240	10\$:	NOP		:ONLY A BRANCH POINT


```

5765
5766
5767
5768
5769
5770
5771
5772
5773
5774
5775
5776 027724 000004
5777
5778
5779
5780
5781
5782
5783
5784 027726 012706 001000
5785 027732 012737 000067 002020
5786
5787
5788
5789 027740 012700 000001
5790 027744 012701 003134
5791 027750 010021
5792 027752 006100
5793 027754 103375
5794 027756 012700 177776
5795 027762 012701 003174
5796 027766 010021
5797 027770 000261
5798 027772 006100
5799 027774 103774
5800
5801 027776 004037 040440
5802 030002 003234
5803 030004 004132
5804 030006 000001
5805
5806
5807
5808 030010 012700 000001
5809 030014 012701 047070
5810 030020 010021
5811 030022 006100
5812 030024 103375
5813
5814 030026 012700 177776
5815 030032 012701 047130
5816 030036 010021
5817 030040 000261
5818 030042 006100

```

```

*****
;*TEST 67      WRITE CHECK DATA
*****
;*      WRITE CHECK DATA CYLINDER 0 FORMAT 16 BITS PER WORD
;*      TRACK 1, SECTOR 1, KEYS 0, 32 WORDS OF DATA
;*      ANY DEVICE LOGIC ERROR INDICATIONS ARE NOT CONCLUSIVE ON FIRST PASS
;*      BECAUSE ERROR LOGIC HAS NOT YET (ON FIRST PASS) BEEN CHECKED
;*      ONLY RH WRITE CHECK ERROR IS TESTED
*****
TST67: SCOPE
      ;DATA TABLE
      ;TOTAL OF 32 WORDS CONSISTING OF
      ;16 WORDS OF FLOATING ONES (EG. 1, 2, 4, 10)
      ;16 WORDS OF FLOATING ZEROS (EG. 177776, 177775)
      MOV      #STACK,SP      ;RESET STACK
      MOV      #TTNO,#TSTNM  ;THIS SAVES TEST NUMBER
      ;SET UP "REINTO" FOR WHAT IS TO BE READ
      MOV      #1,R0          ;GETTING READY TO FLOAT 1
      MOV      #REINTO,R1     ;STARTING ADDRESS WHERE 1 GOES
1$:      MOV      R0,(R1)+     ;MOVE FLOATING 1
      ROL      R0             ;GET 1 ONE BIT LEFT
      BCC      1$            ;BRANCH IF 16 NOT DONE
      MOV      #177776,R0     ;GETTING READY TO FLOAT 0
      MOV      #REINTO+(16.*2),R1 ;STARTING ADDRESS WHERE 177776 GOES
2$:      MOV      R0,(R1)+     ;MOVE IN FLOATING 0
      SEC                     ;SET CARRY
      ROL      R0             ;GET 0 ONE BIT LEFT
      BCS      2$            ;BRANCH IF 16 NOT DONE
      JSR      R0,#CLAREA     ;FILL REST OF BUFFER WITH 1
      .WORD   REINTO+(32.*2)  ;FROM
      .WORD   REINTO+776     ;TO
      .WORD   1              ;WITH DATA
      ;SET UP SIMULATED DISK WITH WHAT IS TO BE READ
      MOV      #1,R0          ;GETTING READY TO FLOAT 1
      MOV      #DISK,R1       ;STARTING ADDRESS WHERE 1 GOES
3$:      MOV      R0,(R1)+     ;MOVE FLOATING 1
      ROL      R0             ;GET 1 ONE BIT LEFT
      BCC      3$            ;BRANCH IF 16 NOT DONE
      MOV      #177776,R0     ;GETTING READY TO FLOAT 0
      MOV      #DISK+(16.*2),R1 ;STARTING ADDRESS WHERE 177776 GOES
4$:      MOV      R0,(R1)+     ;MOVE FLOATING 0
      SEC                     ;SET CARRY
      ROL      R0             ;GET 0 ONE BIT LEFT

```

```

5819 030044 103774          BCS      45          ;BRANCH IF 16 NOT DONE
5820
5821 030046 004037 040440    JSR      RD, @#CLAREA ;FILL REST OF BUFFER WITH 0
5822 030052 047170          .WORD   DISK+(32.*2) ;FROM
5823 030054 050066          .WORD   DISK+776     ;TO
5824 030056 000000          .WORD   0            ;WITH DATA
5825
5826 030060 004737 041560    JSR      PC, @#MRCHDA ;WRITE CHECK DATA
5827                                ;CYLINDER 0, TRACK 1, SECTOR 1
5828                                ;KEYS 0, 32 WORDS.
5829
5830                                ;IF THE PROGRAM COMES BACK HERE THEN WRITE CHECK
5831                                ;HAS BEEN COMPLETED NOW WRITE CHECK ERROR BIT IS TESTED
5832
5833 030064 013746 001762    MOV      @#UNIT, -(SP) ;GET UNIT NUMBER
5834 030070 052716 000100    BIS      #IR, (SP)    ;ONLY BIT 6 SHOULD BE SET
5835 030074 004737 040126    JSR      PC, @#PUTREG ;SAVE REGISTERS
5836 030100 022637 001700    CMP      (SP)+, @#CS2 ;COMPARE RHCS2
5837 030104 001407          BEQ      6$          ;BRANCH IF GOOD
5838 030106 032737 040000 001700 BIT      #MCE, @#CS2 ;WRITE CHECK ERROR HIGH?
5839 030114 001402          BEQ      5$          ;BRANCH IF ERROR NOT DUE TO "MCE"
5840 030116 104017          ERROR   17          ;RHDB CONTAINS FAILING WORD
5841 030120 000401          BR      6$          ;RHBA CONTAINS ADDRESS+2
5842                                ;OF THE WORD IN MEMORY FROM
5843                                ;THE DISK THAT DID NOT COMPARE
5844                                ;TRE AND SC WILL BE SET DUE TO MCE
5845 030122 104017          5$:      ERROR   17 ;MCE WAS CORRECTLY NOT SET
5846                                ;BUT SOME BITS OTHER THAN
5847                                ;IR AND UNIT NO. WERE SET
5848
5849                                ;NOW CHECK MEMORY TO SEE IF ANYTHING GOT DESTROYED
5850                                ;FILL "MRFROM" WITH WHAT SHOULD BE IN REINTO THEN CHECK
5851
5852 030124 005037 001774    6$:      CLR      @#ERFLGS ;CLEAR ERROR FLAG
5853 030130 012700 000001    MOV      #1, RD      ;GETTING READY TO FLOAT 1
5854 030134 012701 002070    MOV      #MRFROM, R1 ;START ADDRESS WHERE 1 GOES
5855 030140 010021          7$:      MOV      RD, (R1)+ ;MOVE FLOATING 1
5856 030142 006100          ROL      RD          ;GET 1 ONE BIT LEFT
5857 030144 103375          BCC      7$          ;BRANCH IF 16 NOT DONE
5858
5859 030146 012700 177776    MOV      #177776, RD ;GETTING READY TO FLOAT 0
5860 030152 012701 002130    MOV      #MRFROM+(16.*2), R1 ;STARTING ADDRESS WHERE 177776 GOES
5861 030156 010021          10$:     MOV      RD, (R1)+ ;MOVE IN FLOATING 0
5862 030160 000261          SEC          ;SET CARRY
5863 030162 006100          ROL      RD          ;GET 0 ONE BIT LEFT
5864 030164 103774          BCS      10$         ;BRANCH IF CARRY SET
5865
5866 030166 004037 040440    JSR      RD, @#CLAREA ;FILL REST OF BUFFER WITH 1
5867 030172 002170          .WORD   MRFROM+(32.*2) ;FROM
5868 030174 003066          .WORD   MRFROM+776     ;TO
5869 030176 000001          .WORD   1            ;WITH DATA
5870
5871                                ;NOW THE READ BUFFER WILL BE CHECKED
5872

```


5894
5895
5896
5897
5898
5899
5900
5901
5902
5903
5904
5905
5906
5907
5908
5909
5910
5911
5912
5913
5914
5915
5916
5917
5918
5919
5920
5921
5922
5923
5924
5925
5926
5927
5928
5929
5930
5931
5932
5933
5934
5935
5936
5937
5938
5939
5940
5941
5942
5943
5944
5945
5946
5947

.SBTTL ERROR BIT FUNCTIONAL TESTS

; *TEST 70 ATTENTION WITH ERROR TEST

; * THIS TESTS THE SETTING OF ATA BIT BOTH IN THE RHAS
; * AND THE RHDS1 REGISTERS WITH THE SETTING OF EACH
; * ERROR BIT ON THE THREE ERROR REGISTERS.
; * IN EACH OF THE ABOVE CASES ERR IN RHDS1 SHOULD
; * ALSO SET.

; * "GO" SHOULD CLEAR ERR, ATA IN RHDS1 AND RHAS BUT NOT ERROR REG.
; * PUTTING "1" IN RHAS DRIVE POSITION CLEARS DRIVE BIT IN ATA IN RHDS1
; * UPPER BYTE OF RHAS IS INVALID

†ST70: SCOPE

030232 000004
030234 012706 001000
030240 012737 000070 002020
030246 004737 040522
030252 004737 040556
030256 104400 057176

030262 000000

030264 012700 003134

030270 013720 001634
030274 012720 000000
030300 013720 001640
030304 012720 000000
030310 013720 001646
030314 012720 000000

030320 013704 001650
030324 013705 002004
030330 012737 030356 001110

030336 012737 000003 001172
030344 012700 003134

030350 012002 15:
030352 012701 000001 15:
030356 052777 000040 151244 25:
030364 013777 001762 151236
030372 010112
030374 004737 040126

MOV #STACK, SP ; RESET STACK
MOV #TTNO, @†TSTNM ; THIS SAVES TEST NUMBER
JSR PC, @†CLDISK ; CLEAR DISK REGISTERS
JSR PC, @†CHECKT ; CHECK THAT DVA, RDY, DPR, DRY = 1
TYPE ,CPHALT ; AND THAT NO OTHERS = 1. CANNOT CON-
; TINUE TESTING IF BOTH AREN'T TRUE
HALT ; STOP THE TEST

MOV #REINTO, RO ; BUFFER STARTING FOR 3 ERROR
; REGISTERS
MOV @†RHER1, (RO)+ ; RHER1 STORED IN REINTO
MOV #0, (RO)+ ; BITS NOT TO BE CHECKED IN RHER1
MOV @†RHER2, (RO)+ ; RHER2 STORED IN REINTO+4
MOV #0, (RO)+ ; BITS NOT TO BE CHECKED IN RHER2
MOV @†RHER3, (RO)+ ; RHER3 STORED IN REINTO+10
MOV #0, (RO)+ ; BITS NOT TO BE CHECKED IN RHER3

MOV @†RHAS, R4 ; R4 HAS RHAS
MOV @†ATTENT, R5 ; R5 HAS ATA BIT IN RHAS
MOV #25, @†SLPERR ; THAT SHOULD SET WITH ERROR
; RETURN POINT TO ERROR
MOV #3, @†STMP1 ; ERROR REGISTER COUNTER
MOV #REINTO, RO ; REGISTER BUFFER POINTER

15: MOV (RO)+, R2 ; R2 HAS ADDRESS OF ERROR REG
MOV #BITO, R1 ; R1 WILL HAVE BIT UNDER TEST
25: BIS #CLR, @†RHCS2 ; CLEAR RHCS2
MOV @†UNIT, @†RHCS2 ; REINSTATE UNIT NO.
MOV R1, @†R2 ; SET ERROR BIT
JSR PC, @†PUTREG ; READ AND SAVE REGISTERS


```

5948 030400 120537 001720      CMPB   R5,2#AS      ; ONLY THE BIT IN R5 SHOULD BE
5949                                ; SET IN RHAS
5950 030404 001401      BEQ    3$           ; LOOK @ RHDS1 IF GOOD
5951 030406 104020      ERROR  20          ; WITH THE SETTING OF ONE
5952                                ; ERROR BIT IN AN ERROR
5953                                ; REGISTER, THE CORRESPONDING
5954                                ; RHAS BIT DID NOT SET
5955
5956 030410 013746 001724      3$:   MOV    2#DS1, -(SP) ; GET RHDS1
5957 030414 042716 001100      BIC    #VV!PROG, (SP) ; REMOVE VV AND PROG
5958 030420 022726 140600      CMP    #ATA!ERR!DPR!DRY, (SP)+ ; THESE BITS PLUS VV SHOULD BE IN RHDS1
5959 030424 001401      BEQ    4$           ; CHECK 'GO' NEXT, IF THIS WAS OK
5960 030426 104020      ERROR  20          ; WITH THE SETTING OF ONE
5961                                ; ERROR BIT, COMPOSITE ERROR
5962                                ; OR ATTENTION ACTIVE, OR
5963                                ; ONE OF THE OTHER
5964                                ; PERMANENT BITS DID NOT SET
5965
5966 030430 012777 000001 151174 4$:   MOV    #GO, 2#RHCS1 ; GIVE NO-OP
5967 030436 004737 040126      JSR    PC, 2#PUTREG ; SAVE REGISTERS
5968 030442 020112      CMP    R1, 2#R2    ; GO SHOULD NOT CLEAR ERROR
5969 030444 001410      BEQ    5$           ; FURTHER CHECK OF 'GO' FUNCTIONALITY
5970 030446 010237 040172      MOV    R2, 2#REGADR ; FAILING REGISTER
5971 030452 010137 001124      MOV    R1, 2#SGDDAT ; GOOD DATA
5972 030456 013737 001700 001126      MOV    2#CS2, 2#SBDDAT ; BAD DATA
5973 030464 104001      ERROR  1           ; "GO" WITH NO-OP CHANGED
5974                                ; ERROR REGISTER
5975
5976 030466 013746 001724      5$:   MOV    2#DS1, -(SP) ; GET RHDS1
5977 030472 042716 001100      BIC    #VV!PROG, (SP) ; CLEAR VV AND PROG
5978 030476 022726 140600      CMP    #ATA!ERR!DPR!DRY, (SP)+ ; GO SHOULD NOT CLEAR ANY BITS
5979 030502 001401      BEQ    7$           ; CHECK NEXT ERROR BIT IF A-OK
5980 030504 104020      ERROR  20          ; "GO" WITH NO-OP SHOULD NOT CLEAR
5981                                ; ATA AND/OR ERR
5982
5983                                ; THIS IS THE MAIN BIT TESTING CONTROL LOGIC
5984
5985
5986
5987 030506 006301      7$:   ASL    R1           ; GET NEXT BIT TO THE LEFT
5988 030510 103403      BCS    10$         ; GO ON TO NEXT REGISTER IF DONE
5989 030512 031001      BIT    (R0), R1    ; IS THIS BIT TO BE TESTED ?
5990 030514 001374      BNE    7$          ; IF NOT, GET NEXT ONE
5991 030516 000717      BR     2$          ; IF TO BE TESTED, GO DO IT !
5992
5993 030520 005720      10$:  TST    (R0)+       ; ADVANCE R0 TO NEXT ERROR REG.
5994 030522 005337 001172      DEC    2#STMP1     ; REGISTER COUNTER
5995 030526 001310      BNE    1$          ; DO NEXT ONE, IF 3 NOT COMPLETE
5996
5997
5998                                ; NOW AFTER SETTING ATA IN RHDS1 "1" IN RHAS AT THE
5999                                ; DRIVE POSITION SHOULD CLEAR ATA IN RHDS1
6000
6001 030530 004737 040522      11$:  JSR    PC, 2#CLDISK ; CLEAR

```

E15

6002	030534	012767	030530	150346		MOV	#115,SLPERR	;ERROR RETURN
6003	030542	012714	177777			MOV	#-1,DR4	;SET BIT IN RHAS AND ATA IN RHDS1
6004	030546	013777	002004	151074		MOV	#ATTENT,DRHAS	;WRITE 1 INTO DRIVE BIT POSITION
6005	030554	004737	040126			JSR	PC,DRPUTREG	;SAVE REGISTERS
6006	030560	105737	001720			TSTB	#AS	;THIS SHOULD BE ZERO
6007	030564	001401				BEQ	125	
6008	030566	104020				ERROR	20	;MOVING A "1" INTO RHAS
6009								;AT THE DRIVE BIT POSITION
6010								;DID NOT CLEAR IT
6011								
6012	030570	013746	001724		125:	MOV	#RDS1,-(SP)	;GET RDS1
6013	030574	042716	001000			BIC	#PROG,(SP)	;MASK PROGRAMABLE
6014	030600	022726	040700			CMF	#ERR!VV!DPR!DRY,(SP)+	;RDS1 SHOULD HAVE THESE BITS
6015								;BUT ATA SHOULD BE CLEARED
6016								;CHECK RHER1 IF GOOD
6017	030604	001401				BEQ	135	;MOVING "1" INTO RHAS AT THE
6018	030606	104020				ERROR	20	;DRIVE BIT POSITION DID NOT
6019								;CLEAR ATA IN RHDS1
6020								
6021								
6022	030610	022737	177777	001704	135:	CMF	#-1,DRER1	;RHER1 SHOULD NOT CHANGE
6023								;BY CLEARING RHAS
6024	030616	001401				BEQ	TST71	;BRANCH IF GOOD
6025	030620	104020				ERROR	20	;RHER1 WAS CHANGED BY CLEARING
6026								;RHAS BY MOVING "1" INTO
6027								;THE DRIVE BIT POSITION
6028								
6029								
6030								
6031								
6032								


```

6033
6034 ;*****
6035 ;*TEST 71      BUS ADDRESS INHIBIT
6036
6037 ;*      READ CYLINDER, FORMAT 16 BITS PER WORD
6038 ;*      TRACK0, SECTOR 1, KEYS 0, 10 WORDS OF 177400
6039 ;*      THIS IS DONE WITH BUS ADDRESS INHIBIT SET
6040 ;*      ANY ERROR LOGIC INDICATION IS NOT CONCLUSIVE ON FIRST PASS
6041 ;*      BECAUSE ERROR LOGIC HAS NOT BEEN CHECKED YET (ON FIRST PASS)
6042
6043 ;*****
6044 030622 000004      †ST71: SCOPE
6045 030624 012706 001000      MOV      #STACK,SP      ;RESET STACK
6046
6047 030630 012737 000071 002020      MOV      #TTNO,@#TSTNM      ;THIS SAVES TEST NUMBER
6048 030636 004037 040440      JSR      RD,@#CLAREA      ;CLEAR SIMULATED DISK
6049 030642 047070      .WORD   DISK              ;FROM
6050 030644 050066      .WORD   DISK+776         ;TO
6051 030646 177400      .WORD   177400          ;DATA
6052
6053 030650 004037 040440      JSR      RD,@#CLAREA      ;CLEAR READ INTO BUFFER
6054 030654 003134      .WORD   REINTO           ;FROM
6055 030656 004132      .WORD   REINTO+776       ;TO
6056 030660 000000      .WORD   0                ;DATA
6057
6058 ;THESE ARE TO SETUP FOR DISKLESS USE ONLY
6059
6060 030662 012737 010000 045152      MOV      #FMT22,@#CYL      ;CYLINDER 0 16 BITS PER WORD FORMAT
6061 030670 105037 045155      CLRB    @#SECTOR+1        ;TRACK 0
6062 030674 112737 000001 045154      MOV     #1,@#SECTOR      ;SECTOR 1
6063 030702 005037 045156      CLR     @#KEY1            ;KEY1=0
6064 030706 005037 045160      CLR     @#KEY2            ;KEY2=0
6065 030712 012737 000012 045232      MOV     #10,@#DAMWORD     ;NO. OF DATA WORDS
6066 030720 005037 045162      CLR     @#X               ;THIS IS A READ COMMAND
6067 030724 004537 041730      JSR     RS,@#CRC           ;GO TO CALCULATE CRC
6068 030730 045152      CYL
6069 030732 047052      MCRC
6070
6071 ;THESE ARE REGULAR SETUPS
6072
6073 030734 004737 040522      JSR     PC,@#CLDISK       ;SETUP GENERAL REGISTERS
6074 030740 013711 002050      MOV     @#READAT,@R1      ;READ DATA INTO RHCS1=70
6075 030744 012777 177766 150652      MOV     #-10,@#RMC        ;10 DATA WORDS
6076 030752 012777 003134 150646      MOV     #REINTO,@RHBA     ;STARTING ADDRESS OF READ BUFFER
6077 030760 112746 000001      MOV     #1,-(SP)          ;IN LOWER BYTE GET SECTOR 1
6078 030764 112766 000000 000001      MOV     #0,1(SP)          ;GET TRACK0 IN UPPER BYTE
6079 030772 012677 150640      MOV     (SP)+,@RHST       ;TRACK/SECTOR IN RHST
6080 030776 012777 014000 150636      MOV     #FMT22!ECI,@RHOF  ;16 BITS PER WORD
6081 ;ECC CORRECTION INHIBIT BECAUSE
6082 ;ECC IS NOT CHECKED HERE
6083 031004 005077 150634      CLR     @RHCA             ;CYLINDER 0
6084 031010 004737 040556      JSR     PC,@#CHECKT       ;CHECK THAT DVA,RDY,DPR,DRY = 1
6085 031014 104400 057176      TYPE   ,CPHALT           ;AND THAT NO OTHERS = 1. CANNOT CON-
6086 ;TINUE TESTING IF BOTH AREN'T TRUE

```

```

6087 031020 000000          HALT                ;STOP THE TEST
6088 031022 052777 000010 150600  BIS      #BAI, @RHCS2  ;SET BUS ADDRESS INHIBIT
6089 031030 005037 001774          CLR      @#ERFLGS     ;CLEAR ERROR FLAG
6090 031034 004737 045026          JSR      PC, @#COMHD  ;READ DATA
6091
6092                               ;IF THE PROGRAM COMES BACK HERE WITHOUT ERROR PRINTOUT
6093                               ;FROM "COMHD" ROUTINE IN MEANS DATA IS TO BE CHECKED
6094
6095                               ;NOW THE DATA READ INTO "REINTO" BUFFER WILL
6096                               ;BE CHECKED, ONLY ONE WORD SHOULD BE CHANGED
6097                               ;ALL OTHER WORDS SHOULD REMAIN UNCHANGED
6098                               ;THE "MRFROM" BUFFER IS FILLED WITH EXPECTED DATA AND CHECKED
6099
6100 031040 005037 001774          CLR      @#ERFLGS     ;CLEAR FLAG
6101 031044 004037 040440          JSR      RO, @#CLAREA ;CLEAR BUFFER
6102 031050 002070          MRFROM          ;FROM
6103 031052 003066          MRFROM+776      ;TO
6104 031054 000000          0                ;DATA
6105
6106                               ;EXPECTED DATA IS 177400 IN FIRST LOCATION ONLY
6107 031056 012737 177400 002070  MOV      #177400, @#MRFROM ;EXPECTED DATA
6108
6109                               ;NOW READ DATA BUFFER IS CHECKED
6110
6111 031064 012700 002070          MOV      #MRFROM, RO  ;GOOD DATA
6112 031070 012701 003134          MOV      #REINTO, R1 ;DATA READ
6113 031074 012702 000400          MOV      #256., R2   ;COUNTER
6114 031100 012737 000401 045272 15: MOV      #257., @#ERMWORD ;FOR ERROR WORD NO
6115 031106 022021          CMP      (RO)+, (R1)+ ;COMPARE GOOD WITH READ BUFFER
6116 031110 001424          BEQ      2$        ;BRANCH IF GOOD
6117 031112 014037 001124          MOV      -(RO), @#SGDDAT ;GOOD DATA
6118 031116 014137 001126          MOV      -(R1), @#SBDDAT ;BAD DATA
6119 031122 160237 045272          SUB      R2, @#ERMWORD ;ERROR WORD NO
6120 031126 005737 001774          TST      @#ERFLGS   ;ANY ERRORS ALREADY THERE
6121 031132 001002          BNE      3$        ;IF YES BRANCH DO NOT TYPE HEADER
6122 031134 104004          ERROR   4        ;ERROR ON READ DATA
6123 031136 000401          BR       4$        ;BRANCH TO AVOID PRINTING NEXT ERROR
6124 031140 104005          ERROR   5        ;NO: NO 1-10 ARE DATA
6125                               ;WORDS
6126                               ;WORD NOS 11-256 HAVE NOT BEEN
6127                               ;READ AND BUFFER SHOULD BE
6128                               ;ZERO IF OTHER THAN ZERO
6129                               ;WRONG NUMBER OF WORDS HAVE
6130                               ;BEEN READ IN THE DISK NOW
6131                               ;CONTAINS 177400 ALL 256
6132                               ;WORDS BUT ONLY 10 WORDS
6133                               ;SHOULD BE READ IN
6134
6135 031142 022021          4$:  CMP      (RO)+, (R1)+ ;UNDO -(RO) AND -(R1) FOR ERROR
6136 031144 013746 177570          MOV      @#SMR, -(SP) ;GET SWITCH SETTING
6137 031150 042716 177177          BIC      #177177, (SP) ;KEEP ONLY SWITCH 7 AND 8
6138 031154 022726 000200          CMP      #SM07, (SP)+ ;IS 7 SET AND 8 RESET
6139 031160 001402          BEQ      TST72     ;BRANCH OUT IF YES
6140 031162 005302          2$:  DEC      R2        ;COUNT

```


H15

MAINDEC-11-DZRPS-0 RJPO4 DISKLESS RH11 TEST-PART 1
DZRPSD.P11 T71 BUS ADDRESS INHIBIT

MACY11 27(663) 8-OCT-75 16:14 PAGE 146

SEQ 0188

6141 031164 001345
6142
6143
6144
6145
6146

BNE 15

;BRANCH IF NOT COMPLETE

1

```

6147
6148
6149
6150
6151
6152
6153
6154
6155
6156
6157
6158 031166 000004
6159 031170 012706 001000
6160
6161 031174 012737 000072 002020
6162 031202 004037 040440
6163 031206 047070
6164 031210 050066
6165 031212 177400
6166
6167
6168
6169
6170 031214 012737 010000 045152
6171 031222 105037 045155
6172 031226 112737 000001 045154
6173 031234 005037 045156
6174 031240 005037 045160
6175 031244 012737 000001 045232
6176 031252 005037 045162
6177 031256 004537 041730
6178 031262 045152
6179 031264 047052
6180
6181
6182
6183 031266 004737 040522
6184 031272 013711 002050
6185 031276 012777 177777 150320
6186 031304 012777 160000 150314
6187 031312 052711 001400
6188 031316 112746 000001
6189 031322 112766 000000 000001
6190 031330 012677 150302
6191 031334 012777 014000 150300
6192
6193
6194 031342 005077 150276
6195 031346 004737 040556
6196 031352 104400 057176
6197
6198 031356 000000
6199 031360 052777 000010 150242
6200 031366 005037 001774

```

```

*****
;TEST 72 RHCS2 - BIT # 11 - NEM

```

```

;* READ CYLINDER0, FORMAT 16 BITS PER WORD
;* TRACK0, SECTOR 1, KEYS 0, 1 WORD OF 177400
;* THIS IS DONE WITH BUS ADDRESS INHIBIT SET
;* BUS ADDRESS USED IS 760000 THIS IS ALWAYS NON EXISTANT
;* THIS SHOULD SET NEM

```

```

*****

```

```

TST72: SCOPE
MOV #STACK, SP ;RESET STACK
MOV #TTNO, #TSTNM ;THIS SAVES TEST NUMBER
JSR R0, #CLAREA ;CLEAR SIMULATED DISK
.WORD DISK ;FROM
.WORD DISK+776 ;TO
.WORD 177400 ;DATA

```

```

;THESE ARE TO SETUP FOR DISKLESS USE ONLY

```

```

MOV #FMT22, #CYL ;CYLINDER 0 16 BITS PER WORD FORMAT
CLRB #SECTOR+1 ;TRACK 0
MOVB #1, #SECTOR ;SECTOR 1
CLR #KEY1 ;KEY1=0
CLR #KEY2 ;KEY2=0
MOV #1, #DANWORD ;NO. OF DATA WORDS
CLR #X ;THIS IS A READ COMMAND
JSR R5, #CRC ;GO TO CALCULATE CRC
CYL
MCRC

```

```

;THESE ARE REGULAR SETUPS

```

```

JSR PC, #CLDISK ;SETUP GENERAL REGISTERS
MOV #READAT, #R1 ;READ DATA INTO RHCS1=70
MOV #-1, #RHMC ;10 DATA WORDS
MOV #160000, #RHBA ;STARTING ADDRESS OF READ BUFFER
BIS #A16!A17, #R1 ;IS 760000
MOVB #1, -(SP) ;IN LOWER BYTE GET SECTOR 1
MOVB #0, 1(SP) ;GET TRACK0 IN UPPER BYTE
MOV (SP)+, #RHST ;TRACK/SECTOR IN RHST
MOV #FMT22!ECI, #RHOF ;16 BITS PER WORD
;ECC CORRECTION INHIBIT BECAUSE
;ECC IS NOT CHECKED HERE
CLR #RHCA ;CYLINDER 0
JSR PC, #CHECKT ;CHECK THAT DVA, RDY, DPR, DRY = 1
TYPE ,CPHALT ;AND THAT NO OTHERS = 1. CANNOT CON-
;TINUE TESTING IF BOTH AREN'T TRUE
HALT ;STOP THE TEST
BIS #BAI, #RHCS2 ;SET BUS ADDRESS INHIBIT
CLR #ERFLGS ;CLEAR ERROR FLAG

```



```

6201 031372 004737 045026 JSR PC, @#COMHD ;READ DATA
6202
6203
6204
6205 031376 011137 001126 1S: MOV @R1, @#SBDDAT ;TEST DATA
6206
6207 031402 022737 145670 001126 CMP #SC!TRE!DVA!A16!A17!RDY!70, @#SBDDAT ;COMPARE RHCS1
6208 031410 001406 BEQ 2S ;BRANCH IF GOOD
6209 031412 012737 144270 001124 MOV #SC!TRE!DVA!RDY!70, @#SGDDAT ;GOOD DATA
6210 031420 010137 040172 MOV R1, @#REGADR ;REGISTER RHCS1
6211 031424 104001 ERROR 1 ;REFERENCE NON EXISTANT
6212 ;MEMORY DID NOT SET
6213 ;REQUIRED BITS
6214 031426 013746 001762 2S: MOV @#UNIT, -(SP) ;GET UNIT NUMBER
6215 031432 052716 004110 BIS #NEM!IR!BAI, (SP) ;INCLUDE NEM BAI AND IR
6216 031436 012637 001124 MOV (SP)+, @#SGDDAT
6217 031442 011237 001126 MOV @R2, @#SBDDAT ;TEST DATA
6218 031446 023737 001124 001126 CMP @#SGDDAT, @#SBDDAT ;COMPARE RHCS2
6219 031454 001403 BEQ 3S
6220 031456 010237 040172 MOV R2, @#REGADR ;REGISTER ADDRESS
6221 031462 104001 ERROR 1 ;REFRENCING NONEXISTANT MEMORY
6222 ;CAUSED AN ERROR SHOULD SET NEM
6223 031464 017737 150136 001126 3S: MOV @RHBA, @#SBDDAT ;TEST DATA
6224
6225 031472 022737 160000 001126 CMP #160000, @#SBDDAT ;COMPARE RHBA
6226 031500 001407 BEQ 4S ;BRANCH IF GOOD
6227 031502 012737 160000 001124 MOV #160000, @#SGDDAT ;GOOD DATA
6228 031510 013737 001626 040172 MOV @#RHBA, @#REGADR ;REGISTER ADDRESS RHBA
6229 031516 104001 ERROR 1 ;AFTER A NON EXISTANT MEMORY ERROR
6230 ;RHBA DOES NOT HAVE 160002
6231 031520 4S:
6232
6233
6234
6235
6236
6237
6238

```

K15

```

6239
6240
6241
6242
6243
6244
6245
6246
6247
6248
6249
6250
6251
6252 031520 000004
6253
6254
6255
6256
6257
6258
6259
6260 031522 012706 001000
6261
6262 031526 012737 000073 002020
6263 031534 004737 040522
6264
6265
6266 031540 012700 000001
6267 031544 012701 003134
6268 031550 010021
6269 031552 006100
6270 031554 103375
6271 031556 012700 177776
6272 031562 012701 003174
6273 031566 010021
6274 031570 000261
6275 031572 006100
6276 031574 103774
6277
6278 031576 004037 040440
6279 031602 003234
6280 031604 004132
6281 031606 000001
6282
6283
6284
6285 031610 012700 000001
6286 031614 012701 047070
6287 031620 010021
6288 031622 006100
6289 031624 103375
6290
6291 031626 012700 177776
6292 031632 012701 047130

```

```

*****
;TEST 73 WRITE CHECK ERROR
*****
;
; WRITE CHECK DATA CYLINDER 0 FORMAT 16 BITS PER WORD
; TRACK 1 SECTOR 1 KEYS 0 32 WORDS OF DATA
; FIFTH WORD IS CHANGED ON DISK TO GIVE WRITE CHECK ERROR
; ANY DEVICE LOGIC ERROR INDICATIONS ARE NOT CONCLUSIVE
; ON FIRST PASS
; BECAUSE ERROR LOGIC HAS NOT YET BEEN CHECKED
; ONLY RH WRITE CHECK ERROR IS TESTED
*****
;TEST73: SCOPE
*****
;DATA TABLE
;TOTAL OF 32 WORDS CONSISTING OF
;16 WORDS OF FLOATING ONES (EG. 1, 2, 4, 10)
;16 WORDS OF FLOATING ZEROS (EG. 177776, 177775)

```

```

MOV #STACK,SP ;RESET STACK
MOV #TTNO,#TSTNM ;THIS SAVES TEST NUMBER
JSR PC,#CLDISK ;INIT AND SET GENERAL REGISTERS
;SET UP "REINTO" FOR WHAT IS TO BE READ

```

```

15: MOV #1,RO ;GETTING READY TO FLOAT 1
MOV #REINTO,R1 ;STARTING ADDRESS WHERE 1 GOES
MOV RO,(R1)+ ;MOVE FLOATING 1
ROL RO ;GET 1 ONE BIT LEFT
BCC 15 ;BRANCH IF 16 NOT DONE
MOV #177776,RO ;GETTING READY TO FLOAT 0
MOV #REINTO+(16.*2),R1 ;STARTING ADDRESS WHERE 177776 GOES
25: MOV RO,(R1)+ ;MOVE IN FLOATING 0
SEC ;SET CARRY
ROL RO ;GET 0 ONE BIT LEFT
BCS 25 ;BRANCH IF 16 NOT DONE

```

```

JSR RO,#CLAREA ;FILL REST OF BUFFER WITH 1
.WORD REINTO+(32.*2) ;FROM
.WORD REINTO+776 ;TO
.WORD 1 ;WITH DATA
;SET UP SIMULATED DISK WITH WHAT IS TO BE READ

```

```

35: MOV #1,RO ;GETTING READY TO FLOAT 1
MOV #DISK,R1 ;STARTING ADDRESS WHERE 1 GOES
MOV RO,(R1)+ ;MOVE FLOATING 1
ROL RO ;GET 1 ONE BIT LEFT
BCC 35 ;BRANCH IF 16 NOT DONE

```

```

MOV #177776,RO ;GETTING READY TO FLOAT 0
MOV #DISK+(16.*2),R1 ;STARTING ADDRESS WHERE 177776 GOES

```



```

6293 031636 010021      4S:  MOV    RO,(R1)+    ;MOVE FLOATING 0
6294 031640 000261      SEC          ;SET CARRY
6295 031642 006100      ROL    RO        ;GET 0 ONE BIT LEFT
6296 031644 103774      BCS    4S        ;BRANCH IF 16 NOT DONE
6297
6298 031646 004037 040440  JSR    RO,#CLAREA ;FILL REST OF BUFFER WITH 0
6299 031652 047170      .WORD  DISK+(32.*2) ;FROM
6300 031654 050066      .WORD  DISK+776    ;TO
6301 031656 000000      .WORD  0           ;WITH DATA
6302
6303      ;CHANGE FIFTH WORD TO 0 ON DISK
6304 031660 005037 047100  CLR    #DISK+10   ;CLEAR FIFTH WORD ON DISK
6305 031664 005037 001774  CLR    #ERFLGS    ;CLEAR ERROR FLAG
6306 031670 004737 041560  JSR    PC,#WRCHDA ;WRITE CHECK DATA
6307      ;CYLINDER 0, TRACK 1, SECTOR 1
6308      ;KEYS 0, 32 WORDS.
6309
6310      ;IF THE PROGRAM COMES BACK HERE THEN WRITE CHECK
6311      ;HAS BEEN COMPLETED NOW WRITE CHECK ERROR BIT IS TESTED
6312
6313 031674 013746 001762  MOV    #UNIT, -(SP) ;GET UNIT NUMBER
6314 031700 052716 040300  BIS    #IR!OR!MCE, (SP) ;ONLY BIT 6 SHOULD BE SET
6315 031704 004737 040126  JSR    PC,#PUTREG  ;SAVE REGISTERS
6316 031710 022637 001700  CMP    (SP)+,#CS2  ;COMPARE RHCS2
6317 031714 001407      BEQ    6S          ;BRANCH IF GOOD
6318 031716 032737 040000 001700  BIT    #MCE,#CS2  ;WRITE CHECK ERROR HIGH?
6319 031724 001002      BNE    5S          ;BRANCH IF ERROR NOT DUE TO "MCE"
6320 031726 104017      ERROR  17         ;RHDB CONTAINS FAILING WORD
6321 031730 000401      BR     6S          ;RHBA CONTAINS ADDRESS+2
6322      ;OF THE WORD IN MEMORY FROM
6323      ;THE DISK THAT DID NOT COMPARE
6324      ;TRE AND SC WILL BE SET DUE TO MCE
6325 031732 104017      5S:  ERROR  17         ;MCE WAS CORRECTLY NOT SET
6326      ;BUT SOME BITS OTHER THAN
6327      ;IR AND UNIT NO. WERE SET
6328
6329 031734 022737 177745 001674 6S:  CMP    #-27.,#MC  ;COMPARE RHMC AFTER A FORCED
6330      ;WRITE CHECK ERROR
6331 031742 001402      BEQ    14S        ;BRANCH IF GOOD
6332 031744 104017      ERROR  17         ;WORD COUNT REGISTER IN ERROR AFTER A
6333      ;FORCED WRITECHECK ERROR ON FIFTH WORD
6334 031746 000405      BR     15S        ;BRANCH TO CONTINUE
6335 031750 022737 003146 001676 14S:  CMP    #REINTO+(5*2),#BA ;COMPARE RHBA AFTER FORCED
6336      ;WRITECHECK ERROR IN FIFTH WORD
6337 031756 001401      BEQ    15S        ;BRANCH IF GOOD
6338 031760 104017      ERROR  17         ;BUS ADDRESS REGISTER IN ERROR AFTER
6339      ;FORCED WRITE CHECK ERROR ON FIFTH WORD
6340      ;NOW CHECK MEMORY TO SEE IF ANYTHING GOT DESTROYED
6341      ;FILL "MRFROM" WITH WHAT SHOULD BE IN REINTO THEN CHECK
6342
6343 031762 005037 001774      15S:  CLR    #ERFLGS    ;CLEAR ERROR FLAG
6344 031766 012700 000001      MOV    #1,RO      ;GETTING READY TO FLOAT 1
6345 031772 012701 002070      MOV    #MRFROM,R1 ;START ADDRESS WHERE 1 GOES
6346 031776 010021      7S:  MOV    RO,(R1)+   ;MOVE FLOATING 1

```

M15

MAINDEC-11-DZRP5-D, RJPO4 DISKLESS RH11 TEST-PART 1
 DZRP5D.P11 T73 WRITE CHECK ERROR

MACY11 27(663) 8-OCT-75 16:14 PAGE 151

SEQ 0193

6347	032000	006100		ROL	RO		:GET 1 ONE BIT LEFT
6348	032002	103375		BCC	75		:BRANCH IF 16 NOT DONE
6349							
6350	032004	012700	177776	MOV	#177776,RO		:GETTING READY TO FLOAT 0
6351	032010	012701	002130	MOV	#WRFROM+<16.*2>,R1		:STARTING ADDRESS WHERE 177776 GOES
6352	032014	010021		105: MOV	RO,(R1)+		:MOVE IN FLOATING 0
6353	032016	000261		SEC			:SET CARRY
6354	032020	006100		ROL	RO		:GET 0 ONE BIT LEFT
6355	032022	103774		BCS	105		:BRANCH IF CARRY SET
6356							
6357	032024	004037	040440	JSR	RO,3#CLAREA		:FILL REST OF BUFFER WITH 1
6358	032030	002170		.WORD	WRFROM+<32.*2>		:FROM
6359	032032	003066		.WORD	WRFROM+776		:TO
6360	032034	000001		.WORD	1		:WITH DATA
6361							
6362							:NOW THE READ BUFFER WILL BE CHECKED
6363							
6364	032036	004037	041416	JSR	RO,3#COMPAR		:CHECK
6365	032042	002070		WRFROM			:GOOD BUFFER
6366	032044	003134		REINTO			:TEST BUFFER
6367	032046	000400		256.			:NUMBER OF WORDS CHECKED
6368	032050	032056		115			:RETURN POINT FOR ERROR HEADER
6369	032052	032062		125			:RETURN POINT FOR ERROR DATA
6370							
6371	032054	032070		TST74			:RETURN FOR GOOD COMPARISON
6372							
6373	032056	1040C4		115: ERROR	4		:READ NEXT ERROR 5
6374	032060	000207		RTS	PC		:RETURN TO COMPARISON SUBROUTINE
6375	032062	104005		125: ERROR	5		:DATA IN REINTO BUFFER GOT
6376							:CHANGED AFTER A WRITE
6377							:CHECK DATA COMMAND
6378							:WORD NO CONTAINS THE WORD
6379							:NUMBER THAT GOT CHANGED
6380	032064	000207		RTS	PC		:RETURN TO COMPARISON SUBROUTINE
6381							
6382	032066	000240		135: NOP			:ONLY A BRANCH POINT
6383							
6384							
6385							


```

6386
6387
6388
6389
6390
6391
6392
6393
6394
6395
6396 032070 000004
6397
6398
6399 032072 012706 001000
6400
6401 032076 012737 000074 002020
6402
6403
6404
6405
6406 032104 012746 125252
6407 032110 012705 000400
6408 032114 012700 047070
6409 032120 011620
6410 032122 005305
6411 032124 001375
6412 032126 005726
6413 032130 012705 000021
6414
6415
6416 032134 005020
6417 032136 005305
6418 032140 001375
6419
6420
6421
6422
6423 032142 012737 000000 045152
6424
6425 032150 112737 000001 045155
6426 032156 112737 000000 045154
6427 032164 012737 000000 045156
6428 032172 012737 000000 045160
6429 032200 012737 000004 045232
6430 032206 005037 045162
6431 032212 004537 041730
6432 032216 045152
6433 032220 047052
6434
6435
6436
6437 032222 004737 040522
6438 032226 012777 177770 147370
6439 032234 012777 003134 147364

```

```

*****
*TEST 74 ERROR REGISTER #1-BIT 4 -FORMAT ERROR
* THE SIMULATED DISK IS FILLED WITH CYLINDER 0 TRACK 1
* SECTOR 0 FORMAT=16 BITS PER WORD AND 4 WORDS
* OF 125252. A READ HEADER AND DATA COMMAND IS GIVEN WITH 16 BITS
* PER WORD FORMAT, FER=BIT4 SHOULD SET BUT THE
* READ SHOULD BE COMPLETE
*****
TST74: SCOPE

MOV #STACK,SP ;RESET STACK
MOV #TTNO,#TSTNM ;THIS SAVES TEST NUMBER

;
; SETUP FOR WHAT IS TO BE READ
; HEADER CRC IS RESTORED FROM A SUBROUTINE

MOV #125252,-(SP) ;DATA TO BE READ
MOV #256,R5 ;COUNTER
MOV #DISK,RO ;START OF SIMULATED DISK DATA
1S: MOV (SP),(RO)+ ;MOVE IN DATA ON TO SIMULATED DISK
DEC R5 ;COUNT
BNE 1S ;BRANCH IF 256 NOT COMPLETE
TST (SP)+ ;UNDO -(SP)
MOV #17,R5 ;2 ECC WORDS
;1 DATA GAP
;14 TOLERANCE GAP
2S: CLR (RO)+ ;CLEAR ECC, DATA GAP, AND
DEC R5 ;TOLERANCE GAP
BNE 2S ;BRANCH IF NOT COMPLETE

;THESE ARE TO SETUP FOR DISKLESS USE ONLY
MOV #0!0,#CYL ;16 BITS PER WORD
;CYLINDER 0, FORMAT 16 BITS
MOVB #1,#SECTR+1 ;TRACK 1
MOVB #0,#SECTR ;SECTOR 0
MOV #0,#KEY1 ;KEY1=0
MOV #0,#KEY2 ;KEY2=0
MOV #4,#DAMORD ;NO. OF DATA WORDS
CLR #X ;THIS IS A READ COMMAND
JSR R5,#CRC ;GO TO CALCULATE CRC
CYL
MCRC

;THESE ARE REGULAR SETUPS FOR RH11 AND "REINTO"
JSR PC,#CLDISK ;SETUP GENERAL REGISTERS
MOV #-4,-4,#RHMC ;4. DATA 4 HEADER WORDS
MOV #REINTO,#RHBA ;STARTING ADDRESS OF READ BUFFER

```

6440	032242	112746	000000		MOVB	#0, -(SP)	; IN LOWER BYTE GET SECTOR
6441	032246	112766	000001	000001	MOVB	#1, 1(SP)	; GET TRACK IN HIGHER BYTE
6442	032254	012677	147356		MOV	(SP)+, @RH0ST	; TRACK/SECTOR IN RHDST
6443	032260	012777	014000	147354	MOV	@FMT2!ECI, @RHOF	; 16 BITS PER WORD
6444							; ECC CORRECTION INHIBIT
6445							; BECAUSE ECC IS NOT GOING
6446							; TO BE CHECKED
6447	032266	005077	147352		CLR	@RHCA	; CYLINDER 0
6448							
6449	032272	004737	040556		JSR	PC, @CHECKT	; CHECK THAT DVA, RDY, DPR, DRY = 1
6450	032276	104400	057176		TYPE	, CPHALT	; AND THAT NO OTHERS = 1. CANNOT CON-
6451							; TINUE TESTING IF BOTH AREN'T TRUE
6452	032302	000000			HALT		; STOP THE TEST
6453							
6454	032304	013711	002052		MOV	@REFOR, @R1	; READ HEADER AND DATA=72
6455	032310	005037	001774		CLR	@ERFLG\$; CLEAR ERROR FLAG
6456	032314	004737	045026		JSR	PC, @COMHD	; READ HEADER AND DATA
6457							
6458							
6459							
6460							
6461							
6462							
6463							
6464							
6465							
6466							
6467							
6468	032320	017767	147300	146600	MOV	@RHMC, @BDDAT	; LOAD AND TEST RHMC
6469	032326	001401			BEG	20\$; SHOULD = 0
6470	032330	104040			ERROR	40	; RHMC DOES NOT = 0 AFTER A READ
6471							; HEADER AND DATA
6472							
6473							
6474							
6475							
6476							
6477							
6478	032332	005737	001774		20\$: TST	@ERFLG\$; ANY ERRORS ALREADY THERE
6479	032336	001055			BNE	TST7\$; BRANCH IF YES
6480	032340	004737	040746		JSR	PC, @CHECKE	; CHECK THAT BITS = 1
6481	032344	104400	057176		TYPE	, CPHALT	; CANNOT CONTINUE TESTING IF THEY DON'T
6482	032350	000000			HALT		; STOP THE TEST
6483	032352	012700	002070		MOV	@WFRM, @R0	; GETTING READY TO FILL EXPECTED DATA
6484	032356	012720	000000		MOV	#0, (R0)+	; CYLINDER 0
6485	032362	112746	000000		MOVB	#0, -(SP)	; IN LOWER BYTE GET SECTOR
6486	032366	112766	000001	000001	MOVB	#1, 1(SP)	; GET TRACK IN HIGHER BYTE
6487	032374	012620			MOV	(SP)+, (R0)+	; GET TRACK/SECTOR IN BUFFER
6488	032376	012720	000000		MOV	#0, (R0)+	; KEY1 IN BUFFER
6489	032402	012720	000000		MOV	#0, (R0)+	; KEY2 IN BUFFER
6490	032406	012701	000400		MOV	#256, R1	; DATA WORD COUNTER
6491	032412	012702	125252		MOV	#125252, R2	; DATA
6492	032416	010220			3\$: MOV	R2, (R0)+	; DATA INTO BUFFER
6493	032420	005301			DEC	R1	; COUNT

; IF THE PROGRAM COMES BACK HERE WITHOUT ERROR PRINTOUTS
 ; FROM THE "COMHD" ROUTINE THAT MEANS SECTOR GAP,
 ; FIRST SYNC, HEADER, HEADER CRC, HEADER GAP AND
 ; SYNC BYTE HAVE GONE BY AND SYNC'S WERE CORRECTLY
 ; DETECTED.

; RHMC IS CHECKED TO BE = 0 AFTER THE READ OPERATION

; HEADER AND DATA ARE TO BE CHECKED.
 ; IN CHECKING READ DATA THE WRITE FROM BUFFER
 ; "WFRM" IS FILLED WITH EXPECTED DATA AND
 ; COMPARISONS ARE MADE.


```

6494 032422 001375      BNE      3$          ;BRANCH IF 256 NOT DONE
6495
6496                      ;NOW READ DATA BUFFER WILL BE CHECKED
6497
6498 032424 004037 041416 JSR      RD,@#COMPAR ;CHECK
6499 032430 002070      MRFROM      ;GOOD BUFFER
6500 032432 003134      REINTO      ;TEST BUFFER
6501 032434 000010      4+4.      ;NUMBER OF WORDS CHECKED
6502 032436 032444      4$        ;RETURN POINT FOR ERROR HEADER
6503 032440 032450      5$        ;RETURN POINT FOR ERROR DATA
6504 032442 032454      6$        ;RETURN FOR GOOD COMPARISON
6505 032444 104004      4$:      ERROR 4    ;READ NEXT ERROR
6506 032446 000207      RTS       PC    ;RETURN TO "COMPAR"
6507 032450 104005      5$:      ERROR 5    ;WORD NOS 1 TO 4 ARE
6508                      ;HEADER WORDS
6509                      ;5 TO 260 ARE DATA WORDS
6510 032452 000207      RTS       PC    ;RETURN TO "COMPAR"
6511
6512
6513                      ;NOW SEE THAT FORMAT ERROR BIT GOT SET
6514
6515 032454 004737 040126      6$:      JSR      PC,@#PUTREG ;SAVE REGISTERS
6516
6517 032460 022737 100020 001704 CMP      #FER!DCK,@#ER1 ;FORMAT ERROR SHOULD BE SET
6518 032466 001401      BEQ      TST75 ;BRANCH IF GOOD
6519 032470 104020      ERROR 20 ;A 16 BIT PER WORD READ WAS ATTEMPTED
6520                      ;WHEN THE DISK HAD
6521                      ;THE FORMAT BIT=0= 18 BITS PER
6522                      ;WORD THE READ WAS
6523                      ;COMPLETED BUT ERROR REG
6524                      ;WAS NOT RIGHT
6525                      ;NOTE DCK WILL BE SET BECAUSE
6526                      ;ECC HAS NOT BEEN GENERATED
6527
6528
    
```

```

6529
6530 ;*****
6531 ;#TEST 75 ERROR REGISTER #1-BIT 4 -FORMAT ERROR
6532
6533 ;# THE SIMULATED DISK HEADER IS FILLED WITH CYLINDER 0
6534 ;# TRACK 0, SECTOR 0 FORMAT 18 BITS PER WORD
6535 ;# A WRITE DATA COMMAND IS GIVEN WITH SAME HEADER
6536 ;# EXCEPT FORMAT BIT. THE DATA SHOULD NOT BE WRITTEN.
6537
6538 ;*****
6539 032472 000004 †ST75: SCOPE
6540 ;NOW A WRITE DATA WILL BE ATTEMPTED WITH
6541 ;WRONG FORMAT BIT
6542
6543 032474 012706 001000 MOV #STACK,SP ;RESET STACK
6544
6545 032500 012737 000075 002020 MOV #TTNO,@#TSTNM ;THIS SAVES TEST NUMBER
6546
6547 032506 012737 177777 045266 MOV #-1,@#NOSYNC ;SET FLAG SO THAT DATA SYNC
6548 ;AND DATA IS NOT READ
6549 032514 004037 040440 FRMAT1: JSR RO,@#CLAREA ;CLEAR SIMULATED DISK
6550 032520 047070 .WORD DISK ;FROM
6551 032522 050114 .WORD TOLGAP+16 ;TO
6552 032524 000000 .WORD 0 ;DATA
6553 ;THESE ARE SETUP FOR DISKLESS USE ONLY
6554 032526 005037 045152 CLR @#CYL ;CYLINDER 0, FORMAT 18 BIT WORDS
6555 032532 105037 045155 CLRB @#SECOTR+1 ;TRACK 0
6556 032536 105037 045154 CLRB @#SECOTR ;SECTOR 0
6557 032542 005037 045156 CLR @#KEY1 ;KEY1 0
6558 032546 005037 045160 CLR @#KEY2 ;KEY2 0
6559 032552 012737 000004 045220 MOV #4,@#NOWORD ;NO OF DATA WORDS
6560 032560 012737 000001 045162 MOV #1,@#X ;WRITE DATA
6561 032566 004537 041730 JSR RS,@#CRC ;GO TO CALCULATE CRC
6562 032572 050274
6563 032574 050304
6564
6565 ;THESE AER REGULAR SETUPS
6566
6567 032576 004037 040440 JSR RO,@#CLAREA ;FILL WRITE FROM BUFFER WITH 125252
6568 032602 002070 MRFROM ;FROM
6569 032604 002076 MRFROM+6 ;TO
6570 032606 125252 125252 ;DATA
6571 032610 004737 040522 JSR PC,@#CLDISK ;SETUP GENERAL REGISTERS
6572 032614 012777 177774 147002 MOV #-4,@#RMC ;256 DATA WORDS
6573 032622 012777 002070 146776 MOV #MRFROM,@#RMA ;STARTING ADDRESS OF WRITE BUFFER
6574 032630 005077 147002 CLR @#RDST ;TRACK=0 SECTOR=0
6575 032634 012777 010000 147000 MOV #FMT22,@#RHF ;16 BITS PER WORD FORMAT
6576 032642 005077 146776 CLR @#RMA ;CYLINDER 0
6577 032646 004737 040556 JSR PC,@#CHECKT ;CHECK THAT DVA,RDY,DPR,DRY = 1
6578 032652 104400 057176 TYPE ,CPHALT ;AND THAT NO OTHERS = 1. CANNOT CON-
6579 ;TINUE TESTING IF BOTH AREN'T TRUE
6580 032656 000000 HALT ;STOP THE TEST
6581 032660 013711 002044 MOV @#WRIDAT,@#R1 ;WRITE DATA=60
6582 032664 005037 001774 CLR @#ERFLG ;CLEAR ERROR FLAG

```


E16

6583	032670	004737	045026			<pre> JSR PC, @COMMD ;WRITE DATA ; IF THE PROGRAM COMES BACK HERE WITHOUT ERROR PRINTOUTS ; FROM THE "COMMD" ROUTINE IT MEANS SECTOR GAP, SYNC BYTE ; HEADER, HEADER CRC, HEADER GAP AND SYNC BYTE HAVE GONE BY ; AND SYNCs WERE CORRECTLY DETECTED ; DATA IS TO BE CHECKED </pre>
6584						
6585						
6586						
6587						
6588						
6589	032674	004737	040126			<pre> JSR PC, @PUTREG ;SAVE REGISTERS </pre>
6590	032700	005737	001774			<pre> TST @ERFLGS ;HAS ANY ERRORS OCCURED? </pre>
6591	032704	001041				<pre> BNE 45 ;BRANCH IF YES </pre>
6592	032706	012700	000000			<pre> MOV #0, R0 ;GOOD DATA </pre>
6593	032712	012701	047070			<pre> MOV @DISK, R1 ;DATA WRITTEN INTO "DISK" </pre>
6594	032716	012702	000004			<pre> MOV #4, R2 ;COUNTER </pre>
6595	032722	012737	000005	045272	15:	<pre> MOV @5, @ERMWORD ;FOR ERROR WORD </pre>
6596	032730	020021				<pre> CMP R0, (R1)+ ;COMPARE GOOD DATA WITH DATA ON DISK </pre>
6597	032732	001424				<pre> BEQ 35 ;BRANCH IF GOOD </pre>
6598	032734	010037	001124			<pre> MOV R0, @SGDDAT ;GOOD DATA </pre>
6599	032740	014137	001126			<pre> MOV -(R1), @SBDDAT ;BAD DATA </pre>
6600	032744	160237	045272			<pre> SUB R2, @ERMWORD ;ERROR WORD NO </pre>
6601	032750	005737	001774			<pre> TST @ERFLGS ;ANY ERRORS ALREADY THERE? </pre>
6602	032754	001002				<pre> BNE 25 ;BRANCH IF YES </pre>
6603	032756	104004				<pre> ERROR 4 ;ERROR ON WRITE DATA COMMAND </pre>
6604						<pre> ;ON A WRITE DATA WITH ;WRONG FORMAT NO DATA ;SHOULD BE WRITTEN ;WORD NO GIVES WORD IN ERROR ;BRANCH TO AVOID PRINTING NEXT ERROR </pre>
6605						
6606						
6607						
6608	032760	000401				<pre> BR 55 </pre>
6609	032762	104005		25:		<pre> ERROR 5 </pre>
6610	032764	005721		55:		<pre> TST (R1)+ ;UNDO -(R1) FOR BAD DATA </pre>
6611	032766	013746	177570			<pre> MOV @SMR, -(SP) ;GET SWITCH SETTING </pre>
6612	032772	042716	177177			<pre> BIC @177177, (SP) ;KEEP ONLY SWITCH 7 AND 8 </pre>
6613	032776	022726	000200			<pre> CMP @SM07, (SP)+ ;IS 7 SET AND 8 RESET. </pre>
6614	033002	001402				<pre> BEQ 45 ;BRANCH IF YES </pre>
6615	033004	005302		35:		<pre> DEC R2 ;IF NOT COUNT 256 WORDS </pre>
6616	033006	001345				<pre> BNE 15 ;BRANCH IF 256 NOT DONE </pre>
6617						
6618						<pre> ;NOW CHECK TO SEE THAT FORMAT ERROR BIT GOT SET </pre>
6619						
6620	033010	022737	000020	001704	45:	<pre> CMP @FER, @ERI ;FORMAT ERROR SHOULD BE SET </pre>
6621	033016	001401				<pre> BEQ TST76 ;BRANCH IF GOOD </pre>
6622	033020	104020				<pre> ERROR 20 ;A 16 BIT PER WORD WRITE DATA </pre>
6623						<pre> ;WAS ATTEMPTED WHEN THE DISK ;HAD THE FORMAT BIT =0=18 ;BITS PER WORD THE WRITE ;WAS CORRECTLY ABORTED ;BUT ERROR REG. 1 WAS WRONG </pre>
6624						
6625						
6626						
6627						
6628						

6629
6630
6631
6632
6633
6634
6635
6636
6637
6638
6639
6640
6641
6642
6643
6644
6645
6646
6647
6648
6649
6650
6651
6652
6653
6654
6655
6656
6657
6658
6659
6660
6661
6662
6663
6664
6665
6666
6667
6668
6669
6670
6671
6672
6673
6674
6675
6676
6677
6678
6679
6680
6681
6682

```
*****
;#TEST 76      RHCR1 - BIT #2 - REG. MODIFICATION REFUSED
```

```
;*
;*      IN THIS TEST THE REGISTERS ARE IN TWO GROUPS
;*      FIRST - RHCS1, RHDST, RHOF, RHCA, RHER1, RHER2, RHER3 - SETS RMR
;*      SECOND - RMR, RRS - DOES NOT SET RMR
;*      IF WRITING IS ATTEMPTED DURING AN OPERATION
```

```
;*
;*      ONLY ONE REGISTER IS WRITTEN INTO THAT IS RHCA
```

```
;*
;*      1 THE REGISTERS CONTENTS ARE SAVED IN "REINTO" BUFFER
;*      2 WRITE HEADER AND DATA IS STARTED
;*      3 ATTEMPT IS MADE TO WRITE INTO REGISTERS
;*      4 ALL REGISTERS ARE COMPARED
```

```
*****
;#TEST 76:  SCOPE
```

```
033022 000004
033024 012706 001000
033030 012737 000076 002020
033036 004737 040522
033042 004737 040556
033046 104400 057176
033052 000000
033054 012700 001644
033060 012005
033062 052777 000040 146540
033070 013777 001762 146532
033076 013777 002046 146526
033104 012777 177766 146512
033112 012777 002070 146506
033120 012777 000010 146510
033126 052777 000010 146474
033134 012777 010000 146500
033142 005077 146476
```

```
MOV      #STACK, SP      ;RESET STACK
MOV      #TTNO, #TSTNM   ;THIS SAVES TEST NUMBER
JSR      PC, #CLDISK     ;CLEAR DISK
JSR      PC, #CHECKT     ;CHECK THAT DVA, RDY, DPR, DRY = 1
TYPE     ,CPHALT         ;AND THAT NO OTHERS = 1. CANNOT CON-
                        ;TINUE TESTING IF BOTH AREN'T TRUE
HALT
MOV      #RHCA, RD       ;STOP THE TEST
MOV      (RD)+, R5       ;R5 HAS ADDRESS OF REG. UNDER TEST
BIS      #CLR, #RHCS2
MOV      #UNIT, #RHCS2  ;REINSTATE UNIT NO.
```

```
;SET UP FOR AN OPERATION (WRITE HEADER AND DATA)
```

```
MOV      #MRIFOR, #RHCS1 ;WRITE HEADER AND DATA=62
                        ;IN RHCS1
MOV      #-10, #RHMC     ;10 WORDS
MOV      #MRFROM, #RHBA  ;BUS ADDRESS = MRFROM
MOV      #10, #RHDST     ;DESIRED TRACK=0, SECTOR=10
BIS      #BAI, #RHCS2    ;BUS ADDRESS INCREMENT INHIBIT
MOV      #FMT22, #RHOF   ;FORMAT 16 BIT WORDS
CLR      #RHCA           ;CYLINDER =0
```

```
;SAVE REGISTERS
```

```
JSR      RD, #SAVER      ;SAVE
RHCS1    ;FROM
REINTO   ;TO
14.      ;NUMBER OF REGISTERS SAVED
```

```
;NOW THE COMMAND IS GIVES TO
;WRITE HEADER AND DATA FOR CYL=0, SECTOR=10
;TRACK=0 IT COMES BACK AFTER ONE SECTOR
```



```

6683 ;HAS PASSED
6684
6685 033160 012777 000001 146464 MOV #DMD, @RHRM ;SET DIAGNOSTIC MODE
6686 033166 005277 146440 INC @RHCS1 ;GO TO RHCS1 WITH 62
6687 033172 012715 177672 MOV #177672, @RS ;TRY WRITING ALL BITS EXCEPT
6688 ;GO, RMR, IE
6689 033176 052737 000001 003154 BIS #DMD, @REINTO+20 ;SET DMD IN SAVED REGISTER RHRM
6690 033204 052737 000004 003136 BIS @RMR, @REINTO+2 ;SET RMR IN SAVED REG. RHER1
6691 033212 042737 000200 003156 BIC #DRY, @REINTO+22 ;CLEAR DRY IN RHDS1
6692 033220 052737 040000 003156 BIS #ERR, @REINTO+22 ;SET ERR IN RHDS1
6693 033226 052737 000001 003134 BIS #GO, @REINTO ;SET GO IN SAVED REG. RHCS1
6694 033234 042737 000200 003134 BIC #RDY, @REINTO ;CLEAR RDY BIT
6695
6696 ;AFTER AN ATTEMPT TO WRITE INTO A REGISTER
6697 ;SAVE REGISTERS AGAIN SO THAT COMPARES CAN BE DONE
6698
6699 033242 004037 041214 JSR RO, @SAVER ;SAVE
6700 033246 001632 RHCS1 ;FROM
6701 033250 002070 MRFROM ;TO
6702 033252 000016 14. ;NUMBER
6703
6704 ;AS UPPER BYTE OF RHAS CAN BE CHANGING IN A DUAL PORT
6705 ;OPERATION THE UPPER BYTE OF RHAS WILL BE SAVED AS IS
6706 ;SO THAT THE COMPARES ARE ONLY VALID FOR THE LOWER BYTE
6707 033254 113737 003153 002107 MOVB @REINTO+17, @MRFROM+17;SAVE UPPER RHAS
6708
6709
6710 ;COMPARE REGISTERS BEFORE ATTEMPTED WRITE WITH AFTER
6711
6712 033262 004037 041416 JSR RO, @COMPAR ;COMPAR
6713 033266 003134 REINTO ;GO BUFFER
6714 033270 002070 MRFROM ;TEST BUFFER
6715 033272 000016 14. ;NUMBER
6716 033274 033302 4$ ;RETURN FOR ERROR
6717 033276 033302 4$ ;SAME
6718 033300 033322 5$ ;RETURN FOR GOOD COMPARISON
6719 033302 013705 045272 4$: MOV @ERWORD, R5 ;GETTING READY TO INDEX
6720 033306 060505 ADD R5, R5 ;DOUBLE ERROR WORD
6721 033310 016537 001630 040172 MOV RHCS1-2(R5), @REGADR ;FAILING REG. ADDRESS
6722 033316 104001 ERROR 1 ;CONTENTS OF REGISTER
6723 033320 000207 RTS PC ;CHANGED WITH
6724 ;AN ATTEMPT TO WRITE
6725 ;DURING AN OPERATION
6726 ;THE FOLLOWING CLEAR MAY SET THE ATA BIT BECAUSE GO IS HIGH
6727
6728 033322 004737 040522 5$: JSR PC, @CLDISK ;CLEAR DISK
6729
6730

```

6731
6732
6733
6734
6735
6736 033326 000004
6737 033330 012706 001000
6738 033334 004737 040522
6739 033340 012777 000001 146304
6740 033346 004037 043074
6741
6742
6743 033352 000001
6744
6745
6746
6747
6748
6749
6750
6751
6752
6753
6754
6755
6756
6757
6758
6759
6760 033354 000004
6761
6762
6763 033356 012706 001000
6764 033362 012737 000100 002020
6765
6766 033370 005037 045266
6767
6768
6769
6770 033374 004737 042204
6771
6772
6773
6774 033400 004037 040440
6775 033404 003134
6776 033406 004134
6777 033410 000000
6778
6779
6780
6781 033412 012700 002070
6782 033416 012720 010000
6783 033422 012720 000401
6784 033426 012720 000001

```

*****
;#TEST 77 MAKE CURRENT CYLINDER = 1
*****
TST77: SCOPE
MOV #STACK, SP ;RESET STACK
JSR PC, @@CLDISK ;INIT DRIVE
MOV #DMD, @RMR ;SET DIAGNOSTIC MODE
JSR RO, @@MAKECYL ;SUBROUTINE TO GIVE A SEEK
;COMMAND FOLLOMED BY A INIT
;THIS SHUOLD CHANGE RHCC
;CHANGE RHCC TO 1

```

```

*****
;#TEST 100 ERROR REG1 - BIT #7 - HEADER COMPARE ERROR
*****
;# THE SIMULATED DISK IS SET TO READ CYLINDER=0, TRACK=1
;# SECTOR=1, KEYS=1, 256 WORDS OF 177400
;# A READ HEADER AND DATA COMMAND IS GIVEN TO READ
;# CYLINDER=1, TRACK=1, SECTOR=1, KEY1=1, KEY2=1
;# REINTO BUFFER IS FILLED WITH 0
;# MRFROM IS FILLED WITH 10000,401,1,1, AND ALL 177400
;# AFTER THE READ THE REINTO BUFFER IS EXPECTED TO
;# HAVE WHAT IS IN MRFROM - 10000,401,1,1 AND ALL 177400

```

```

*****
TST100: SCOPE
MOV #STACK, SP ;RESET STACK
MOV #TTNO, @TSTNM ;THIS SAVES TEST NUMBER
CLR @NOSYNC ;SET FLAG SO THAT DATA SYNC
;AND DATA IS READ
;FILL SIMULATED DISK
JSR PC, @SETDSK ;SET UP SIMULATED DISK
;FILL REINTO BUFFER WITH 0
JSR RO, @CLAREA ;FILL REINTO BUFFER
REINTO ;FROM LOCATION
REINTO+(256.*2) ;TO LOCATION
0 ;DATA
;FILL MRFROM WITH 10000,401,1,1, AND ALL 177400
MOV #MRFROM, RO
MOV #FMT22, (RO)+ ;10000 INTO MRFROM
MOV #401, (RO)+ ;401=TRACK1, SECTOR1
MOV #1, (RO)+ ;1 INTO MRFROM+

```



```

6785 033432 012720 000001      MOV    #1,(RD)+      ;1 INTO WRFROM+6
6786                                     ;FILL ALL 0
6787                                     ;FILL WRFROM
6788                                     ;FROM
6789 033436 004037 040440      JSR    RD,#CLAREA   ;FILL WRFROM
6790 033442 002100                                     ;FROM
6791 033444 003070      WRFROM+10           ;TO
6792 033446 177400      WRFROM+(256.*2)    ;DATA
6793                                     ;DATA
6794                                     ;NOW GIVE A READ HEADER AND DATA COMMAND
6795                                     ;CYLINDER=1
6796                                     ;TRACK = 1
6797                                     ;SECTOR = 1
6798
6799 033450 004037 042332      JSR    RD,#HCCRCE
6800 033454 000072      72                ;READ HEADER AND DATA
6801 033456 000001      1                ;CYLINDER
6802 033460 000001      1                ;SECTOR
6803 033462 000001      1                ;TRACK
6804 033464 177400      -256.            ;WORD COUNT
6805 033466 003134      REINTO           ;RHBA BUFFER
6806 033470 000000      0                ;READ
6807
6808 033472 000001      1                ;HEADER COMPARE
6809 033474 000240      1S:  NOP         ;RETURN POINT FROM HCCRCE
6810
6811
    
```

```

6812
6813
6814
6815
6816 033476 000004
6817 033500 012706 001000
6818 033504 004737 040522
6819 033510 012777 000001 146134
6820 033516 004037 043074
6821
6822
6823 033522 000000
6824
6825
6826
6827
6828
6829
6830
6831
6832
6833
6834
6835
6836
6837
6838
6839
6840 033524 000004
6841
6842
6843 033526 012706 001000
6844 033532 012737 000102 002020
6845
6846 033540 005037 045266
6847
6848
6849
6850 033544 004737 042204
6851
6852
6853
6854 033550 004037 040440
6855 033554 003134
6856 033556 004134
6857 033560 000000
6858
6859
6860
6861 033562 012700 002070
6862 033566 012720 010000
6863 033572 012720 000401
6864 033576 012720 000001
6865 033602 012720 000001

;*****
;#TEST 101 MAKE CURRENT CYLINDER = 0
;*****
TST101: SCOPE
MOV #STACK, SP ;RESET STACK
JSR PC, @#CLDISK ;INIT DRIVE
MOV #DMD, @RHMR ;SET DIAGNOSTIC MODE
JSR RO, @#MAKECYL ;SUBROUTINE TO GIVE A SEEK
;COMMAND FOLLOED BY A INIT
;THIS SHUOLD CHANGE RHCC
;CHANGE RHCC TO 0
0

;*****
;#TEST 102 ERROR REG1 - BIT #7 - HEADER COMPARE ERROR
;*****
;# THE SIMULATED DISK IS SET TO READ CYLINDER=0, TRACK=1
;# SECTOR=1, KEYS=1, 256 WORDS OF 177400
;# A READ HEADER AND DATA COMMAND IS GIVEN TO READ
;# CYLINDER=0, TRACK=0, SECTOR=1, KEY1=1, KEY2=1
;# REINTO BUFFER IS FILLED WITH 0
;# WRFROM IS FILLED WITH 10000,401,1,1, AND ALL 177400
;# AFTER THE READ THE REINTO BUFFER IS EXPECTED TO
;# HAVE WHAT IS IN WRFROM - 10000,401,1,1 AND ALL 177400
;*****
TST102: SCOPE
MOV #STACK, SP ;RESET STACK
MOV @TTNO, @#TSTNM ;THIS SAVES TEST NUMBER
CLR @#NOSYNC ;SET FLAG SO THAT DATA SYNC
;AND DATA IS READ
;FILL SIMULATED DISK
JSR PC, @#SETDSK ;SET UP SIMULATED DISK
;FILL REINTO BUFFER WITH 0
JSR RO, @#CLAREA ;FILL REINTO BUFFER
REINTO ;FROM LOCATION
REINTO+(256.*2) ;TO LOCATION
0 ;DATA
;FILL WRFROM WITH 10000,401,1,1, AND ALL 177400
MOV #WRFROM, RO
MOV #FMT22, (RO)+ ;10000 INTO WRFROM
MOV #401, (RO)+ ;401=TRACK1, SECTOR1
MOV #1, (RO)+ ;1 INTO WRFROM+
MOV #1, (RO)+ ;1 INTO WRFROM+6
    
```



```

6866
6867
6868
6869 033606 004037 040440 JSR RO,2#CLAREA ;FILL ALL 0 ;FILL WRFROM
6870 033612 002100 WRFROM+10 ;FROM
6871 033614 003070 WRFROM+(256.*2) ;TO
6872 033616 177400 177400 ;DATA
6873
6874 ;NOW GIVE A READ HEADER AND DATA COMMAND
6875 ;CYLINDER=0
6876 ;TRACK = 0
6877 ;SECTOR = 1
6878
6879 033620 004037 042332 JSR RO,2#HCCRCE
6880 033624 000072 72 ;READ HEADER AND DATA
6881 033626 000000 0 ;CYLINDER
6882 033630 000001 1 ;SECTOR
6883 033632 000000 0 ;TRACK
6884 033634 177400 -256. ;WORD COUNT
6885 033636 003134 REINTO ;RIBA BUFFER
6886 033640 000000 0 ;READ
6887
6888 033642 000001 1 ;HEADER COMPARE
6889 033644 000240 15: NOP ;RETURN POINT FROM HCCRCE
6890
6891

```

```

6892
6893
6894
6895
6896 033646 000004
6897 033650 012706 001000
6898 033654 004737 040522
6899 033660 012777 000001 145764
6900 033666 004037 043074
6901
6902
6903 033672 000001
6904
6905
6906
6907
6908
6909
6910
6911
6912
6913
6914
6915
6916
6917
6918
6919
6920 033674 000004
6921
6922
6923 033676 012706 001000
6924 033702 012737 000104 002020
6925
6926 033710 012737 177777 045266
6927
6928
6929
6930 033716 004737 042204
6931
6932
6933
6934 033722 004037 040440
6935 033726 002070
6936 033730 003070
6937 033732 125252
6938
6939
6940
6941
6942
6943 033734 004037 040440
6944 033740 003134
6945 033742 004134

;*****
;*TEST 103 MAKE CURRENT CYLINDER = 1
;*****
TST103: SCOPE
MOV #STACK, SP ;RESET STACK
JSR PC, @#CLDISK ;INIT DRIVE
MOV #DMD, @RHMR ;SET DIAGNOSTIC MODE
JSR RO, @#MAKECYL ;SUBROUTINE TO GIVE A SEEK
;COMMAND FOLLOMED BY A INIT
;THIS SHUOLD CHANGE RHCC
;CHANGE RHCC TO 1

;*****
;*TEST 104 ERROR REG.1 - BIT #7 - HEADER COMPARE ERROR
;*****
;* THE SIMULATED DISK IS SET UP FOR CYLINDER=0, TRACK=1
;* SECTOR=1, KEYS=1, 256 WORDS OF 177400
;* A WRITE DATA COMMAND IS GIVEN TO WRITE CYLINDER=1
;* TRACK=1, SECTOR=1, KEY1=1, KEY2=1
;* WRFROM BUFFER IS FILLED WITH 125252
;* REINTO BUFFER IS FILLED WITH 177400
;* AFTER THE WRITE COMMAND THE DISK IS EXPECTED TO
;* HAVE 177400

;*****
TST104: SCOPE
MOV #STACK, SP ;RESET STACK
MOV #TTNO, @#TSTNM ;THIS SAVES TEST NUMBER
MOV #-1, @#NOSYNC ;SET FLAG SO THAT DATA SYNC
;AND DATA IS NOT READ
;FILL SIMULATED DISK
JSR PC, @#SETDSK ;SETUP SIMULATED DISK
;FILL WRFROM WITH 125252
JSR RO, @#CLAREA ;FILL WRFROM BUFFER
WRFROM ;FROM LOCATION
WRFROM+<256.*2> ;TO LOCATION
125252 ;DATA

;FILL REINTO WITH 256 WORDS OF 177400
;THIS IS WHAT IS EXPECTED TO BE ON DISK EVEN AFTER
;AN ATTEMPT TO WRITE 125252
JSR RO, @#CLAREA ;FILL REINTO BUFFER
REINTO ;FROM LOCATION
REINTO+<256.*2> ;TO

```


M16

MAINDEC-11-DZEPS-D, RJPO4 DISKLESS RH11 TEST-PART 1 MACY11 27(663) 8-OCT-75 16:14 PAGE 164
DZEPSD.P11 T104 ERROR REG.1 - BIT #7 - HEADER COMPARE ERROR

SEQ 0206

6946	033744	177400		177400	
6947					
6948					:NOW GIVE A WRITE DATA COMMAND
6949					:CYLINDER = 1;
6950					:TRACK = 1
6951					:SECTOR = 1
6952					
6953	033746	004037	042332	JSR	RO, @#HCCRCE
6954	033752	000060		60	:WRITE DATA
6955	033754	000001		1	:CYLINDER
6956	033756	000001		1	:SECTOR
6957	033760	000001		1	:TRACK
6958	033762	177400		-256.	:WORD COUNT
6959	033764	002070		MRFROM	:RHBA BUFFER
6960	033766	000001		1	:WRITE
6961					
6962	033770	000001		1	:HEADER COMPARE
6963	033772	000240	15:	NOP	:RETURN POINT FROM HCCRCE
6964					

6965
6966
6967
6968
6969
6970
6971
6972
6973
6974
6975
6976
6977
6978
6979
6980
6981
6982
6983
6984
6985
6986
6987
6988
6989
6990
6991
6992
6993
6994
6995
6996
6997
6998
6999
7000
7001
7002
7003
7004
7005
7006
7007
7008
7009
7010
7011
7012
7013
7014
7015
7016
7017
7018

033774 000004
033776 012706 001000
034002 004737 040522
034006 012777 000001 145636
034014 004037 043074

034020 000000

034022 000004

034024 012706 001000
034030 012737 000106 002020

034036 012737 177777 045266

034044 004737 042204

034050 004037 040440

034054 002070

034056 003070

034060 125252

034062 004037 040440

```
*****  
*TEST 105 MAKE CURRENT CYLINDER = 0  
*****  
TST105: SCOPE  
MOV #STACK, SP ;RESET STACK  
JSR PC, @#CLDISK ;INIT DRIVE  
MOV #DMD, @RMR ;SET DIAGNOSTIC MODE  
JSR RO, @#MAKECYL ;SUBROUTINE TO GIVE A SEEK  
;COMMAND FOLLOMED BY A INIT  
;THIS SHUOLD CHANGE RHCC  
;CHANGE RHCC TO 0
```

```
*****  
*TEST 106 ERROR REG.1 - BIT #7 - HEADER COMPARE ERROR  
*****  
* THE SIMULATED DISK IS SET UP FOR CYLINDER=0, TRACK=1  
* SECTOR=1, KEYS=1, 256 WORDS OF 177400  
* A WRITE DATA COMMAND IS GIVEN TO WRITE CYLINDER=0  
* TRACK=0, SECTOR=1, KEY1=1, KEY2=1  
* MRFROM BUFFER IS FILLED WITH 125252  
* REINTO BUFFER IS FILLED WITH 177400  
* AFTER THE WRITE COMMAND THE DISK IS EXPECTED TO  
* HAVE 177400  
*****
```

```
*****  
TST106: SCOPE  
MOV #STACK, SP ;RESET STACK  
MOV #TTNO, @#TSTNM ;THIS SAVES TEST NUMBAER  
MOV #-1, @#NOSYNC ;SET FLAG SO THAT DATA SYNC  
;AND DATA IS NOT READ  
;FILL SIMULATED DISK  
JSR PC, @#SETDSK ;SETUP SIMULATED DISK  
;FILL MRFROM WITH 125252  
JSR RO, @#CLAREA ;FILL MRFROM BUFFER  
MRFROM ;FROM LOCATION  
MRFROM+(<256.*2) ;TO LOCATION  
125252 ;DATA  
;FILL REINTO WITH 256 WORDS OF 177400  
;THIS IS WHAT IS EXPECTED TO BE ON DISK EVEN AFTER  
;AN ATTEMPT TO WRITE 125252  
JSR RO, @#CLAREA ;FILL REINTO BUFFER
```



```

7019 034066 003134 REINTO ;FROM LOCATION
7020 034070 004134 REINTO+(256.*2) ;TO
7021 034072 177400 177400
7022
7023 ;NOW GIVE A WRITE DATA COMMAND
7024 ;CYLINDER = 0,
7025 ;TRACK = 0
7026 ;SECTOR = 1
7027
7028 034074 004037 042332 JSR RO,3#HCCRCE
7029 034100 000050 60 ;WRITE DATA
7030 034102 000000 0 ;CYLINDER
7031 034104 000001 1 ;SECTOR
7032 034106 000000 0 ;TRACK
7033 034110 177400 -256. ;WORD COUNT
7034 034112 002070 MRFROM ;RHBA BUFFER
7035 034114 000001 1 ;WRITE
7036
7037 034116 000001 1 ;HEADER COMPARE
7038 034120 000240 15: NOP ;RETURN POINT FROM HCCRCE
7039

```

7040
7041
7042
7043
7044
7045
7046
7047
7048
7049
7050
7051
7052
7053
7054
7055
7056
7057
7058
7059
7060
7061
7062
7063
7064
7065
7066
7067
7068
7069
7070
7071
7072
7073
7074
7075
7076
7077
7078
7079
7080
7081
7082
7083
7084
7085
7086
7087
7088
7089
7090
7091
7092
7093

034122 000004
034124 012706 001000
034130 012737 000107 002020
034136 005037 045266
034142 004737 042204
034146 005137 047052
034152 004037 040440
034156 003134
034160 004134
034162 000000
034164 012700 002070
034170 012720 010000
034174 012720 000401
034200 012720 000001
034204 012720 000001
034210 004037 040440
034214 002100
034216 003070
034220 177400

```
*****  
*TEST 107 RHER1 - BIT #8 - CRC ERROR (READING)  
*****  
* THE SIMULATED DISK IS SET TO READ CYLINDER=0, TRACK=1  
* SECTOR=1, KEYS=1, 256 WORDS OF 177400  
* A READ HEADER AND DATA COMMAND IS GIVEN TO READ  
* CYLINDER=0, TRACK=1, SECTOR=1, KEY1=1, KEY2=1  
* REINTO BUFFER IS FILLED WITH 0  
* MRFROM IS FILLED WITH 10000,401,1,1,1, AND ALL 177400  
* AFTER THE READ THE REINTO BUFFER IS EXPECTED TO  
* HAVE WHAT IS IN MRFROM - 10000,401,1,1 AND ALL 177400  
*****  
TST107: SCOPE  
  
MOV #STACK, SP ;RESET STACK  
MOV #TTNO, @TSTNM ;THIS SAVES TEST NUMBER  
  
CLR @NOSYNC ;SET FLAG SO THAT DATA SYNC  
;AND DATA IS READ  
;FILL SIMULATED DISK  
  
JSR PC, @SETDSK ;SET UP SIMULATED DISK  
COM @MCRC ;CHANCE CRC TO GIVE HCRC  
  
;FILL REINTO BUFFER WITH 0  
  
JSR RO, @CLAREA ;FILL REINTO BUFFER  
REINTO ;FROM LOCATION  
REINTO+(256.*2) ;TO LOCATION  
0 ;DATA  
  
;FILL MRFROM WITH 10000,401,1,1, AND ALL 177400  
  
MOV #MRFROM, RO  
MOV #FMT22, (RO)+ ;10000 INTO MRFROM  
MOV #401, (RO)+ ;401=TRACK1, SECTOR1  
MOV #1, (RO)+ ;1 INTO MRFROM+  
MOV #1, (RO)+ ;1 INTO MRFROM+6  
  
;FILL ALL 0  
  
JSR RO, @CLAREA ;FILL MRFROM  
MRFROM+10 ;FROM  
MRFROM+(256.*2) ;TO  
177400 ;DATA  
  
;NOW GIVE A READ HEADER AND DATA COMMAND  
;CYLINDER=0  
;TRACK = 1  
;SECTOR = 1
```



```

7108
7109
7110      ;*****
7111      ;*TEST 110      RHER1 - BIT 8 - CRC ERROR (WRITING)
7112
7113      ;*
7114      ;*   THE SIMULATED DISK IS SET UP FOR CYLINDER=0, TRACK=1
7115      ;*   SECTOR=1, KEYS=1, 256 WORDS OF 177400
7116      ;*   A WRITE DATA COMMAND IS GIVEN TO WRITE CYLINDER=0
7117      ;*   TRACK=1, SECTOR=1, KEY1=1, KEY2=1
7118      ;*   MRFROM BUFFER IS FILLED WITH 125252
7119      ;*   REINTO BUFFER IS FILLED WITH 177400
7120      ;*   AFTER THE WRITE COMMAND THE DISK IS EXPECTED TO
7121      ;*   HAVE 177400
7122      ;*****
7123      034250  000004      ;TST110: SCOPE
7124
7125
7126      034252  012706  001000      MOV      #STACK,SP      ;RESET STACK
7127      034256  012737  000110  002020      MOV      #TTNO,#TSTNM  ;THIS SAVES TEST NUMBER
7128
7129      034264  012737  177777  045266      MOV      #-1,#NOSYNC   ;SET FLAG SO THAT DATA SYNC
7130                                ;AND DATA IS NOT READ
7131                                ;FILL SIMULATED DISK
7132
7133      034272  004737  042204      JSR      PC,#SETDSK    ;SETUP SIMULATED DISK
7134      034276  005137  047052      COM      #MCRC         ;CHANGE CRC TO GIVE HCRC
7135
7136                                ;FILL MRFROM WITH 125252
7137
7138      034302  004037  040440      JSR      RO,#CLAREA   ;FILL MRFROM BUFFER
7139      034306  002070                                ;FROM LOCATION
7140      034310  003070      MRFROM+(<256.*2>      ;TO LOCATION
7141      034312  125252      125252                ;DATA
7142
7143                                ;FILL REINTO WITH 256 WORDS OF 177400
7144                                ;THIS IS WHAT IS EXPECTED TO BE ON DISK EVEN AFTER
7145                                ;AN ATTEMPT TO WRITE 125252
7146
7147      034314  004037  040440      JSR      RO,#CLAREA   ;FILL REINTO BUFFER
7148      034320  003134      REINTO                ;FROM LOCATION
7149      034322  004134      REINTO+(<256.*2>     ;TO
7150      034324  177400      177400
7151
7152                                ;NOW GIVE A WRITE DATA COMMAND
7153                                ;CYLINDER = 0,
7154                                ;TRACK = 1
7155                                ;SECTOR = 1
7156
7157      034326  004037  042332      JSR      RO,#HCCRCE   ;WRITE DATA
7158      034332  000060      60                    ;CYLINDER
7159      034334  000000      0                      ;SECTOR
7160      034336  000001      1                      ;TRACK
7161      034340  000001      1
    
```


GO1

7162	034342	177400		-256.	:WORD COUNT
7163	034344	002070		MRFROM	:RHBA BUFFER
7164	034346	000001		1	:WRITE
7165					
7166	034350	000000		0	:CRC ERROR
7167	034352	000240	15:	NOP	:RETURN POINT FROM HCCRCE
7168					
7169					

H01

```
7170
7171
7172
7173
7174 034354 000004
7175 034356 012706 001000
7176 034362 004737 040522
7177 034366 012777 000001 145256
7178 034374 004037 043074
7179
7180
7181 034400 000632
7182
7183
7184
7185
7186
7187
7188
7189
7190
7191
7192
7193
7194
7195 034402 000004
7196
7197 034404 012706 001000
7198 034410 012737 000112 002020
7199
7200 034416 004037 040440
7201 034422 047070
7202 034424 050114
7203 034426 000000
7204
7205
7206
7207 034430 012737 010632 045152
7208
7209 034436 112737 000022 045155
7210 034444 112737 000025 045154
7211 034452 005037 045156
7212 034456 005037 045160
7213 034462 012737 000400 045220
7214 034470 012737 000001 045162
7215 034476 004537 041730
7216 034502 045152
7217 034504 047052
7218
7219
7220
7221
7222 034506 004037 040440
7223 034512 002070
```

```
*****
;TEST 111 MAKE CURRENT CYLINDER = 410.
*****
TST111: SCOPE
MOV #STACK,SP ;RESET STACK
JSR PC,@CLDISK ;INIT DRIVE
MOV #DMD,@RMR ;SET DIAGNOSTIC MODE
JSR RO,@MAKECYL ;SUBROUTINE TO GIVE A SEEK
;COMMAND FOLLOVED BY A INIT
;THIS SHUOLD CHANGE RHCC
;CHANGE RHCC TO 410.
410.

*****
;TEST 112 RHDS1 (BIT #10) - LAST SECTOR TRANSFERRED, 'LST'
; WRITE CYLINDER 410. FORMAT 16 BITS PER WORD
; TRACK 18. SECTOR 21., KEYS 0, NUMBER OF WORDS
; 256. OF 377
; LST BIT # 10 RHDS1 SHOULD SET AFTER WRITE
; IS COMPLETE.

*****
TST112: SCOPE
MOV #STACK,SP ;RESET STACK
MOV #TTNO,@TSTNM ;THIS SAVES TEST NUMBER
JSR RO,@CLAREA ;CLEAR SIMULATED DISK
.MORD DISK ;FROM
.MORD TOLGAP+16 ;TO
.MORD 0 ;DATA
;THESE ARE SETUP FOR DISKLESS USE ONLY
MOV #410.!FMT22,@CYL;CYLINDER 410.
;16 BITS PER WORD
MOV #18.,@SECOTR+1;TRACK 18.
MOV #21.,@SECOTR ;SECTOR 21.
CLR @KEY1 ;KEY1 0
CLR @KEY2 ;KEY2 0
MOV #256.,@NOWORD ;NO OF DATA WORDS
MOV #1,@X ;WRITE DATA
JSR R5,@CRC ;GO TO CALCULATE CRC
CYL
WCRC

;THESE ARE REGULAR SETUPS
JSR RO,@CLAREA ;FILL WRITE BUFFER WITH 377
WRFROM ;FROM LOCATION
```



```

7224 034514 003070 MRFROM+<256.*2> ;TO LOCATION
7225 034516 000377 377 ;DATA
7226 034520 004737 040522 JSR PC, @#CLDISK ;SETUP GENERAL REGISTERS
7227 034524 012777 177400 145072 MOV @-256., @RHMC ;256. DATA WORDS
7228 034532 012777 002070 145066 MOV @MRFROM, @RHBA ;STARTING ADDRESS OF WRITE BUFFER
7229 034540 012746 000025 MOV @21., -(SP) ;SECTOR 21.
7230 034544 112766 000022 000001 MOV @18., 1(SP) ;TRACK 18.
7231 034552 012677 145060 MOV (SP)+, @RHST ;SECTOR 21. TRACK 18.
7232 034556 012777 010000 145056 MOV @FMT22, @RHOF ;16 BITS PER WORD FORMAT
7233 034564 012777 000632 145052 MOV @410., @RHCA ;CYLINDER 410.
7234 034572 004737 040556 JSR PC, @#CHECKT ;CHECK THAT DVA, RDY, DPR, DRY = 1
7235 034576 104400 057176 TYPE ,CPHALT ;AND THAT NO OTHERS = 1. CANNOT CON-
7236 ;TIME TESTING IF BOTH AREN'T TRUE
7237 034602 000000 HALT ;STOP THE TEST
7238 034604 013711 002044 MOV @#MRIDAT, @R1 ;WRITE DATA=60
7239 034610 005037 001774 CLR @#ERFLGS ;CLEAR ERROR FLAG
7240 034614 004737 045026 JSR PC, @#COMHD ;WRITE DATA
7241
7242
7243 ; IF THE PROGRAM COMES BACK HERE WITHOUT ERROR PRINTOUTS
7244 ; FROM THE "COMHD" ROUTINE IT MEANS SECTOR GAP, SYNC BYTE
7245 ; HEADER, HEADER CRC, HEADER GAP AND SYNC BYTE HAVE GONE BY
7246 ; AND SYNCs WERE CORRECTLY DETECTED, DATA IS TO BE CHECKED.
7247
7248 034620 004737 040126 JSR PC, @#PUTREG ;SAVE REGISTERS
7249 034624 005737 001774 TST @#ERFLGS ;HAVE ANY ERRORS OCCURED?
7250 034630 001062 BNE 55 ;BRANCH IF YES
7251 034632 012700 000377 MOV @377, R0 ;GOOD DATA
7252 034636 012701 047070 MOV @DISK, R1 ;DATA WRITTEN INTO "DISK"
7253 034642 012702 000400 MOV @256., R2 ;COUNTER
7254
7255 034646 012737 000401 045272 15: MOV @256.+1, @#ERMWORD ;FOR ERROR WORD
7256 034654 020021 CMP R0, (R1)+ ;COMPARE GOOD DATA WITH DATA ON DISK
7257 034656 001424 BEQ 35 ;BRANCH IF GOOD
7258 034660 010037 001124 MOV R0, @#SGDDAT ;GOOD DATA
7259 034664 014137 001126 MOV -(R1), @#SBDDAT ;BAD DATA
7260 034670 160237 045272 SUB R2, @#ERMWORD ;ERROR WORD NO
7261 034674 005737 001774 TST @#ERFLGS ;ANY ERRORS ALREADY THERE?
7262 034700 001002 BNE 25 ;BRANCH IF YES
7263 034702 104004 ERROR 4 ;ERROR ON WRITE DATA COMMAND
7264 034704 000401 BR 645 ;BRANCH TO AVOID PRINTING NEXT ERROR
7265
7266 034706 104005 25: ERROR 5 ;WORD NO GIVES WORD IN ERROR
7267 034710 005721 645: TST (R1)+ ;UNDO -(R1) FOR BAD DATA
7268 034712 013746 177570 MOV @#SMR, -(SP) ;GET SWITCH SETTING
7269 034716 042716 177177 BIC @177177, (SP) ;KEEP ONLY SWITCH 7 AND 8
7270 034722 022726 000200 CMP @SMO7, (SP)+ ;IS 7 SET AND 8 RESET
7271 034726 001402 BEQ 45 ;BRANCH OUT IF YES
7272 034730 005302 35: DEC R2 ;IF NOT COUNT 256 WORDS
7273 034732 001345 BNE 15 ;BRANCH IF 256. NOT DONE
7274
7275
7276
7277 034734 013746 001724 45: MOV @#DS1, -(SP) ;GET RHDS1

```

J01

MAINDEC-11-DZRPS-D, RJPO4 DISKLESS RH11 TEST-PART 1 MACY11 27(663) 8-OCT-75 16:14 PAGE 173
DZRPSD.P11 T112 RHDS1 (BIT #10) - LAST SECTOR TRANSFERRED, 'LST'

SEQ 0215

7278	034740	042716	001000			BIC	#PROG (SP)	:CLEAR PROG
7279	034744	022726	002700			CMP	#LST:DPR:DRY:VV, (SP)+	:IS LST HIGH ?
7280	034750	001412				BEQ	SS	:BRANCH IF GOOD
7281	034752	013737	001654	040172		MOV	#RHDS1, #REGADR	:FAILING REG. ADDRESS
7282	034760	012737	002700	001124		MOV	#LST:DPR:DRY:VV, #SGDDAT	:GOOD DATA
7283	034766	013737	001724	001126		MOV	#DS1, #SBDDAT	:BAD DATA
7284	034774	104001				ERROR	1	:LST DID NOT SET AFTER
7285								:LAST SECTOR ON LAST TRACK
7286								:ON LAST CYLINDER WAS
7287								:WRITTEN
7288								:VV BIT #6 MAY OR MAY NOT BE HIGH
7289	034776	013737	001632	035006	5S:	MOV	#RHCS1, #6S	
7290	035004	104424				WAT		
7291	035006	000000			6S:	O	:RHCS1 ADDRESS	
7292	035010	000200				RDY		:WAIT FOR READY
7293								
7294								
7295								

11

K01

7296
7297
7298
7299
7300
7301
7302
7303
7304
7305
7306
7307
7308
7309
7310
7311
7312
7313
7314
7315
7316
7317
7318
7319
7320
7321
7322
7323
7324
7325
7326
7327
7328
7329
7330
7331
7332
7333
7334
7335
7336
7337
7338
7339
7340
7341
7342
7343
7344
7345
7346
7347
7348
7349

```
*****
;TEST 113      ERROR REGISTER 1 - BIT #9 AOE
```

```
;*      A WRITE DATA COMMAND IS GIVEN TO CYLINDER 410
;*      SECTOR 21 TRACK 18, KEYS 0, DATA 377
;*      WORD COUNT REGISTER FOR 326 (256+66+4) WORDS
;*
;*      AFTER 256 WORDS HAVE BEEN WRITTEN
;*      AOE SHOULD COME UP
;*      RHC WILL SHOW 4 BECAUSE THE SILO IS 66 WORDS AND
;*      256 WORDS HAVE BEEN WRITTEN - TOTAL 322
;*      THIS IS 4 SHORT OF 326
```

```
*****
†ST113: SCOPE
```

```
MOV      #STACK,SP      ;RESET STACK
MOV      #TTNO, @#TSTNM ;THIS SAVES TEST NUMBER
JSR      PC, @#CLDISK   ;INIT AND SET GENERAL REGISTERS
JSR      RO, @#CLAREA   ;CLEAR SIMULATED DISK
          .WORD         ;FROM
          .WORD         ;TO
          .WORD         ;DATA
          0
;THESE ARE SETUP FOR DISKLESS USE ONLY
MOV      #410, !#FMT22, @#CYL ;CYLINDER 410.
          ;16 BITS PER WORD
MOV      #18, @#SECOTR+1 ;TRACK 18.
MOV      #21, @#SECOTR   ;SECTOR 21.
CLR      @#KEY1          ;KEY1 0
CLR      @#KEY2          ;KEY2 0
MOV      #256, @#NOMORD  ;NO OF DATA WORDS
MOV      #1, @#X         ;WRITE DATA
JSR      RS, @#CRC       ;GO TO CALCULATE CRC
CYL
MCRC
```

```
;THESE ARE REGULAR SETUPS
```

```
JSR      RO, @#CLAREA   ;FILL WRITE BUFFER WITH 377
MRFROM   ;FROM
MRFROM+(256.*2) ;TO
377 ;DATA
JSR      PC, @#CLDISK   ;SETUP GENERAL REGISTERS
MOV      #-326, @#RHC   ;326. DATA WORDS
MOV      #MRFROM, @#RBA ;STARTING ADDRESS OF WRITE BUFFER
MOV      #21, -(SP)     ;SECTOR 21.
MOV      #18, 1(SP)     ;TRACK 18.
MOV      (SP)+, @#RDST  ;SECTOR 21. TRACK 18.
MOV      #FMT22, @#RHF  ;16 BITS PER WORD FORMAT
MOV      #410, @#HCA    ;CYLINDER 410.
JSR      PC, @#CHECKT   ;CHECK THAT DVA, RDY, DPR, DRY = 1
TYPE     ,CPHALT        ;AND THAT NO OTHERS = 1. CANNOT CON-
                          ;TINUE TESTING IF BOTH AREN'T TRUE
HALT ;STOP THE TEST
```

7350	035220	013711	002044		MOV	2#MRIDAT,2R1	:WRITE DATA=60
7351	035224	005037	001774		CLR	2#ERFLGS	:CLEAR ERROR FLAG
7352							
7353							:THE REGISTERS WILL BE SAVED IN REINTO BUFFER
7354	035230	004037	041214		JSR	RD,2#SAVER	:SAVE
7355	035234	001624			RHMC		:FROM
7356	035236	003134			REINTO		:TO
7357	035240	000023			19.		:NUMBER SAVED
7358							
7359							:GIVE WRITE DATA COMMAND
7360	035242	004737	045026		JSR	PC,2#COMMD	:WRITE DATA COMMAND
7361							
7362							:CHANGE SAVED REGISTERS TO EXPECTED VALUE
7363	035246	012737	177774	003134	MOV	8-4,2#REINTO	:SAVED RHMC SHOULD BE=4
7364	035254	012737	003274	003136	MOV	2#RFRM+(2*256.)+(2*66.),2#REINTO+2	:SAVED RFRM SHOULD BE MRFRM+256+66
7365							
7366	035262	052737	000200	003140	BIS	2#R,2#REINTO+4	:SAVED RHCS2
7367	035270	042737	000100	003140	BIC	2#R,2#REINTO+4	:SAVED RHCS2
7368	035276	052737	140000	003142	BIS	2#SC!TRE,2#REINTO+6	:SAVED RHCS1 SHOULD HAVE SC TRE
7369	035304	012737	001000	003144	MOV	2#AOE,2#REINTO+10	:SAVED RHER1 SHOULD HAVE AOE
7370	035312	017737	144320	003146	MOV	2#RHDST,2#REINTO+12	:SAVED RHDST SHOULD HAVE=
7371							:RHDST IS UNDEFINED
7372	035320	012737	000633	003154	MOV	2#411,2#REINTO+20	:SAVED DESIRED CYLINDER ADDRESS
7373	035326	013737	002004	003160	MOV	2#ATTENT,2#REINTO+24	:SAVED RHAS SHOULD HAVE APPROPRIATE BIT
7374	035334	052737	000001	003162	BIS	2#DMD,2#REINTO+26	:SAVED RHR
7375	035342	052737	142000	003164	BIS	2#ATA!ERR!LST,2#REINTO+30	:SAVED RHD51
7376							
7377							:AFTER A WRITE DATA COMMAND WITH AOE ERROR
7378							:SAVE REGISTERS AGAIN SO THAT COMPARES CAN BE DONE
7379	035350	004037	041214		JSR	RD,2#SAVER	:SAVE
7380	035354	001624			RHMC		:FROM
7381	035356	002070			MRFRM		:TO
7382	035360	000021			17.		:NUMBER OF REGISTERS SAVED
7383							
7384							:AS UPPER BYTE OF RHAS CAN BE CHANGING IN A DUAL PORT
7385							:OPERATION THE UPPER BYTE OF RHAS WILL BE SAVED AS IS
7386							:SO THAT THE COMPARES ARE ONLY VALID FOR THE LOWER BYTE
7387	035362	113737	003161	002115	MOVB	2#REINTO+25,2#MRFRM+25	:SAVE UPPER RHAS
7388							
7389							
7390							:COMPARE REGISTERS BEFORE WRITE DATA COMMAND
7391							:WITH AFTER COMMAND
7392	035370	004037	041416		JSR	RD,2#COMPAR	:COMPARE
7393	035374	003134			REINTO		:GOOD BUFFER
7394	035376	002070			MRFRM		:TEST BUFFER
7395	035400	000021			17.		:NUMBER OF REGISTERS
7396	035402	035410			15		:RETURN FOR ERROR
7397	035404	035410			15		:SAME
7398	035406	035430			25		:RETURN FOR GOOD COMPARISON
7399							
7400	035410	013705	045272	15:	MOV	2#ERWORD,R5	:GETTING READY TO INDEX
7401	035414	060505			ADD	R5,R5	:DOUBLE ERROR WORD
7402	035416	016537	001622	040172	MOV	RHMC-2(R5),2#REGADR	:FAILING REG. ADDRESS
7403	035424	104001			ERROR	1	:FORCED AOE ERROR CAUSED IMPROPER


```

7436
7437
7438
7439
7440 035542 000004
7441 035544 012706 001000
7442 035550 004737 040522
7443 035554 012777 000001 144070
7444 035562 004037 043074
7445
7446
7447 035566 000000
7448
7449
7450
7451
7452
7453
7454
7455
7456
7457
7458
7459
7460
7461 035570 000004
7462 035572 012706 001000
7463 035576 012737 000115 002020
7464 035604 004737 040522
7465
7466
7467 035610 012777 000001 144034
7468 035616 052777 000004 144026
7469 035624 042777 000004 144020
7470
7471
7472
7473
7474 035632 012777 177400 143764
7475 035640 012700 003134
7476 035644 010077 143756
7477
7478 035650 012720 010000
7479
7480 035654 012720 012000
7481 035660 005020
7482 035662 005020
7483 035664 012705 000400
7484 035670 012720 177777
7485 035674 005305
7486 035676 001374
7487 035700 012777 012000 143730
7488
7489 035706 004737 040556

```

```

*****
: *TEST 114 MAKE CURRENT CYLINDER = 0
*****
TST114: SCOPE
MOV #STACK, SP ;RESET STACK
JSR PC, @#CLDISK ;INIT DRIVE
MOV #DMD, @RHMR ;SET DIAGNOSTIC MODE
JSR RD, @#MAKECYL ;SUBROUTINE TO GIVE A SEEK
;COMMAND FOLLOMED BY A INIT
;THIS SHUOLD CHANGE RHCC
;CHANGE RHCC TO 0

```

```

*****
: *TEST 115 ERROR REGISTER 1 - BIT #10 IAE
:
: A READ HEADER AND DATA IS GIVEN TO TRACK 20, SECTOR 0
:
: AN INDEX PULSE IS GIVEN TO GET RHLA TO 0
:
: IAE BIT SHOULD SET

```

```

*****
TST115: SCOPE
MOV #STACK, SP ;RESET STACK
MOV #TTNO, @#TSTNM ;THIS SAVES TEST NUMBER
JSR PC, @#CLDISK ;CLEAR REGISTERS AND SET UNIT NO.
;GIVE INDEX PULSE
MOV #DMD, @RHMR ;SET DIAGNOSTIC MODE
BIS #MINX, @RHMR ;SET INDEX
BIC #MINX, @RHMR ;CLEAR INDEX
;THESE ARE REGULAR SETUPS
MOV #-256, @RHMC ;256 DATA WORDS 4 HEADER WORDS
MOV #REINT0, RO ;THESE TWO INSTRUCTIONS GETS
MOV RO, @RHBA ;ADDR. OF WRFROM INTO RO AND
;BUS ADDRESS REGISTER
MOV #FMT22, (RO)+ ;FORMAT=16 BIT WORDS
;CYLINDER=0
MOV #12000, (RO)+ ;TRACK=20 SECTOR=0 KEYS=0
CLR (RO)+ ;KEY1=0
CLR (RO)+ ;KEY2=0
MOV #256, R5 ;COUNTER
1S: MOV #-1, (RO)+ ;MOVE ALL ONES FOR DATA
DEC R5
BNE 1S ;BRANCH IF DATA NOT COMPLETE
MOV #12000, @RHDS ;TRACK=20 SECTOR=0
JSR PC, @#CHECKT ;CHECK THAT DVA, RDY, DPR, DRY = 1

```



```

7490 035712 104400 057176          TYPE ,CPHALT          ;AND THAT NO OTHERS = 1. CANNOT CON-
7491                                HALT                    ;TINUE TESTING IF BOTH AREN'T TRUE
7492 035716 000000                                ;STOP THE TEST
7493
7494 035720 013711 002052          MOV    @#REFOR,@R1    ;GET READY FOR WRITE HEADER AND
7495                                ;DATA WITH 62 IN RHCS1
7496 035724 005037 001774          CLR    @#ERFLGS      ;CLEAR ERROR FLAG
7497 035730 012777 010000 143704    MOV    #FMT22,@RHOF  ;FORMAT BIT=1 (16 BIT WORDS)
7498 035736 005077 143702          CLR    @RHCA         ;CYLINDER =0
7499
7500                                ;THE REGISTERS WILL BE SAVED IN REINTO BUFFER
7501 035742 004037 041214          JSR    RO,@#SAVER    ;SAVE
7502 035746 001624                                ;FROM
7503 035750 003134                                ;TO
7504 035752 000023          19.                ;NUMBER SAVED
7505
7506                                ;GO TO WRITE HEADER AND DATA
7507
7508 035754 013700 001652          MOV    @#RMR,RO     ;NOW RO HAS MAINTENANCE REG. ADDR.
7509 035760 012710 000001          MOV    #DMD,@RO     ;SET DIAGNOSTIC MODE
7510 035764 052777 000001 143640    BIS    #GO,@RHCS1   ;GO
7511
7512                                ;CHANGE SAVED REGISTERS TO EXPECTED VALUE
7513 035772 052737 140000 003142    BIS    #SC:TR @#REINTO+6 ;SAVED RHCS1
7514 036000 012737 002000 003144    MOV    #IAE,@#REINTO+10 ;SAVED RHER1
7515 036006 012737 012001 003146    MOV    #12001,@#REINTO+12 ;SAVED RHDST
7516 036014 013737 002004 003160    MOV    @#ATTENT,@#REINTO+24 ;SAVED RHAS
7517 036022 052737 000001 003162    BIS    #DMD,@#REINTO+26 ;SAVED RMR
7518 036030 052737 140000 003164    BIS    #ATA!ERR,@#REINTO+30 ;SAVED RHDS1
7519
7520                                ;SAVE REGISTERS AGAIN SO THAT COMPARES CAN BE DONE
7521 036036 004037 041214          JSR    RO,@#SAVER    ;SAVE
7522 036042 001624                                ;FROM
7523 036044 002070                                ;TO
7524 036046 000023          19.                ;NUMBER OF REGISTERS SAVED
7525
7526                                ;AS UPPER BYTE OF RHAS CAN BE CHANGING IN A DUAL PORT
7527                                ;OPERATION THE UPPER BYTE OF RHAS WILL BE SAVED AS IS
7528                                ;SO THAT THE COMPARES ARE ONLY VALID FOR THE LOWER BYTE
7529 036050 113737 003161 002115    MOVB  @#REINTO+25,@#MRFROM+25;SAVE UPPER RHAS
7530
7531                                ;COMPARE REGISTERS BEFORE READ IN PRESET COMMAND
7532                                ;WITH AFTER COMMAND
7533
7534 036056 004037 041416          JSR    RO,@#COMPAR   ;COMPARE
7535 036062 003134                                ;GOOD BUFFER
7536 036064 002070                                ;TEST BUFFER
7537 036066 000021          17.                ;NUMBER OF REGISTERS
7538 036070 036076          25.                ;RETURN FOR ERROR
7539 036072 036076          25.                ;SAME
7540 036074 036116          35.                ;RETURN FOR GOOD COMPARISON
7541
7542 036076 013705 045272 25:      MOV    @#ERWORD,R5   ;GETTING READY TO INDEX
7543 036102 060505          ADD    R5,R5        ;DOUBLE ERROR WORD

```

7544	036104	016537	001622	040172	MOV	RHMC-2(R5), 2#REGADR	: FAILING REG. ADDRESS
7545	036112	104001			ERROR	1	: FORCED IAE CAUSED IMPROPER
7546							: REGISTER CHANGE
7547	036114	000207			RTS	PC	: RETURN FOR FURTHER COMPARISONS
7548							
7549							: NO ERRORS
7550							
7551	036116	004737	040522	35:	JSR	PC, 2#CLDISK	: CLEAR GO BIT
7552							

[Handwritten mark]


```

7553
7554
7555 ;*****
7556 ;*TEST 116 ERROR REGISTER 1- BIT #10 IAE
7557
7558 ;* A WRITE HEADER AND DATA IS GIVEN TO SECTOR 22
7559 ;* TRACK 0 CYLINDER 0
7560 ;*
7561 ;* WORD COUNT IS SET TO 256.
7562 ;*
7563 ;* AN INDEX PULSE IS GIVEN TO GET RHLA TO 0
7564 ;*
7565 ;* IAE BIT SHOULD SET
7566 ;*****
7567 036122 000004 001000 002020 †ST116: SCOPE
7568 036124 012706 000116 002020 MOV #STACK,SP ;RESET STACK
7569 036130 012737 000116 002020 MOV #TTNO, @STSTNM ;THIS SAVES TEST NUMBER
7570 036136 004737 040522 JSR PC, @CLDISK ;CLEAR REGISTERS AND SET UNIT NO.
7571
7572 ;GIVE INDEX PULSE
7573 036142 012777 000001 143502 MOV #DMD, @RHMR ;SET DIAGNOSTIC MODE
7574 036150 052777 000004 143474 BIS #MINX, @RHMR ;SET INDEX
7575 036156 042777 000004 143466 BIC #MINX, @RHMR ;CLEAR INDEX
7576
7577
7578 ;THESE ARE REGULAR SETUPS
7579
7580 036164 012777 177400 143432 MOV #-256, @RHMC ;256 DATA WORDS 4 HEADER WORDS
7581 036172 012700 002070 MOV #MRFROM, RO ;THESE TWO INSTRUCTIONS GETS
7582 036176 010077 143424 MOV RO, @RHBA ;ADDR. OF MRFROM INTO RO AND
7583 ;BUS ADDRESS REGISTER
7584 036202 012720 010000 MOV #FMT22, (RO)+ ;FORMAT=16 BIT WORDS
7585 ;CYLINDER=0
7586 036206 012720 000026 MOV #22, (RO)+ ;TRACK=0, SECTOR=22, KEYS=0
7587 036212 005020 CLR (RO)+ ;KEY1=0
7588 036214 005020 CLR (RO)+ ;KEY2=0
7589 036216 012705 000400 MOV #256, R5 ;COUNTER
7590 036222 012720 177777 1S: MOV #-1, (RO)+ ;MOVE ALL ONES FRO DATA
7591 036226 005305 DEC R5
7592 036230 001374 BNE 1S ;BRANCH IF DATA NOT COMPLETE
7593 036232 012777 000026 143376 MOV #22, @RHST ;TRACK=0 SECTOR=22
7594
7595 036240 004737 040556 JSR PC, @CHECKT ;CHECK THAT DVA, RDY, DPR, DRY = 1
7596 036244 104400 057176 TYPE ,CPHALT ;AND THAT NO OTHERS = 1. CANNOT CON-
7597 ;TINUE TESTING IF BOTH AREN'T TRUE
7598 036250 000000 HALT ;STOP THE TEST
7599
7600 036252 013711 002046 MOV @MRIFOR, @RI ;GET READY FOR WRITE HEADER AND
7601 ;DATA WITH 62 IN RHCS1
7602 036256 005037 001774 CLR @ERFLGS ;CLEAR ERROR FLAG
7603 036262 012777 010000 143352 MOV #FMT22, @RHOF ;FORMA BIT=1 (16 BIT WORDS)
7604 036270 005077 143350 CLR @RHCA ;CYLINDER =0
7605
7606 ;AS EXCEPTION IS ASSERTED BEFORE RUN IS

```

```

7607 ;LATCHED RHMC,RHBA,RHCS1,RHCS2 CANNOT BE CHECKED
7608 ;BECAUSE RHMC WILL VARY DEPENDING UPON GATE DELAYS
7609 ;ON DIFFERENT UNITS
7610
7611 ;THE REGISTERS WILL BE SAVED IN REINTO BUFFER
7612 036274 004037 041214 JSR RO,@SAVER ;SAVE
7613 036300 001634 RHER1 ;FROM
7614 036302 003134 REINTO ;TO
7615 036304 000015 13. ;NUMBER SAVED
7616
7617 ;GO TO WRITE HEADER AND DATA
7618
7619 036306 013700 001652 MOV @RHMR,RO ;NOW RO HAS MAINTENANCE REG. ADDR.
7620 036312 012710 000001 MOV @DMD,@RO ;SET DIAGNOSTIC MODE
7621 036316 052777 000001 143306 BIS @GO,@RHCS1 ;GO
7622
7623 ;CHANGE SAVED REGISTERS TO EXPECTED VALUE
7624 036324 012737 002000 003134 MOV @IAE,@REINTO ;SAVED RHER1
7625 036332 012737 000027 003136 MOV @23,@REINTO+2 ;SAVED RHDST
7626 036340 013737 002004 003150 MOV @ATTENT,@REINTO+14 ;SAVED RHAS
7627 036346 052737 000001 003152 BIS @DMD,@REINTO+16 ;SAVED F:MR
7628 036354 052737 140000 003154 BIS @ATA!ERR,@REINTO+20 ;SAVED RHDS1
7629
7630 ;SAVE REGISTERS AGAIN SO THAT COMPARES CAN BE DONE
7631 036362 004037 041214 JSR RO,@SAVER ;SAVE
7632 036366 001634 RHER1 ;FROM
7633 036370 002070 WRFROM ;TO
7634 036372 000015 13. ;NUMBER OF REGISTERS SAVED
7635
7636 ;AS UPPER BYTE OF RHAS CAN BE CHANGING IN A DUAL PORT
7637 ;OPERATION THE UPPER BYTE OF RHAS WILL BE SAVED AS IS
7638 ;SO THAT THE COMPARES ARE ONLY VALID FOR THE LOWER BYTE
7639 036374 113737 003151 002105 MOVB @REINTO+15,@WRFROM+15;SAVE UPPER RHAS
7640
7641
7642 ;COMPARE REGISTERS BEFORE READ IN PRESET COMMAND
7643 ;WITH AFTER COMMAND
7644 036402 004037 041416 JSR RO,@COMPAR ;COMPARE
7645 036406 003134 REINTO ;GOOD BUFFER
7646 036410 002070 WRFROM ;TEST BUFFER
7647 036412 000015 13. ;NUMBER OF REGISTERS
7648 036414 036422 25 ;RETURN FOR ERROR
7649 036416 036422 25 ;SAME
7650 036420 036442 35 ;RETURN FOR GOOD COMPARISON
7651
7652 036422 013705 045272 25: MOV @ERWORD,R5 ;GETTING READY TO INDEX
7653 036426 060505 ADD R5,R5 ;DOUBLE ERROR WORD
7654 036430 016537 001632 040172 MOV RHER1-2(R5),@REGADR ;FAILING REG. ADDRESS
7655 036436 104001 ERROR 1 ;FORCED IAE CAUSED IMPROPER
7656 ;REGISTER CHANGE
7657 036440 000207 RTS PC ;RETURN FOR FURTHER COMPARISONS
7658
7659 ;NO ERRORS
7660
    
```


F02

MAINDEC-11-DZRPS-C, RJPO4 DISKLESS RH11 TEST-PART 1
DZRPSD.P11 T116 ERROR REGISTER 1- BIT #10 IAE

MACY11 27(663) 8-OCT-75 16:14 PAGE 182

SEQ 0224

7661 036442 004737 040522 35: JSR PC,2#CLDISK ;CLEAR GO BIT
7662
7663

```

7664
7665
7666
7667
7668
7669
7670
7671
7672
7673
7674
7675
7676
7677
7678 036446 000004
7679 036450 012706 001000
7680 036454 012737 000117 002020
7681 036462 004737 040522
7682
7683
7684 036466 012777 000001 143156
7685 036474 052777 000004 143150
7686 036502 042777 000004 143142
7687
7688
7689
7690
7691 036510 012777 177400 143106
7692 036516 012700 002070
7693 036522 010077 143100
7694
7695 036526 012705 000400
7696 036532 012720 177777
7697 036536 005305
7698 036540 001374
7699 036542 012777 000000 143066
7700
7701 036550 004737 040556
7702 036554 104400 057176
7703
7704 036560 000000
7705
7706 036562 013711 002044
7707
7708 036566 005037 001774
7709 036572 012777 010000 143042
7710 036600 012777 000633 143036
7711
7712
7713
7714
7715
7716
7717

```

```

;*****
;#TEST 117 ERROR REGISTER 1- BIT #10 IAE
;
;# A WRITE DATA IS GIVEN TO SECTOR 0
;# TRACK 0 CYLINDER 411
;#
;# WORD COUNT IS SET TO 256.
;# AN INDEX PULSE IS GIVEN TO GET RHLA TO 0
;#
;# IAE BIT SHOULD SET
;*****
†ST117: SCOPE
MOV #STACK, SP ;RESET STACK
MOV #TTNO, #STSTNM ;THIS SAVES TEST NUMBER
JSR PC, #CLDISK ;CLEAR REGISTERS AND SET UNIT NO.
;GIVE INDEX PULSE
MOV #DMD, #RHR ;SET DIAGNOSTIC MODE
BIS #MIX, #RHR ;SET INDEX
BIC #MIX, #RHR ;CLEAR INDEX
;THESE ARE REGULAR SETUPS
MOV #-256, #RMC ;256 DATA WORDS 4 HEADER WORDS
MOV #MFROM, RO ;THESE TWO INSTRUCTIONS GETS
MOV RO, #RHA ;ADDR. OF MFROM INTO RO AND
;BUS ADDRESS REGISTER
;COUNTER
IS: MOV #-1, (RO)+ ;MOVE ALL ONES FRO DATA
DEC RS
BNE IS ;BRANCH IF DATA NOT COMPLETE
MOV #0., #RDST ;TRACK=0 SECTOR=0
JSR PC, #CHECKT ;CHECK THAT DVA, RDY, DPR, DRY = 1
TYPE , #PHALT ;AND THAT NO OTHERS = 1. CANNOT CON-
;TINUE TESTING IF BOTH AREN'T TRUE
;STOP THE TEST
MOV #MRIDAT, #RI ;GET READY FOR WRITE
;DATA WITH 60 IN RHCS1
CLR #ERFLG ;CLEAR ERROR FLAG
MOV #FMT22, #RHOF ;FORMA BIT=1 (16 BIT WORDS)
MOV #411., #RHCA ;CYLINDER =411
;AS EXCEPTION IS ASSERTED BEFORE RUN IS
;LATCHED RMC, RHA, RHCS1, RHCS2 CANNOT BE CHECKED
;BECAUSE RMC WILL VARY DEPENDING UPON GATE DELAYS
;ON DIFFERENT UNITS
;THE REGISTERS WILL BE SAVED IN REINTO BUFFER

```



```

7718 036606 004037 041214      JSR    RD, @SAVER      ;SAVE
7719 036612 001634              RHER1                  ;FROM
7720 036614 003134              REINTO                 ;TO
7721 036616 000015              13.                   ;NUMBER SAVED
7722
7723                          ;GO TO WRITE HEADER AND DATA
7724
7725 036620 013700 001652      MOV    @RHR, RD        ;NOW RD HAS MAINTENANCE REG. ADDR.
7726 036624 012710 000001      MOV    @DMD, @RO      ;SET DIAGNOSTIC MODE
7727 036630 052777 000001 142774  BIS    @GO, @RHC51    ;GO
7728
7729                          ;CHANGE SAVED REGISTERS TO EXPECTED VALUE
7730 036636 012737 002000 003134  MOV    @IAE, @REINTO  ;SAVED RHER1
7731 036644 012737 000001 003136  MOV    @1, @REINTO+2 ;SAVED RHDST
7732 036652 013737 002004 003150  MOV    @ATTENT, @REINTO+14 ;SAVED RHAS
7733 036660 052737 000001 003152  BIS    @DMD, @REINTO+16 ;SAVED RHR
7734 036666 052737 140000 003154  BIS    @ATA!ERR, @REINTO+20 ;SAVED RHD51
7735
7736                          ;SAVE REGISTERS AGAIN SO THAT COMPARES CAN BE DONE
7737 036674 004037 041214      JSR    RD, @SAVER      ;SAVE
7738 036700 001634              RHER1                  ;FROM
7739 036702 002070              MRFROM                 ;TO
7740 036704 000015              13.                   ;NUMBER OF REGISTERS SAVED
7741
7742                          ;AS UPPER BYTE OF RHAS CAN BE CHANGING IN A DUAL PORT
7743                          ;OPERATION THE UPPER BYTE OF RHAS WILL BE SAVED AS IS
7744                          ;SO THAT THE COMPARES ARE ONLY VALID FOR THE LOWER BYTE
7745 036706 113737 003151 002105  MOVB  @REINTO+15, @MRFROM+15;SAVE UPPER RHAS
7746
7747
7748                          ;COMPARE REGISTERS BEFORE READ IN PRESET COMMAND
7749                          ;WITH AFTER COMMAND
7750 036714 004037 041416      JSR    RD, @COMPAR    ;COMPARE
7751 036720 003134              REINTO                 ;GOOD BUFFER
7752 036722 002070              MRFROM                 ;TEST BUFFER
7753 036724 000015              13.                   ;NUMBER OF REGISTERS
7754 036726 036734              25                     ;RETURN FOR ERROR
7755 036730 036734              25                     ;SAME
7756 036732 036754              35                     ;RETURN FOR GOOD COMPARISON
7757
7758 036734 013705 045272      25:  MOV    @ERWORD, R5    ;GETTING READY TO INDEX
7759 036740 060505              ADD    R5, R5          ;DOUBLE ERROR WORD
7760 036742 016537 001632 040172  MOV    RHER1-2(R5), @REGADR ;FAILING REG. ADDRESS
7761 036750 104001              ERROR 1                ;FORCED IAE CAUSED IMPROPER
7762                          ;REGISTER CHANGE
7763 036752 000207              RTS    PC              ;RETURN FOR FURTHER COMPARISONS
7764
7765                          ;NO ERRORS
7766
7767 036754 004737 040522      35:  JSR    PC, @CLDISK   ;CLEAR GO BIT

```

```

7768
7769
7770
7771
7772
7773
7774
7775
7776
7777 036760 000004
7778 036762 012767 000001 142214
7779 036770 012767 000000 141000
7780 036776 104400 037004
7781 037002 000425
7782
7783 037056
7784 037056 013746 001762
7785 037062 104410
7786 037064 104400 037072
7787 037070 000402
7788
7789 037076
7790 037076 013746 001112
7791 037102 104410
7792 037104 005037 001112
7793 037110 005737 001770
7794 037114 001415
7795 037116 005067 141760
7796 037122 005237 001100
7797 037126 104400 037324
7798 037132 013746 001100
7799 037136 104410
7800 037140 104400 037341
7801 037144 000137 007272
7802 037150 005337 001764
7803 037154 001413
7804 037156 013700 001762
7805 037162 012701 001742
7806 037166 022100
7807 037170 001401
7808 037172 000775
7809 037174 011137 001762
7810 037200 000137 007272
7811
7812
7813
7814
7815
7816
7817
7818
7819
7820
7821

```

```

;*****
;#TEST 120      END OF DRIVE

;#      THIS IS THE END OF TEST FOR ONE DRIVE
;#      IF THERE ARE MORE DRIVES THEN THE PROGRAM
;#      JUMPS TO TEST 5 FOR NEXT DRIVE TEST
;#      END PASS IS REACHED ONLY AFTER ALL DRIVES ARE COMPLETE

;*****
†ST120: SCOPE
MOV      #1,STIMES      ;;DO 1 ITERATION
MOV      #0,PS          ;;REINSTATE PS TO 0
TYPE     +4             ;;TYPE ASCIZ STRING
BR       64$           ;;GET OVER THE ASCIZ
;;.ASCIZ      <15><12>/TOTAL ERRORS ON THIS PASS ON UNIT NO. /
64$:     MOV      @#UNIT,-(SP)  ;GET READY TO TYPE UNIT NUMBER
TYPDS
TYPE     +4             ;;TYPE ASCIZ STRING
BR       65$           ;;GET OVER THE ASCIZ
;;.ASCIZ      /= /
65$:     MOV      @#SERTTL,-(SP) ;GET READY TO TYPE NUMBER OF ERRORS
TYPDS
CLR      @#SERTTL      ;CLEAR TOTAL NUMBER OF ERRORS
TST      @#SELECT      ;STARTING FROM 200 ?
BEQ      3$           ;BRANCH IF YES
CLR      @#STNN        ;CLEAR TEST NUMBER
INC      @#SPASS       ;INCREASE PASS COUNT
TYPE     @#SERRG       ;TYPE END PASS #
MOV      @#SPASS,-(SP)
TYPDS
TYPE     @#SERRG
JMP      @#TST5        ;JUMP TEST 5
3$:     DEC      @#NUNITS    ;NO. OF UNITS PRESENT DECREMENT
BEQ      SEOP          ;BRANCH IF ALL DRIVES COMPLETE
MOV      @#UNIT,R0     ;UNIT UNDER TEST
MOV      @#UNITS,R1    ;TABLE
1$:     CMP      (R1)+,R0    ;IS THIS UNIT JUST TESTED
BEQ      2$           ;BRANCH IF YES
BR       1$           ;BRANCH IF NO
2$:     MOV      (R1),@#UNIT ;THIS IS NEXT UNIT
JMP      @#TST5        ;GO FOR NEXT TESTS.

.SBTTL
.SBTTL  ***SUBROUTINES***
.SBTTL

;*****
.SBTTL  END OF PASS ROUTINE

```



```

7822      ;*INCREMENT THE PASS NUMBER ($PASS)
7823      ;*TYPE "END PASS #XXXXX" (WHERE XXXXX IS A DECIMAL NUMBER)
7824      ;*IF THERES A MONITOR GO TO IT
7825      ;*IF THERE ISN'T JUMP TO TST1
7826
7827      037204      SEOP:
7828      037204      000004      SCOPE
7829      037206      005067      141670      CLR      $TSTNM      ;; ZERO THE TEST NUMBER
7830      037212      005067      141766      CLR      $TIMES      ;; ZERO THE NUMBER OF ITERATIONS
7831      037216      005267      141656      INC      $PASS      ;; INCREMENT THE PASS NUMBER
7832      037222      042767      100000      141650      BIC      #100000,$PASS      ;; DON'T ALLOW A NEG. NUMBER
7833      037230      005327      DEC      (PC)+      ;; LOOP?
7834      037232      000001      SEOPCT: .WORD      1
7835      037234      003031      BGT      $DOAGN      ;; YES
7836      037236      012737      MOV      (PC)+,$(PC)+      ;; RESTORE COUNTER
7837      037240      000001      SENDCT: .WORD      1
7838      037242      037232
7839      037244      104400      037324      TYPE     $SENDMG      ;; TYPE "END PASS #"
7840      037250      016746      141624      MOV      $PASS,-(SP)      ;; SAVE $PASS FOR TYPEOUT
7841      037254      104410      TYPDS
7842      037256      104400      037341      TYPE     $ENULL      ;; GO TYPE--DECIMAL ASCII WITH SIGN
7843      037262      013700      000042      SGET42: MOV      #42,R0      ;; TYPE A NULL CHARACTER
7844      037266      001414      BEQ      $DOAGN      ;; GET MONITOR ADDRESS
7845      037270      022700      037310      CMP      #SENDAD,R0      ;; BRANCH IF NO MONITOR
7846      037274      001004      BNE      $RESET      ;; IS MONITOR ACT11?
7847      037276      022760      177777      000002      CMP      #-1,2(R0)      ;; NO--BRANCH (IT'S XODP)
7848      037304      001001      BNE      $SENDAD      ;; YES--IS THIS THE LAST PASS?
7849      037306      000005      SRESET: RESET      ;; NO--MAKE ANOTHER PASS
7850      037310      004710      SENDAD: JSR      PC,(R0)      ;; CLEAR THE WORLD
7851      037312      000240      NOP
7852      037314      000240      NOP      ;; GO TO MONITOR
7853      037316      000240      NOP      ;; SAVE ROOM
7854      037320      000137      005346      SDOAGN: JMP      #TST1      ;; FOR
7855      037324      005015      047105      020104      SENDMG: .ASCIZ <15><12>/END PASS #/      ;; ACT11
7856      037332      040520      051523      021440      RETURN
7857      037340      000
7858      037341      377      000      SNULL: .BYTE      -1,-1,0      ;; NULL CHARACTER STRING
7859
7860
7861      ;*****
7862      ;*HERE IS A DETAILED EXPLANATION OF HOW THE LOOP ON ERROR WORKS.
7863      ;*ON HITTING AN ERROR IF THE LOOP ON ERROR SWITCH IS SET, THE
7864      ;*PROGRAM GOES BACK - USUALLY BACK TO THE BEGINNING OF THE TEST.
7865
7866      ;*WHEN THIS OPERATOR SELECTABLE SCOPE LOOP IS USED THEN THE POINT
7867      ;*THE PROGRAM GOES BACK TO CAN BE CHANGED.
7868      ;*THE RESTRICTIONS TO THE POINT WHERE THE PROGRAM CAN GO ARE: -
7869      ;*1. IT MUST BE WITHIN THE TEST UNDER CONSIDERATION
7870      ;*2. LOOP ON ERROR SWITCH MUST BE SET
7871      ;*3. THE ERROR MUST OCCUR WITHIN THE TEST UNDER CONSIDERATION
7872      ;*IF THE ERROR DOES NOT OCCUR WITHIN THE TEST UNDER CONSIDERATION
7873      ;*THE PROGRAM WILL REVERT TO NORMAL OPERATION. HOWEVER, IF LOOP ON
7874      ;*TEST SWITCH IS SET AND THIS OPERATOR SELECTABLE SCOPE LOOP IS USED
7875      ;*THEN THE PROGRAM WILL LOOP BACK TO THE SELECTED POINT WHEN IT

```

```

7876
7877
7878
7879
7880
7881
7882
7883 037344 000000
7884
7885 037346
7886 037346 005067 140424
7887 037352 104400 037360
7888 037356 000421
7889
7890 037422
7891 037422 013746 002020
7892 037426 104402
7893 037430 104400 037436
7894 037434 000414
7895
7896 037466
7897 037466 013746 001110
7898 037472 104402
7899 037474 104400 001215
7900 037500 104400 037506
7901 037504 000430
7902
7903 037566
7904 037566 104400 037574
7905 037572 000430
7906
7907 037654
7908 037654 104400 037662
7909 037660 000422
7910
7911 037726
7912 037726 104416
7913 037730 062716 000002
7914 037734 012637 001106
7915 037740 104400 037746
7916 037744 000417
7917
7918 040004
7919 040004 104400 040012
7920 040010 000440
7921
7922 040112
7923 040112 104416
7924 040114 012637 001110
7925 040120 013746 001106
7926 040124 000002
7927
7928
7929

```

```

;#COMES TO THE END OF THE TEST UNDER CONSIDERATION.
;#
;#AFTER LOOPING FOR SOME TIME IF THE LOOP SWITCH IS PUT DOWN THEN
;#NORMAL OPERATION WILL CONTINUE.
;#*****
TESTAD: 0 ;FIRST ADDRESS OF TEST
OPERSEL:
CLR PS ;MAKE PROCESSOR STATUS ZERO
TYPE +4 ;TYPE ASCIZ STRING
BR 64$ ;GET OVER THE ASCIZ
;:.ASCIZ <15><12>/THE PROGRAM WAS IN TEST NUMBER /
64$:
MOV @TSTNM,-(SP) ;GET READY TO TYPE TEST
TYPOC ;NUMBER
TYPE +4 ;TYPE ASCIZ STRING
BR 65$ ;GET OVER THE ASCIZ
;:.ASCIZ <15><12>/THE LOOP BACK PC WAS /
65$:
MOV @SLPERR,-(SP) ;GET READY TO TYPE LOOP BACK PC
TYPOC
TYPE ,SCRLF
TYPE +4 ;TYPE ASCIZ STRING
BR 66$ ;GET OVER THE ASCIZ
;:.ASCIZ <15><12>/SET SWITCH FOR LOOP ON ERROR OR LOOF ON TEST/
66$:
TYPE +4 ;TYPE ASCIZ STRING
BR 67$ ;GET OVER THE ASCIZ
;:.ASCIZ <15><12>/TYPE THE FIRST PC OF THE TEST TO BE LOOPED ON/
67$:
TYPE +4 ;TYPE ASCIZ STRING
BR 68$ ;GET OVER THE ASCIZ
;:.ASCIZ <15><12>/ FOLLOWED BY A CARRIAGE RETURN /<15><12>
68$:
RDOCT
ADD #2,(SP) ;GET LPADR
MOV (SP)+,@SLPADR
TYPE +4 ;TYPE ASCIZ STRING
BR 69$ ;GET OVER THE ASCIZ
;:.ASCIZ <15><12>/TYPE THE PC WHERE YOU WANT/
69$:
TYPE +4 ;TYPE ASCIZ STRING
BR 70$ ;GET OVER THE ASCIZ
;:.ASCIZ <15><12>/ THE PROGRAM TO LOOP BACK TO FOLLOWED BY A CARRIAGE RETUR
70$:
RDOCT
MOV (SP)+,@SLPERR ;GET LPERR
MOV @SLPADR,-(SP)
RTI

```



```

7930
7931
7932
7933
7934
7935
7936
7937
7938
7939
7940
7941 040126
7942 040126 010046
7943 040130 010146
7944 040132 010246
7945 040134 012700 001624
7946 040140 012701 001674
7947 040144 012702 000023
7948 040150 013021
7949 040152 005302
7950 040154 001375
7951 040156 012602
7952 040160 012601
7953 040162 012600
7954 040164 000207
7955
7956
7957
7958
7959
7960
7961
7962
7963
7964
7965
7966 040166 000000
7967 040170 000000
7968 040172 000000
7969
7970 040174 012567 177766
7971 040200 012504
7972 040202 010467 177764
7973 040206 010567 177756
7974 040212 062705 000004
7975 040216 012703 000001
7976 040222 004767 000016
7977 040226 004767 000012
7978 040232 000241
7979 040234 006103
7980 040236 005703
7981 040240 001370
7982 040242 000205
7983 040244 005103
    
```

.SBTTL SAVE REGISTERS ROUTINE

```

;THIS SAVES THE CONTENTS OF ALL HARDWARE REGISTERS
;IN MEMORY LOCATIONS TAGED FROM "MC" TO "EC2"
    
```

```

;THIS IS DONE SO THAT COMPARES ARE DONE WITH SAVED LOCATIONS
;AND NOT THE REGISTERS THEMSELVES. THIS WILL MAKE
;ERROR PRINTOUTS FOR GOOD AND BAD DATA ALWAYS DIFFRENT
    
```

```

PUTREG:
MOV    R0,-(SP)      ;;PUSH R0 ON STACK
MOV    R1,-(SP)      ;;PUSH R1 ON STACK
MOV    R2,-(SP)      ;;PUSH R2 ON STACK
MOV    #RMC,R0       ;;STARTING ADDRESS OF REG
MOV    #MC,R1         ;;STARTING ADDRESS OF WERE SAVED
MOV    #RHCC-RHMC+2/2,R2 ;;NUMBER OF REG. INTO R2
10S:   MOV    2(R0)+,(R1)+ ;;SAVE HARDWARE REG.
DEC    R2
BNE    10S
MOV    (SP)+,R2      ;;POP STACK INTO R2
MOV    (SP)+,R1      ;;POP STACK INTO R1
MOV    (SP)+,R0      ;;POP STACK INTO R0
RTS    PC
    
```

.SBTTL FLOAT 1 AND 0

```

;FLOAT A ONE AND A ZERO THRU A DESIGNATED REGISTER
;ABSOLUTE ADDRESS OF REG. UNDER TEST IS IN R4
    
```

```

MASK:  0      ;;BITS UNDER TEST
LERR:  0      ;;ERROR HLT ADDRESS
REGADR: 0
BITST:  MOV    (R5)+, MASK ;;FETCH DATA MASK
        MOV    (R5)+, R4   ;;GET ADDRESS OF REG. UNDER TEST
        MOV    R4, REGADR
        MOV    R5, LERR    ;;GET ERROR RETURN ADDR.
        ADD    #4, R5      ;;MODIFY RETURN ADDR. TO JUMP OVER RTS
        MOV    #1, R3      ;;INITIALIZE DATA PATTERN
BLT1:   JSR    PC, BLT2    ;;OUTPUT FLOATING ZERO
        JSR    PC, BLT2    ;;OUTPUT FLOATING ONE
        CLC
        ROL    R3          ;;SHIFT PATTERN
        TST    R3
        BNE    BLT1        ;;BRANCH IF NOT COMPLETE
        RTS    R5          ;;RETURN TO TEST
BLT2:   COM    R3          ;;COMPLEMENT PATTERN
    
```

```

7984 040246 012737 040254 040502      MOV    #BLT3, @#LAD      ;SET SCOPE LOOP
7985 040254 032737 001000 177570 BLT3: BIT    #SM09,@#SMR      ;LOOP ON ERROR
7986 040262 001411                BEQ    45                ;BRANCH IF NO
7987 040264 105737 001103                TSTB  @#SERFLG          ;ANY ERRORS
7988 040270 001406                BEQ    45                ;BRANCH IF NO
7989 040272 000005                RESET ;START WITH AN INIT
7990 040274 013777 001762 141326      MOV    @#UNIT,@RHCS2    ;SET UNIT NUMBER UNDER TEST
7991 040302 004737 052512                JSR    PC,@#TKINT       ;INITILIZE TK
7992                                ;INIT FOR SCOPING LOOPS
7993 040306 010337 001124                4S:  MOV    R3,@#SGDDAT  ;STORE GOOD DATA
7994 040312 005137 040166                COM    @#MASK           ;AND MASK WITH PATTERN
7995 040316 043737 040166 001124      BIC    @#MASK, @#SGDDAT ;CLEAR THE REST
7996 040324 005137 040166                COM    @#MASK           ;RESTORE MASK
7997 040330 013714 001124                MOV    @#SGDDAT, (R4)   ;OUTPUT TO REGISTER
7998 040334 011437 001126                MOV    (R4), @#SBDDAT  ;INPUT FROM REGISTER
7999 040340 005137 040166                COM    @#MASK
8000 040344 043737 040166 001126      BIC    @#MASK, @#SBDDAT ;AND MASK OUT RECEIVED DATA
8001 040352 005137 040166                COM    @#MASK           ;RESTORE MASK
8002 040356 023737 001124 001126      CMP    @#SGDDAT, @#SBDDAT ;IS DATA CORRECT
8003 040364 001424                BEQ    15                ;BRANCH IF GOOD
8004 040366 011437 001126                MOV    (R4), @#SBDDAT
8005 040372 023704 001632                CMP    @#RHCS1, R4      ;REGISTER UNDER TEST RHCS1?
8006 040376 001004                BNE    25                ;BRANCH IF NOT
8007 040400 052737 004200 001124      BIS    #RDY!DVA, @#SGDDAT ;SET RDY AND DVA
8008 040406 000410                BR     35
8009 040410 023704 001630                2S:  CMP    @#RHCS2, R4  ;REGISTER UNDER TEST RHCS2?
8010 040414 001005                BNE    35                ;BRANCH IF NOT
8011 040416 011446                MOV    @R4, -(SP)       ;GET RHCS2
8012 040420 042716 177477                BIC    #1C<IR!OR>, (SP) ;KEEP IR AND OR BIT
8013 040424 052637 001124                BIS    (SP)+, @#SGDDAT ;SET IR OR BITS IF NEEDED
8014 040430 004777 177534                3S:  JSR    PC, @#LERR     ;GO TO REPORT ERROR
8015 040434 000240                NOP
8016 040436 000207                1S:  RTS    PC          ;REPLACE BY 104420 FOR LOCAL SCOPE LOOP

```

.SBTTL CLEAR MEMORY ROUTINE

THIS CLEARS ANY BLOCK OF MEMORY
FILLING IT WITH ANY DATA

```

CALL
JSR    R0, CLAREA
X
Y
Z
;STARTING ADDRESS OF BLOCK
;DATA TO BE FILLED

```

;;R1 WILL HAVE STARTING ADDRESS OF BLOCK TO BE FILLED
;;R2 AFTER SUBTRACTION WILL HAVE TWICE NUMBER OF LOCATIONS
;;R3 WILL HAVE DATA TO BE FILLED
;;TO AVOID DIVIDE ROUTINE TWO DECREMENT R2 WILL BE USED

CLAREA:

```

8035 040440 010146      MOV    R1, -(SP)      ;;PUSH R1 ON STACK
8036 040440 010246      MOV    R2, -(SP)      ;;PUSH R2 ON STACK
8037 040442

```


8038	040444	010346		MOV	R3, -(SP)	:: PUSH R3 ON STACK
8039	040446	012001		MOV	(R0)+, R1	:: FROM
8040	040450	012002		MOV	(R0)+, R2	:: TO
8041	040452	012003		MOV	(R0)+, R3	:: DATA
8042	040454	160102		SUB	R1, R2	:: NO. OF LOCATIONS MINUS TWO
8043	040456	062702	000002	ADD	#2, R2	:: GET TWICE NO OF LOCATIONS
8044	040462	010321	15:	MOV	R3, (R1)+	:: MOVE IN DATA
8045	040464	005302		DEC	R2	
8046	040466	005302		DEC	R2	
8047	040470	001374		BNE	15	:: BRANCH IF NOT COMPLETE
8048	040472	012603		MOV	(SP)+, R3	:: POP STACK INTO R3
8049	040474	012602		MOV	(SP)+, R2	:: POP STACK INTO R2
8050	040476	012601		MOV	(SP)+, R1	:: POP STACK INTO R1
8051	040500	000200		RTS	R0	:: RETURN

```

8052
8053 040502 000000          LAD: 0          .SBTTL LOCAL TRAPS
8054
8055 040504 032737 001000 177570 T.SCOPI: BIT      #SMD9, @#SMR
8056 040512 001402          BEQ      15
8057 040514 013716 040502          MOV      @#LAD, (SP)
8058 040520 000002          IS:      RTI

```

```

8059
8060 ;EXAMPLE OF THE USE OF THE ABOVE
8061 ;THIS WILL LOOP BETWEEN X: AND SCOP1 PROVIDED THERE IS NO "NEWTST"
8062 ;MOV      #X, @#LAD
8063 ;X:      ---
8064 ;      ---
8065 ;      ---
8066 ;      ---
8067 ;      SCOP1
8068

```

```

8069          .SBTTL CLEAR DISK ROUTINE
8070
8071 040522 013701 001632          CLDISK: MOV      @#RHCS1, R1      ;R1 WILL BE CONTROL AND STATUS1
8072 040526 013702 001630          MOV      @#RHCS2, R2      ;R2 WILL BE CONTROL AND STATUS2
8073 040532 013703 001654          MOV      @#RHDS1, R3      ;R3 WILL BE DISK STATUS REGISTER1
8074 040536 013704 001634          MOV      @#RHER1, R4      ;R4 WILL BE ERROR REGISTER #1
8075
8076 040542 012712 000040          MOV      #CLR, @R2          ;CLEAR ALL REG.
8077 040546 013712 001762          MOV      @#UNIT, @R2      ;REINSTATE UNIT NO.
8078 040552 005011          CLR      @R1              ;CLEAR FUNCTION BITS
8079 040554 000207          RTS      PC

```


8080
8081
8082
8083
8084
8085
8086
8087
8088
8089
8090
8091
8092
8093
8094
8095
8096
8097
8098
8099
8100
8101
8102
8103
8104
8105
8106
8107
8108
8109
8110
8111
8112
8113
8114
8115
8116
8117
8118
8119
8120
8121
8122
8123
8124
8125
8126
8127
8128
8129
8130
8131
8132
8133

.SBTTL CHECK DISK STATUS ROUTINES

; THIS CHECKS THAT DEVICE AVAILABLE (DVA) AND READY (RDY) IN RHCS1 = 1
; AND CHECKS THAT DEVICE PRESENT (DPR), DEVICE READY (DRY) IN RHDS1 = 1
; IT ALSO CHECKS THAT NO OTHER BITS IN THESE REGISTERS = 1

```

CHECKT: MOV      (SP), @PCJSR      ;SAVE PC OF JSR+4
          SUB      #4, @PCJSR      ;GET PC OF JSR
          JSR      PC, @PUTREG     ;SAVE REGISTERS
          CMP      @DVA!RDY, @CS1  ;RHCS1 SHOULD HAVE DEVICE AVAILABLE
          BEQ      3$              ;AND BE READY
          ;BRANCH IF GOOD

          BIT      @DVA, @CS1      ;BAD SO TEST DEVICE AVAILABLE
          BNE      1$              ;BRANCH IF DVA THERE
          MOV      R1, @SBDADR     ;ADDRESS OF BAD REGISTER (RHCS1)
          ERROR   2$              ;RHCS1 DID NOT HAVE DEVICE
          ;AVAILABLE AT START OF TEST
          BR       3$              ;BRANCH TO NEXT COMPARE

          BIT      @RDY, @CS1      ;TEST READY
          BNE      2$              ;IF RDY THERE BRANCH
          MOV      R1, @SBDADR     ;ADDRESS OF BAD REGISTER (RHCS1)
          ERROR   2$              ;RHCS1 DID NOT HAVE READY
          ;RIGHT AT START OF TEST
          BR       3$              ;BRANCH TO NEXT COMPARE

          MOV      R1, @SBDADR     ;ADDRESS OF BAD REGISTER (RHCS1)
          ERROR   2$              ;RHCS1 HAD SOME BITS OTHER
          ;THAN DVA AND RDY SET
          ;ALL OTHER BITS SHOULD BE 0

          MOV      @DS1, -(SP)     ;GET RHDS1
          BIC      @VV!PROG, (SP)  ;CLEAR VV AND PROGRAMABLE BIT
          CMP      @DPR!DRY, (SP)+;RHDS1 SHOULD HAVE THESE SET
          BEQ      8$              ;BRANCH IF THEY ARE

          BIT      @DPR, @DS1      ;TEST DRIVE PRESENT
          BNE      5$              ;CONTINUE IF THERE
          MOV      R3, @SBDADR     ;ADDRESS OF BAD REGISTER (RHDS1)
          ERROR   2$              ;RHDS1 DOES NOT HAVE DPR
          BR       7$              ;BRANCH OUT

          BIT      @DRY, @DS1      ;TEST DRIVE READY
          BNE      6$              ;IF DPR WAS THERE, BRANCH IF GOOD
          MOV      R3, @SBDADR     ;ADDRESS OF BAD REGISTER (RHDS1)
          ERROR   2$              ;RHDS1 DOES NOT HAVE DRY
          BR       7$              ;BRANCH OUT

          MOV      R3, @SBDADR     ;ADDRESS OF BAD REGISTER (RHDS1)
          ERROR   2$              ;RHDS1 HAS SOME BITS OTHER
          ;THAN MOL, DRY, DPR, SET
          ;ALL OTHER BITS SHOULD BE 0
          RTS      PC              ;RETURN TO TEST AND HALT
    
```

D03

MAINDEC-11-DZRPS-D, RJPO4 DISKLESS RH11 TEST-PART 1
DZRPSD.P11 CHECK DISK STATUS ROUTINES

MACY11 27(663) 8-OCT-75 16:14 PAGE 193

SEQ 0235

8134 040740 062716 000006
8135 040744 000207
8136

BS:

ADD
RTS

#6, (SP)
PC

;ADJUST STACK TO GET OVER HALT IN TEST
;RETURN TO TEST AND CONTINUE TESTING


```

8137
8138
8139
8140
8141 040746 011637 002002          CHECKE: MOV      (SP),@PCJSR      ;SAVE PC OF JSR+4
8142 040752 162737 000004 002002  SUB      #4,@PCJSR      ;GET PC OF JSR
8143 040760 004737 040126          JSR      PC,@PUTREG     ;READ & SAVE REGISTERS
8144 040764 032737 000200 001702  BIT      #RDY,@CSI      ;RHCSI SHOULD HAVE DEVICE AVAILABLE
8145                                     AND BE READY
8146 040772 001004          BNE      1$            ;BRANCH IF GOOD
8147 040774 010137 001122          MOV      R1,@SBDADR     ;FAILING REGISTER
8148 041000 104026          ERROR   26            ;RHCSI IS IN ERROR
8149                                     DOES NOT HAVE DVA, RDY
8150 041002 000427          BR       4$            ;BRANCH OUT
8151 041004 032737 004000 001702 1$: BIT      #DVA,@CSI      ;RHCSI SHOULD HAVE DEVICE AVAILABLE
8152                                     AND BE READY
8153 041012 001004          BNE      2$            ;BRANCH IF GOOD
8154 041014 010137 001122          MOV      R1,@SBDADR     ;FAILING REGISTER
8155 041020 104026          ERROR   26            ;RHCSI IS IN ERROR
8156                                     DOES NOT HAVE DVA, RDY
8157 041022 000417          BR       4$            ;BRANCH OUT
8158 041024 032737 000200 001724 2$: BIT      #DRY,@DS1      ;RHDS1 SHOULD HAVE DPR,DRY
8159 041032 001004          BNE      3$            ;BRANCH IF THERE
8160 041034 010337 001122          MOV      R3,@SBDADR     ;FAILING REGISTER RHDS1
8161 041040 104026          ERROR   26            ;RHDS1 DOES NOT HAVE DPR,DRY
8162 041042 000407          BR       4$            ;BRANCH OUT
8163 041044 032737 000400 001724 3$: BIT      #DPR,@DS1      ;RHDS1 SHOULD HAVE DPR,DRY
8164 041052 001004          BNE      5$            ;BRANCH OUT AND CONTINUE IF THERE
8165 041054 010337 001122          MOV      R3,@SBDADR     ;FAILING REGISTER RHDS1
8166 041060 104026          ERROR   26            ;RHDS1 DOES NOT HAVE DPR,DRY
8167
8168 041062 000207          4$:  RTS      PC        ;RETURN TO TEST AND HALT
8169
8170 041064 062716 000006          5$:  ADD      #6,(SP)    ;ADJUST STACK TO GET OVER HALT IN TEST
8171 041070 000207          RTS      PC        ;RETURN TO TEST AND CONTINUE TESTING

```

```

8172
8173
8174
8175
8176
8177
8178
8179 041072 177777 TIMCNT: 177777 ;WAITING COUNT
8180 041074 010046 WAIT.T: MOV R0,-(SP) ;SAVE R0
8181 041076 016600 000002 MOV 2(SP),R0 ;GET ADDRESS OF REG. ADDRESS
8182 041102 010037 001176 MOV R0,@STMP3 ;MAT PC+2 IN STMP3
8183 041106 162737 000002 001176 SUB #2,@STMP3 ;MAT PC FOR TYPEOUT
8184 041114 012037 001170 MOV (R0)+,@STMP0 ;WAIT REGISTER ADDRESS
8185 041120 012037 001172 MOV (R0)+,@STMP1 ;WAIT ON BIT
8186 041124 010066 000002 MOV R0,2(SP) ;RESTORE RETURN ON STACK
8187 041130 012600 MOV (SP)+,R0 ;RESTORE R0
8188 041132 013737 041072 001174 MOV @TIMCNT,@STMP2 ;TEMPORARY COUNT
8189 041140 033777 001172 140022 1S: BIT @STMP1,@STMP0 ;IS REQUIRED BIT THERE?
8190 041146 001021 BNE 2S ;BRANCH IF YES
8191 041150 005337 001174 DEC @STMP2 ;COUNT
8192 041154 001371 BNE 1S ;BRANCH IF NOT TIME UP
8193 041156 013737 041072 001174 MOV @TIMCNT,@STMP2 ;TEMPORARY COUNT
8194 041164 033777 001172 137776 3S: BIT @STMP1,@STMP0 ;IS REQUIRED BIT THERE?
8195 041172 001007 BNE 2S ;BRANCH IF YES
8196 041174 005337 001174 DEC @STMP2 ;COUNT
8197 041200 001371 BNE 3S ;BRANCH IF NOT TIME UP
8198 041202 017737 137762 001126 MOV @STMP0,@SBDDAT ;REGISTER CONTENTS
8199 041210 104016 ERROR 16 ;WAITED ON BIT FAILED TO SET
8200 041212 000002 2S: RTI
8201 CALL FOR THE ABOVE WAITLOOP IS
8202
8203 MOV @A,@XS ;A CONTAINS REGISTER ADDRESS
8204 - - - ;HENCE XS WILL HAVE ABSOLUTE REG. ADR.
8205 - - -
8206 - - -
8207
8208 XS: MAT 0 ;ABSOLUTE REG. ADDRESS UNDER WAIT
8209 .WORD 0 ;BIT WAITED FOR
8210 ;CONTINUE
8211
8212 .SBTTL SAVE ROUTINE
8213
8214 ;THIS IS A SUBROUTINE TO SAVE REGISTERS
8215 ;IN THE REGISTER TABLE TO ANY LOCATION
8216 ;THE CALL IS
8217 ;JSR R0,@SAVER
8218 ;FROM
8219 ;TO
8220 ;NUMBER OF WORDS SAVED
8221
8222 041214 SAVER:
8223 041214 010146 MOV R1,-(SP) ;PUSH R1 ON STACK
8224 041216 010246 MOV R2,-(SP) ;PUSH R2 ON STACK
8225 041220 010346 MOV R3,-(SP) ;PUSH R3 ON STACK

```



```

8226 041222 012001      MOV      (R0)+,R1      ;FROM
8227 041224 012002      MOV      (R0)+,R2      ;TO
8228 041226 012003      MOV      (R0)+,R3      ;NUMBER
8229 041230 013122      IS:      MOV      @R1+,(R2)+ ;SAVE REGISTER CONTENTS
8230 041232 005303      DEC      R3           ;COUNT
8231 041234 001375      BNE     1$           ;BRANCH IF NOT DONE
8232 041236 012603      MOV      (SP)+,R3     ;POP STACK INTO R3
8233 041240 012602      MOV      (SP)+,R2     ;POP STACK INTO R2
8234 041242 012601      MOV      (SP)+,R1     ;POP STACK INTO R1
8235 041244 000200      RTS      R0
8236
8237
8238
8239
8240
8241
8242
8243
8244
8245
8246 041246 012737 010000 045152 WRCHHD: MOV      #FMT22,@#CYL ;CYLINDER 0 FORMAT 16 BIT WORDS
8247 041254 112737 000001 045155      MOVVB   #1,@#SECOTR+1 ;TRACK=1
8248 041262 112737 000001 045154      MOVVB   #1,@#SECOTR   ;SECTOR=1
8249 041270 005037 045156      CLR     @#KEY1        ;KEY1=0
8250 041274 005037 045160      CLR     @#KEY2        ;KEY2=0
8251 041300 012767 000044 003724      MOV     #36.,@#NWORD  ;NO OF DATA WORDS
8252 041306 005037 045162      CLR     @#X           ;THIS IS A READ OPERATION
8253 041312 004537 041730      JSR     R5,@#CRC      ;GO TO CALCULATE CRC
8254 041316 045152
8255 041320 047052
8256
8257
8258
8259 041322 004737 040522      JSR     PC,@#CLDISK  ;SET UP GENERAL REGISTERS
8260
8261 041326 012777 177730 140270      MOV     #-40.,@#RMC  ;AND CLEAR DISK REGISTERS
8262 041334 012777 003134 140264      MOV     #REINTO,@#RHA ;36 DATA WORDS 4 HEADER WORDS
8263 041342 112746 000001      MOVVB   #1,-(SP)     ;STARTING ADDRESS OF READ BUFFER
8264 041346 112766 000001 000001      MOVVB   #1,1(SP)    ;SECTOR=1
8265 041354 012677 140256      MOV     (SP)+,@#RDST ;TRACK=1 IN UPPER BYTE
8266 041360 012777 014000 140254      MOV     #FMT22!ECI,@#RHF ;TRACK=1, SECTOR=1 IN RDST
8267
8268
8269 041366 005077 140252      CLR     @#RHCA       ;16 BIT WORDS
8270 041372 004737 040556      JSR     PC,@#CHECKT  ;ECC CORRECTION INHIBIT BECAUSE
8271 041376 104400 057176      TYPE    ,CPHALT     ;ECC LOGIC IS NOT CHECKED YET
8272
8273 041402 000000      HALT
8274 041404 013711 002042      MOV     @#WRCHDT,@R1 ;CYLINDER=0
8275
8276 041410 004737 045026      JSR     PC,@#COMHD  ;CHECK THAT DVA,RDY,DPR,DRY = 1
8277
8278
8279 041414 000207      RTS     PC           ;AND THAT NO OTHERS = 1. CANNOT CON-
;TINUE TESTING IF BOTH AREN'T TRUE
;STOP THE TEST
;WRITE CHECK HEADER AND DATA=52
;INTO RHCS1
;WRITE CHECK HEADER AND DATA
;SAME AS READ HEADER AND DATA
;RETURN TO WRITE CHECK TEST

```

8280
8281
8282
8283
8284
8285
8286
8287
8288
8289
8290
8291
8292
8293
8294
8295 041416
8296 041416 010146
8297 041420 010246
8298 041422 010346
8299 041424 010446
8300 041426 010546
8301 041430 012001
8302 041432 012002
8303 041434 012003
8304 041436 012067 137526
8305 041442 012067 137524
8306 041446 011000
8307 041450 010304
8308 041452 005204
8309 041454 010437 045272
8310 041460 022122
8311 041462 001426
8312
8313 041464 014137 001124
8314 041470 014237 001126
8315 041474 160337 045272
8316 041500 005737 001774
8317 041504 001003
8318 041506 004777 137456
8319 041512 000402
8320 041514 004777 137452
8321 041520 022122
8322 041522 013746 177570
8323 041526 042716 177177
8324 041532 022726 000200
8325 041536 001402
8326 041540 005303
8327 041542 001344
8328 041544
8329 041544 012605
8330 041546 012604
8331 041550 012603
8332 041552 012602
8333 041554 012601

.SBTTL COMPARE ROUTINE

: THIS IS A SUBROUTINE TO COMPARE TWO BLOCKS IN MEMORY
: R1 HAS GOOD DATA BUFFER ADDRESS
: R2 HAS TEST DATA BUFFER ADDRESS
: STMP0 HAS ADDRESS OF RETURN ON ERROR TO PRINT HEADER
: STMP1 HAS ADDRESS OF RETURN ON ERROR TO PRINT DATA
: R3 HAS NUMBER OF WORDS TO BE COMPARED
: R4 HAS ONE MORE THAN NUMBER OF WORDS TO BE COMPARED

COMPAR:

```

MOV R1,-(SP)      ;; PUSH R1 ON STACK
MOV R2,-(SP)      ;; PUSH R2 ON STACK
MOV R3,-(SP)      ;; PUSH R3 ON STACK
MOV R4,-(SP)      ;; PUSH R4 ON STACK
MOV R5,-(SP)      ;; PUSH R5 ON STACK
MOV (R0)+,R1      ;; ADDRESS OF GOOD DATA BUFFER
MOV (R0)+,R2      ;; ADDRESS OF TEST DATA BUFFER
MOV (R0)+,R3      ;; NO OF WORDS TO BE COMPARED
MOV (R0)+,STMP0   ;; RETURN ON ERROR TO PRINT HEADER
MOV (R0)+,STMP1   ;; RETURN ON ERROR TO PRINT DATA
MOV (R0),R0       ;; RETURN ON NO ERROR
MOV R3,R4         ;; NO OF WORDS TO BE COMPARED
INC R4
15: MOV R4,@#ERMWORD ;; FOR ERROR WORD NO
    CMP (R1)+,(R2)+ ;; COMPARE GOOD WITH TEST DATA
    BEQ 35          ;; BRANCH IF GOOD

MOV -(R1),@#SGDDAT ;; GOOD DATA
MOV -(R2),@#SBDDAT ;; BAD DATA
SUB R3,@#ERMWORD   ;; ERROR WORD NO.
TST @#ERFLGS       ;; ANY ERRORS ALREAY THERE
BNE 25             ;; BRANCH IF YES
JSR PC,@STMP0      ;; RETURN TO PRINT HEADER
BR 55              ;; BRANCH TO AVOID PRINTING NEXT ERROR
25: JSR PC,@STMP1   ;; RETURN TO PRINT DATA
55: CMP (R1)+,(R2)+ ;; UNDO -(R1) AND -(R2) FOR ERRORS
    MOV @#SMR,-(SP) ;; GET SWITCH SETTING
    BIC #1C600,(SP) ;; KEEP ONLY SWITCH 7 AND 8
    CMP #SM07,(SP)+ ;; IS 7 SET AND 8 RESET
    BEQ 45          ;; BRANCH OUT IF YES
35: DEC R3          ;; COUNT
    BNE 15          ;; BRANCH IF ALL NOT DEVICE
45: MOV (SP)+,R5    ;; POP STACK INTO R5
    MOV (SP)+,R4    ;; POP STACK INTO R4
    MOV (SP)+,R3    ;; POP STACK INTO R3
    MOV (SP)+,R2    ;; POP STACK INTO R2
    MOV (SP)+,R1    ;; POP STACK INTO R1
    
```



```

8334 041556 000200          RTS      RO          ;RETURN TO MAIN PROGRAM
8335
8336
8337
8338
8339          ;THIS IS A SUBROUTINE TO DO WRITE CHECK DATA
8340          ;CYLINDER 0, TRACK 1, SECTOR 1, KEYS 0
8341
8342          ;THESE ARE TO SET UP FOR DISKLESS USE ONLY
8343
8344 041560 012737 010000 045152 WRCHDA: MOV      #FMT22, @#CYL      ;CYLINDER 0 FORMAT 16 BIT WORDS
8345 041566 112737 000001 045155          MOVVB   #1, @#SECOTR+1    ;TRACK=1
8346 041574 112737 000001 045154          MOVVB   #1, @#SECOTR      ;SECTOR=1
8347 041602 005037 045156          CLR     @#KEY1           ;KEY1=0
8348 041606 005037 045160          CLR     @#KEY2           ;KEY2=0
8349 041612 012737 000040 045232          MOV     #32., @#DANORD    ;NO OF DATA WORDS
8350 041620 005037 045162          CLR     @#X              ;THIS IS A READ OPERATION
8351
8352 041624 004537 041730          JSR     R5, @#CRC         ;GO TO CALCULATE CRC
8353 041630 045152
8354 041632 047052
8355
8356          ;THESE ARE REGULAR SETUPS
8357
8358 041634 004737 040522          JSR     PC, @#CLDISK     ;SET UP GENERAL REGISTERS
8359                                     ;AND CLEAR DISK REGISTERS
8360
8361 041640 012777 177740 137756          MOV     #-32., @#RHMC     ;36 DATA WORDS 4 HEADER WORDS
8362 041646 012777 003134 137752          MOV     #REINT0, @#RHBA  ;STARTING ADDRESS OF READ BUFFER
8363 041654 112746 000001          MOVVB   #1, -(SP)        ;SECTOR=1
8364 041660 112766 000001 000001          MOVVB   #1, 1(SP)        ;TRACK=1 IN UPPER BYTE
8365 041666 012677 137744          MOV     (SP)+, @#RHST    ;TRACK=1, SECTOR=1 IN RHST
8366 041672 012777 014000 137742          MOV     #FMT22!ECI, @#RHOF ;16 BIT WORDS
8367                                     ;ECC CORRECTION INHIBIT BECAUSE
8368                                     ;ECC LOGIC IS NOT CHECKED YET
8369 041700 005077 137740          CLR     @#RHCA           ;CYLINDER=0
8370 041704 004737 040556          JSR     PC, @#CHECKT     ;CHECK THAT DVA, RDY, DPR, DRY = 1
8371 041710 104400 057176          TYPE   ,CPHALT         ;AND THAT NO OTHERS = 1. CANNOT CON-
8372                                     ;TINUE TESTING IF BOTH AREN'T TRUE
8373 041714 000000          HALT                    ;STOP THE TEST
8374 041716 013711 002040          MOV     @#WRCHK, @#R1    ;WRITE CHECK DATA=50 INTO RHCS1
8375 041722 004737 045026          JSR     PC, @#COMHD     ;WRITE CHECK HEADER AND DATA
8376                                     ;SAME AS READ HEADER AND DATA
8377
8378 041726 000207          RTS      PC          ;RETURN TO WRITE CHECK TEST

```

8379
8380
8381
8382
8383
8384
8385
8386
8387
8388
8389
8390
8391
8392
8393
8394
8395
8396
8397
8398
8399
8400
8401
8402
8403
8404
8405
8406
8407
8408
8409
8410
8411
8412
8413
8414
8415
8416
8417
8418
8419
8420
8421
8422
8423
8424
8425
8426
8427
8428
8429
8430
8431
8432

.SBTTL CRC GENERATION ROUTINE

```

: THIS IS A SUBROUTINE TO CALCULATE CRC FOR THE FOUR
: HEADER WORDS AND STORE THEM IN "MCRC" AND "GCRC"
: R1 - REGISTER FOR CRC, INCREMENTED CRC VALUE IS HERE
: R2 - THIS HAS BIT POSITION 2 VALUE C
: R3 - THIS HAS BIT POSITION 16 I.E. OUTPUT BIT VALUE B
: R4 - THIS HAS BIT POSITION 15 VALUE E
: STMP0 - NUMBER OF WORDS
: STMP2 - NUMBER OF BITS PER WORD = 16
: STMP3 - TEMPORARY REG.
: STMP4 - TEMPORARY REG TO TRANSFER CARRY
: STMP5 - THIS HAS DATA BIT VALUE D
    
```

```

: FETCH DATA BIT D
: B = D XOR 16
: C = B XOR 2
: E = B XOR 15
: ROTATE RIGHT ONE POSITION
: B GOES TO POSITION 1
: C GOES TO POSITION 3
: E GOES TO POSITION 16
: REPET 64 TIMES
: CALL JSR R5,@#CRC
: X :FIRST LOCATION AT
: Y :PUT CRC IN MCRC FOR READ GCRC FOR WRITE
    
```

CRC:

```

MOV RD,-(SP) ;: PUSH RD ON STACK
MOV (R5)+,RD ;: GET POINTER TO CYL NO.
MOV R1,-(SP) ;: PUSH R1 ON STACK
MOV R2,-(SP) ;: PUSH R2 ON STACK
MOV R3,-(SP) ;: PUSH R3 ON STACK
MOV R4,-(SP) ;: PUSH R4 ON STACK
CLR R1 ;: CLEAR WORKING LOCATION
CLR @#STMP5
MOV #4,@#STMP0 ;: WORD COUNT
MOV (R0)+,@#STMP3 ;: TEMPORARY WORD STORAGE
MOV #16,@#STMP2 ;: BIT COUNT
MOV @#STMP3,@#STMP4 ;: TEMPORARY WORD STORAGE
ROR @#STMP3 ;: GET LSB INTO "C"
ROR @#STMP5 ;: GET ABOVE "C" INTO STMP5
BIT #BIT0,R1 ;: IS POSITION 15 HIGH
BEQ 1$ ;: BRANCH IF POSITION 16 LOW
MOV #BIT15,R3 ;: GET POSITION 16
BR 2$
1$: CLR R3 ;: GET POSITION 16
2$: ADD @#STMP5,R3 ;: XOR POSITION 16 WITH D
;: TO GIVE B
BIT #BIT14,R1 ;: IS POSITION 2 HIGH
BEQ 3$ ;: BRANCH IF POSITION 2 LOW
MOV #BIT15,R2 ;: GET POSITION 2
    
```

```

041730 010046
041730 012500
041732 010146
041734 010246
041736 010346
041740 010446
041742 005001
041744 005037 001202
041746 012737 000004 001170
041752 012037 001176 16$:
041760 012767 000020 137202
041764 013737 001176 001200
041772 006037 001202 15$:
042000 032701 000001
042010 001403
042014 012703 100000
042016 000401
042022 005003 1$:
042024 063703 25$:
042026 040000
042032 001403
042036 012702 100000
    
```


8433	042044	000401		BR	45	
8434	042046	005002	35:	CLR	R2	;GET POSITION 2
8435	042050	060302	45:	ADD	R3,R2	;XOR B WITH POSITION 2
8436						;TO GIVE C
8437	042052	032701	000002	BIT	#BIT1,R1	;IS POSITION 15 HIGH
8438	042056	001403		BEQ	55	;BRANCH IF POSITION 15 LOW
8439	042060	012704	100000	MOV	#BIT15,R4	;GET POSITION 15
8440	042064	000401		BR	65	
8441	042066	005004		55:	CLR	;GET POSITION 15
8442	042070	060304		65:	ADD	;XOR POSITION 15 WITH B
8443						;TO GIVE E
8444	042072	006037	001200	ROR	#STMP4	;GET LSB INTO "C"
8445	042076	006001		ROR	R1	;GET ABOVE C INTO R1
8446	042100	005703		TST	R3	;TEST B
8447	042102	100403		BMI	75	;BRANCH IF B=1
8448	042104	042701	100000	BIC	#BIT15,R1	;SET B IN POSITION 1
8449	042110	000402		BR	105	
8450	042112	052701	100000	75:	BIS	;SET B IN POSITION 1
8451	042116	005702		105:	TST	;TEST C
8452	042120	100403		BMI	115	;BRANCH IF C=1
8453	042122	042701	020000	BIC	#BIT13,R1	;GET C IN POSITION 3
8454	042126	000402		BR	125	
8455	042130	052701	020000	115:	BIS	;GET C IN POSITION 3
8456	042134	005704		125:	TST	;TEST E
8457	042136	100403		BMI	135	;BRANCH IF E=1
8458	042140	042701	000001	BIC	#BIT0,R1	;GET E IN POSITION 16
8459	042144	000402		BR	145	
8460	042146	052701	000001	135:	BIS	;GET E IN POSITION 16
8461	042152	005337	001174	145:	DEC	;BIT COUNTER
8462	042156	001310		BNE	155	;BRANCH IF 16 NOT DONE
8463	042160	005337	001170	DEC	#STMP0	;WORD COUNTER
8464	042164	001275		BNE	165	;BRANCH IF 4 NOT DONE
8465	042166	010135		MOV	R1,#(R5)+	;PUT CRC WHERE DESIRED
8466	042170	012604		MOV	(SP)+,R4	;POP STACK INTO R4
8467	042172	012603		MOV	(SP)+,R3	;POP STACK INTO R3
8468	042174	012602		MOV	(SP)+,R2	;POP STACK INTO R2
8469	042176	012601		MOV	(SP)+,R1	;POP STACK INTO R1
8470	042200	012600		MOV	(SP)+,R0	;POP STACK INTO R0
8471	042202	000205		RTS	R5	
8472						
8473						
8474						
8475						
8476						
8477						
8478						
8479						
8480						
8481						
8482						
8483						
8484						
8485						
8486	042204					

```

;THIS IS A SUBROUTINE TO SET UP THE SIMULATOR DISK FOR
;CYLINDER 0 (16 BITS PER WORD)
;TRACK 1, SECTOR 1
;KEY1 1
;KEY2 1
;CRC THROUGH THE JSR R5,#CRC
;256 WORDS OF 177400

```

```

;CALL JSR PC,#SETDSK

```

```

SETDSK:

```

8487	042204	010046			MOV	RO, -(SP)	:: PUSH RO ON STACK
8488	042206	010146			MOV	R1, -(SP)	:: PUSH R1 ON STACK
8489	042210	010246			MOV	R2, -(SP)	:: PUSH R2 ON STACK
8490	042212	012700	177400		MOV	#177400, RO	:: DATA IN THE DISK
8491	042216	012701	000400		MOV	#256., R1	:: COUNTER
8492	042222	012702	047070		MOV	#DISK, R2	:: START OF SIMULATOR DISK
8493	042226	010022		15:	MOV	RO, (R2)+	:: MOVE IN DATA
8494	042230	005301			DEC	R1	:: COUNT FOR 256
8495	042232	001375			BNE	15	:: BRANCH IF 256 NOT COMPLETE
8496	042234	012701	000021		MOV	#17., R1	:: 2 ECC WORDS, 1 DATA GAP
8497							:: 14 TOLERANCE GAP
8498	042240	005022		25:	CLR	(R2)+	:: CLEAR ECC, DATA GAP AND
8499							:: TOLERANCE GAP
8500	042242	005301			DEC	R1	:: COUNT
8501	042244	001375			BNE	25	:: BRANCH IF NOT COMPLETE
8502							
8503							
8504							
8505	042246	012737	010000	045152	MOV	#FMT22, @#CYL	:: CYLINDER 0 (16 BIT WORDS)
8506	042254	112737	000001	045155	MOVB	#1, @#SECOTR+1	:: TRACK=1
8507	042262	112737	000001	045154	MOVB	#1, @#SECOTR	:: SECTOR=1
8508	042270	012737	000001	045156	MOV	#1, @#KEY1	:: KEY1=1
8509	042276	012737	000001	045160	MOV	#1, @#KEY2	:: KEY2=1
8510	042304	016737	136070	045232	MOV	256., @#DAMWORD	:: NO. OF DATA WORDS
8511	042312	004537	041730		JSR	R5, @#CRC	:: GO TO CALCULATE CRC
8512	042316	045152			CYL		:: FIRST CRC WORD
8513	042320	047052			MCRC		:: PUT CALCULATED CRC
8514	042322	012602			MOV	(SP)+, R2	:: POP STACK INTO R2
8515	042324	012601			MOV	(SP)+, R1	:: POP STACK INTO R1
8516	042326	012600			MOV	(SP)+, RO	:: POP STACK INTO RO
8517	042330	000207			RTS	PC	

;NOW SET UP FOR DISKLESS USE


```

8518
8519 ; THIS IS A SUBROUTINE TO CHECK HEADER COMPARE ERROR
8520 ; (BIT #7) AND CRC ERROR (BIT #8)
8521
8522 ; CALL JSR RD, @#HCCRCE
8523
8524 ;          COM          ; COMMAND-READ HEADER AND DATA
8525 ;          -WRITE DATA
8526 ;          C          ; CYLINDER
8527 ;          S          ; SECTOR
8528 ;          T          ; TRACK
8529 ;          -N.        ; WORD COUNT
8530 ;          B          ; RHBA BUFFER START
8531 ;          X          ; 1=WRITE DATA 0=READ
8532 ;          H          ; H=1 HEADER CHECK, H=0 CRC CHECK
8533
8534 042332 010037 002002          HCCRCE: MOV    RD, @#PCJSR      ; SAVE PC OF JSR+4
8535 042336 162737 000004 002002 SUB    #4, @#PCJSR    ; GET PC OF JSR
8536 042344 004737 040522          JSR    PC, @#CLDISK ; INIT AND SETUP GENERAL REG.
8537 042350 004737 040556          JSR    PC, @#CHECK1 ; CHECK THAT DVA, RDY, DPR, DRY = 1
8538 042354 104400 057176          TYPE  ,CPHALT     ; AND THAT NO OTHERS = 1. CANNOT CON-
8539                                     ; TINUE TESTING IF BOTH AREN'T TRUE
8540 042360 000000          HALT                    ; STOP THE TEST
8541 042362 011037 001202          MOV    (RD), @#STMP5 ; SAVE COMMAND
8542 042366 012011          MOV    (RD)+, @R1     ; COMMAND
8543 042370 012077 137250          MOV    (RD)+, @RHCA   ; CYLINDER
8544 042374 112046          MOVB  (RD)+, -(SP)    ; SECTOR
8545 042376 105720          TSTB  (RD)+          ; UP DATE RD
8546 042400 112066 000001          MOVB  (RD)+, 1(SP)   ; TRACK
8547 042404 105720          TSTB  (RD)+          ; UPDATE RD
8548 042406 012677 137224          MOV    (SP)+, @RHDS  ; TRACK SECTOR
8549 042412 012077 137206          MOV    (RD)+, @RHMC  ; NO. OF DATA WORDS +4 HEADER
8550                                     ; IF A READ HEADER AND DATA
8551 042416 012077 137204          MOV    (RD)+, @RHBA  ; STARTING ADDRESS OF BUFFER
8552 042422 012037 045162          MOV    (RD)+, @#X   ; X=0 READ HEADER AND DATA
8553                                     ; X=1 WRITE DATA
8554 042426 012777 014000 137206          MOV    #FMT22!ECI, @RHOF ; 16 BITS PER WORD
8555                                     ; ECC CORRECTION INHIBIT
8556 042434 005037 001774          CLR   @#ERFLGS      ; CLEAR ERROR FLAG
8557 042440 004737 045026          JSR   PC, @#COMHD   ; COMMAND
8558
8559 ; IF THE PROGRAM COMES BACK HERE WITHOUT ERROR PRINTOUTS
8560 ; FROM THE "COMHD" ROUTINE THAT MEANS SECTOR GAP,
8561 ; FIRST SYNC, HEADER, HEADER CRC, HEADER GAP AND
8562 ; SYNC BYTE HAVE GONE BY AND SYNC'S WERE CORRECTLY
8563 ; DETECTED
8564 ; HEADER AND DATA ARE TO BE CHECKED.
8565
8566 042444 004737 040126          JSR   PC, @#PUTREG   ; SAVE REGISTERS
8567 042450 005737 001774          TST   @#ERFLGS      ; ANY ERRORS ALREADY THERE
8568 042454 001034          BNE   10$           ; BRANCH IF YES
8569 042456 005737 045162          TST   @#X           ; IS THIS A READ
8570 042462 001015          BNE   3$           ; IF A WRITE DATA BRANCH

```

```

8571
8572
8573
8574
8575
8576
8577
8578 042464 004037 041416 JSR RO,2#COMPAR ;CHECK
8579 042470 002070 MRFROM ;GOOD DATA
8580 042472 003134 REINTO ;TEST BUFFER
8581 042474 000400 256. ;4 HEADER 252 DATA
8582 042476 042504 1$ ;RETURN POINT FOR ERROR HEADER
8583 042500 042510 2$ ;RETURN POINT FOR ERROR DATA
8584 042502 042546 10$ ;RETURN FOR GOOD COMPARISON
8585 042504 104004 1$: ERROR 4 ;READ NEXT ERROR 5
8586 042506 000207 RTS PC ;RETURN TO COMPARISON SUBROUTINE
8587 042510 104005 2$: ERROR 5 ;WORD NO 1 THRU 4 ARE
8588 ;HEADER WORDS AND HENCE
8589 ;SHOULD BE READ AS WRITTEN ON
8590 ;DISK, WORD NOS. 5 ONWARDS
8591 ;SHOULD NOT BE READ AND HENCE
8592 ;READ INTO BUFFER
8593 ;SHOULD BE UNCHANGED
8594 042512 000207 RTS PC ;RETURN TO COMPARISON
8595
8596 042514 000414 BR 10$ ;JUMP OUT
8597
8598 ;NOW THE DISK WILL BE CHECKED
8599 ;NO DATA SHOULD BE WRITTEN
8600 ;REINTO BUFFER HAS BEEN FILLED WITH EXPECTED DATA
8601 ;DISK HAS BEEN FILLED WITH 177400
8602 ;MRFROM HAS BEEN FILLED WITH 125252
8603
8604 042516 004037 041416 3$: JSR RO,2#COMPAR ;CHECK
8605 042522 003134 REINTO ;GOOD DATA BUFFER
8606 042524 047070 DISK ;TEST BUFFER
8607 042526 000400 256.
8608 042530 042536 4$ ;RETURN POINT FOR ERROR HEADER
8609 042532 042542 5$ ;RETURN POINT FOR ERROR DATA
8610 042534 042546 10$ ;RETURN POINT FOR GOOD COMPARISON
8611 042536 104004 4$: ERROR 4 ;READ NEXT ERROR 5
8612 042540 000207 RTS PC ;RETURN TO COMPARISON SUBROUTINE
8613 042542 104005 5$: ERROR 5 ;WORD NO ARE ALL DATA
8614 ;WORDS THE SHOULD NOT
8615 ;HAVE BEEN CHANGED BY THE
8616 ;WRITE COMMAND
8617 042544 000207 RTS PC ;RETURN TO COMPARISON SUBROUTINE
8618 042546 005720 10$: TST (RO)+ ;IS THIS A HCRC ON HCE CHECK?
8619 042550 001442 BEQ 6$ ;BRANCH IF HCRC
8620 042552 022737 000072 001202 CMP #72,2#STMP5 ;IS THIS A READ COMMAND
8621 042560 001417 BEQ 11$ ;BRANCH IF YES
8622 042562 017737 137046 001126 MOV 2#RHER1,2#SBDDAT ;TEST DATA
8623 042570 022737 000200 001126 CMP #HCE,2#SBDDAT ;ONLY HEADER COMPARE BIT?
8624 ;SHOULD BE SET

```



```

8625 042576 001470 BEQ 75 ;BRANCH IF GOOD
8626 042600 013737 001634 040172 MOV @RHER1,@REGADR ;REGISTER ADDRESS RHER1
8627 042606 012737 000200 001124 MOV #HCE,@#SGDDAT ;GOOD DATA
8628 042614 104027 ERROR 27 ;AFTER AN ERROR ON THE
8629 ;HEADER ONLY HCE SHOULD
8630 042616 000460 BR 75 ;BE SET
8631 042620 115:
8632 042620 017737 137010 001126 MOV @RHER1,@#SBDDAT ;TEST DATA
8633 042626 022737 100200 001126 CMP #DCK!HCE,@#SBDDAT ;ONLY HEADER COMPARE BIT?
8634 ;SHOULD BE SET
8635 ;DCK IS SET BECAUSE ECC IS NOT READ
8636 042634 001451 BEQ 75 ;BRANCH IF GOOD
8637 042636 013737 001634 040172 MOV @RHER1,@REGADR ;REGISTER ADDRESS RHER1
8638 042644 012737 100200 001124 MOV #DCK!HCE,@#SGDDAT ;GOOD DATA
8639 042652 104027 ERROR 27 ;AFTER AN ERROR ON THE
8640 ;HEADER ONLY HCE SHOULD
8641 042654 000441 BR 75 ;BE SET
8642 042656 022737 000072 001202 65: CMP #72,@#STMP5 ;IS THIS A READ COMMAND?
8643 042664 001417 BEQ 125 ;BRANCH IF A READ
8644 042666 017737 136742 001126 MOV @RHER1,@#SBDDAT ;TEST DATA
8645 042674 022737 000400 001126 CMP #HCRC,@#SBDDAT ;ONLY CRC ERROR SHOULD BE THERE
8646 042702 001426 BEQ 75
8647 042704 013737 001634 040172 MOV @RHER1,@REGADR ;REG. ADDR = RHER1
8648 042712 012737 000400 001124 MOV #HCRC,@#SGDDAT ;GOOD DATA
8649 042720 104027 ERROR 27 ;AFTER A CRC ERROR ONLY CRC
8650 ;SHOULD BE SET
8651 042722 000416 BR 75 ;BRANCH OUT
8652 042724 017737 136704 001126 125: MOV @RHER1,@#SBDDAT ;TEST DATA
8653
8654 042732 022737 100400 001126 CMP #DCK!HCRC,@#SBDDAT ;HCRC AND DCK SHOULD BE SET
8655 ;DCK IS SET BECAUSE ECC IS NOT READ
8656 042740 001407 BEQ 75 ;BRANCH IF GOOD
8657 042742 012737 100400 001124 MOV #DCK!HCRC,@#SGDDAT ;GOOD DATA
8658 042750 013737 001634 040172 MOV @RHER1,@REGADR ;FAILING REGISTER RHER1
8659 042756 104027 ERROR 27 ;AFTER A CRC ERROR ON A READ
8660 ;DCK AND HCRC SHOULD BE SET
8661 ;DCK IS SET BECAUSE ECC IS NOT READ
8662 042760 000200 75: RTS R0 ;RETURN TO MAIN TEST
8663
8664
8665
8666
8667
8668
8669
8670
8671
8672

```

```

;THIS IS A SUBROUTINE TO LEAVE AT THE MIDDLE OF
;A WRITE HEADER AND DATA COMMAND
;IT TRYS TO GET SECTOR 10, TRACK 0, CYLINDER 0
;BUT COMES OUT AFTER ONE SECTOR
;THE COMMAND OS JSR PC,@#MIDDLE
;BAI IS SET

```

```

8673 042762 MIDDLE:
8674 042762 010046 MOV R0,-(SP) ;:PUSH R0 ON STACK
8675 042764 010146 MOV R1,-(SP) ;:PUSH R1 ON STACK
8676 042766 013777 002046 136636 MOV @#RIFOR,@RHCS1 ;WRITE HEADER AND DATA=62
8677 ;IN RHCS1
8678 042774 012777 177766 136622 MOV #-10.,@RHMC ;10 WORDS

```

8679	043002	012777	002070	136616	MOV	#WRFROM, @RHBA	:BUS ADDRESS=WRFROM
8680	043010	012777	000010	136620	MOV	#10, @RHDS1	:DESIRED TRACK=0 SECTOR=10
8681	043016	052777	000010	136604	BIS	#BA1, @RHCS2	:BUS ADDRESS INCREMENT INHIBIT
8682	043024	012777	010000	136610	MOV	#FMT22, @RHOF	:FORMAT 16 BIT WORDS
8683	043032	005077	136606		CLR	@RHCA	:CYLINDER=0
8684	043036	012737	000001	043064	MOV	#1, @#MID	:SECTOR IS SET TO 1 SO THAT
8685							:WE CAN GET OUT AT THE
8686							:MIDDLE OF AN OPERATION
8687							:LOOKING FOR SECTOR 10
8688	043044	012777	000001	136600	MOV	#DMD, @RHMR	:SET DIAGNOSTIC MODE
8689	043052	052777	000001	136552	BIS	#GO, @RHCS1	:GO TO RHCS1 WITH 62
8690	043060	004137	051204		JSR	R1, @#SEARCH	
8691	043064	000000			.WORD	0	:SECTOR
8692	043066	012601			MOV	(SP)+, R1	:POP STACK INTO R1
8693	043070	012600			MOV	(SP)+, R0	:POP STACK INTO R0
8694	043072	000207			RTS	PC	

MID:

.SBTTL JAM CURRENT CYLINDER ROUTINE

:THIS SUBROUTINE WILL CHANGE THE CURRENT CYLINDER REGISTER
:THIS IS DONE BY GIVING A SEEK COMMAND THEN AN INIT
:WHICH WILL LOAD THE CURRENT CYLINDER WITH THE DESIRED CYLINDER VALUE

:CALL IS
:JSR RO, @#MAKECYL
:XC ;DESIRED VALUE OF CURRENT CYLINDER

MAKECYL:

8710	043074				MOV	R5, -(SP)	:PUSH R5 ON STACK
8711	043074	010546			MOV	R0, @#PCJSR	:PC OF JSR+4
8712	043076	010037	002002		SUB	#4, @#PCJSR	:SAVE PC OF JSR
8713	043102	162737	000004	002002	MOV	(R0)+, R5	:GETTING READY TO FILL DESIRED CYLINDER
8714	043110	012005			MOV	R5, @RHCA	:FILL DESIRED CYLINDER REGISTER
8715	043112	010577	136526		CLR	@RHDS1	:MAKE SURE DESIRED SECTOR TRACK IS NOT ILLEGAL
8716	043116	005077	136514		MOV	@#SEECOM, @RHCS1	:FILL SEEK COMMAND
8717	043122	013777	002054	136502	MOV	#DMD, @RHMR	:SET DIAGNOSTIC MODE
8718	043130	012777	000001	136514	BIS	#GO, @RHCS1	:GO TO SEEK
8719	043136	052777	000001	136466	NOP		:ALLOW TIME FOR SEEK TO HANG UP
8720	043144	000240			NOP		:ALLOW TIME FOR SEEK TO HANG UP
8721	043146	000240			NOP		:ALLOW TIME FOR SEEK TO HANG UP
8722	043150	000240			NOP		:ALLOW TIME FOR SEEK TO HANG UP
8723	043152	000240			JSR	PC, @#CLDISK	:GIVE INIT
8724	043154	004737	040522		MOV	@RHCC, @#SBDDAT	:TEST DATA
8725	043160	017737	136504	001126	CMP	R5, @#SBDDAT	:COMPARE CURRENT CYLINDER
8726	043166	020537	001126		BEQ	1\$:BRANCH IF GOOD
8727	043172	001406			MOV	R5, @#SGDDAT	:GOOD VALUE OF RHCC
8728	043174	010537	001124		MOV	@#RHCC, @#REGADR	:FAILING REGISTER ADDRESS
8729	043200	013737	001670	040172	MOV		:CURRENT CYLINDER DOES NOT MATCH DESIRED CYLINDER
8730	043206	104030			ERROR	30	:REGISTER AFTER A SEEK AND AN INIT
8731							
8732	043210						

1\$:


```

8733 043210 012605      MOV   (SP)+,R5      ;;POP STACK INTO R5
8734 043212 000200      RTS    RO

```

.SBTTL ECC GENERATION AND COMPARISON ROUTINE

```

;THIS SUBROUTINE GENERATES AND TESTS ECC
;CALL JSR PC,ECTEST

```

```

8749      100000      PIE1   =100000
8750      040000      PIE2   =40000
8751      020000      PIE3   =20000
8752      010000      PIE4   =10000
8753      004000      PIE5   =4000
8754      002000      PIE6   =2000
8755      001000      PIE7   =1000
8756      000400      PIE8   =400
8757      000200      PIE9   =200
8758      000100      PIE10  =100
8759      000040      PIE11  =40
8760      000020      PIE12  =20
8761      000010      PIE13  =10
8762      000004      PIE14  =4
8763      000002      PIE15  =2
8764      000001      PIE16  =1
8765      100000      PIE17  =100000
8766      040000      PIE18  =40000
8767      020000      PIE19  =20000
8768      010000      PIE20  =10000
8769      004000      PIE21  =4000
8770      002000      PIE22  =2000
8771      001000      PIE23  =1000
8772      000400      PIE24  =400
8773      000200      PIE25  =200
8774      000100      PIE26  =100
8775      000040      PIE27  =40
8776      000020      PIE28  =20
8777      000010      PIE29  =10
8778      000004      PIE30  =4
8779      000002      PIE31  =2
8780      000001      PIE32  =1

```

```

8781      043214 000000      ECDATA: 0      ;DATA BIT FOR ECC
8782      ;IF ALL ONES THEN CURRENT BIT IS A ONE
8783      ;IF ZERO THEN CURRENT BIT IS A ZERO
8784
8785      043216 000000      GECC1: 0      ;LOW ORDER ECC WORD TO BE GENERATED HERE
8786

```

8787
8788
8789 043220 000000
8790
8791
8792 043222 000000
8793
8794
8795 043224 113713
8796 043226 000000
8797 043230 000000
8798 043232 010041
8799
8800
8801 043234 000000
8802
8803
8804 043236 000000
8805
8806
8807
8808
8809
8810
8811 043240 000000
8812 043242 000000
8813 043244 000000
8814 043246 000000
8815 043250 000000
8816
8817
8818
8819
8820
8821 043252
8822 043252 010046
8823 043254 010146
8824 043256 010246
8825 043260 010346
8826 043262 010446
8827 043264 010546
8828 043266 013701 043216
8829 043272 013702 043220
8830 043276 005737 043214
8831 043302 001406
8832
8833
8834
8835
8836 043304 010103
8837 043306 052703 177776
8838 043312 005103
8839 043314 010300
8840 043316 000404

GECC2: 0
TSECCG: 0
NCODE: 38859.
NCOUNT: 0
POSITI: 0
HARDER: 4129.
DATENV: 0
ZCODE: 0
HADTMP: 0
P3: 0
P12: 0
P22: 0
P24: 0

:=R1
:HIGH ORDER ECC WORD TO BE GENERATED HERE
:=R2
:IF =177777 GENERATE AND TEST ECC FOR THIS BIT
:IF =0 DO NOT GENERATE AND TEST ECC FOR THIS BIT
:N-CODE WORD
:TEMPORARY N CODE
:POSITION REGISTER
:HARD ERROR COUNT
:TRUE COUNT IS 4128 BUT AS COMPARES ARE
:DONE ONE STAGE LATER SO 4129
:DATA ENVELOPE FOR TYPE OUT
:MAX FOR WRITE IS 4096
:MAX FOR READ IS 4128
:LEADING ZEROS ENVELOPE FOR TYPE OUT
:THIS IS SHUT OFF WHEN POSITION COUNTER
:IN ENABLED
:MAX COUNT IS 38859

:TEMPORARY HARD ERROR COUNT

ECTEST:

```

MOV R0,-(SP)      ;; PUSH R0 ON STACK
MOV R1,-(SP)      ;; PUSH R1 ON STACK
MOV R2,-(SP)      ;; PUSH R2 ON STACK
MOV R3,-(SP)      ;; PUSH R3 ON STACK
MOV R4,-(SP)      ;; PUSH R4 ON STACK
MOV R5,-(SP)      ;; PUSH R5 ON STACK
MOV @#GECC1,R1    ;; ECC1 WORD
MOV @#GECC2,R2    ;; ECC2 WORD
TST @#ECDATA      ;; IS CURRENT BIT A ONE
BEQ 25            ;; BRANCH IF CURRENT DATA D=0

```

:IF CARRY IS NOT ZERO THEN D=1
:INVERT X32 TO GIVE R0

15: MOV R1,R3
BIS #1CPIE32,R3
COM R3
MOV R3,R0
BR 35


```

8841
8842 ; IF CARRY IS ZERO THEN D=0
8843 ; X32 BECOMES R0
8844 043320 010103 2S: MOV R1,R3
8845 043322 042703 177776 BIC #1CPIE32,R3
8846 043326 010300 MOV R3,R0
8847
8848 043330 000241 3S: CLC
8849 043332 006000 ROR R0
8850 043334 006000 ROR R0
8851 043336 005700 TST R0
8852 043340 001462 BEQ 10S ; BRANCH IF R0=0
8853 ; INVERT X2
8854
8855 043342 010203 MOV R2,R3
8856 043344 052703 137777 BIS #1CPIE2,R3
8857 043350 005103 COM R3
8858 043352 010337 043242 MOV R3,@P3
8859 043356 006237 043242 ASR @P3
8860
8861 ; INVERT X11
8862
8863
8864 043362 010203 MOV R2,R3
8865 043364 052703 177737 BIS #1CPIE11,R3
8866 043370 005103 COM R3
8867 043372 010337 043244 MOV R3,@P12
8868 043376 006237 043244 ASR @P12
8869
8870 ; INVERT X21
8871
8872 043402 010103 MOV R1,R3
8873 043404 052703 173777 BIS #1CPIE21,R3
8874 043410 005103 COM R3
8875 043412 010337 043246 MOV R3,@P22
8876 043416 006237 043246 ASR @P22
8877
8878 ; INVERT X23
8879
8880 043422 010103 MOV R1,R3
8881 043424 052703 176777 BIS #1CPIE23,R3
8882 043430 005103 COM R3
8883 043432 010337 043250 MOV R3,@P24
8884 043436 006237 043250 ASR @P24
8885
8886 ; NOW THAT R0 FOR POSITION 1
8887 ; F3 FOR POSITION 3
8888 ; P12 FOR POSITION 12
8889 ; P22 FOR POSITION 22
8890 ; P24 FOR POSITION 24
8891 ; ARE KNOWN THE ROTATE WILL BE DONE AND
8892 ; THESE BITS JAMED IN
8893
8894 043442 006002 ROR R2
    
```

```

8895 043444 006001 ROR R1
8896 043446 053700 043242 BIS @#P3,RO
8897 043452 053700 043244 BIS @#P12,RO
8898 043456 042702 120020 BIC #PIE1!PIE3!PIE12,R2
8899 043462 050002 BIS RO,R2
8900
8901 043464 005000 CLR RO
8902 043466 053700 043246 BIS @#P22,RO
8903 043472 053700 043250 BIS @#P24,RO
8904 043476 042701 002400 BIC #PIE22!PIE24,R1
8905 043502 050001 BIS RO,R1
8906 043504 000404 BR 12$
8907
8908 ;THE PROGRAM COMES HERE IF RO=0
8909 ;SO AFTER ROTATE RO GETS PUT INTO POSITION 1
8910
8911 043506 006002 10$: ROR R2
8912 043510 006001 ROR R1
8913 043512 042702 100000 BIC #PIE1,R2
8914 043516 010137 043216 12$: MOV R1,@#GECC1 ;SAVE ECC1
8915 043522 010237 043220 MOV R2,@#GECC2 ;SAVE ECC2
8916 043526 005737 043222 TST @#TSECCG ;IS HARDWARE TO BE CHECKED
8917 ;IF =1777777 TEST HARDWARE
8918 ;IF = 0 DO NOT TEST HARDWARE
8919 043532 001432 BEQ 14$ ;BRANCH IF HARDWARE NOT TO BE CHECKED
8920
8921
8922 ;CHECK HARDWARE
8923 043534 032737 000400 177570 BIT #SM8,@#SMR ;IS SWITCH 8 SET
8924 043542 001005 BNE 15$ ;BRANCH IF SM8 IS SET
8925 043544 032737 000100 177570 BIT #SM6,@#SMR ;IS SWITCH 6 SET
8926 043552 001401 BEQ 15$ ;BRANCH IF SM6 IS NOT SET
8927 043554 000421 BR 14$ ;IF SWITCH 8 IS NOT SET AND
;SWITCH 6 IS SET THEN
;DO NOT DO COMPARES
8928
8929
8930 043556 010146 15$: MOV R1,-(SP) ;GOOD PATTERN REGISTER
8931 043560 042716 174000 BIC #174000,(SP) ;GET ONLY PATTERN BITS
8932 043564 022677 136074 CMP (SP)+,@#RHEC2 ;COMPARE PATTERN REGISTER
8933 043570 001404 BEQ 13$ ;BRANCH IF GOOD
8934 ;TO SAVE TIME
8935 043572 004737 040126 JSR PC,@#PUTREG ;SAVE REGISTERS
8936 043576 104035 ERROR 35 ;PATTERN REGISTER IN 11 BITS IN ERROR
8937 043600 000407 BR 14$ ;BRANCH OUT
8938 043602 023777 043230 136052 13$: CMP @#POSITI,@#RHEC1 ;COMPARE POSITION REGISTER
8939 043610 001403 BEQ 14$ ;BRANCH IF GOOD
8940 ;TO SAVE TIME
8941 043612 004737 040126 JSR PC,@#PUTREG ;SAVE REGISTERS
8942 043616 104035 ERROR 35 ;POSITION REGISTER IN ERROR
8943 ;"DATA ENVELOP" GIVES NUMBER OF CLOCK
8944 ;PULSES FROM BEGINING OF COMMAND
8945 ;THAT IS THE CLOCKS IN THE R/W DATA FIELD ENVELOPE
8946
8947 ;IN A WRITE THERE ARE 10000 OCTAL CLOCKS
8948 ;IN A READ THERE ARE 10040 OCTAL CLOCKS

```


8949
8950
8951
8952
8953
8954
8955
8956
8957
8958
8959
8960
8961
8962
8963
8964
8965
8966
8967
8968

043620
043620 012605
043622 012604
043624 012603
043626 012602
043630 012601
043632 012600
043634 000207

145:

MOV (SP)+,R5
MOV (SP)+,R4
MOV (SP)+,R3
MOV (SP)+,R2
MOV (SP)+,R1
MOV (SP)+,R0
RTS PC

:"N-CODE ZEROS" GIVE THE NUMBER OF CLOCKS
:GIVEN FOR THE LEADING ZEROS FIELD
:MAX COUNT IS 113713 OCTAL
:"GOOD POSITION" GIVES NUMBER OF CLOCKS
:GIVEN AFTER LEADING ZEROS WHICH IS FOR THE DATA
:FIELD
:MAX COUNT IS 10040 OR 10041 OCTAL

::POP STACK INTO R5
::POP STACK INTO R4
::POP STACK INTO R3
::POP STACK INTO R2
::POP STACK INTO R1
::POP STACK INTO R0



```

8969
8970
8971      ; THIS SUBROUTINE WILL CONTROL THE ECC GENERATION ROUTINE
8972      ; FOR ERROR CORRECTION PROCESS
8973      ; CALL JSR, PC, @#ECORR
8974      ; XP      ; EXPECTED POSITION REGISTER WHEN CORRECTION IS COMPLETE
8975
8976
8977
8978 043636 000000      ERPOS: 0      ; POSITION REG. WHEN CORRECTION IS COMPLETE
8979
8980
8981
8982 043640 010037 002002      ECORR: MOV      RD, @#PCJSR      ; SAVE PC OF JSR + 4
8983 043644 162737 000004 002002      SUB      #4, @#PCJSR      ; SAVE PC OF JSR
8984 043652 012037 043636      MOV      (RD)+, @#ERPOS      ; GET POSITION REG. WHEN CORRECTION IS COMPLETE
8985 043656 010146      MOV      R1, -(SP)      ; PUSH R1 ON STACK
8986 043650 013701 001652      MOV      @#RHMR, R1      ; MAINTENANCE REGISTER
8987 043664 012711 000001      MOV      #DMD, @R1      ; SET DIAGNOSTIC MODE BIT
8988 043670 005037 043214      CLR      @#ECDATA      ; ECC DATA IS ZERO
8989
8990
8991
8992 043674 005737 043230      1S:   TST      @#POSITI      ; IS SOFTWARE POSITION NON ZERO
8993 043700 001007      BNE      2S      ; BRANCH IF N-CODE S COMPLETE
8994 043702 005337 043226      DEC      @#NCOUNT      ; DECREMENT N-CODE
8995 043706 001001      BNE      6S      ; BRANCH IF N-CODE IS NOT COMPLETE
8996 043710 000403      BR      2S      ; BRANCH AS N-CODE IS COMPLETE
8997 043712 005237 043236      6S:   INC      @#ZCODE      ; INCREMENT CLOCKS GIVEN FOR LEADING ZEROS
8998 043716 000420      BR      3S      ; BRANCH AS N-CODE IS NOT COMPLETE
8999
9000 043720 005237 043230      2S:   INC      @#POSITI      ; INCREMENT SOFTWARE POSITION
9001 043724 023737 043636 043230      CMP      @#ERPOS, @#POSITI ; HAVE ENOUGH CLOCKS BEEN GIVEN TO DETECT ERROR
9002 043732 103012      BHS      3S      ; BRANCH IF MORE CLOCKS TO BE GIVEN
9003 043734 023737 043240 043230      CMP      @#HADTMP, @#POSITI ; HAVE ENOUGH CLOCKS BEEN GIVEN FOR HARD ERROR
9004
9005 043742 001415      BEQ      5S      ; THAT IS HAVE 4128 MORE CLOCKS BEEN GIVEN
9006 043744 032711 000400      BIT      #ZER, @R1      ; BRANCH IF YES
9007 043750 001016      BNE      4S      ; CHECK ZERO DETECT BIT IN RHMR
9008
9009 043752 004737 040126      ; TO SAVE TIME
9010 043756 104034      JSR      PC, @#PUTREG      ; SAVE REGISTERS
9011
9012
9013
9014 043760 052711 000002      3S:   BIS      #MCLK, @R1      ; SET CLOCK
9015 043764 042711 000002      BIC      #MCLK, @R1      ; CLEAR CLOCK
9016 043770 004737 043252      JSR      PC, @#ECTEST      ; GO TO GENERATE AND TEST ECC
9017 043774 000737      BR      1S      ; CONTINUE
9018
9019      ; THIS EXTRA CLOCK IS TO BRING ECH HIGH
9020      ; AFTER THIS CLOCK POSITION REGISTER MAY BE 10040 OR 10041 OCTAL
9021
9022 043776 052711 000002      5S:   BIS      #MCLK, @R1      ; SET CLOCK

```



```

9023 044002 042711 000002          BIC    #MCLK,R1      ;CLEAR CLOCK
9024
9025          4$:
9026 044006 012601          MOV    (SP)+,R1     ;;POP STACK INTO R1
9027 044010 000200          RTS    R0
9028
9029
9030
9031
9032
9033
9034
9035
9036
9037
9038

```

; THIS SUBROUTINE GENERATES THE ECC FOR WHAT IS ON DISK AND INSERTS THEM
; ON LOCATIONS "DISK+1000" AND "DISK+1002"

```

9039 044012          FILLEC:
9040 044012 010046          MOV    R0,-(SP)     ;;PUSH R0 ON STACK
9041 044014 010146          MOV    R1,-(SP)     ;;PUSH R1 ON STACK
9042 044016 010246          MOV    R2,-(SP)     ;;PUSH R2 ON STACK
9043 044020 010346          MOV    R3,-(SP)     ;;PUSH R3 ON STACK
9044 044022 010446          MOV    R4,-(SP)     ;;PUSH R4 ON STACK
9045 044024 010546          MOV    R5,-(SP)     ;;PUSH R5 ON STACK
9046 044026 005037 043230          CLR    @#POSITI    ;CLEAR POSITION
9047 044032 005037 043216          CLR    @#GECC1     ;CLEAR GECC1
9048 044036 005037 043220          CLR    @#GECC2     ;CLEAR
9049 044042 012701 047070          MOV    #DISK,R1     ;POINTER TO DATA FOR ECC GENERATION
9050 044046 012702 000400          MOV    #256,R2     ;COUNTER FOR NUMBER OF DATA WORDS
9051 044052 012703 000020          9$: MOV    #16,R3     ;COUNTER FOR NUMBER OF BITS PER WORD
9052 044056 012104          MOV    (R1)+,R4    ;DATA IN R4
9053 044060 006004          10$: ROR    R4       ;GET ONE DATA BIT IN CARRY
9054 044062 103004          BCC    11$         ;BRANCH IF DATA BIT IS ZERO
9055 044064 012737 177777 043214          MOV    #-1,@#ECDATA ;ECC DATA BIT IS A ONE
9056 044072 000402          BR     12$         ;BRANCH TO GENERATE ECC
9057 044074 005037 043214          11$: CLR    @#ECDATA  ;ECC DATA BIT IS A ZERO
9058 044100 004737 043252          12$: JSR    PC,@#ECTEST ;GO TO GENERATE ECC
9059 044104 005303          DEC    R3         ;DECREMENT BIT COUNT
9060 044106 001364          BNE    10$        ;BRANCH IF 16 BITS NOT DONE
9061 044110 005302          DEC    R2         ;DECREMENT WORD COUNT
9062 044112 001357          BNE    9$         ;BRANCH IF 256 WORDS NOT DONE
9063 044114 013737 043216 050070          MOV    @#GECC1,@#DISK+<256.*2>;INSERT ECC1 ON DISK
9064 044122 013737 043220 050072          MOV    @#GECC2,@#DISK+<257.*2>;INSERT ECC2 ON DISK
9065 044130 012605          MOV    (SP)+,R5    ;;POP STACK INTO R5
9066 044132 012604          MOV    (SP)+,R4    ;;POP STACK INTO R4
9067 044134 012603          MOV    (SP)+,R3    ;;POP STACK INTO R3
9068 044136 012602          MOV    (SP)+,R2    ;;POP STACK INTO R2
9069 044140 012601          MOV    (SP)+,R1    ;;POP STACK INTO R1
9070 044142 012600          MOV    (SP)+,R0    ;;POP STACK INTO R0
9071 044144 000207          RTS    PC
9072
9073
9074
9075
9076

```

```

9077          .SBTTL  RH BASE ADDRESS CHANGE ROUTINE
9078
9079          ;#      THIS ROUTINE WILL ALLOW THE CHANGE OF THE BASE
9080          ;#      ADDRESS FROM 176700 TO ANY TYPED VALUE
9081
9082          BASECH:
9083          044146 104400 044154      TYPE      +4      ;;TYPE ASCIZ STRING
9084          044152 000425              BR      64$      ;;GET OVER THE ASCIZ
9085          ;;.ASCIZ      <15><12>/PRESENT BASE ADDRESS OF REGISTERS IS /
9086          64$:
9087          044226 013746 001632      MOV      @#RHCS1,-(SP) ;GET READY TO TYPE OLD BASE
9088          044232 104402              TYP0C
9089          044234 104400 044242      TYPE      +4      ;;TYPE ASCIZ STRING
9090          044240 000425              BR      65$      ;;GET OVER THE ASCIZ
9091          ;;.ASCIZ      <15><12>/TYPE NEW BASE ADDRESS FOLLOWED BY 'CR' /
9092          65$:
9093          044314 004737 052512      JSR      PC,@#STKINT ;INITIALIZE THE TTY KEYBOARD
9094          044320 104416              RDOCT
9095          044322 012700 001622      MOV      @#RHDB,RO ;GET STARTING ADDRESS OF REGISTERS
9096          044326 012701 000024      MOV      #20,R1 ;NUMBER OF REGISTERS
9097          044332 042710 177700      BIC      #1C77,(RO) ;CLEAR OLD BASE
9098          044336 051620              BIS      (SP),(RO)+ ;SET NEW BASE
9099          044340 005301              DEC      R1 ;COUNT
9100          044342 001373              BNE      1$ ;BRANCH IF 20 NOT DONE
9101          044344 104400 044352      TYPE      +4      ;;TYPE ASCIZ STRING
9102          044350 000417              BR      66$      ;;GET OVER THE ASCIZ
9103          ;;.ASCIZ      <15><12>/PRESENT VECTOR ADDRESS IS /
9104          66$:
9105          044410 013746 001620      MOV      @#RPVEC,-(SP) ;GET READY TO TYPE OLD VECTOR ADDRESS
9106          044414 104402              TYP0C
9107          044416 104400 044424      TYPE      +4      ;;TYPE ASCIZ STRING
9108          044422 000437              BR      67$      ;;GET OVER THE ASCIZ
9109          ;;.ASCIZ      <15><12>/TYPE NEW VECTOR ADDRESS OR RETYPE OLD ONE FOLLOWED BY "CR
9110          67$:
9111          044522 104416              RDOCT
9112          044524 012637 001620      MOV      (SP)+,@#RPVEC ;SETUP VECTOR ADDRESS
9113          044530 104400 044536      TYPE      +4      ;;TYPE ASCIZ STRING
9114          044534 000402              BR      68$      ;;GET OVER THE ASCIZ
9115          ;;.ASCIZ      <15><12>/
9116          68$:
9117          044542 104400 044550      TYPE      +4      ;;TYPE ASCIZ STRING
9118          044546 000421              BR      69$      ;;GET OVER THE ASCIZ
9119          ;;.ASCIZ      <15><12>/RESTART PROGRAM FROM 200 OR 210/
9120          69$:
9121          044612 104400 044620      TYPE      +4      ;;TYPE ASCIZ STRING
9122          044616 000416              BR      70$      ;;GET OVER THE ASCIZ
9123          ;;.ASCIZ      <15><12>/NEW BASE WILL REMAIN - /
9124          70$:
9125          044654 013746 001632      MOV      @#RHCS1,-(SP)
9126          044660 104402              TYP0C
9127          044662 104400 044670      TYPE      +4      ;;TYPE ASCIZ STRING
9128          044666 000416              BR      71$      ;;GET OVER THE ASCIZ
9129          ;;.ASCIZ      <15><12>/NEW VECTOR WILL REMAIN - /
9130          71$:

```



```

9131 044724 013746 001620
9132 044730 104402
9133 044732 104400 044740
9134 044736 000416
9135
9136 044774
9137 044774 000000
9138
9139
9140
9141
9142
9143
9144
9145
9146
9147 044776 000000
9148 045000 004737 040522
9149 045004 013712 044776
9150 045010 005714
9151 045012 032712 010000
9152 045016 001401
9153 045020 000773
9154 045022 000772

```

```

MOV @#RPVEC,-(SP)
TYPOC
TYPE +4 ;;TYPE ASCIZ STRING
BR 72$ ;;GET OVER THE ASCIZ
;;.ASCIZ <15><12>/UNTIL PROGRAM IS RELOADED/
72$: HALT

```

```

:THIS IS A LITTLE ROUTINE THAT TESTS NED BIT 11 IN RHCS2
:THIS LOOPS HERE FOR EVER
:TO BE USED ONLY IF DRIVES PRESENT LOOKING AT NED DOES NOT AGREE
:WITH WHAT IS REALY THERE
ERUNIT: 0 ;UNIT UNDER MANUAL TEST
ERSTART: JSR PC,@#CLDISK ;SET GENERAL REG.
MOV @#ERUNIT,@R2 ;SELECT UNIT
1$: TST @R4 ;TEST RHER1
BIT @NED,@R2 ;TEST NED
BEQ 2$ ;BRANCH IF GOOD
BR 1$ ;NED NOT SET
2$: BR 1$ ;NED SET

```

9155
9156
9157
9158
9159
9160
9161
9162
9163
9164
9165
9166
9167
9168
9169
9170
9171
9172
9173
9174
9175
9176
9177
9178
9179
9180
9181
9182
9183
9184
9185
9186
9187
9188
9189
9190
9191
9192
9193
9194
9195
9196
9197
9198
9199
9200
9201
9202
9203
9204
9205
9206
9207
9208

.SBTTL DISK SIMULATION

: IN A WRITE HEADER AND DATA COMMAND FILL THE FOLLOWING
: WCLY=WITH CYLINDER TO BE ON DISK
: WSECTR=WITH SECTOR AND TRACK TO BE ON DISK
: WKEY1= WITH KEY1 TO BE ON DISK
: WKEY2= WITH KEY2 TO BE ON DISK
: FNWORD= NO OF DATA WORDS TO BE WRITTEN ON DISK
: THE COMMAND THEN IS JSR PC,COMHD

: IN A WRITE DATA COMMAND FILL THE FOLLOWING
: CYL=WITH CYLINDER TO BE FOUND ON DISK
: SECOTR= WITH SECTOR AND TRACK TO BE FOUND ON DISK
: KEY1= WITH KEY1 TO BE FOUND ON DISK
: KEY2= WITH KEY2 TO BE FOUND ON DISK
: X= 1 MUST BE ONE
: NOWORD= WITH NUMBER OF DATA WORDS TO BE WRITTEN
: THE COMMAND THEN IS JSR PC,COMHD

: IN A READ HEADER AND DATA COMMAND FILL THE FOLLOWING
: CYL= WITH CYLINDER TO BE FOUND ON DISK
: SECOTR= WITH SECTOR AND TRACK TO BE FOUND ON DISK
: KEY1= WITH KEY1 TO BE FOUND ON DISK
: KEY2=WITH KEY2 TO BE FOUND ON DISK
: DAMORD= WITH NUMBER OF WORDS TO BE FOUND ON DISK
: X=0 MUST BE ZERO
: THE COMMAND THEN IS JSR PC,COMHD

: IN A READ DATA COMMAND FILL THE FOLLOWING
: CYL= WITH CYLINDER TO BE FOUND ON DISK
: SECOTR= WITH SECTOR AND TRACK TO BE FOUND ON DISK
: KEY1= WITH KEY1 TO BE FOUND ON DISK
: KEY2=WITH KEY2 TO BE FOUND ON DISK
: DAMORD= WITH NUMBER OF WORDS TO BE FOUND ON DISK
: X=0 MUST BE ZERO

NO4

MAINDEC-11-DZRPS-D, RJPO4 DISKLESS RH11 TEST-PART 1
DZRPSD.P11 DISK SIMULATION

MACY11 27(663) 8-OCT-75 16:14 PAGE 216

SEQ 0258

9209
9210

; THE COMMAND THEN IS JSR PC, COMHD
;

9211
9212
9213
9214
9215
9216
9217
9218
9219
9220
9221
9222
9223
9224
9225
9226
9227
9228
9229
9230
9231
9232
9233
9234
9235
9236
9237
9238
9239
9240
9241
9242
9243
9244
9245
9246
9247
9248
9249
9250
9251
9252
9253
9254
9255
9256
9257
9258
9259
9260
9261
9262
9263
9264

045024 000000
045026 011637 002002
045032 162737 000004 002002
045040 010046
045042 010146
045044 010246
045046 010346
045050 010446
045052 010546
045054 012777 000001 134570
045062 052777 000001 134542

045070 012737 000113 045024
045076 005337 045024
045102 001375

045104 016746 000044
045110 042716 177740
045114 012637 045124
045120 004137 051204
045124 000000
045126 012701 000240
045132 010137 045164
045136 010137 045166
045142 010137 045170
045146 004137 045274

045152 000000
045154 000000
045156 000000
045160 000000
045162 000000

045164 000240

:WRITE DATA COMMAND
:OR READ COMMAND I.E DATA ONLY OR HEADER AND DATA

RUNCTR: .WORD 0
COMHD: MOV (SP), @PCJSR ;SAVE PC OF JSR + 4
SUB #4, @PCJSR ;SAVE PC OF JSR
MOV R0, -(SP) ;PUSH R0 ON STACK
MOV R1, -(SP) ;PUSH R1 ON STACK
MOV R2, -(SP) ;PUSH R2 ON STACK
MOV R3, -(SP) ;PUSH R3 ON STACK
MOV R4, -(SP) ;PUSH R4 ON STACK
MOV R5, -(SP) ;PUSH R5 ON STACK
MOV #DMD, @RHMR ;SET DIAGNOSTIC MODE
BIS #GO, @RHCSI ;ISSUE 'GO' BIT & STALL 'TILL 'RUN' LINE
;FUNCTION CODE IS ISSUED BY THE TEST

RUNMAT: MOV #75., @RUNCTR ;LOAD STALL COUNT = APPROX. 450US
;FOR 11/50 CPU WITH CORE MEMORY
IS: DEC @RUNCTR ;COUNT DOWN ONE
BNE IS ;CONTINUE UNTIL = 0

MOV SECOTR, -(SP) ;GET DESIRED SECTOR/TRACK
BIC #177740, (SP) ;MAKE ONLY SECTOR
MOV (SP)+, @TRK ;SAVE SECTOR
JSR R1, @SEARCH ;DO SEARCH SECTOR & ISSUE SECTOR CLOCKS

TRK: .WORD 0
MOV #+NOP, R1 ;GOING TO MOVE NOPS
MOV R1, @SSYN ;NOP INTO SSYN
MOV R1, @HEDGAP ;NOP INTO HEDGAP
MOV R1, @HEDSYN ;NOP INTO HEDSYN
JSR R1, @RDHEAD

CYL: .WORD 0 ;CYLINDER ADDRESS
SECOTR: .WORD 0 ;SECTOR/TRACK ADDRESS
KEY1: .WORD 0 ;KEY1 WORD
KEY2: .WORD 0 ;KEY2 WORD
X: .WORD 0 ;X=1 WRITE COMMAND
;X=0 READ COMMAND

SSYN: NOP
; IF "ERROR 2" INSERTED BY RDHEAD
; SUBROUTINE THEN THE FIRST SYNC.
; IS NOT DETECTED. NO BAD DATA
; IS GIVEN BECAUSE SYNC=144000
; CANNOT BE READ. WORD NO
; IS "1" BECAUSE THIS IS THE FIRST
; WORD TESTED

9265											
9266	045166	000240									
9267											
9268											
9269											
9270											
9271											
9272											
9273											
9274											
9275											
9276											
9277	045170	000240									
9278											
9279											
9280											
9281											
9282											
9283											
9284											
9285											
9286											
9287											
9288	045172	005737	001774		TST	2#ERFLGS					
9289	045176	001017			BNE	OUT					
9290	045200	005737	045162		TST	2#X					
9291	045204	001410			BEQ	DAREAD					
9292	045206	005737	045266		TST	2#NOSYNC					
9293											
9294											
9295											
9296	045212	001011			BNE	OUT					
9297	045214	004137	046540		JSR	R1,2#WRDATA					
9298											
9299	045220	000000			NOWORD:	.WORD	0				
9300	045222	000000			Y:	.WORD	0				
9301	045224	000404				BR	OUT				
9302	045226	004137	051460		DAREAD:	JSR	R1,2#REDATA				
9303	045232	000000			DAMORD:	.WORD	0				
9304	045234	000000				.WORD	0				
9305	045236				OUT:						
9306	045236	012605			MOV	(SP)+,R5					
9307	045240	012604			MOV	(SP)+,R4					
9308	045242	012603			MOV	(SP)+,R3					
9309	045244	012602			MOV	(SP)+,R2					
9310	045246	012601			MOV	(SP)+,R1					
9311	045250	012600			MOV	(SP)+,R0					
9312	045252	000207			RTS	PC					

```

; IF "ERROR 3" INSERTED BY
; RDHEAD SUBROUTINE THEN THE
; HEADER GAP 0'S WERE NOT
; WRITTEN RIGHT.
; IF "WORD NO" CONTAINS, SAY
; 3(8), THEN IT IS THE THIRD
; WORD OF A 5 WORD HEADER
; GAP THAT IS WRONG
; "BAD DATA" CONTAINS WHAT IS
; GOING ON THE DISK

; IF "ERROR 3" INSERTED BY RDHEAD
; SUBROUTINE THEN THE HEADER SYNC.
; GENERATED BY DCL IS WRONG
; OR THE LAST BYTE
; OF THE HEADER GAP 0'S IS WRONG
; IN EITHER CASE WORD NO=6
; RIGHT BYTE IS HEADER 0
; LEFT BYTE IS SYNC
; "BAD DATA" HAS WHAT IS GOING
; ON DISK

; ARE ANY ERRORS DETECTED
; IF YES BRANCH

; IS THIS FORCED HEADER ERROR COMMAND
; IF YES NOSYNC=-1 THEN WRITE OR READ
; IS SHUT OFF SO BRANCH OUT
; IF NOSYNC=0 THEN CONTINUE
; BRANCH IF SET
; WRITE DATA

; NO OF WORDS TO BE WRITTEN
;
; READ DATA
; NO OF WORDS TO BE READ

; POP STACK INTO R5
; POP STACK INTO R4
; POP STACK INTO R3
; POP STACK INTO R2
; POP STACK INTO R1
; POP STACK INTO R0
; EXIT ROUTINE
    
```

9313
9314
9315
9316
9317
9318
9319
9320
9321
9322
9323
9324
9325
9326
9327
9328
9329
9330
9331
9332
9333
9334
9335
9336
9337
9338

045254 014400
045256 000000
045260 000000
045262 000000
045264 000000

; THE DISK SECTOR IS DEVIDED AS FOLLOWS
; 19 WORDS OF 0, ONE WORD 144000
; THESE MAKE 39 BYTES FOR SECTOR GAP AND ONE SYNC. BYTE

RSYNC: 14400
RCYL: 0
RSETR: 0
RKEY1: 0
RKEY2: 0

; 5 WORDS OF 0 ONE WORD 144000
; THESE MAKE 11 BYTES FOR HEADER GAP AND ONE SYNC. BYTE
; THESE ARE DCL GENERATED

; THERE ARE 256 WORDS OF DATA
; THERE ARE 2 WORDS FOR ECC GENERATED BY DCL
; 15 WORDS OF 0 FOR DATA GAP AND TOLERANCE GAP

```

9339
9340
9341
9342
9343
9344
9345
9346
9347
9348
9349
9350
9351 045266 000000
9352
9353 045270 000000
9354 045272 000000
9355
9356
9357
9358
9359 045274 012137 045256
9360 045300 012137 045260
9361 045304 012137 045262
9362 045310 012137 045264
9363 045314 012137 046064
9364 045320 010146
9365 045322 013700 001652
9366 045326 012705 000002
9367 045332 012710 000001
9368 045336 052710 000010
9369 045342 052710 000002
9370 045346 042710 000012
9371 045352 000404
9372 045354 012710 000013
9373 045360 042710 000012
9374 045364 012702 000007
9375 045370 052710 000002
9376 045374 042710 000002
9377 045400 005302
9378 045402 001372
9379 045404 005305
9380 045406 001362
9381 045410 012702 000022
9382 045414 005037 046062
9383 045420 004737 046066
9384 045424 005302
9385 045426 001372
9386 045430 013737 045254 046062
9387 045436 004737 046066
9388 045442 032710 001000
9389 045446 001012
9390 045450 012737 000001 045272
9391 045456 013737 045254 001124
9392 045464 012737 104002 045164

```

```

*****
:READ DISK HEADER
*****

```

```

NOSYNC: 0 ;FORCED HEADER ERROR = -1
TY: 0 ;NORMAL = 0
ERNORD: 0 ;ERROR TYPE NO.
;ERROR WORD NO.

```

```

RDHEAD: MOV (R1)+, @#RCYL ;STORE CYLINDER ADDRESS
MOV (R1)+, @#RSETR ;STORE SECTOR AND TRACK ADDRESS
MOV (R1)+, @#RKEY1 ;STORE KEY1
MOV (R1)+, @#RKEY2 ;STORE KEY2
MOV (R1)+, @#COMPA ;STORE COMPARE OR NOT
MOV R1, -(SP) ;PUSH R1 ON STACK
MOV @#RMR, R0 ;R0 CONTAINS MAINTANENCE REG.
MOV #2, R5 ;R5 IS A COUNTER FOR WORDS
MOV @#DMD, @R0 ;DIAG. MODE
BIS @#MSTCK, @R0 ;SET SECTOR FOR FIRST WORD
BIS @#MCLK, @R0 ;SET CLOCK FOR FIRST WORD
BIC @#MSTCK!MCLK, @R0 ;RESET SECTOR AND CLOCK
BR 2$ ;BRANCH OVER GIVING SECTOR FOR FIRST TIME
1$: MOV @#MSTCK!MCLK!DMD, @R0 ;SET SECTOR, CLOCK, DIAG. MODE, RESET INDEX
BIC @#MSTCK!MCLK, @R0 ;RESET SECTOR, CLOCK
2$: MOV #7, R2 ;R2 IS A COUNTER FOR BYTES
3$: BIS @#MCLK, @R0 ;SET CLOCK
BIC @#MCLK, @R0 ;RESET CLOCK
DEC R2 ;BYTE COUNTER
BNE 3$ ;BRANCH IF BYTE NOT COMPLETE
DEC R5 ;WORD COUNTER
BNE 1$ ;BRANCH IF WORD NOT COMPLETE
MOV #10, R2 ;NO OF WORDS OF ZEROS
4$: CLR @#WORD ;READ 0
JSR PC, @#READ ;GO TO READ
DEC R2 ;COUNT
BNE 4$
MOV @#RSYNC, @#WORD ;SYNC. WORD
JSR PC, @#READ
BIT @#DTSY, @R0 ;SYNC. BYTE DETECTED?
BNE 5$ ;BRANCH IF SYNC DETECTED
MOV #1, @#ERNORD ;ERROR WORD NO
MOV @#RSYNC, @#SGDDAT ;SYNC WORD
MOV #104002, @#SSYN ;INSERT "ERROR 2" IN SSYN

```

9393	045472	000571				BR	135		: BRANCH OUT
9394	045474	013737	045256	046062	55:	MOV	2#RCYL, 2#WORD		: SETUP CYLINDER
9395	045502	004737	046066			JSR	PC, 2#READ		: READ
9396	045506	013737	045260	046062		MOV	2#RSETR, 2#WORD		: SETUP SECTOR/TRACK
9397	045514	004737	046066			JSR	PC, 2#READ		: READ
9398	045520	013737	045262	046062		MOV	2#RKEY1, 2#WORD		: SETUP KEY1
9399	045526	004737	046066			JSR	PC, 2#READ		: READ
9400	045532	013737	045264	046062		MOV	2#RKEY2, 2#WORD		: SETUP KEY2
9401	045540	004737	046066			JSR	PC, 2#READ		: READ
9402	045544	013737	047052	046062		MOV	2#MCRC, 2#WORD		: SETUP CRC
9403	045552	004737	046066			JSR	PC, 2#READ		: READ
9404	045556	005737	002014			TST	2#TESDTE		: IS THIS A DRIVE TIMING ERROR
9405	045562	001135				BNE	135		: BRANCH OUT IF YES
9406	045564	005737	046064			TST	2#COMPA		: IS THIS A READ OR WRITE COMMAND
9407	045570	001472				BEQ	115		
9408	045572	012705	047054			MOV	#HEGAP, R5		: POINTER FOR HEADER GAP
9409	045576	012702	000005			MOV	#5, R2		: NO OF WORDS OF ZEROS
9410	045602	012737	000006	045272	65:	MOV	#6, 2#ERMWORD		: ERROR WORD NO SET
9411	045610	004737	046320			JSR	PC, 2#WRITE		: FOR HEADER GAP
9412	045614	005737	046316			TST	2#MWORD		: TEST WRITTEN WORD
9413	045620	001413				BEQ	75		: BRANCH IF GOOD THAT IS 0
9414	045622	160237	045272			SUB	R2, 2#ERMWORD		: WORD NO IN ERROR
9415	045626	005037	001124			CLR	2#SGDDAT		: GOOD WORD SHOULD BE 0
9416	045632	013767	046315	133266		MOV	2#MWORD, SBDDAT		: BAD DATA
9417	045640	012737	104003	045166		MOV	#104003, 2#HEDGAP		: "ERROR 2" GOES IN HEDGAP
9418	045646	000503				BR	135		: BRANCH OUT
9419	045650	013725	046316		75:	MOV	2#MWORD, (R5)+		: SAVE HEADER GAP
9420	045654	005302				DEC	R2		
9421	045656	001351				BNE	65		
9422	045660	004737	046320			JSR	PC, 2#WRITE		: WRITE HEADER (DATA) GAP SYNC
9423	045664	023737	045254	046316		CMP	2#RSYNC, 2#MWORD		
9424	045672	001426				BEQ	105		
9425	045674	005737	045266			TST	2#NOSYNC		: IS THIS FORCED HEADER ERROR COMMAND
9426									: IF YES NOSYNC=-1 THEN WRITE OR READ
9427									: IS SHUT OFF SO BRANCH OUT
9428									: IF NO NOSYNC=0 THEN CONTINUE
9429	045700	001406				BEQ	145		: BRANCH IF TRUE ERROR
9430	045702	005737	046316			TST	2#MWORD		
9431	045706	001420				BEQ	105		: BRANCH IF GOOD
9432	045710	005037	001124			CLR	2#SGDDAT		: IT SHOULD BE ZERO
9433	045714	000403				BR	155		: BRANCH TO TYPE ERROR
9434	045716	013737	045254	001124	145:	MOV	2#RSYNC, 2#SGDDAT		: GOOD DATA
9435	045724	013737	046316	001126	155:	MOV	2#MWORD, 2#SBDDAT		: BAD DATA
9436	045732	012737	000006	045272		MOV	#6, 2#ERMWORD		
9437	045740	012737	104003	045170		MOV	#104003, 2#HEDSYN		
9438	045746	000443				BR	135		: BRANCH OUT
9439	045750	013725	046316		105:	MOV	2#MWORD, (R5)+		: SAVE DATA SYNC.
9440	045754	000440				BR	135		
9441									: READ COMMAND START FROM HERE
9442	045756	012702	000005		115:	MOV	#5, R2		
9443	045762	005067	000074		125:	CLR	WORD		
9444	045766	004767	000074			JSR	PC, READ		: READ HEADER GAP
9445	045772	005302				DEC	R2		: IS 5 HEADER GAP ZEROS COMPLETE
9446	045774	001372				BNE	125		: IF NOT BRANCH

9447	045776	013737	045254	046062		MOV	@#RSYNC,@#WORD	: SYNC WORD
9448	046004	004767	000056			JSR	PC, READ	: READ HEADER (DATA) SYNC)
9449	046010	005737	045266			TST	@#NOSYNC	
9450	046014	001404				BEQ	16S	: IF NOT ERROR COMMAND BRANCH
9451	046016	032710	001000			BIT	@#DTSY,@#RO	: SYNC. DETECTED
9452	046022	001415				BEQ	13S	: IF ZERO BRANCH OUT
9453	046024	000403				BR	17S	: IF NOT ZERO BRANCH TO ERROR
9454	046026	032710	001000		16S:	BIT	@#DTSY, @#RO	: SYNC. DETECTED?
9455	046032	001011				BNE	13S	: BRANCH IF YES
9456	046034	012737	000006	045272	17S:	MOV	@#ERWORD	: ERROR WORD NO.
9457	046042	013737	045254	001124		MOV	@#RSYNC,@#SGDAT	: SYNC WORD
9458	046050	012737	104002	045170		MOV	@#104002,@#HEDSYN	
9459	046056				13S:			
9460	046056	012601				MOV	(SP)+,R1	:; POP STACK INTO R1
9461	046060	000201				RTS	R1	
9462								
9463								

```

9464
9465
9466
9467
9468
9469
9470
9471
9472
9473
9474
9475
9476
9477 046062 000000
9478 046064 000000
9479
9480
9481
9482
9483 046066
9484 046066 010246
9485 046070 012705 000002
9486 046074 012710 000001
9487 046100 006037 046062
9488 046104 103002
9489 046106 052710 000020
9490 046112 012702 000007
9491 046116 052710 000012
9492 046122 005737 043222
9493 046126 001411
9494 046130 032710 000020
9495 046134 001404
9496 046136 012737 177777 043214
9497 046144 000402
9498 046146 005037 043214
9499 046152 012746 000001
9500 046156 006037 046062
9501 046162 103002
9502 046164 012716 000021
9503 046170 012610
9504 046172 005737 043222
9505 046176 001404
9506 046200 005237 043234
9507 046204 004737 043252
9508 046210 052710 000002
9509 046214 005737 043222
9510 046220 001411
9511 046222 032710 000020
9512 046226 001404
9513 046230 012737 177777 043214
9514 046236 000402
9515 046240 005037 043214
9516 046244 012746 000001
9517 046250 006037 046062

```

```

*****
:READ ONE WORD IN "WORD"
*****

```

```

WORD: 0
COMPA: 0

```

READ:

```

MOV R2,-(SP) ;:PUSH R2 ON STACK
MOV #2,R5 ;:WORD COUNTER
MOV #DMD, @R0 ;:SET DIAG. MODE
ROR @#WORD ;:CHECKING IF THERE IS A ONE
BCC 1$ ;:IF NO ONE BRANCH
BIS #MRD, @R0 ;:SET BIT 4 IF DATA HAS ONE
MOV #7,R2 ;:BYTE COUNTER
BIS #MSTCK!MCLK, @R0 ;:SET CLOCK DATA IF ANY SECTOR
TST @#TSECCG ;:IS THIS BIT TO GENERATE AND TEST ECC
BEQ 6$ ;:BRANCH IF NO
BIT #MRD,@R0 ;:IS DATA BIT A ONE
BEQ 5$ ;:BRANCH IF DATA BIT IS 0
MOV #-1,@#ECDATA ;:ECC DATA BIT IS A ONE
BR 6$ ;:BRANCH
CLR @#ECDATA ;:ECC DATA BIT IS A 0
MOV #DMD, -(SP) ;:KEEP ONLY DIAG. MODE
ROR @#WORD ;:CHECKING IF THERE IS A ONE
BCC 2$ ;:IF NO ONE BRANCH
MOV #MRD!DMD, (SP)+, @R0 ;:KEEP DATA AND DIAG. MODE
MOV (SP)+, @R0 ;:PUT IN DATA, RESET CLOCK, SECTOR
TST @#TSECCG ;:IS ECC TO BE GENERATED FOR THIS BIT
BEQ 3$ ;:BRANCH IF NO
INC @#DATENV ;:NUMBER OF CLOCKS GIVEN FOR DATA ENVELOPE
JSR PC,@#ECTEST ;:GO TO GENERATE AND TEST ECC
BIS #MCLK, @R0 ;:SET CLOCK
TST @#TSECCG ;:IS THIS BIT TO GENERATE ECC
BEQ 8$ ;:BRANCH IF NO
BIT #MRD,@R0 ;:IS DATA BIT A ONE
BEQ 7$ ;:BRANCH IF DATA BIT IS = 0
MOV #-1,@#ECDATA ;:ECC DATA BIT IS A ONE
BR 8$ ;:BRANCH
CLR @#ECDATA ;:ECC DATA BIT IS = 0
MOV #DMD, -(SP) ;:KEEP DIAG. MODE
ROR @#WORD ;:CHECKING IF THERE IS A ONE

```

1\$:

5\$:

6\$:

2\$:

3\$:

7\$:

8\$:

9518	046254	103002		BCC	45	: BRANCH IF NO ONE
9519	046256	012716	000021	MOV	#MRD:DMD,(SP)	: KEEP DIAG. MODE AND DATA
9520	046262	012610		45: MOV	(SP)+,R0	: SET DATA, DIAG. MODE, CLEAR CLOCK
9521	046264	005737	043222	TST	@#TSECCG	: IS THIS BIT TO GENERATE ECC
9522	046270	001404		BEQ	95	: BRANCH IF NO
9523	046272	005237	043234	INC	@#DATENV	: NUMBER OF CLOCKS GIVEN FOR DATA ENVELOPE
9524	046276	004737	043252	JSR	PC,@#ECTEST	: GO TO GENERATE AND TEST ECC
9525	046302	005302		95: DEC	R2	: BYTE COUNTER
9526	046304	001341		BNE	35	: BRANCH IF ONE BYTE NOT COMPLETE
9527	046306	005305		DEC	R5	: WORD COUNTER
9528	046310	001300		BNE	15	: BRANCH IF ONE WORD NOT COMPLETE
9529	046312	012602		MOV	(SP)+,R2	: POP STACK INTO R2
9530	046314	000207		RTS	PC	
9531						
9532						

9533
9534
9535
9536
9537
9538
9539
9540
9541
9542
9543
9544
9545
9546
9547
9548
9549
9550
9551
9552
9553
9554
9555
9556
9557
9558
9559
9560
9561
9562
9563
9564
9565
9566
9567
9568
9569
9570
9571
9572
9573
9574
9575
9576
9577
9578
9579
9580
9581
9582
9583
9584
9585
9586

046316 000000

MMWORD: 0

046320

WRITE:

046320 010046
046322 010246
046324 010346
046326 010546
046330 012705 000002
046334 012710 000001
046340 012702 000007
046344 012710 000013
046350 032710 000040
046354 001406
046356 012737 177777 043214
046364 000261
046366 006003
046370 000404
046372 005037 043214
046376 000241
046400 006003
046402 012710 000001
046406 005737 043222
046412 001404
046414 005237 043234
046420 004737 043252
046424 052710 000002
046430 032710 000040
046434 001406
046436 012737 177777 043214
046444 000261
046446 006003
046450 000404
046452 005037 043214
046456 000241
046460 006003
046462 012710 000001
046466 005737 043222

MOV R0, -(SP)
MOV R2, -(SP)
MOV R3, -(SP)
MOV R5, -(SP)
MOV #2, R5
MOV #1, @R0
15: MOV #7, R2
MOV #MSTCK!MCLK!DMD, @R0
BIT #MWR, @R0
BEQ 25
MOV #-1, @#ECDATA
SEC R3
ROR R3
BR 35
25: CLR @#ECDATA
CLC R3
ROR R3
35: MOV #DMD, @R0
TST @#TSECCG
BEQ 45
INC @#DATENV
JSR PC, @#ECTEST
45: BIS #MCLK, @R0
BIT #MWR, @R0
BEQ 55
MOV #-1, @#ECDATA
SEC R3
ROR R3
BR 65
55: CLR @#ECDATA
CLC R3
ROR R3
65: MOV #DMD, @R0
TST @#TSECCG

;; PUSH R0 ON STACK
;; PUSH R2 ON STACK
;; PUSH R3 ON STACK
;; PUSH R5 ON STACK
;; WORD COUNTER
;; SET DIAG. MODE
;; R0 HAS RMR ADDRESS IN IT
;; BYTE COUNTER
;; @R0: SET SECTOR AND CLOCK
;; CHECK WRITE BIT IN MAINT. REG.
;; BRANCH IF ZERO
;; ECC DATA BIT IS A ONE
;; SET CARRY
;; MOVE 1 FORWARD
;; ECC DATA BIT IS = 0
;; CLEAR CARRY
;; MOVE 0 FOR MMWORD
;; CLEAR SECTOR AND CLOCK
;; IS THIS BIT TO GENERATE ECC
;; BRANCH IF NO
;; NUMBER OF CLOCKS GIVEN FOR DATA ENVELOPE
;; GO TO GENERATE AND TEST ECC
;; SET CLOCK IN RMR
;; CHECK WRITE BIT IN RMR
;; BRANCH IF ZERO
;; ECC DATA BIT IS A ONE
;; SET CARRY
;; MOVE 1 FOR MMWORD
;; ECC DATA BIT IS ZERO
;; CLEAR CARRY
;; MOVE 0 FOR MMWORD
;; CLEAR CLOCK
;; IS THIS BIT TO GENERATE ECC

: WRITE ONE WORD WHICH COMES BACK IN "MMWORD"
: *****

9587	046472	001404		BEQ	75	; BRANCH IF NO
9588	046474	005237	043234	INC	2#DATENV	; NUMBER OF CLOCKS GIVEN FOR DATA ENVELOPE
9589	046500	004737	043252	JSR	PC, 2#ECTEST	; GO TO GENERATE AND TEST ECC
9590	046504	005302		DEC	R2	; COUNT FOR BYTE END
9591	046506	001346		BNE	45	; IF NOT BYTE END BRANCH
9592	046510	005305		DEC	R5	; COUNT FOR WORD END
9593	046512	001312		BNE	15	; IF NOT WORD END BRANCH
9594						
9595	046514	010337	046316	MOV	R3, 2#WORD	; STORE THE WORD
9596						
9597	046520	012605		MOV	(SP)+, R5	; POP STACK INTO R5
9598	046522	012603		MOV	(SP)+, R3	; POP STACK INTO R3
9599	046524	012602		MOV	(SP)+, R2	; POP STACK INTO R2
9600	046526	012600		MOV	(SP)+, R0	; POP STACK INTO R0
9601	046530	000207		RTS	PC	
9602						
9603						

```

9604
9605
9606
9607
9608
9609
9610
9611
9612
9613
9614
9615
9616
9617
9618
9619 046532 000000
9620 046534 000400
9621 046536 000000
9622 046540
9623 046540 011137 046532
9624 046544 012102
9625 046546 012137 046064
9626
9627 046552 010046
9628 046554 010146
9629 046556 010246
9630 046560 010346
9631 046562 010446
9632
9633 046564 012701 000016
9634 046570 012703 050076
9635 046574 012723 177777
9636 046600 005301
9637 046602 001374
9638 046604 013700 001652
9639 046610 013746 046534
9640 046614 163716 046532
9641 046620 011637 046536
9642 046624 012604
9643 046626 005737 002012
9644 046632 001403
9645 046634 012737 177777 043222
9646 046642 012703 047070
9647 046646 004737 046320
9648 046652 013723 046316
9649 046656 005302
9650 046660 001372
9651 046662 005704
9652 046664 001406
9653 046666 004737 046320
9654 046672 013723 046316
9655 046676 005304
9656 046700 001372
9657 046702 005037 043222

```

```

*****
:WRITE DATA - PUT DATA INTO "DISK" AREA FROM "MMWORD"
:ONE WORD AT A TIME
*****

```

```

COUNT: 0
FORMAT: 256.
ZWORDS: 0
MRDATA:
MOV (R1), @#COUNTD ;STORE NO. OF WORDS TO BE WRITTEN
MOV (R1)+, R2 ;SAME IN R2
MOV (R1)+, @#COMPA ;COMPARE OR NOT
MOV R0, -(SP) ;: PUSH R0 ON STACK
MOV R1, -(SP) ;: PUSH R1 ON STACK
MOV R2, -(SP) ;: PUSH R2 ON STACK
MOV R3, -(SP) ;: PUSH R3 ON STACK
MOV R4, -(SP) ;: PUSH R4 ON STACK
MOV #14, R1 ;NO. OF TOLERANCE GAP WORDS
MOV @TOLGAP, R3 ;START OF TOLERANCE GAP TABLE
15: MOV #-1, (R3)+ ;MAKE IT 177777
DEC R1 ;IS 14 COMPLETED
BNE 15 ;IF NO BRANCH
MOV @#RHMR, R0 ;R0 CONTAINS MAINTANENCE REG.
MOV @#FORMAT, -(SP)
SUB @#COUNTD, (SP)
MOV (SP), @#ZWORDS ;NO. OF ZERO WORDS TO BE WRITTEN
MOV (SP)+, R4
TST @#TSECC ;IS THIS AN ECC TEST ?
BEQ 75 ;BRANCH IF NO
MOV #-1, @#TSECCG ;THESE BITS ARE TO GENERATE ECC
75: MOV @DISK, R3 ;ADDRESS THE "DISK" AREA
25: JSR PC, @#WRITE ;WRITE INTO "MMWORD"
MOV @#MMWORD, (R3)+ ;STORE ON SIMULATED DISK
DEC R2 ;COUNT DOWN
BNE 25 ;CONTINUE IF ALL WORDS NOT WRITTEN
TST R4 ;ANY ZEROS TO BE WRITTEN ?
BEQ 45 ;BRANCH IF NONE TO BE WRITTEN
35: JSR PC, @#WRITE ;WRITE ZEROS INTO "MMWORD"
MOV @#MMWORD, (R3)+ ;STORE INTO "DISK"
DEC R4
BNE 35
45: CLR @#TSECCG ;NO MORE ECC TO BE GENERATED

```


9658	046706	012701	000002		MOV	#2, R1	
9659	046712	004767	177402	55:	JSR	PC, WRITE	;WRITE ECC1 AND ECC2 ON SIMULATED DISK
9660	046716	013723	046316		MOV	@#MWORD, (R3)+	;STORE ON WEEC1 AND WEEC2
9661	046722	005301			DEC	R1	
9662	046724	001372			BNE	55	
9663	046726	004767	177366		JSR	PC, WRITE	;WRITE DATA GAP INTO "MWORD"
9664	046732	013723	046316		MOV	@#MWORD, (R3)+	;STORE INTO "DISK"
9665	046736	012701	000016		MOV	#14, R1	
9666	046742	004737	046320	65:	JSR	PC, @#WRITE	;WRITE TOLERANCE GAP ZEROS
9667	046746	013723	046316		MOV	@#MWORD, (R3)+	;STORE INTO "DISK"
9668	046752	005301			DEC	R1	
9669	046754	001372			BNE	65	
9670							
9671	046756	012604			MOV	(SP)+, R4	::POP STACK INTO R4
9672	046760	012603			MOV	(SP)+, R3	::POP STACK INTO R3
9673	046762	012602			MOV	(SP)+, R2	::POP STACK INTO R2
9674	046764	012601			MOV	(SP)+, R1	::POP STACK INTO R1
9675	046766	012600			MOV	(SP)+, R0	::POP STACK INTO R0
9676							
9677	046770	000201			RTS	R1	
9678							
9679							

9680
9681
9682
9683
9684
9685
9686
9687
9688
9689
9690
9691
9692
9693
9694
9695
9696
9697
9698
9699
9700
9701
9702
9703
9704
9705
9706
9707
9708
9709
9710
9711
9712
9713

046772 000023
047040 000001
047042 000004
047052 000001
047054 000005
047066 000001
047070
047070 000400
050070 000001
050072 000001
050074 000001
050076 000016

:WRITE HEADER AND DATA
:
:THIS IS THE SIMULATED DISK
:ONLY ONE SECTOR OF SPACE IS ALLOWED
:*****

SECGAP: .BLKM 19.
WSSYNC: .BLKM 1
HEADER: .BLKM 4
MCRC: .BLKM 1
HEGAP: .BLKM 5
HDWSYN: .BLKM 1
SILOTB:
DISK: .BLKM 256.
MECC1: .BLKM 1
MECC2: .BLKM 1
DTAGAP: .BLKM 1
TOLGAP: .BLKM 14.

;SECTOR GAP 38 BYTES OF 0
;SECTOR GAP 1 BYTE OF 0 ONE SYNC BYTE
;HEADER = CYL, SECTOR/TRACK, KEY1, KEY2
;CRC
;HEADER GAP 10 BYTES OF 0
;HEADER GAP 1 BYTE OF 0 ONE SYNC. BYTE
;USED IN SILO TEST AS SILO TABLE
;DATA SPACE
;ECC1
;ECC2
;DATA GAP 2 BYTES OF 0
;TOLERANCE GAP 28 BYTES OF 0

9714
9715
9716
9717
9718
9719
9720
9721
9722
9723
9724
9725
9726
9727
9728
9729
9730
9731
9732
9733
9734
9735
9736
9737
9738
9739
9740
9741
9742
9743
9744
9745
9746
9747
9748
9749
9750
9751
9752
9753
9754
9755
9756
9757
9758
9759
9760
9761
9762
9763
9764
9765
9766
9767

050132 000000
050134 011637 002002
050140 162737 000004 002002
050146 010046
050150 010146
050152 010246
050154 010346
050156 010446
050160 010546
050162 012777 000001 131462
050170 052777 000001 131434
050176 012737 000113 050132
050204 005337 050132
050210 001375
050212 013746 050276
050216 042716 177740
050222 012637 050232
050226 004137 051204
050232 000000
050234 012701 000240
050240 010137 050306
050244 010137 050310
050250 010137 050312
050254 010137 050314
050260 010137 050316
050264 010137 050320
050270 004137 050370
050274 000000
050276 000000
050300 000000
050302 000000
050304 000000
050306 000240

:WRITE HEADER AND DATA

RNCTR1: .WORD 0 ;'RUN' LINE STALL COUNTER
COMMD: MOV (SP), @PCJSR ;SAVE PC OF JSR + 4
SUB #4, @PCJSR ;SAVE PC OF JSR
MOV R0, -(SP) ;PUSH R0 ON STACK
MOV R1, -(SP) ;PUSH R1 ON STACK
MOV R2, -(SP) ;PUSH R2 ON STACK
MOV R3, -(SP) ;PUSH R3 ON STACK
MOV R4, -(SP) ;PUSH R4 ON STACK
MOV R5, -(SP) ;PUSH R5 ON STACK
MOV @DMD, @RHMR ;SET DIAGNOSTIC MODE
BIS #GO, @RHCS1 ;SET 'GO' BIT & STALL 'TILL 'RUN'
RNMAT1: MOV #75., @RNCTR1 ;LOAD STALL COUNTER = APPROX. 450US
IS: DEC @RNCTR1 ;FOR 11/50 CPU WITH CORE MEMORY
BNE IS ;COUNT DOWN 1 TIME
 ;CONTINUE UNTIL 0
MOV @MSECTR, -(SP) ;GET DESIRED SECTOR/TRACK
BIC #177740, (SP) ;MAKE ONLY SECTOR
MOV (SP)+, @WTRK ;SAVE SECTOR
JSR R1, @SEARCH ;DO SEARCH SECTOR & ISSUE SECTOR CLOCKS
WTRK: .WORD 0 ;SECTOR NO.
MOV #+NOP, R1 ;GOING TO MOVE NOPS
MOV R1, @SEGPER ;NOP INTO SEGAP
MOV R1, @FSYNER ;NOP INTO FSYNER
MOV R1, @ERHEAD ;NOP INTO ERHEAD
MOV R1, @ERCRC ;NOP INTO ERCRC
MOV R1, @ERHDGP ;NOP INTO ERHDGAP
MOV R1, @HDESYN ;NOP INTO HDESYN
JSR R1, @MRHEAD ;WRITE THE HEADER
MAYL: 0 ;CYLINDER
MSECTR: 0 ;SECTOR AND TRACK
MKEY1: 0 ;KEY1
MKEY2: 0 ;KEY2
GCRC: 0 ;GOOD CRC
SEGPER: NOP ;IF "ERROR 6" INSERTED BY
 ;MRHEAD SUBROUTINE THEN
 ;SECTOR GAP GOING ON DISK
 ;IS NOT RIGHT.

```

9768
9769
9770
9771
9772
9773
9774 050310 000240          FSYNER: NOP
9775
9776
9777
9778
9779
9780
9781
9782
9783
9784
9785
9786
9787 050312 000240          ERHEAD: NOP
9788
9789
9790
9791
9792
9793
9794
9795
9796
9797
9798
9799 050314 000240          ERCRC:  NOP
9800
9801
9802
9803
9804
9805
9806
9807 050316 000240          ERHDGF: NOP
9808
9809
9810
9811
9812
9813
9814
9815
9816
9817
9818
9819
9820 050320 000240          HDESYN: NOP
9821

```

;WORD NO. CONTAINS WHICH
;WORD IS WRONG, THAT IS
;FIRST OF TENTH OR WHAT EVER NO.
;BAD WORD IS GOING ON DISK

;IF "ERROR 6" INSERTED BY
;MRHEAD SUBROUTINE THEN
;THE LAST 0 BYTE OF SECTOR
;GAP, OR FIRST SYNC. BYTE
;AFTER SECTOR GAP IS IN
;ERROR.

;WORD NO. CONTAINS 20
;RIGHT BYTE IS SECTOR GAP
;LEFT BYTE IS SYNC. BYTE
;BAD WORD IS WHAT IS GOING ON
;DISK.

;IF "ERROR 6" INSERTED BY
;MRHEAD SUBROUTINE THEN
;HEADER GOING ON DISK
;IS WRONG.

;WORD NO 1 = CYLINDER NO
;WORD NO 2 = SECTOR/TRACK
;WORD NO 3 = KEY1
;WORD NO 4 = KEY2
;BAD WORD IS WHAT IS GOING ON
;DISK

;IF "ERROR 6" INSERTED BY
;MRHEAD SUBROUTINE THEN CRC WRITTEN
;ON DISK IS IN ERROR.

;GOOD DATA IS WHAT SHOULD BE ON DISK
;BAD DATA IS WHAT IS GOING ON DISK
;WORD NO IS 5.

;IF "ERROR 6" INSERTED BY
;MRHEAD SUBROUTINE THEN HEADER
;GAP GOING ON DISK IS WRONG.

;WORD NO. GIVES WHICH OF
;THE HEADER GAP WORDS
;ARE WRONG. FOR EXAMPLE:

;WORD NO 1 = FIRST HEADER
; GAP WORD
;BAD WORD IS WHAT IS GOING ON DISK

;IF "ERROR 6" INSERTED BY

9849
9850
9851
9852
9853
9854
9855
9856
9857
9858
9859
9860
9861
9862
9863
9864
9865
9866
9867
9868
9869
9870
9871
9872
9873
9874
9875
9876
9877
9878
9879
9880
9881
9882
9883
9884
9885
9886
9887
9888
9889
9890
9891
9892
9893
9894
9895
9896
9897
9898
9899
9900
9901
9902

050356 000000
050360 000000
050362 000000
050364 000000
050366 000000
050370 012137 050356
050374 012137 050360
050400 012137 050362
050404 012137 050364
050410 012137 050366
050414 010146
050416 012701 046772
050422 013700 001652
050426 012710 000001
050432 012705 000002
050436 052710 000010
050442 012710 000013
050446 032710 000040
050452 001403
050454 000261
050456 006003
050460 000402
050462 000241
050464 006003
050466 012710 000001
050472 012702 000007
050476 052710 000002
050502 032710 000040
050506 001403
050510 000261
050512 006003
050514 000402
050516 000241
050520 006003
050522 012710 000001

:WRITE HEADER

:R0 = MAINT.REG.
:R1 = SIMULATED DISK
:R2 = BYTE COUNT
:R3 = WRITE WORD
:R5 = WORD COUNT

SCYL: 0
SSECTR: 0
SKEY1: 0
SKEY2: 0
SCRC: 0

WRHEAD: MOV (R1)+, @#SCYL
MOV (R1)+, @#SSECTR
MOV (R1)+, @#SKEY1
MOV (R1)+, @#SKEY2
MOV (R1)+, @#SCRC
MOV R1, -(SP) ;: PUSH R1 ON STACK
MOV #SECGAP, R1 ;: SIMULATED DISK INDICATOR
MOV @#RHMR, R0 ;: R0 NOW HAS MAINT. REG. ADDR.
MOV #DMD, @R0 ;: SET DIAG. MODE IN RHMR
MOV #2, R5 ;: WORD COUNTER
BIS #MSTCK, @R0 ;: SET SECTOR FOR FIRST BYTE
1\$: MOV #MSTCK!MCLK!DMD, @R0 ;: SET SECTOR, CLOCK, DIAG. MODE, RESET INDEX
BIT #MWR, @R0 ;: CHECK WRITE BIT IN MAINT. REG.
BEQ 2\$
SEC ;: SET CARRY
ROR R3 ;: MOVE ONE FORWARD
BR 3\$
2\$: CLC ;: CLEAR CARRY
ROR R3 ;: MOVE ZERO FORWARD
3\$: MOV #DMD, @R0 ;: CLEAR CLOCK, SECTOR
MOV #7, R2 ;: BYTE COUNTER
4\$: BIS #MCLK, @R0 ;: SET CLOCK
BIT #MWR, @R0 ;: CHECK WRITE BIT IN MAINT.REG.
BEQ 5\$;: BRANCH IF ZERO
SEC ;: SET CARRY
ROR R3 ;: MOVE ONE FORWARD
BR 6\$
5\$: CLC
ROR R3
6\$: MOV #DMD, @R0 ;: SET DIAG. MODE AGAIN IN RHMR


```

9903 050526 005302          DEC      R2
9904 050530 001362          BNE      45
9905 050532 005305          DEC      R5
9906 050534 001342          BNE      15          ;CONTINUE
9907
9908 050536 010321          MOV      R3,(R1)+
9909 050540 005703          TST      R3
9910 050542 001414          BEQ      75
9911 050544 012737 000001 045272  MOV      #1,@#ERWORD
9912 050552 005037 001124          CLR      @#SGDDAT
9913 050556 010337 001126          MOV      R3,@#SBDDAT
9914 050562 012737 104006 050306  MOV      #104006,@#SEGP
9915 050570 000137 051176          JMP      @#175          ;BRANCH OUT ----->
9916
9917 050574 012702 000022          75:     MOV      #18.,R2          ;COUNT NO. OF SECTOR GAP
9918 050600 012737 000024 045272 105:     MOV      #20.,@#ERWORD  ;COUNT TO GIVE ERROR WORD
9919 050606 004737 046320          JSR      PC,@#WRITE    ;WRITE SECTOR GAP
9920 050612 013721 046316          MOV      @#MWORD,(R1)+ ;STORE SECTOR GAP WORD
9921 050616 001413          BEQ      115
9922 050620 160237 045272          SUB      R2,@#ERWORD   ;IF NOT GET ERROR WORD NO.
9923 050624 005037 001124          CLR      @#SGDDAT     ;GOOD WORD
9924 050630 013737 046316 001126  MOV      @#MWORD,@#SBDDAT ;BAD WORD
9925 050636 012737 104006 050306  MOV      #104006,@#SEGP ;STORE "ERROR 5" IN SEGP
9926 050644 000554          BR       175          ;BRANCH OUT ----->
9927
9928 050646 005302          115:    DEC      R2          ;HAVE 18 WORDS OF ZEROS BEEN WRITTEN ?
9929 050650 001353          BNE      105          ;IF NOT DO SO
9930
9931          ;AT THIS POINT THE SECTOR FOUND FLOP SHOULD
9932          ;BE HIGH. SO THAT THE HEADER SYNC BYTE CAN BE GIVEN
9933
9934          ;HOWEVER IN THE DRIVE TIMING ERROR TEST THE REST OF THE ROUTINE
9935          ;IS ABORTED - HEADER SYNC BYTE IS NOT GIVEN
9936
9937 050652 005737 002014          TST      @#TESDTE     ;IS THIS A DRIVE TIMING ERROR
9938 050656 001147          BNE      175          ;BRANCH OUT IF YES
9939 050660 004737 046320          JSR      PC,@#WRITE   ;WRITE ONE SECTOR GAP 0 BYTE
9940          ;AND ONE SYNC. BYTE = 230
9941 050664 013711 046316          MOV      @#MWORD,(R1) ;SAVE 0 BYTE AND SYNC BYTE
9942 050670 023721 045254          CMP      @#Rsync,(R1)+ ;IF SYNC. BYTE RIGHT
9943 050674 001414          BEQ      125          ;IF YES BRANCH
9944 050676 012737 000024 045272  MOV      #20.,@#ERWORD ;IF NOT GET READY FOR ERROR
9945 050704 013737 045254 001124  MOV      @#Rsync,@#SGDDAT ;GOOD WORD
9946 050712 014137 001126          MOV      -(R1),@#SBDDAT ;BAD WORD
9947 050716 012737 104006 050310  MOV      #104006,@#FSYNER ;INSERT "ERROR 6" IN FSYNER
9948 050724 000524          BR       175          ;BRANCH OUT ----->
9949
9950 050726 012702 000004          125:    MOV      #4,R2          ;FOUR HEADER WORDS
9951 050732 012703 050356          MOV      #SYL,R3       ;POINTER FOR HEADER TABLE
9952 050736 012737 000005 045272 135:    MOV      #5,@#ERWORD   ;ERROR WORD NO SET
9953 050744 004737 046320          JSR      PC,@#WRITE   ;WRITE 4 HEADER WORDS
9954 050750 013711 046316          MOV      @#MWORD,(R1) ;STORE WRITTEN WORD
9955 050754 022321          CMP      (R3)+,(R1)+  ;IS IT RIGHT?
9956 050756 001412          BEQ      145          ;IF GOOD CONTINUE

```

```

9957                                     ; IF NOT GET READY FOR PRINT
9958 050760 160237 045272 SUB R2, @#ERMWORD ; WORD NO
9959 050764 014337 001124 MOV -(R3), @#SGDDAT ; GOOD DATA
9960 050770 014137 001126 MOV -(R1), @#SBDDAT ; BAD DATA
9961 050774 012737 104006 050312 MOV #104006, @#ERHEAD ; INSERT "ERROR 6"
9962 051002 000475 BR 17$ ; BRANCH OUT ----->
9963
9964 051004 005302 14$: DEC R2 ; ARE 4 HEADER WORDS DONE?
9965 051006 001353 BNE 13$ ; IF NOT DO THEM
9966 051010 004737 046320 JSR PC, @#WRITE ; WRITE CRC
9967 051014 013711 046316 MOV @#(WORD, (R1)) ; STORE CRC
9968 051020 022137 050304 CMP (R1)+, @#GCRC ; COMPARE GOOD CRC
9969 051024 001414 BEQ 20$ ; BRANCH IF GOOD
9970 051026 014137 001126 MOV -(R1), @#SBDDATA ; BAD CRC WRITTEN
9971 051032 013737 050304 001124 MOV @#GCRC, @#SGDDAT ; GOOD CRC
9972 051040 012737 000005 045272 MOV #5, @#ERMWORD ; ERROR WORD NO
9973 051046 012737 104006 050314 MOV #104006, @#ERCRC ; INSERT ERROR 6
9974 051054 000450 BR 17$ ; EXIT ----->
9975
9976 051056 012702 000005 20$: MOV #5, R2 ; NO OF HEADER GAP
9977 051062 012737 000006 045272 15$: MOV #6, @#ERMWORD ; ERROR WORD NO SET
9978 051070 004737 046320 JSR PC, @#WRITE ; WRITE HEADER GAP
9979 051074 013721 046316 MOV @#(WORD, (R1))+ ; STORE
9980 051100 001412 BEQ 16$ ; IF GOOD BRANCH
9981 051102 160237 045272 SUB R2, @#ERMWORD ; ERROR WORD NO
9982 051106 005037 001124 CLR @#SGDDAT ; GOOD DATA
9983 051112 014137 001126 MOV -(R1), @#SBDDAT ; BAD DATA
9984 051116 012737 104006 050316 MOV #104006, @#ERHDP ; STORE "ERROR 6"
9985 051124 000424 BR 17$ ; BRANCH OUT ----->
9986
9987 051126 005302 16$: DEC R2 ; ARE 5 HEADER GAP ZEROS DONE
9988 051130 001354 BNE 15$ ; IF NOT BRANCH
9989 051132 004737 046320 JSR PC, @#WRITE
9990 051136 013711 046316 MOV @#(WORD, (R1))
9991 051142 023721 045254 CMP @#RSYNC, (R1)+
9992 051146 001413 BEQ 17$ ; EXIT ----->
9993 051150 012737 000005 045272 MOV #5, @#ERMWORD
9994 051156 014137 001126 MOV -(R1), @#SBDDAT
9995 051162 013737 045254 001124 MOV @#RSYNC, @#SGDDAT
9996 051170 012737 104006 050320 MOV #104006, @#HDESYN
9997
9998 051176 17$: MOV (SP)+, R1 ;; POP STACK INTO R1
9999 051176 012601
10000
10001 051200 000201 RTS R1
10002
10003

```


10004
10005
10006
10007
10008
10009
10010
10011
10012
10013
10014
10015
10016
10017
10018
10019
10020
10021
10022
10023
10024
10025
10026
10027
10028
10029
10030
10031
10032
10033
10034
10035
10036
10037
10038
10039
10040
10041
10042
10043
10044
10045
10046
10047
10048
10049
10050
10051
10052
10053
10054
10055
10056
10057

:SEARCH SECTOR

: RO=RMR ADDRESS
: R1=PASSED ARGUMENT (SECTOR SEARCHED FOR)
: R2=CLOCK COUNT (PER BYTE)
: R3=SECTOR COUNTER FROM R1
: R5=BYTES PER WORD COUNT
: BEFORE INDEX IS GIVEN TWO SECTOR CLOCKS ARE GIVEN TO RESET
: SECTOR PULSE IN CASE IT IS SET
: AT BEGINNING OF EACH SECTOR ONE SECTOR CLOCK HAS TO RISE
: BEFORE CLOCK THEN EVERY EIGHT CLOCKS ONE SECTOR CLOCK IS
: IDENTICAL WITH CLOCK
: NUMBERING THE SECTOR CLOCKS AS FOLLOWS
: THE SECTOR CLOCK UNDER INDEX - 0
: THE NEXT - 1
: THE NEXT - 2
: ETC.
: THEN THE LAST SECTOR CLOCK IN ONE SECTOR HAS NUMBER - 608
: THE NEXT SECTOR THEN HAS 608 SECTOR CLOCKS
: THE NEXT SECTOR THEN HAS ANOTHER 608 SECTOR CLOCKS
: AND SO ON

051202 000000
051204 012137 051202
051210 010046
051212 010146
051214 010246
051216 010346
051220 010446
051222 010546
051224 013700 001652
051230 013703 051202
051234 012710 000001
051240 052710 000010
051244 042710 000010
051250 052710 000010
051254 042710 000010

SECTR: 0 ;SECTOR SEARCHED FOR
SEARCH: MOV (R1)+ @SECTR ;SAVE SECTOR SEARCHED FOR
MOV RO,-(SP) ;PUSH RO ON STACK
MOV R1,-(SP) ;PUSH R1 ON STACK
MOV R2,-(SP) ;PUSH R2 ON STACK
MOV R3,-(SP) ;PUSH R3 ON STACK
MOV R4,-(SP) ;PUSH R4 ON STACK
MOV R5,-(SP) ;PUSH R5 ON STACK
MOV @RMR, RO ;NOW RO HAS MAINTENANCE REG. ADR.
MOV @SECTR, R3 ;SECTOR COUNTER
MOV #DMD, JRO ;SET DIAGNOSTIC MODE
BIS #MSTCK, JRO ;SET SECTOR CLOCK
BIC #MSTCK, JRO ;CLEAR SECTOR CLOCK
BIS #MSTCK, JRO ;SET SECTOR CLOCK
BIC #MSTCK, JRO ;CLEAR SECTOR CLOCK
;THE ABOVE TWO SECTOR CLOCKS ARE GIVEN FOR

```

10058                                     ;RESETTING SECTOR PULSE
10059                                     ;IN CASE IT STARTS SET
10060 051260 052710 000014      BIS      #MINX!MSTCK,ARO ;SET INDEX AND SECTOR CLOCK
10061 051264 012710 000001      MOV      #DMD, ARO    ;RESET INDEX AND SECTOR CLOCK
10062 051270 005703              TST      R3          ;IF SECTOR REQUIRED JUMP OUT
10063 051272 001461              BEQ      7$          ;BRANCH OF SECTOR ZERO REQUIRED
10064                                     ;NOW THE 304 WORDS WILL START
10065
10066
10067
10068                                     ;FOR FIRST BYTE SECTOR CLOCK WILL GO HIGH THEN CLOCK WILL GO HIGH
10069                                     ;BOTH WILL COME DOWN TOGETHER THEN SEVEN CLOCKS WILL BE GIVEN
10070                                     ;FOR SECOND BYTE AND ALL OTHER BYTES TILL NEXT SECTOR SECTOR CLOCK
10071                                     ;WILL BE IDENTICAL WITH ONE CLOCK
10072
10073
10074                                     ;ONE WORD ONLY
10075
10076 051274 012702 000010      1$: MOV      #8., R2      ;BYTE COUNTER
10077 051300 012705 000002      MOV      #2, R5        ;BYTES PER WORD
10078 051304 052710 000010      BIS      #MSTCK,ARO    ;SET SECTOR CLOCK
10079 051310 052710 000002      BIS      #MCLK,ARO     ;SET CLOCK
10080 051314 000402              BR       3$            ;BRANCH TO CLEAR SECTOR AND CLOCK
10081 051316 052710 000012      2$: BIS      #MSTCK!MCLK,ARO ;SET SECTOR AND CLOCK
10082 051322 042710 000012      3$: BIC      #MSTCK!MCLK,ARO ;CLEAR SECTOR AND CLOCK
10083 051326 052710 000002      8$: BIS      #MCLK, ARO  ;SET CLOCK
10084 051332 042710 000002      BIC      #MCLK, ARO   ;CLEAR CLOCK
10085 051336 005302              DEC      R2           ;BYTE COUNTER
10086 051340 001372              BNE      8$          ;BRANCH IF BYTE NOT COMPLETE
10087 051342 012702 000007      MOV      #7, R2       ;SETUP FOR SECOND BYTE
10088 051346 005305              DEC      R5          ;IS WORD COMPLETE?
10089 051350 001362              BNE      2$          ;BRANCH IF NOT COMPLETE
10090                                     ;TO GIVE SECTOR CLOCK AND CLOCK
10091
10092
10093                                     ;NOW 303 WORDS ARE LEFT AND ALL ARE IDENTICAL
10094
10095 051352 012701 000457      4$: MOV      #303., R1  ;WORDS PER SECTOR COUNTER
10096 051356 012705 000002      MOV      #2, R5        ;BYTES PER WORD COUNTER
10097 051362 012702 000007      5$: MOV      #7, R2     ;BYTE COUNTER (CLOCK COUNTER)
10098 051366 052710 000012      BIS      #MSTCK!MCLK,ARO ;SET SECTOR CLOCK AND CLOCK
10099 051372 042710 000012      BIC      #MSTCK!MCLK,ARO ;CLEAR SECTOR CLOCK AND CLOCK
10100 051376 052710 000002      6$: BIS      #MCLK, ARO  ;SET CLOCK
10101 051402 042710 000002      BIC      #MCLK, ARO   ;RESET CLOCK
10102 051406 005302              DEC      R2           ;IS BYTE COMPLETE?
10103 051410 001372              BNE      6$          ;BRANCH IF NOT COMPLETE
10104 051412 005305              DEC      R5          ;IS WORD COMPLETE?
10105 051414 001362              BNE      5$          ;BRANCH IF NOT
10106 051416 005301              DEC      R1          ;IS SECTOR COMPLETE
10107 051420 001356              BNE      4$          ;BRANCH IF NOT
10108 051422 052710 000010      BIS      #MSTCK,ARO    ;SET SECTOR
10109 051426 042710 000010      BIC      #MSTCK,ARO    ;CLEAR SECTOR
10110 051432 005303              DEC      R3          ;IS REQUIRED NO OF SECTORS COMPLETE
10111 051434 001317              BNE      1$          ;BRANCH IF NOT

```



```

10112
10113 051436
10114 051436 012605
10115 051440 012604
10116 051442 012603
10117 051444 012602
10118 051446 012601
10119 051450 012600
10120 051452 000201
10121
10122
10123
10124
10125
10126
10127 051454 000000
10128 051456 000000
10129
10130
10131
10132 051460 012137 051454
10133 051464 012137 051456
10134 051470 010146
10135 051472 005737 002012
10136 051476 001403
10137 051500 012737 177777 043222
10138 051506 012702 000402
10139
10140 051512 012703 047070
10141 051516 012337 046062
10142 051522 004737 046066
10143 051526 005302
10144 051530 001372
10145 051532 005737 002012
10146 051536 001012
10147 051540 005037 043222
10148 051544 012702 000017
10149 051550 012337 046062
10150 051554 004737 046066
10151 051560 005302
10152 051562 001372
10153 051564
10154 051564 012601
10155 051566 000201
10156
10157

```

```

75:
MOV (SP)+,R5 ;:POP STACK INTO R5
MOV (SP)+,R4 ;:POP STACK INTO R4
MOV (SP)+,R3 ;:POP STACK INTO R3
MOV (SP)+,R2 ;:POP STACK INTO R2
MOV (SP)+,R1 ;:POP STACK INTO R1
MOV (SP)+,R0 ;:POP STACK INTO R0
RTS R1

;*****
;READ ONE SECTOR OF DATA
;*****

RNO: 0 ;NO. OF WORDS READ
RCOM: 0 ;EXTRA STORAGE

REDATA: MOV (R1)+,RNO ;SAVE NO. OF WORDS ONLY FOR INFORMATION
MOV (R1)+,RCOM ;EXTRA WORD ONLY FOR INFORMATION
MOV R1, -(SP) ;:PUSH R1 ON STACK
TST R2 ;IS THIS AN ECC TEST
BEQ 15 ;BRANCH IF NO
MOV R2, #-1, R2 ;THESE BITS ARE TO GENERATE ECC
MOV R2, #258., R2 ;256 WORDS PER SECTOR
;PLUS 2 ECC WORDS
MOV R3, #DISK, R3 ;POINTE TO DISK SIMULATION
MOV (R3)+, R2 ;READY TO READ CONTENTS
JSR PC, R2 ;READ
DEC R2 ;IS 256 WORDS DONE?
BNE 25 ;IF NOT BRANCH
TST R2 ;IS THIS AN ECC TEST
BNE 45 ;BRANCH OUT IF YES
CLR R2 ;NO MORE ECC BITS ARE TO BE GENERATED
MOV R2, #15., R2 ;ONE DATA GAP, 14 TOLERANCE GAP
MOV (R3)+, R2 ;READY TO READ CONTENTS OF WORD
JSR PC, R2 ;READ
DEC R2 ;COUNT
BNE 35 ;BRANCH IF 14 NOT DONE

35:
MOV (SP)+, R1 ;:POP STACK INTO R1
RTS R1 ;RETURN

```

10158								
10159								
10160	051570			RPVECT:				
10161	051570	104400	051576		TYPE	64	++	::TYPE ASCIZ STRING
10162	051574	000421			BR	64\$::GET OVER THE ASCIZ
10163					::ASCIZ			/UNEXPECTED RPO4 INTERRUPT @ PC = /
10164	051640			64\$:				
10165	051640	104402			TYPOC			:TYPE FROM PC
10166	051642	012777	051570 127750		MOV	#RPVECT,@RPVEC		:RESTORE TRAP RPO4 VECTOR
10167	051650	000000			HALT			:CHANGE TO CONTINUE


```

10168 .SBTTL SYSMAC LIBRARY ROUTINES
10169 ;*****
10170
10171 .SBTTL SCOPE HANDLER ROUTINE
10172
10173
10174 ;*THIS ROUTINE CONTROLS THE LOOPING OF SUBTESTS. IT WILL INCREMENT
10175 ;*AND LOAD THE TEST NUMBER($TSTNM) INTO THE DISPLAY REG.(DISPLAY<7:0>)
10176 ;*AND LOAD THE ERROR FLAG (SERFLG) INTO DISPLAY<15:08>
10177 ;*THE SWITCH OPTIONS PROVIDED BY THIS ROUTINE ARE:
10178 ;*SM14=1 LOOP ON TEST
10179 ;*SM11=1 INHIBIT ITERATIONS
10180 ;*SM09=1 LOOP ON ERROR
10181 ;*SM08=1 LOOP ON TEST IN SMR<7:0>
10182 ;*CALL
10183 ;* SCOPE ;;SCOPE=IOT
10184
10185 051652 $SCOPE:
10186 051652 005037 045266 CLR @#NOSYNC ;CLEAR FLAG FOR HEADER ERROR COMMANDS
10187 051656 005037 002012 CLR @#TSECC ;CLEAR FLAG FOR ECC TEST
10188 ;WHEN =177777 IT IS AN ECC TEST
10189 ;WHEN =0 IT IS NOT AN ECC TEST
10190
10191 051662 005037 043222 CLR @#TSECCG ;EVEN IN AN ECC TEST EVERY CLOCK
10192 ;IS NOT TO GENERATE ECC
10193 ;IF =177777 GENERATE ECC
10194 ;IF =0 DO NOT GENERATE ECC
10195 051666 005037 002014 CLR @#TESDTE ;DRIVE TIMING ERROR TEST
10196 051672
10197 051672 006137 177570 1$: ROL @#SMR ;;;LOOP ON PRESENT TEST?
10198 051676 100511 BMI $OVER ;;;YES IF SM14=1
10199 ;####START OF CODE FOR THE XOR TESTER####
10200 051700 000416 $XTSTR: BR 6$ ;IF RUNNING ON THE "XOR" TESTER CHANGE
10201 ;THIS INSTRUCTION TO A "NOP" (NOP=240)
10202 051702 013746 000004 MOV @#ERRVEC, -(SP) ;SAVE THE CONTENTS OF THE ERROR VECTOR
10203 051706 012737 051726 000004 MOV $S, @#ERRVEC ;SET FOR TIMEOUT
10204 051714 005737 177060 TST @#177060 ;TIME OUT ON XOR?
10205 051720 012637 000004 MOV (SP)+, @#ERRVEC ;RESTORE THE ERROR VECTOR
10206 051724 000463 BR $SVLAD ;GO TO THE NEXT TEST
10207 051726 022626 5$: CMP (SP)+, (SP)+ ;CLEAR THE STACK AFTER A TIME OUT
10208 051730 012637 000004 MOV (SP)+, @#ERRVEC ;RESTORE THE ERROR VECTOR
10209 051734 000423 BR 7$ ;LOOP ON THE PRESENT TEST
10210 051736 6$: ;####END OF CODE FOR THE XOR TESTER####
10211 051736 032737 000400 177570 BIT #BIT08, @#SMR ;LOOP ON SPEC. TEST?
10212 051744 001404 BEQ 2$ ;BR IF NO
10213 051746 123767 177570 127126 CMPB @#SMR, $TSTNM ;ON THE RIGHT TEST? SMR<7:0>
10214 051754 001462 BEQ $OVER ;BR IF YES
10215 051756 105767 127121 2$: TSTB SERFLG ;HAS AN ERROR OCCURRED?
10216 051762 001421 BEQ 3$ ;BR IF NO
10217 051764 126767 127125 127111 CMPB SERMAX, SERFLG ;MAX. ERRORS FOR THIS TEST OCCURRED?
10218 051772 101015 BHI 3$ ;BR IF NO
10219 051774 032737 001000 177570 BIT #BIT09, @#SMR ;LOOP ON ERROR?
10220 052002 001404 BEQ 4$ ;BR IF NO
10221 052004 016767 127100 127074 7$: MOV $LPERR, $LPADR ;SET LOOP ADDRESS TO LAST SCOPE

```

10222	052012	000443				BR	SOVER		
10223	052014	105067	127063		4S:	CLRB	SERFLG	::: ZERO THE ERROR FLAG	
10224	052020	005067	127160			CLR	STIMES	::: CLEAR THE NUMBER OF ITERATIONS TO MAKE	
10225	052024	000415				BR	IS	::: ESCAPE TO THE NEXT TEST	
10226	052026	032737	004000	177570	3S:	BIT	#BIT11, 2#SWR	::: INHIBIT ITERATIONS?	
10227	052034	001011				BNE	IS	::: BR IF YES	
10228	052036	005767	127036			TST	SPASS	::: IF FIRST PASS OF PROGRAM	
10229	052042	001406				BEQ	IS	::: INHIBIT ITERATIONS	
10230	052044	005267	127034			INC	SICNT	::: INCREMENT ITERATION COUNT	
10231	052050	026767	127130	127026		CMP	STIMES, SICNT	::: CHECK THE NUMBER OF ITERATIONS MADE	
10232	052056	002021				BGE	SOVER	::: BR IF MORE ITERATION REQUIRED	
10233	052060	012767	000001	127016	1S:	MOV	#1, SICNT	::: REINITIALIZE THE ITERATION COUNTER	
10234	052066	016767	000044	127110		MOV	SMXCNT, STIMES	::: SET NUMBER OF ITERATIONS TO DO	
10235	052074	105267	127002		SSVLAD:	INCB	STSTNM	::: COUNT TEST NUMBERS	
10236	052100	011667	127002			MOV	(SP), \$LPADR	::: SAVE SCOPE LOOP ADDRESS	
10237	052104	011667	127000			MOV	(SP), \$LPERR	::: SAVE ERROR LOOP ADDRESS	
10238	052110	005067	127072			CLR	\$ESCAPE	::: CLEAR THE ESCAPE FROM ERROR ADDRESS	
10239	052114	112767	000001	126773		MOVB	#1, SERMAX	::: ONLY ALLOW ONE(1) ERROR ON NEXT TEST	
10240	052122	016737	126754	177570	SOVER:	MOV	STSTNM, 2#DISPLAY	::: DISPLAY TEST NUMBER	
10241	052130	016716	126752			MOV	SLPADR, (SP)	::: FUDGE RETURN ADDRESS	
10242	052134	000002				RTI		::: FIXES PS	
10243	052136	000004			SMXCNT:	4		::: MAX. NUMBER OF ITERATIONS	


```

10244 ;*****
10245
10246 .SBTTL CONVERT BINARY TO DECIMAL AND TYPE ROUTINE
10247
10248 ;*THIS ROUTINE IS USED TO CHANGE A 16-BIT BINARY NUMBER TO A 5-DIGIT
10249 ;*SIGNED DECIMAL (ASCII) NUMBER AND TYPE IT. DEPENDING ON WHETHER THE
10250 ;*NUMBER IS POSITIVE OR NEGATIVE A SPACE OR A MINUS SIGN WILL BE TYPED
10251 ;*BEFORE THE FIRST DIGIT OF THE NUMBER. LEADING ZEROS WILL ALWAYS BE
10252 ;*REPLACED WITH SPACES.
10253 ;*CALL:
10254 ;*      MOV      NUM,-(SP)      ;;PUT THE BINARY NUMBER ON THE STACK
10255 ;*      TYPDS                    ;;GO TO THE ROUTINE
10256
10257 STYPDS:
10258 052140 010046      MOV      R0,-(SP)      ;;PUSH R0 ON STACK
10259 052142 010146      MOV      R1,-(SP)      ;;PUSH R1 ON STACK
10260 052144 010246      MOV      R2,-(SP)      ;;PUSH R2 ON STACK
10261 052146 010346      MOV      R3,-(SP)      ;;PUSH R3 ON STACK
10262 052150 010546      MOV      R5,-(SP)      ;;PUSH R5 ON STACK
10263 052152 012746 020200      MOV      #20200,-(SP)  ;;SET BLANK SWITCH AND SIGN
10264 052156 016605 000020      MOV      20(SP),R5    ;;GET THE INPUT NUMBER
10265 052162 100004      BPL      1$           ;;BR IF INPUT IS POS.
10266 052164 005405      NEG      R5           ;;MAKE THE BINARY NUMBER POS.
10267 052166 112766 000055 000001      MOVVB   #'-,1(SP)    ;;MAKE THE ASCII NUMBER NEG.
10268 052174 005000      CLR      R0           ;;ZERO THE CONSTANTS INDEX
10269 052176 012703 052354      MOV      #SDBLK,R3    ;;SETUP THE OUTPUT POINTER
10270 052202 112723 000040      MOVVB   #' ,(R3)+    ;;SET THE FIRST CHARACTER TO A BLANK
10271 052206 005002      CLR      P2           ;;CLEAR THE BCD NUMBER
10272 052210 016001 052344      MOV      $DTBL(R0),R1 ;;GET THE CONSTANT
10273 052214 160105      SUB      R1,R5        ;;FORM THIS BCD DIGIT
10274 052216 002402      BLT     4$           ;;BR IF DONE
10275 052220 005202      INC      R2           ;;INCREASE THE BCD DIGIT BY 1
10276 052222 000774      BR      3$
10277 052224 060105      4$:      ADD      R1,R5        ;;ADD BACK THE CONSTANT
10278 052226 005702      TST     R2           ;;CHECK IF BCD DIGIT=0
10279 052230 001002      BNE     5$           ;;FALL THROUGH IF 0
10280 052232 105716      TSTB   (SP)         ;;STILL DOING LEADING 0'S?
10281 052234 100407      BMI     7$           ;;BR IF YES
10282 052236 106316      5$:      ASLB   (SP)         ;;MSD?
10283 052240 103003      BCC     6$           ;;BR IF NO
10284 052242 116663 000001 177777      MOVVB   1(SP),-1(R3)  ;;YES--SET THE SIGN
10285 052250 052702 000060      6$:      BIS    #'0,R2        ;;MAKE THE BCD DIGIT ASCII
10286 052254 052702 000040      7$:      BIS    #' ,R2        ;;MAKE IT A SPACE IF NOT ALREADY A DIGIT
10287 052260 110223      MOVVB   R2,(R3)+    ;;PUT THIS CHARACTER IN THE OUTPUT BUFFER
10288 052262 005720      TST    (R0)+        ;;JUST INCREMENTING
10289 052264 020027 000010      CMP    R0,#10       ;;CHECK THE TABLE INDEX
10290 052270 002746      BLT    2$           ;;GO DO THE NEXT DIGIT
10291 052272 003002      BGT    8$           ;;GO TO EXIT
10292 052274 010502      MOV    R5,R2        ;;GET THE LSD
10293 052276 000764      BR     6$           ;;GO CHANGE TO ASCII
10294 052300 105726      8$:      TSTB   (SP)+        ;;WAS THE LSD THE FIRST NON-ZERO?
10295 052302 100003      BPL    9$           ;;BR IF NO
10296 052304 116663 177777 177776      MOVVB   -1(SP),-2(R3) ;;YES--SET THE SIGN FOR TYPING
10297 052312 105013      9$:      CLRB   (R3)        ;;SET THE TERMINATOR

```

10298	052314	012605			MOV	(SP)+,R5	::POP STACK INTO R5
10299	052316	012603			MOV	(SP)+,R3	::POP STACK INTO R3
10300	052320	012602			MOV	(SP)+,R2	::POP STACK INTO R2
10301	052322	012601			MOV	(SP)+,R1	::POP STACK INTO R1
10302	052324	012600			MOV	(SP)+,R0	::POP STACK INTO R0
10303	052326	104400	052354		TYPE	\$DBLK	::NOW TYPE THE NUMBER
10304	052332	016666	000002	000004	MOV	2(SP),4(SP)	::ADJUST THE STACK
10305	052340	012616			MOV	(SP)+,(SP)	
10306	052342	000002			RTI		::RETURN TO USER
10307	052344	023420			\$DTBL:	10000.	
10308	052346	001750				1000.	
10309	052350	000144				100.	
10310	052352	000012				10.	
10311	052354	000004			\$DBLK:	.BLKW 4	


```

10312 ;*****
10313
10314 .SBTTL TYPE ROUTINE
10315
10316 ;*ROUTINE TO TYPE ASCIZ MESSAGE. MESSAGE MUST TERMINATE WITH A 0 BYTE.
10317 ;*THE ROUTINE WILL INSERT A NUMBER OF NULL CHARACTERS AFTER A LINE FEED.
10318 ;*NOTE1: SNULL CONTAINS THE CHARACTER TO BE USED AS THE FILLER CHARACTER.
10319 ;*NOTE2: SFILLS CONTAINS THE NUMBER OF FILLER CHARACTERS REQUIRED.
10320 ;*NOTE3: SFILLC CONTAINS THE CHARACTER TO FILL AFTER.
10321 ;*
10322 ;*CALL:
10323 ;*1) USING A TRAP INSTRUCTION
10324 ;* TYPE ,MESADR ;;MESADR IS FIRST ADDRESS OF AN ASCIZ STRING
10325 ;*OR
10326 ;* TYPE
10327 ;* MESADR
10328 ;*
10329 ;*2) USING A JSR INSTRUCTION
10330 ;* MOV PS,-(SP) ;;PUSH PROCESSOR STATUS WORD ON THE STACK
10331 ;* JSR PC,STYPE ;;CALL TYPE ROUTINE
10332 ;* MESADDR ;;FIRST ADDRESS OF MESSAGE
10333
10334 052364 105767 126561 STYPE: TSTB STPFLG ;;IS THERE A TERMINAL?
10335 052370 100002 BPL 1$ ;;BR IF YES
10336 052372 000000 HALT ;;HALT HERE IF NO TERMINAL
10337 052374 000407 BR 3$ ;;LEAVE
10338 052376 010046 1$: MOV RO,-(SP) ;;SAVE RO
10339 052400 017600 000002 MOV 22(SP),RO ;;GET ADDRESS OF ASCIZ STRING
10340 052404 112046 2$: MOVB (RO)+,-(SP) ;;PUSH CHARACTER TO BE TYPED ONTO STACK
10341 052406 001005 BNE 4$ ;;BR IF IT ISN'T THE TERMINATOR
10342 052410 005726 TST (SP)+ ;;IF TERMINATOR POP IT OFF THE STACK
10343 052412 012600 MOV (SP)+,RO ;;RESTORE RO
10344 052414 062716 000002 3$: ADD #2,(SP) ;;ADJUST RETURN PC
10345 052420 000002 RTI ;;RETURN
10346 052422 004767 000026 4$: JSR PC,STYPEC ;;GO TYPE THIS CHARACTER
10347 052426 126726 126516 5$: CMPB SFILLC,(SP)+ ;;IS IT TIME FOR FILLER CHARS.?
10348 052432 001364 BNE 2$ ;;IF NO GO GET NEXT CHAR.
10349 052434 016746 126506 MOV SNULL,-(SP) ;;GET # OF FILLER CHARS. NEEDED
10350 ;;AND THE NULL CHAR.
10351 052440 105366 000001 6$: DECB 1(SP) ;;DOES A NULL NEED TO BE TYPED?
10352 052444 002770 BLT 5$ ;;BR IF NO--GO POP THE NULL OFF OF STACK
10353 052446 004767 000002 JSR PC,STYPEC ;;GO TYPE A NULL
10354 052452 000772 BR 6$ ;;LOOP
10355 052454 105777 126462 STYPEC: TSTB 2$TPS ;;WAIT UNTIL PRINTER IS READY
10356 052460 100375 BPL STYPEC
10357 052462 116677 000002 126454 MOVB 2(SP),2$TPB ;;LOAD CHAR TO BE TYPED INTO DATA REG.
10358 052470 000207 RTS PC

```

```

10359
10360
10361
10362
10363 052472 000000
10364 052474 000000
10365 052476 000000
10366 052500 000011
10367          052511
10368          052512
10369
10370
10371
10372
10373
10374
10375
10376
10377
10378 052512 005067 177754
10379 052516 012767 052500 177750
10380 052524 016767 177744 177744
10381 052532 012737 052562 000060
10382 052540 012737 000200 000062
10383 052546 005777 126366
10384 052552 012777 000100 126356
10385 052560 000207
10386
10387
10388
10389
10390 052562 117746 126352
10391 052566 042716 177600
10392 052572 021627 000003
10393 052576 001006
10394 052600 104400 053073
10395 052604 004767 177702
10396 052610 000167 164532
10397 052614 022767 000011 177650 1S:
10398 052622 001004
10399 052624 104400 001210
10400 052630 005726
10401 052632 000002
10402 052634 005267 177632 2S:
10403 052640 112677 177630
10404 052644 005267 177624
10405 052650 026727 177620 052511
10406 052656 001003
10407 052660 012767 052500 177606
10408 052666 000002
10409
10410
10411
10412

```

.SBTTL TTY INPUT ROUTINE

```

STKCNT: .WORD 0          ;; NUMBER OF ITEMS IN QUEUE
STKQIN: .WORD 0          ;; INPUT POINTER
STKQOUT: .WORD 0         ;; OUTPUT POINTER
STKQSRV: .BLKB 9         ;; TTY KEYBOARD QUEUE
STKQEND=.
.EVEN

```

```

;#TK INITIALIZE ROUTINE
;#THIS ROUTINE WILL INITIALIZE THE TTY KEYBOARD INPUT QUEUE
;#SETUP THE INTERRUPT VECTOR AND TURN ON THE KEYBOARD INTERRUPT

```

```

;#CALL
;# JSR PC,STKINT
;# RETURN

```

```

$TKINT: CLR STKCNT          ;; CLEAR COUNT OF ITEMS IN QUEUE
        MOV $STKQSRV,STKQIN ;; MOVE THE STARTING ADDRESS OF THE
        MOV STKQIN,STKQOUT  ;; QUEUE INTO THE INPUT & OUTPUT POINTERS.
        MOV $STKSRV,$$TKVEC ;; INITIALIZE THE KEYBOARD VECTOR
        MOV $200,$$TKVEC+2  ;; "BR" LEVEL 4
        TST $STKB          ;; CLEAR DONE FLAG
        MOV $BIT06,$$STKS  ;; ENABLE INTERRUPT
        RTS PC              ;; RETURN TO CALLER

```

```

;#TK SERVICE ROUTINE
;#THIS ROUTINE WILL SERVICE THE TTY KEYBOARD INTERRUPT

```

```

$TKSRV: MOVB $$STKB,-(SP)  ;; PICKUP THE CHARACTER
        BIC $1C177,(SP)   ;; STRIP THE JUNK
        CMP (SP),#$3      ;; IS IT A CONTROL C?
        BNE 1$            ;; BRANCH IF NO
        TYPE $CNTLC       ;; TYPE A CONTROL-C (1C)
        JSR PC,STKINT     ;; INIT THE KEYBOARD
        JMP OPERSL        ;; CONTROL C RESTART
1$:     CMP $9,$$STKCNT   ;; IS THE QUEUE FULL?
        BNE 2$            ;; BRANCH IF NO
        TYPE $BELL        ;; RING THE TTY BELL
        TST (SP)+        ;; CLEAN CHARACTER OFF OF STACK
        RTI              ;; RETURN
2$:     INC STKCNT        ;; COUNT THIS CHARACTER
        MOVB (SP)+,$$STKQIN ;; AND PUT IT IN QUEUE
        INC STKQIN        ;; UPDATE THE POINTER
        CMP STKQIN,$$STKQEND ;; GO OFF THE END?
        BNE 3$            ;; BRANCH IF NO
        MOV $STKQSRV,STKQIN ;; RESET THE POINTER
3$:     RTI              ;; RETURN

```

;#THIS ROUTINE WILL INPUT A SINGLE CHARACTER FROM THE TTY

```

;#CALL:
;# RDCHR          ;; INPUT A SINGLE CHARACTER FROM THE TTY

```



```

10413 ;* RETURN HERE ;: CHARACTER IS ON THE STACK
10414 ;
10415 ;
10416 052670 011646 SRDCHR: MOV (SP), -(SP) ;: PUSH DOWN THE PC AND
10417 052672 016666 000004 000002 MOV 4(SP), 2(SP) ;: THE PS
10418 052700 0050F6 000004 CLR 4(SP) ;: GET READY FOR A CHARACTER
10419 052704 005C37 177776 CLR 2#PS ;: ALLOW INTERRUPTS
10420 052710 U05767 177556 1S: TST STKCNT ;: WAIT ON A CHARACTER
10421 052714 001775 BEQ 1S
10422 052716 005367 177550 DEC STKCNT ;: DECREMENT THE COUNTER
10423 052722 117766 177550 000004 MOVB 2#STKQOUT, 4(SP) ;: GET ONE CHARACTER
10424 052730 005267 177542 INC STKQOUT ;: UPDATE THE POINTER
10425 052734 02E727 177536 052511 CMP STKQOUT, #STKQEND ;: DID IT GO OFF OF THE END?
10426 052742 001003 BNE 2S ;: BRANCH IF NO
10427 052744 012767 052500 177524 MOV #STKQSR, STKQOUT ;: RESET THE POINTER
10428 052752 000002 2S: RTI ;: RETURN
10429 ;*****
10430 ;*THIS ROUTINE WILL INPUT A STRING FROM THE TTY
10431 ;*CALL:
10432 ;* RDLIN ;: INPUT A STRING FROM THE TTY
10433 ;* RETURN HERE ;: ADDRESS OF FIRST CHARACTER WILL BE ON THE STACK
10434 ;* ;: TERMINATOR WILL BE A BYTE OF ALL 0'S
10435 ;
10436 052754 010346 SRDLIN: MOV R3, -(SP) ;: SAVE R3
10437 052756 012703 053062 1S: MOV #STTYIN, R3 ;: GET ADDRESS
10438 052762 022703 053073 2S: CMP #STTYIN+9., R3 ;: BUFFER FULL?
10439 052766 101405 BLOS 4S ;: BR IF YES
10440 052770 104412 RDRCHR ;: GO READ ONE CHARACTER FROM THE TTY
10441 052772 112613 MOVB (SP)+, (R3) ;: GET CHARACTER
10442 052774 122713 000177 CMPB #177, (R3) ;: IS IT A RUBOUT
10443 053000 001003 BNE 3S ;: SKIP IF NOT
10444 053002 104400 001214 4S: TYPE $QUES ;: TYPE A '?'
10445 053006 000763 BR 1S ;: CLEAR THE BUFFER AND LOOP
10446 053010 111367 000044 3S: MOVB (R3), 9S ;: ECHO THE CHARACTER
10447 053014 104400 053060 TYPE 9S
10448 053020 122723 000015 CMPB #15, (R3)+ ;: CHECK FOR RETURN
10449 053024 001356 BNE 2S ;: LOOP IF NOT RETURN
10450 053026 105063 177777 CLRB -1(R3) ;: CLEAR RETURN (THE 15)
10451 053032 104400 001216 TYPE $LF ;: TYPE A LINE FEED
10452 053036 012603 MOV (SP)+, R3 ;: RESTORE R3
10453 053040 011646 MOV (SP), -(SP) ;: ADJUST THE STACK AND PUT ADDRESS OF THE
10454 053042 016666 000004 000002 MOV 4(SP), 2(SP) ;: FIRST ASCII CHARACTER ON IT
10455 053050 012766 053062 000004 MOV #STTYIN, 4(SP)
10456 053056 000002 RTI ;: RETURN
10457 053060 000 9S: .BYTE 0 ;: STORAGE FOR ASCII CHAR. TO TYPE
10458 053061 000 .BYTE 0 ;: TERMINATOR
10459 053062 000011 $TTYIN: .BLKB 9. ;: RESERVE 9. BYTES FOR TTY INPUT
10460 053073 136 006503 000012 $CNTLC: .ASCIZ /iC/<15><12> ;: CONTROL "C"
10461 ;: FROM THE TTY

```

```

10462
10463
10464
10465
10466
10467
10468
10469
10470
10471
10472
10473
10474
10475
10476
10477 053100 011646
10478 053102 016666 000004 000002
10479 053110 010046
10480 053112 010146
10481 053114 010246
10482 053116 104414
10483 053120 012600
10484 053122 010067 000100
10485 053126 005001
10486 053130 005002
10487 053132 112046
10488 053134 001420
10489 053136 122716 000060
10490 053142 003026
10491 053144 122716 000067
10492 053150 002423
10493 053152 006301
10494 053154 006102
10495 053156 006301
10496 053160 006102
10497 053162 006301
10498 053164 006102
10499 053166 042716 177770
10500 053172 062601
10501 053174 000756
10502 053176 005726
10503 053200 010166 000012
10504 053204 010267 000026
10505 053210 012602
10506 053212 012601
10507 053214 012600
10508 053216 000002
10509 053220 005726
10510 053222 105010
10511 053224 104400
10512 053226 000000
10513 053230 104400 001214
10514 053234 000730
10515 053236 000000

```

```

;*****
.SBTTL READ AN OCTAL NUMBER FROM THE TTY

;*THIS ROUTINE WILL READ AN OCTAL (ASCII) NUMBER FROM THE TTY AND
;*CHANGE IT TO BINARY.
;*THE INPUT CHARACTERS WILL BE CHECKED TO INSURED THEY ARE LEGAL
;*OCTAL DIGITS. IF AN ILLEGAL CHARACTER IS READ A "?" WILL BE TYPED
;*FOLLOWED BY A CARRIAGE RETURN-LINE FEED. THE COMPLETE NUMBER MUST
;*THEN BE RETYPED. THE INPUT IS TERMINATED BY TYPING A CARRIAGE RETURN.
;*CALL:
;*   RDOCT           ;; READ AN OCTAL NUMBER
;*   RETURN HERE    ;; LOW ORDER BITS ARE ON TOP OF THE STACK
;*                 ;; HIGH ORDER BITS ARE IN SHIOCT

SRDOCT: MOV      (SP),-(SP)           ;; PROVIDE SPACE FOR THE
        MOV      4(SP),2(SP)        ;; INPUT NUMBER
        MOV      R0,-(SP)           ;; PUSH R0 ON STACK
        MOV      R1,-(SP)           ;; PUSH R1 ON STACK
        MOV      R2,-(SP)           ;; PUSH R2 ON STACK
1$:     RDLIN                    ;; READ AN ASCII LINE
        MOV      (SP)+,R0           ;; GET ADDRESS OF 1ST CHARACTER
        MOV      R0,5$             ;; AND SAVE IT
        CLR      R1                 ;; CLEAR DATA WORD
        CLR      R2
2$:     MOVB     (R0)+,-(SP)         ;; PICKUP THIS CHARACTER
        BEQ      3$                 ;; IF ZERO GET OUT
        CMPB    #'0,(SP)           ;; MAKE SURE THIS CHARACTER
        BGT      4$                 ;; IS AN OCTAL DIGIT
        CMPB    #'7,(SP)
        BLT      4$
        ASL     R1                   ;;#2
        ROL     R2
        ASL     R1                   ;;#4
        ROL     R2
        ASL     R1                   ;;#8
        ROL     R2
        BIC     #'C7,(SP)           ;; STRIP THE ASCII JUNK
        ADD     (SP)+,R1           ;; ADD IN THIS DIGIT
        BR      2$                 ;; LOOP
3$:     TST     (SP)+               ;; CLEAN TERMINATOR FROM STACK
        MOV     R1,12(SP)          ;; SAVE THE RESULT
        MOV     R2,SHIOCT
        MOV     (SP)+,R2           ;; POP STACK INTO R2
        MOV     (SP)+,R1           ;; POP STACK INTO R1
        MOV     (SP)+,R0           ;; POP STACK INTO R0
        RTI
4$:     TST     (SP)+               ;; CLEAN PARTIAL FROM STACK
        CLRB   (R0)                ;; SET A TERMINATOR
        TYPE   TYPE UP THRU THE BAD CHAR.
5$:     .WORD   0
        TYPE   $QUES               ;; "?" "CR" & "LF"
        BR     1$                 ;; TRY AGAIN
SHIOCT: .WORD   0                 ;; HIGH ORDER BITS GO HERE

```



```

10516 ;*****
10517
10518 .SBTTL ERROR HANDLER ROUTINE
10519
10520 ;*THIS ROUTINE WILL INCREMENT THE ERROR FLAG AND THE ERROR COUNT,
10521 ;*SAVE THE ERROR ITEM NUMBER AND THE ADDRESS OF THE ERROR CALL
10522 ;*AND GO TO SERRTYP ON ERROR
10523 ;*THE SWITCH OPTIONS PROVIDED BY THIS ROUTINE ARE:
10524 ;*SM15=1 HALT ON ERROR
10525 ;*SM13=1 INHIBIT ERROR TYPEOUTS
10526 ;*SM10=1 BELL ON ERROR
10527 ;*SM09=1 LOOP ON ERROR
10528 ;*CALL
10529 ;* ERROR N ;;ERROR=EMT AND N=ERROR ITEM NUMBER
10530
10531 053240 SERROR:
10532 053240 012737 177777 001774 MOV #-1, @#ERFLGS ;SET ERROR FLAG
10533 053246 REGSA1:
10534 053246 105267 125631 7S: INCB SERFLG ;;SET THE ERROR FLAG
10535 053252 001775 BEQ 7S ;;DON'T LET THE FLAG GO TO ZERO
10536 053254 016737 125622 177570 MOV $STNM, @#DISPLAY ;;DISPLAY TEST NUMBER AND ERROR FLAG
10537 053262 032737 002000 177570 BIT #BIT10, @#SMR ;;BELL ON ERROR?
10538 053270 001402 BEQ 1S ;;NO - SKIP
10539 053272 104400 001210 TYPE SBELL ;;RING BELL
10540 053276 005267 125610 1S: INC $ERTTL ;;COUNT THE NUMBER OF ERRORS
10541 053302 011667 125610 MOV (SP), $ERRPC ;;GET ADDRESS OF ERROR INSTRUCTION
10542 053306 162767 000002 125602 SUB #2, $ERRPC
10543 053314 117767 125576 125572 MOVB @SERRPC, $ITEMB ;;STRIP AND SAVE THE ERROR ITEM CODE
10544 053322 032737 020000 177570 BIT #BIT13, @#SMR ;;SKIP TYPEOUT IF SET
10545 053330 001004 BNE 2S ;;SKIP TYPEOUTS
10546 053332 004737 053402 JSR PC, @#SERRTYP ;;GO TO USER ERROR ROUTINE
10547 053336 104400 001215 TYPE $CRLF
10548 053342 005737 177570 2S: TST @#SMR ;;HALT ON ERROR
10549 053346 100001 BPL 3S ;;SKIP IF CONTINUE
10550 053350 000000 HALT ;;HALT ON ERROR!
10551 053352 032737 001000 177570 3S: BIT #BIT09, @#SMR ;;LOOP ON ERROR SWITCH SET?
10552 053360 001402 BEQ 4S ;;BR IF NO
10553 053362 016716 125522 MOV $LPERR, (SP) ;;FUDGE RETURN FOR LOOPING
10554 053366 005767 125614 4S: TST $ESCAPE ;;CHECK FOR AN ESCAPE ADDRESS
10555 053372 001402 BEQ 5S ;;BR IF NONE
10556 053374 016716 125606 MOV $ESCAPE, (SP) ;;FUDGE RETURN ADDRESS FOR ESCAPE
10557 053400 5S:
10558 053400 000002 RTI ;;RETURN

```

```

10559
10560
10561
10562
10563
10564
10565
10566
10567 053402
10568 053402 104400 001215
10569 053406 010046
10570 053410 005000
10571 053412 153700 001114
10572 053416 001004
10573
10574 053420 016746 125472
10575
10576 053424 104402
10577 053426 000445
10578 053430 005300
10579 053432 006300
10580 053434 006300
10581 053436 006300
10582 053440 062700 001220
10583 053444 012067 000004
10584 053450 001404
10585 053452 104400
10586 053454 000000
10587 053456 104400 001215
10588 053462 012067 000004
10589 053466 001404
10590 053470 104400
10591 053472 000000
10592 053474 104400 001215
10593 053500 010146
10594 053502 012001
10595 053504 001415
10596 053506 012000
10597 053510 105720
10598 053512 001003
10599 053514 013146
10600 053516 104402
10601 053520 000402
10602 053522
10603 053522 013146
10604 053524 104410
10605 053526 005711
10606 053530 001403
10607 053532 104400 053552
10608 053536 000764
10609
10610 053540 012601
10611 053542 012600
10612 053544 104400 001215

```

.SBTTL ERROR MESSAGE TYPEOUT ROUTINE

```

; *THIS ROUTINE USES THE "ITEM CONTROL BYTE" (SITEMB) TO DETERMINE WHICH
; *ERROR IS TO BE REPORTED. IT THEN OBTAINS, FROM THE "ERROR TABLE" (SERRTB),
; *AND REPORTS THE APPROPRIATE INFORMATION CONCERNING THE ERROR.

```

SERRTYP:

```

TYPE $CRLF ;;"CARRIAGE RETURN" & "LINE FEED"
MOV RO,-(SP) ;:SAVE RO
CLR RO ;:PICKUP THE ITEM INDEX
BISB @SITEMB,RO
BNE IS ;:IF ITEM NUMBER IS ZERO, JUST
;:TYPE THE PC OF THE ERROR
MOV SERRPC,-(SP) ;:SAVE SERRPC FOR TYPEOUT
;:ERROR ADDRESS
;:GO TYPE--OCTAL ASCII(ALL DIGITS)
BR 10$ ;:GET OUT
DEC RO ;:ADJUST THE INDEX SO THAT IT WILL
ASL RO ;:WORK FOR THE ERROR TABLE
ASL RO
ASL RO
ADD #SERRTB,RO ;:FORM TABLE POINTER
MOV (RO)+,2$ ;:PICKUP "ERROR MESSAGE" POINTER
BEQ 3$ ;:SKIP TYPEOUT IF NO POINTER
TYPE ;:TYPE THE "ERROR MESSAGE"
;:"ERROR MESSAGE" POINTER GOES HERE
;: "CARRIAGE RETURN" & "LINE FEED"
MOV (RO)+,4$ ;:PICKUP "DATA HEADER" POINTER
BEQ 5$ ;:SKIP TYPEOUT IF 0
TYPE ;:TYPE THE "DATA HEADER"
;:"DATA HEADER" POINTER GOES HERE
;:"CARRIAGE RETURN" & "LINE FEED"
MOV R1,-(SP) ;:SAVE R1
MOV (RO)+,R1 ;:PICKUP "DATA TABLE" POINTER
BEQ 9$ ;:BR IF NO DATA TO BE TYPED
MOV (RO)+,RO ;:PICKUP "DATA FORMAT" POINTER
;:"OCTAL" OR "DECIMAL"
TSTB (RO)+ ;:BR IF DECIMAL
BNE 7$ ;:SAVE @ (R1)+ FOR TYPEOUT
MOV @ (R1)+,-(SP) ;:GO TYPE--OCTAL ASCII(ALL DIGITS)
BR 8$
;:SAVE @ (R1)+ FOR TYPEOUT
MOV TYPDS @ (R1)+,-(SP) ;:GO TYPE--DECIMAL ASCII WITH SIGN
;:IS THERE ANOTHER NUMBER?
TST (R1) ;:BR IF NO
BEQ 9$ ;:TYPE TWO(2) SPACES
TYPE ,11$ ;:LOOP
BR 6$
;:RESTORE R1
MOV (SP)+,R1 ;:RESTORE RO
MOV (SP)+,RO ;:"CARRIAGE RETURN" & "LINE FEED"
TYPE $CRLF

```


10613	053550	000207			RTS	PC	:::RETURN
10614	053552	020040	000	11\$:	.ASCIZ	/ /	:::TWO(2) SPACES
10615		053556			.EVEN		

10616
10617
10618
10619
10620
10621
10622
10623
10624
10625
10626
10627
10628
10629
10630
10631
10632
10633
10634
10635
10636
10637
10638
10639
10640
10641
10642
10643
10644
10645
10646
10647
10648
10649
10650
10651
10652
10653
10654
10655
10656
10657
10658
10659
10660
10661
10662
10663
10664
10665
10666
10667
10668
10669

053556 017646 000000
053562 116667 000001 000211
053570 112667 000207
053574 062716 000002
053600 000406
053602 112767 000001 000171
053610 112767 000006 000165
053616 112767 000005 000154
053624 010346
053626 010446
053630 010546
053632 116704 000145
053636 005404
053640 062704 000006
053644 110467 000132
053650 116704 000125
053654 016605 000012
053660 005003
053662 006105
053664 000404
053666 006105
053670 006105
053672 006105
053674 010503
053676 006103
053700 105367 000076
053704 100016
053706 042703 177770

```
*****  
.SBTTL BINARY TO OCTAL (ASCII) AND TYPE  
#THIS ROUTINE IS USED TO CHANGE A 16-BIT BINARY NUMBER TO A 6-DIGIT  
#OCTAL (ASCII) NUMBER AND TYPE IT.  
#STYPOS---ENTER HERE TO SETUP SUPPRESS ZEROS AND NUMBER OF DIGITS TO TYPE  
#CALL:  
#   MOV     NUM,-(SP)      ;;NUMBER TO BE TYPED  
#   TYPOS   ;;CALL FOR TYPEOUT  
#   .BYTE  N               ;;N=1 TO 6 FOR NUMBER OF DIGITS TO TYPE  
#   .BYTE  M               ;;M=1 OR 0  
#                               ;;1=TYPE LEADING ZEROS  
#                               ;;0=SUPPRESS LEADING ZEROS  
#STYPON---ENTER HERE TO TYPE OUT WITH THE SAME PARAMETERS AS THE LAST  
#STYPOS OR STYPOC  
#CALL:  
#   MOV     NUM,-(SP)      ;;NUMBER TO BE TYPED  
#   TYPON   ;;CALL FOR TYPEOUT  
#STYPOC---ENTER HERE FOR TYPEOUT OF A 16 BIT NUMBER  
#CALL:  
#   MOV     NUM,-(SP)      ;;NUMBER TO BE TYPED  
#   TYPOC   ;;CALL FOR TYPEOUT  
STYPOS: MOV     2(SP),-(SP)  ;; PICKUP THE MODE  
        MOV     1(SP),SOFILL ;; LOAD ZERO FILL SWITCH  
        MOV     (SP)+,SOMODE+1 ;; NUMBER OF DIGITS TO TYPE  
        ADD     #2,(SP)      ;; ADJUST RETURN ADDRESS  
        BR     STYPON  
STYPOC: MOV     #1,SOFILL   ;; SET THE ZERO FILL SWITCH  
        MOV     #6,SOMODE+1 ;; SET FOR SIX(6) DIGITS  
STYPON: MOV     #5,SOCNT    ;; SET THE ITERATION COUNT  
        MOV     R3,-(SP)    ;; SAVE R3  
        MOV     R4,-(SP)    ;; SAVE R4  
        MOV     R5,-(SP)    ;; SAVE R5  
        MOV     SOMODE+1,R4 ;; GET THE NUMBER OF DIGITS TO TYPE  
        NEG     R4  
        ADD     #6,R4       ;; SUBTRACT IT FOR MAX. ALLOWED  
        MOV     R4,SOMODE   ;; SAVE IT FOR USE  
        MOV     SOFILL,R4   ;; GET THE ZERO FILL SWITCH  
        MOV     12(SP),R5   ;; PICKUP THE INPUT NUMBER  
        CLR     R3         ;; CLEAR THE OUTPUT WORD  
1$:    ROL     R5          ;; ROTATE MSB INTO "C"  
        BR     3$         ;; GO DO MSB  
2$:    ROL     R5          ;; FORM THIS DIGIT  
        ROL     R5  
        ROL     R5  
        MOV     R5,R3  
3$:    ROL     R3          ;; GET LSB OF THIS DIGIT  
        DECB   SOMODE     ;; TYPE THIS DIGIT?  
        BPL    7$         ;; BR IF NO  
        BIC   #177770,R3  ;; GET RID OF JUNK
```


10670	053712	001002		BNE	4\$:: TEST FOR 0
10671	053714	005704		TST	R4	:: SUPPRESS THIS 0?
10672	053716	001403		BEQ	5\$:: BR IF YES
10673	053720	005204		INC	R4	:: DON'T SUPPRESS ANYMORE 0'S
10674	053722	052703	000060	BIS	#'0,R3	:: MAKE THIS DIGIT ASCII
10675	053726	052703	000040	BIS	#' ,R3	:: MAKE ASCII IF NOT ALREADY
10676	053732	110367	000040	MOVB	R3,8\$:: SAVE FOR TYPING
10677	053736	104400	053776	TYPE	8\$:: GO TYPE THIS DIGIT
10678	053742	105367	000032	7\$: DECB	\$OCNT	:: COUNT BY 1
10679	053746	003347		BGT	2\$:: BR IF MORE TO DO
10680	053750	002402		BLT	6\$:: BR IF DONE
10681	053752	005204		INC	R4	:: INSURE LAST DIGIT ISN'T A BLANK
10682	053754	000744		BR	2\$:: GO DO THE LAST DIGIT
10683	053756	012605		6\$: MOV	(SP)+,R5	:: RESTORE R5
10684	053760	012604		MOV	(SP)+,R4	:: RESTORE R4
10685	053762	012603		MOV	(SP)+,R3	:: RESTORE R3
10686	053764	016666	000002 000004	MOV	2(SP),4(SP)	:: SET THE STACK FOR RETURNING
10687	053772	012616		MOV	(SP)+,(SP)	
10688	053774	000002		RTI		:: RETURN
10689	053776	000		8\$: .BYTE	0	:: STORAGE FOR ASCII DIGIT
10690	053777	000		.BYTE	0	:: TERMINATOR FOR TYPE ROUTINE
10691	054000	000		\$OCNT: .BYTE	0	:: OCTAL DIGIT COUNTER
10692	054001	000		\$OFILL: .BYTE	0	:: ZERO FILL SWITCH
10693	054002	000000		\$OMODE: .WORD	0	:: NUMBER OF DIGITS TO TYPE

```

10694
10695
10696
10697
10698
10699
10700
10701
10702
10703 054004 010046
10704 054006 016600 000002
10705 054012 005740
10706 054014 111000
10707 054016 016000 054024
10708 054022 000200
10709
10710
10711
10712
10713
10714
10715
10716
10717
10718 054024
10719 054024 052364
10720 054026 053602
10721 054030 053556
10722 054032 053616
10723 054034 052140
10724 054036 052670
10725 054040 052754
10726 054042 053100
10727 054044 040504
10728 054046 040556
10729 054050 041074

```

.SBTTL TRAP DECODER

```

; *THIS ROUTINE WILL PICKUP THE LOWER BYTE OF THE "TRAP" INSTRUCTION
; *AND USE IT TO INDEX THROUGH THE TRAP TABLE FOR THE STARTING ADDRESS
; *OF THE DESIRED ROUTINE. THEN USING THE ADDRESS OBTAINED IT WILL
; *GO TO THAT ROUTINE.

```

```

$TRAP:  MOV    RO, -(SP)           ;; SAVE RO
        MOV    2(SP), RO         ;; GET TRAP ADDRESS
        TST   -(RO)             ;; BACKUP BY 2
        MOVB  (RO), RO          ;; GET RIGHT BYTE OF TRAP
        MOV   $TRPAD(RO), RO     ;; INDEX TO TABLE
        RTS   RO                ;; GO TO ROUTINE

```

.SBTTL TRAP TABLE

```

; *THIS TABLE CONTAINS THE STARTING ADDRESSES OF THE ROUTINES CALLED
; *BY THE "TRAP" INSTRUCTION.

```

	ROUTINE		

\$TRPAD:	STYPER	;; CALL=TYPE	TRAP+0(104400) TTY TYPEOUT ROUTINE
	STYPOC	;; CALL=TYPOC	TRAP+2(104402) TYPE OCTAL NUMBER (WITH LEADING ZEROS)
	STYPOS	;; CALL=TYPOS	TRAP+4(104404) TYPE OCTAL NUMBER (NO LEADING ZEROS)
	STYPON	;; CALL=TYPON	TRAP+6(104406) TYPE OCTAL NUMBER (AS PER LAST CALL)
	STYPDS	;; CALL=TYPDS	TRAP+10(104410) TYPE DECIMAL NUMBER (WITH SIGN)
	\$RDCHR	;; CALL=RDCHR	TRAP+12(104412) TTY TYPEIN CHARACTER ROUTINE
	\$RDLIN	;; CALL=RDLIN	TRAP+14(104414) TTY TYPEIN STRING ROUTINE
	\$RDOCT	;; CALL=RDOCT	TRAP+16(104416) READ AN OCTAL NUMBER FROM TTY
	T.SCOPI	;; CALL=SCOPI	TRAP+20(104420) MY LOCAL SCOPES
	CHECKT	;; CALL=CHECKD	TRAP+22(104422) CHECK DVA, RDY, DPR, DRY
	WAIT.T	;; CALL=MAT	TRAP+24(104424) WAIT LOOP


```

10730 ;*****
10731
10732 .SBTTL POWER DOWN AND UP ROUTINES
10733
10734 :POWER DOWN ROUTINE
10735 054052 012737 054200 000024 $PWRDN: MOV $SILLUP, @#PWRVEC ;;SET FOR FAST UP
10736 054060 012737 000340 000026 MOV #340, @#PWRVEC+2 ;;PRIO:7
10737 054066 010046 MOV RO, -(SP) ;;PUSH R0 ON STACK
10738 054070 010146 MOV R1, -(SP) ;;PUSH R1 ON STACK
10739 054072 010246 MOV R2, -(SP) ;;PUSH R2 ON STACK
10740 054074 010346 MOV R3, -(SP) ;;PUSH R3 ON STACK
10741 054076 010446 MOV R4, -(SP) ;;PUSH R4 ON STACK
10742 054100 010546 MOV R5, -(SP) ;;PUSH R5 ON STACK
10743 054102 010667 000076 MOV SP, $SAVR6 ;;SAVE SP
10744 054106 012737 054120 000024 MOV $SPWRUP, @#PWRVEC ;;SET UP VECTOR
10745 054114 000000 HALT
10746 054116 000776 BR .-2 ;;HANG UP
10747
10748 :POWER UP ROUTINE
10749 054120 016706 000060 $PWRUP: MOV $SAVR6, SP ;;GET SP
10750 054124 005067 000054 CLR $SAVR6 ;;WAIT LOOP FOR THE TTY
10751 054130 005267 000050 1$: INC $SAVR6 ;;WAIT FOR THE INC
10752 054134 001375 BNE 1$ ;;OF WORD
10753 054136 012605 MOV (SP)+, R5 ;;POP STACK INTO R5
10754 054140 012604 MOV (SP)+, R4 ;;POP STACK INTO R4
10755 054142 012603 MOV (SP)+, R3 ;;POP STACK INTO R3
10756 054144 012602 MOV (SP)+, R2 ;;POP STACK INTO R2
10757 054146 012601 MOV (SP)+, R1 ;;POP STACK INTO R1
10758 054150 012600 MOV (SP)+, R0 ;;POP STACK INTO R0
10759 054152 012737 054052 000024 MOV $SPWRDN, @#PWRVEC ;;SET UP THE POWER DOWN VECTOR
10760 054160 012737 000340 000026 MOV #340, @#PWRVEC+2 ;;PRIO:7
10761 054166 104400 TYPE REPORT THE POWER FAILURE
10762 054170 054206 $PWRMG: .WORD $POWER ;;POWER FAIL MESSAGE POINTER
10763 054172 012716 MOV (PC)+, (SP) ;;RESTART AT BEGIN
10764 054174 004220 $PWRAD: .WORD BEGIN ;;RESTART ADDRESS
10765 054176 000002 RTI
10766 054200 000000 $SILLUP: HALT ;;THE POWER UP SEQUENCE WAS STARTED
10767 054202 000776 BR .-2 ;;BEFORE THE POWER DOWN WAS COMPLETE
10768 054204 000000 $SAVR6: 0 ;;PUT THE SP HERE
10769 054206 005015 047520 042527 $POWER: .ASCIZ <15><12>"POWER"
10770 054214 000122
10771 .EVEN

```

```

10772
10773
10774
10775
10776
10777
10778
10779
10780
10781 054216 051127 047117 020107
10782 054224 040504 040524 044440
10783 054232 020116 042522 042101
10784 054240 047111 020107 051117
10785 054246 053440 044522 044524
10786 054254 043516 044040 051101
10787 054262 053504 051101 020105
10788 054270 042522 044507 052123
10789 054276 051105 000
10790 054301 105 051122 051117
10791 054306 047440 020116 042040
10792 054314 052101 020101 047503
10793 054322 046515 047101 000104
10794 054330 051105 047522 020122
10795 054336 047117 053440 044522
10796 054344 042524 044040 040505
10797 054352 042504 020122 047101
10798 054360 020104 040504 040524
10799 054366 000
10800 054367 103 047117 051124
10801 054374 046117 042514 020122
10802 054402 051117 042040 044522
10803 054410 042526 051440 040524
10804 054416 052524 000123
10805 054422 042522 044507 052123
10806 054430 051105 043040 044501
10807 054436 042514 000104
10808 054442 050123 041505 043111
10809 054450 042511 020104 042522
10810 054456 044507 052123 051105
10811 054464 047040 047117 042440
10812 054472 044530 052123 047101
10813 054500 020124 047523 040440
10814 054506 047502 052122 000
10815 054513 127 044501 020124
10816 054520 047514 050117 043040
10817 054526 044501 042514 000104
10818 054534 051127 052111 020105
10819 054542 044103 041505 020113
10820 054550 040506 046111 047111
10821 054556 000107
10822 054560 042522 044507 052123
10823 054566 051105 043040 044501
10824 054574 044514 043516 000
10825 054601 111 052116 051105

```

```

:*****
:ERROR AND MESSAGE TABLE CONDIMENTS
:*****

```

EM1: .ASCIZ /WRONG DATA IN READING OR WRITING HARDWARE REGISTER/

EM2: .ASCIZ /ERROR ON DATA COMMAND/

EM6: .ASCIZ /ERROR ON WRITE HEADER AND DATA/

EM11: .ASCIZ /CONTROLLER OR DRIVE STATUS/

EM14: .ASCIZ /REGISTER FAILED/

EM15: .ASCIZ /SPECIFIED REGISTER NON EXISTANT SO ABORT/

EM16: .ASCIZ /WAIT LOOP FAILED/

EM17: .ASCIZ /WRITE CHECK FAILING/

EM20: .ASCIZ /REGISTER FAILING/

EM21: .ASCIZ /INTERRUPT FAILING/

10826	054606	052522	052120	043040	
10827	054614	044501	044514	043516	
10828	054622	000			
10829	054623	105	051122	051117	EM22: .ASCII /ERROR ON DRIVES PRESENT -/ <15> <12>
10830	054630	047440	020116	051104	
10831	054636	053111	051505	050040	
10832	054644	042522	042523	052116	
10833	054652	026440	005015		
10834	054656	044124	020105	047125	.ASCII /THE UNIT NO'S FOUND BY SETTING RHAS USING RHER1/ <15> <12>
10835	054664	052111	047040	023517	
10836	054672	020123	047506	047125	
10837	054700	020104	054502	051440	
10838	054706	052105	044524	043516	
10839	054714	051040	040510	020123	
10840	054722	051525	047111	020107	
10841	054730	044122	051105	006461	
10842	054736	012			
10843	054737	050	032124	020051	.ASCII /(T4) DO NOT AGREE WITH THE UNIT NO'S FOUND/ <15> <12>
10844	054744	047504	047040	052117	
10845	054752	040440	051107	042505	
10846	054760	053440	052111	020110	
10847	054766	044124	020105	047125	
10848	054774	052111	047040	023517	
10849	055002	020123	047506	047125	
10850	055010	006504	012		
10851	055013	102	020131	042523	.ASCII /BY SETTING RHCS2 - 'NED' (BIT #12)/ <15> <12> <15> <12>
10852	055020	052124	047111	020107	
10853	055026	044122	051503	020062	
10854	055034	020055	047047	042105	
10855	055042	020047	041050	052111	
10856	055050	021440	031061	006451	
10857	055056	006412	012		
10858	055061	116	052117	035105	.ASCII /NOTE: ON DUAL PORT SYSTEM, A DRIVE ON OTHER PORT WILL / <15> <12>
10859	055066	047440	020116	052504	
10860	055074	046101	050040	051117	
10861	055102	020124	054523	052123	
10862	055110	046505	020054	020101	
10863	055116	051104	053111	020105	
10864	055124	047117	047440	044124	
10865	055132	051105	050040	051117	
10866	055140	020124	044527	046114	
10867	055146	006440	012		
10868	055151	116	052117	043440	.ASCII /NOT GIVE 'NED', BUT WILL GIVE RHAS RESPONSES/ <15> <12>
10869	055156	053111	020105	047047	
10870	055164	042105	026047	041040	
10871	055172	052125	053440	046111	
10872	055200	020114	044507	042526	
10873	055206	051040	040510	020123	
10874	055214	042522	050123	047117	
10875	055222	042523	006523	012	
10876	055227	110	047105	042503	.ASCII /HENCE THERE WILL BE AN EXTRA DRIVE/
10877	055234	052040	042510	042522	
10878	055242	053440	046111	020114	
10879	055250	042502	040440	020116	

10880	055256	054105	051124	020101	
10881	055264	051104	053111	000105	
10882	055272	047514	045517	040440	EM24: .ASCIZ /LOOK AHEAD REGISTER AT THE BEGINNING OF SECTOR IS IN ERROR/
10883	055300	042510	042101	051040	
10884	055306	043505	051511	042524	
10885	055314	020122	052101	052040	
10886	055322	042510	041040	043505	
10887	055330	047111	044516	043516	
10888	055336	047440	020106	042523	
10889	055344	052103	051117	044440	
10890	055352	020123	047111	042440	
10891	055360	051122	051117	000	
10892	055365	114	047517	020113	EM25: .ASCIZ /LOOK AHEAD REGISTER IS IN ERROR/
10893	055372	044101	040505	020104	
10894	055400	042522	044507	052123	
10895	055406	051105	044440	020123	
10896	055414	047111	042440	051122	
10897	055422	051117	000		
10898	055425	103	051125	042522	EM30: .ASCII /CURRENT CYLINDER DOES NOT MATCH DESIRED CYLINDER REGISTER/<15><12>
10899	055432	052116	041440	046131	
10900	055440	047111	042504	020122	
10901	055446	047504	051505	047040	
10902	055454	052117	046440	052101	
10903	055462	044103	042040	051505	
10904	055470	051111	042105	041440	
10905	055476	046131	047111	042504	
10906	055504	020122	042522	044507	
10907	055512	051510	042524	006522	
10908	055520	012			
10909	055521	101	052106	051105	.ASCIZ /AFTER A SEEK AND INIT/
10910	055526	040440	051440	042505	
10911	055534	020113	047101	020104	
10912	055542	047111	052111	000	
10913	055547	105	041503	043440	EM31: .ASCII /ECC GENERATED IS INCORRECT/<15><12>
10914	055554	047105	051105	052101	
10915	055562	042105	044440	020123	
10916	055570	047111	047503	051122	
10917	055576	041505	006524	012	
10918	055603	105	042526	054522	.ASCIZ /EVERY WORD ON THIS SECTOR IS THAT GIVEN IN "DATA USED"/
10919	055610	053440	051117	020104	
10920	055616	047117	052040	044510	
10921	055624	020123	042523	052103	
10922	055632	051117	044440	020123	
10923	055640	044124	052101	043440	
10924	055646	053111	047105	044440	
10925	055654	020116	042042	052101	
10926	055662	020101	051525	042105	
10927	055670	000042			
10928	055672	047117	051040	040505	EM32: .ASCII /ON READ COMMAND, AFTER DATA AND ECC HAVE BEEN READ,<15><12>
10929	055700	020104	047503	046515	
10930	055706	047101	026104	040440	
10931	055714	052106	051105	042040	
10932	055722	052101	020101	047101	
10933	055730	020104	041505	020103	

10934	055736	040510	042526	041040
10935	055744	042505	020116	042522
10936	055752	042101	006454	012
10937	055757	105	041503	051040
10938	055764	043505	051511	042524
10939	055772	051522	047440	020122
10940	056000	044122	051105	020061
10941	056006	051101	020105	047111
10942	056014	042440	051122	051117
10943	056022	005015		
10944	056024	047117	054514	046040
10945	056032	053517	051105	030440
10946	056040	020061	044502	051524
10947	056046	047440	020106	040520
10948	056054	052124	051105	020116
10949	056062	042522	027107	041440
10950	056070	047101	041040	020105
10951	056076	042522	042101	005015
10952	056104	044124	051511	051440
10953	056112	047510	046125	020104
10954	056120	040515	041524	020110
10955	056126	047514	042527	020122
10956	056134	030461	041040	052111
10957	056142	020123	043117	043440
10958	056150	047517	020104	041505
10959	056156	030503	000	
10960	056161	110	043511	020110
10961	056166	047503	047125	020124
10962	056174	044502	020124	047516
10963	056202	020124	042523	020124
10964	056210	043101	042524	020122
10965	056216	034063	032470	020071
10966	056224	046103	041517	051513
10967	056232	000		
10968	056233	132	051105	020117
10969	056240	042504	042524	052103
10970	056246	041040	052111	047040
10971	056254	052117	044040	043511
10972	056262	020110	044127	047105
10973	056270	031440	020062	044502
10974	056276	020124	041505	020103
10975	056304	042522	027107	044040
10976	056312	051501	031040	020061
10977	056320	042532	047522	000123
10978	056326	047520	044523	044524
10979	056334	047117	051040	043505
10980	056342	051511	042524	020122
10981	056350	051117	030440	020061
10982	056356	044502	051524	047440
10983	056364	020106	040520	052124
10984	056372	051105	020116	042522
10985	056400	044507	052123	051105
10986	056406	044440	041516	051117
10987	056414	042522	052103	005015

.ASCII /ECC REGISTERS OR RHER1 ARE IN ERROR/<15><12>

.ASCII /ONLY LOWER 11 BITS OF PATTERN REG. CAN BE READ/<15><12>

.ASCIZ /THIS SHOULD MATCH LOWER 11 BITS OF GOOD ECC1/

EM33: .ASCIZ /HIGH COUNT BIT NOT SET AFTER 38859 CLOCKS/

EM34: .ASCIZ /ZERO DETECT BIT NOT HIGH WHEN 32 BIT ECC REG. HAS 21 ZEROS/

EM35: .ASCII /POSITION REGISTER OR 11 BITS OF PATTERN REGISTER INCORRECT/<15><12>

10988	056422	047514	042527	020122
10989	056430	030461	041040	052111
10990	056436	020123	043117	050040
10991	056444	052101	042524	047122
10992	056452	051040	043505	051511
10993	056460	042524	020122	044123
10994	056466	052517	042114	046440
10995	056474	052101	044103	046040
10996	056502	053517	051105	005015
10997	056510	030461	041040	052111
10998	056516	020123	043117	043440
10999	056524	047517	020104	041505
11000	056532	030503	005015	
11001	056536	040504	020124	047105
11002	056544	046126	050117	043440
11003	056552	047517	020104	047520
11004	056560	044523	044524	047117
11005	056566	040440	042116	047040
11006	056574	041455	042117	020105
11007	056602	042532	047522	020123
11008	056610	051101	020105	047111
11009	056616	047440	052103	046101
11010	056624	000		
11011	056625	117	020116	042522
11012	056632	042101	041440	046517
11013	056640	040515	042116	053440
11014	056646	052111	020110	047516
11015	056654	026516	047503	051122
11016	056662	041505	040524	046102
11017	056670	020105	051105	047522
11018	056676	020122	042047	045503
11019	056704	020047	047101	020104
11020	056712	042447	044103	020047
11021	056720	044123	052517	042114
11022	056726	041040	020105	042523
11023	056734	000124		
11024	056736	051120	043517	040522
11025	056744	020115	051105	047522
11026	056752	020122	044502	020124
11027	056760	030443	020060	047111
11028	056766	051040	041510	031123
11029	056774	042040	042111	047040
11030	057002	052117	051440	052105
11031	057010	005015		
11032	057012	043111	050040	051517
11033	057020	052111	047511	020116
11034	057026	042522	044507	052123
11035	057034	051105	036440	030061
11036	057042	032060	020060	051117
11037	057050	030440	030060	030464
11038	057056	020054	052111	044440
11039	057064	020123	047507	042117
11040	057072	000		
11041				

.ASCII /LOWER 11 BITS OF PATTERN REGISTER SHOULD MATCH LOWER/<15><12>

.ASCII /11 BITS OF GOOD ECC1/<15><12>

.ASCIZ /DAT ENVELOP GOOD POSITION AND N-CODE ZEROS ARE IN OCTAL/

EM36: .ASCIZ /ON READ COMMAND WITH NON-CORRECTABLE ERROR 'DCK' AND 'ECH' SHOULD BE SE

EM37: .ASCII /PROGRAM ERROR BIT #10 IN RHCS2 DID NOT SET/<15><12>

.ASCIZ /IF POSITION REGISTER =10040 OR 10041, IT IS GOOD/

11042	057073	122	053510	020103	EM40: .ASCII /RHC DID NOT = 0 UPON COMPLETION OF READ/<15><12>
11043	057100	044504	020104	047516	
11044	057106	020124	020075	020060	
11045	057114	050125	047117	041440	
11046	057122	046517	046120	052105	
11047	057130	047511	020116	043117	
11048	057136	051040	040505	006504	
11049	057144	012			
11050	057145	117	020122	051127	.ASCIZ /OR WRITE HEADER AND DATA/
11051	057152	052111	020105	042510	
11052	057160	042101	051105	040440	
11053	057166	042116	042040	052101	
11054	057174	000101			

```

11055
11056 057176 040506 040524 020114 CPHALT: .ASCII /FATAL ERROR - SEE DOCUMENT LISTING/<15><12>
11057 057204 051105 047522 020122
11058 057212 020055 042523 020105
11059 057220 047504 052503 042515
11060 057226 052116 046040 051511
11061 057234 044524 043516 005015
11062 057242 006440 103412 177777 .ASCII / /<15><12><207><377><377><207><377><377><207><377><377>
11063 057250 177607 103777 177777
11064 057256 044124 020105 047503 .ASCII /THE CONTROLLER OR DEVICE HAS GONE OFFLINE, LOST/<15><12>
11065 057264 052116 047522 046114
11066 057272 051105 047440 020122
11067 057300 042504 044526 042503
11068 057306 044040 051501 043440
11069 057314 047117 020105 043117
11070 057322 046106 047111 026105
11071 057330 046040 051517 006524
11072 057336 012
11073 057337 047 042522 042101 .ASCII /'READY', BECOME UNAVAILABLE, OR HAS STATUS BITS/<15><12>
11074 057344 023531 020054 042502
11075 057352 047503 042515 052440
11076 057360 040516 040526 046111
11077 057366 041101 042514 020054
11078 057374 051117 044040 051501
11079 057402 051440 040524 052524
11080 057410 020123 044502 051524
11081 057416 005015
11082 057420 044127 041511 020110 .ASCIZ /WHICH CANNOT BE CLEARED/
11083 057426 040503 047116 052117
11084 057434 041040 020105 046103
11085 057442 040505 042522 000104
11086
11087 057450 020040 020040 020040 SPACE8: .ASCII / / / /
11088 057456 020040 000 SPACE2: .ASCIZ / / / /
11089
11090
11091 057461 120 020103 020040 DH1: .ASCII /PC TEST REG. GOOD RECEIVED/<15><12>
11092 057466 020040 052040 051505
11093 057474 020124 020040 051040
11094 057502 043505 020056 020040
11095 057510 043440 047517 020104
11096 057516 020040 051040 041505
11097 057524 044505 042526 006504
11098 057532 012
11099 057533 040 020040 020040 .ASCIZ / NO ADDR. DATA DATA /
11100 057540 020040 047040 020117
11101 057546 020040 020040 040440
11102 057554 042104 027122 020040
11103 057562 042040 052101 020101
11104 057570 020040 042040 052101
11105 057576 020101 020040 000040
11106 057604 041520 020040 020040 DH2: .ASCII /PC TEST WORD GOOD BAD /<15><12>
11107 057612 020040 042524 052123
11108 057620 020040 020040 047527

```


11109	057626	042122	020040	020040
11110	057634	047507	042117	020040
11111	057642	020040	040502	020104
11112	057650	020040	020040	005015
11113	057656	020040	020040	020040
11114	057664	020040	047516	020040
11115	057672	020040	020040	047516
11116	057700	020040	020040	020040
11117	057706	040504	040524	020040
11118	057714	020040	040504	040524
11119	057722	020040	020040	000
11120	057727	120	020103	020040
11121	057734	020040	052040	051505
11122	057742	020124	020040	043040
11123	057750	044501	044514	043516
11124	057756	041440	047117	027124
11125	057764	020040	041440	047117
11126	057772	027124	020040	041440
11127	060000	047117	027124	020040
11128	060006	041440	047117	027124
11129	060014	020040	006440	012
11130	060021	040	020040	020040
11131	060026	020040	047040	020117
11132	060034	020040	020040	051040
11133	060042	043505	040440	051104
11134	060050	051040	041510	030523
11135	060056	020040	051040	041510
11136	060064	031123	020040	051040
11137	060072	042110	030523	020040
11138	060100	051040	042510	030522
11139	060106	000		
11140	060107	120	020103	020040
11141	060114	020040	052040	051505
11142	060122	020124	020040	043040
11143	060130	044501	044514	043516
11144	060136	041440	047117	027124
11145	060144	020040	041440	047117
11146	060152	027124	020040	041440
11147	060160	047117	027124	020040
11148	060166	041440	047117	027124
11149	060174	020040	041440	047117
11150	060202	027124	020040	006440
11151	060210	012		
11152	060211	040	020040	020040
11153	060216	020040	047040	020117
11154	060224	020040	020040	051040
11155	060232	043505	040440	051104
11156	060240	041040	042101	051040
11157	060246	043505	051040	041510
11158	060254	030523	020040	051040
11159	060262	041510	031123	020040
11160	060270	051040	042110	030523
11161	060276	020040	051040	042510
11162	060304	030522	000	

.ASCIZ / NO NO DATA DATA /

DH11: .ASCII /PC TEST FAILING CONT. CONT. CONT. CONT. /<15><12>

.ASCIZ / NO REG ADR RHCS1 RHCS2 RHDS1 RHER1/

DH14: .ASCII /PC TEST FAILING CONT. CONT. CONT. CONT. CONT. /<15><1

.ASCIZ / NO REG ADR BAD REG RHCS1 RHCS2 RHDS1 RHER1/

11163	060307	120	020103	020040	DH15:	.ASCIZ	/PC	TEST	REG	ADR	/							
11164	060314	020040	052040	051505														
11165	060322	020124	020040	051040														
11166	060330	043505	040440	051104														
11167	060336	000040																
11168	060340	041520	020040	020040	DH16:	.ASCII	/PC	TEST	WAIT	BIT	REG	REG						<15><12>
11169	060346	020040	042524	052123														
11170	060354	020040	020040	040527														
11171	060362	052111	020040	020040														
11172	060370	044502	020124	020040														
11173	060376	020040	042522	020107														
11174	060404	020040	020040	042522														
11175	060412	020107	020040	020040														
11176	060420	005015																
11177	060422	020040	020040	020040		.ASCIZ	/	NO	PC	WANTED	ADDRESS	CONT.						/
11178	060430	020040	047516	020040														
11179	060436	020040	020040	041520														
11180	060444	020040	020040	020040														
11181	060452	040527	052116	042105														
11182	060460	020040	042101	051104														
11183	060466	051505	020123	047503														
11184	060474	052116	020056	020040														
11185	060502	000																
11186	060503	120	020103	020040	DH17:	.ASCII	/PC	TEST	CONT.	CONT.	CONT.	CONT.	CONT.	CONT.				<15><12>
11187	060510	020040	052040	051505														
11188	060516	020124	020040	041440														
11189	060524	047117	027124	020040														
11190	060532	041440	047117	027124														
11191	060540	020040	041440	047117														
11192	060546	027124	020040	041440														
11193	060554	047117	027124	020040														
11194	060562	041440	047117	027124														
11195	060570	020040	006440	012														
11196	060575	040	020040	020040		.ASCIZ	/	NO	RHBA	RHDB	RHMC	RHCS1	RHCS2					/
11197	060602	020040	047040	020117														
11198	060610	020040	020040	051040														
11199	060616	041110	020101	020040														
11200	060624	051040	042110	020102														
11201	060632	020040	051040	053510														
11202	060640	020103	020040	051040														
11203	060646	041510	030523	020040														
11204	060654	051040	041510	031123														
11205	060662	020040	000040															
11206	060666	041520	020040	020040	DH20:	.ASCII	/PC	TEST	CONT	CONT	CONT	CONT	CONT					<15><12>
11207	060674	020040	042524	052123														
11208	060702	020040	020040	047503														
11209	060710	052116	020040	020040														
11210	060716	047503	052116	020040														
11211	060724	020040	047503	052116														
11212	060732	020040	020040	047503														
11213	060740	052116	020040	020040														
11214	060746	047503	052116	020040														
11215	060754	020040	005015															
11216	060760	020040	020040	020040		.ASCIZ	/	NO	RHER1	RHER2	RHER3	RHAS	RHDS1					/

11325	062122	020040	020040	045040
11326	062130	051123	020040	020040
11327	062136	040440	042104	042522
11328	062144	051523	042040	052101
11329	062152	020101	020040	042040
11330	062160	052101	020101	020040
11331	062166	000040		
11332	062170	041520	020040	020040
11333	062176	020040	042524	052123
11334	062204	020040	020040	047527
11335	062212	042122	020040	020040
11336	062220	047507	042117	020040
11337	062226	020040	040502	020104
11338	062234	020040	020040	047503
11339	062242	052116	020056	020040
11340	062250	047503	052116	020056
11341	062256	020040	047503	052116
11342	062264	020040	020040	005015
11343	062272	020040	020040	020040
11344	062300	020040	047516	020040
11345	062306	020040	020040	047516
11346	062314	020040	020040	020040
11347	062322	040504	040524	020040
11348	062330	020040	040504	040524
11349	062336	020040	020040	044122
11350	062344	051503	020061	020040
11351	062352	044122	051504	020061
11352	062360	020040	044122	051105
11353	062366	020061	020040	
11354	062372	041520	020040	020040
11355	062400	020040	042524	052123
11356	062406	020040	020040	041520
11357	062414	047440	020106	020040
11358	062422	047527	042122	020040
11359	062430	020040	047507	042117
11360	062436	020040	020040	040502
11361	062444	020104	020040	020040
11362	062452	047503	052116	020056
11363	062460	020040	047503	052116
11364	062466	020056	020040	047503
11365	062474	052116	020056	020040
11366	062502	005015		
11367	062504	020040	020040	020040
11368	062512	020040	047516	020040
11369	062520	020040	020040	051512
11370	062526	020122	020040	020040
11371	062534	047516	020040	020040
11372	062542	020040	040504	040524
11373	062550	020040	020040	040504
11374	062556	040524	020040	020040
11375	062564	044122	051503	020061
11376	062572	020040	044122	051504
11377	062600	020061	020040	044122
11378	062606	051105	020061	020040

DH31: .ASCII /PC TEST WORD GOOD BAD CONT. CONT. CONT /<15><1

.ASCII / NO NO DATA DATA RHCS1 RHDS1 RHER1 /

DH32: .ASCII /PC TEST PC OF WORD GOOD BAD CONT. CONT. CONT.

.ASCIZ / NO JSR NO DATA DATA RHCS1 RHDS1 RHER1

11487	063756	051105	051517	000					
11488									
11489	063763	120	020103	020040	DH40:	.ASCII	/PC	TEST	CONT./<15><12>
11490	063770	020040	052040	051505					
11491	063776	020124	020040	041440					
11492	064004	047117	027124	005015					
11493	064012	020040	020040	020040		.ASCII2	/	IN	IN
11494	064020	020040	047516	020040					
11495	064026	020040	020040	044122					
11496	064034	041527	000						
11497									
11498		064040						.EVEN	
11499									
11500	064040	001116	002020	040172	DT1:	.WORD		SERRPC, TSTNM, REGADR, SGDDAT, SBDDAT, 0	
11501	064046	001124	001126	000000					
11502	064054	001116	002020	045272	DT2:	.WORD		SERRPC, TSTNM, ERWORD, SGDDAT, 0	
11503	064062	001124	000000						
11504	064066	001116	002020	045272	DT3:	.WORD		SERRPC, TSTNM, ERWORD, SGDDAT, SBDDAT, 0	
11505	064074	001124	001126	000000					
11506									
11507	064102	001116	002020	001122	DT11:	.WORD		SERRPC, TSTNM, SBDADR, CS1, CS2, DS1, ER1, 0	
11508	064110	001702	001700	001724					
11509	064116	001704	000000						
11510	064122	001116	002020	001122	DT14:	.WORD		SERRPC, TSTNM, SBDADR, SBDDAT, CS1, CS2, DS1, ER1, 0	
11511	064130	001126	001702	001700					
11512	064136	001724	001704	000000					
11513	064144	001116	002020	001172	DT15:	.WORD		SERRPC, TSTNM, STMP1, 0	
11514	064152	000000							
11515	064154	001116	002020	001176	DT16:	.WORD		SERRPC, TSTNM, STMP3, STMP1, STMP0, SBDDAT, 0	
11516	064162	001172	001170	001126					
11517	064170	000000							
11518	064172	001116	002020	001676	DT17:	.WORD		SERRPC, TSTNM, BA, DB, MC, CS1, CS2, 0	
11519	064200	001672	001674	001702					
11520	064206	001700	000000						
11521									
11522	064212	001116	002020	001704	DT20:	.WORD		SERRPC, TSTNM, ER1, ER2, ER3, AS, DS1, 0	
11523	064220	001710	001716	001720					
11524	064226	001724	000000						
11525	064232	001116	002020	001702	DT21:	.WORD		SERRPC, TSTNM, CS1, AS, DS1, 0	
11526	064240	001720	001724	000000					
11527	064246	001116	002020	000000	DT22:	.WORD		SERRPC, TSTNM, 0	
11528	064254	001116	002020	001706	DT24:	.WORD		SERRPC, TSTNM, DST, SBDDAT, STMP1, STMP2, STMP3, 0	
11529	064262	001126	001172	001174					
11530	064270	001176	000000						
11531	064274	001116	002020	002002	DT26:	.WORD		SERRPC, TSTNM, PCJSR, SBDADR, CS1, CS2, DS1, ER1, 0	
11532	064302	001122	001702	001700					
11533	064310	001724	001704	000000					
11534	064316	001116	002020	002002	DT27:	.WORD		SERRPC, TSTNM, PCJSR, REGADR, SGDDAT, SBDDAT, 0	
11535	064324	040172	001124	001126					
11536	064332	000000							
11537									
11538	064334	001116	002020	002002	DT30:	.WORD		SERRPC, TSTNM, PCJSR, REGADR, SGDDAT, SBDDAT, 0	
11539	064342	040172	001124	001126					
11540	064350	000000							

11541	064352	001116	002020	045272	DT31:	.WORD	SERRPC, TSTNM, ERWORD, SGDDAT, SBDDAT, CS1, DS1, ER1, 0
11542	064360	001124	001126	001702			
11543	064366	001724	001704	000000			
11544	064374	001116	002020	002002	DT32:	.WORD	SERRPC, TSTNM, PCJSR, ERWORD, SGDDAT, SBDDAT, CS1, DS1, ER1, 0
11545	064402	045272	001124	001126			
11546	064410	001702	001724	001704			
11547	064416	000000					
11548	064420	001116	002020	002002	DT33:	.WORD	SERRPC, TSTNM, PCJSR, ERWORD, SGDDAT, CS1, DS1, ER1, 0
11549	064426	045272	001124	001702			
11550	064434	001724	001704	000000			
11551	064442	001116	002020	043216	DT34:	.WORD	SERRPC, TSTNM, GECC1, GECC2, MECC1, MECC2, DISK, 0
11552	064450	043220	050070	050072			
11553	064456	047070	000000				
11554	064462	001116	002020	043216	DT35:	.WORD	SERRPC, TSTNM, GECC1, GECC2, EC2, EC1, POSITI, ER1, 0
11555	064470	043220	001734	001732			
11556	064476	043230	001704	000000			
11557	064504	001116	002020	002002	DT36:	.WORD	SERRPC, TSTNM, PCJSR, MR, EC1, EC2, 0
11558	064512	001722	001732	001734			
11559	064520	000000					
11560	064522	001116	002020	001732	DT37:	.WORD	SERRPC, TSTNM, EC1, POSITI, GECC1, GECC2, EC2, DATENV, ZCODE, 0
11561	064530	043230	043216	043220			
11562	064536	001734	043234	043236			
11563	064544	000000					
11564							
11565	064546	001116	002020	001126	DT40:	.WORD	SERRPC, TSTNM, SBDDAT, 0
11566	064554	000000					
11567							
11568	064556	000	000	000	DF1:	.BYTE	0,0,0,0,0
11569	064561	000	000				
11570	064563	000	000	001	DF2:	.BYTE	0,0,1,0
11571	064566	000					
11572	064567	000	000	001	DF3:	.BYTE	0,0,1,0,0
11573	064572	000	000				
11574							
11575	064574	000	000	000	DF11:	.BYTE	0,0,0,0,0,0,0
11576	064577	000	000	000			
11577	064602	000					
11578	064603	000	000	000	DF14:	.BYTE	0,0,0,0,0,0,0,0
11579	064606	000	000	000			
11580	064611	000	000				
11581	064613	000	000	000	DF15:	.BYTE	0,0,0
11582	064616	000	000	000	DF16:	.BYTE	0,0,0,0,0
11583	064621	000	000				
11584	064623	000	000	000	DF17:	.BYTE	0,0,0,0,0,0,0
11585	064626	000	000	000			
11586	064631	000					
11587							
11588	064632	000	000	000	DF20:	.BYTE	0,0,0,0,0,0,0
11589	064635	000	000	000			
11590	064640	000					
11591	064641	000	000	000	DF21:	.BYTE	0,0,0,0,0
11592	064644	000	000				
11593	064646	000	000	000	DF22:	.BYTE	0,0,0,0
11594	064651	000					

11595	064652	000	000	000	DF24:	.BYTE	0,0,0,0,0,0,0
11596	064655	000	000	000			
11597	064660	000					
11598	064661	000	000	000	DF26:	.BYTE	0,0,0,0,0,0,0
11599	064664	000	000	000			
11600	064667	000	000				
11601	064671	000	000	000	DF27:	.BYTE	0,0,0,0,0,0
11602	064674	000	000	000			
11603							
11604	064677	000	000	000	DF30:	.BYTE	0,0,0,0,0,0
11605	064702	000	000	000			
11606	064705	000	000	001	DF31:	.BYTE	0,0,1,0,0,0,0,0
11607	064710	000	000	000			
11608	064713	000	000				
11609	064715	000	000	000	DF32:	.BYTE	0,0,0,1,0,0,0,0,0
11610	064720	001	000	000			
11611	064723	000	000	000			
11612	064726	000	000	000	DF33:	.BYTE	0,0,0,1,0,0,0,0
11613	064731	001	000	000			
11614	064734	000	000				
11615	064736	000	000	000	DF34:	.BYTE	0,0,0,0,0,0,0
11616	064741	000	000	000			
11617	064744	000					
11618	064745	000	000	000	DF35:	.BYTE	0,0,0,0,0,0,0,0
11619	064750	000	000	000			
11620	064753	000	000				
11621	064755	000	000	000	DF36:	.BYTE	0,0,0,0,0,0
11622	064760	000	000	000			
11623	064763	000	000	000	DF37:	.BYTE	0,0,0,0,0,0,0,0,0
11624	064766	000	000	000			
11625	064771	000	000	000			
11626							
11627	064774	000	000	000	DF40:	.BYTE	0,0,0
11628		065000				.EVEN	
11629							
11630		000001				.END	

RDY = 000200	773*	1596	1599	2403	2479	2491	2761	2763	2770	2772	3120	3188	6207
READ 046066	6209	6694	7292	8007	8092	8102	8144						
READAT 002050	9383	9387	9395	9397	9399	9401	9403	9444	9448	9483*	10142	10150	
READIN 002064	1097*	5559	6074	6184									
RECALI 002030	1103*	3035											
REDATA 051460	1089*	4173											
REFOR 002052	9302	10132*											
REGADR 040172	1098*	5109	5236	5362	6454	7494							
	1303*	1600*	1611*	1620*	1720*	1742*	2077*	2281*	2347*	2442*	2555*	2562*	2611*
	2661*	2717*	2725*	2738*	2764*	2773*	2802*	2817*	2844*	2852*	2877*	2885*	3081*
	3149*	3217*	3486*	3577*	3628*	3688*	3738*	3819*	3865*	3962*	4008*	4092*	4138*
	4224*	4269*	4340*	4571*	5970*	6210*	6220*	6228*	6721*	7281*	7402*	7544*	7654*
	7760*	7968*	7972*	8626*	8637*	8647*	8658*	8729*	11500	11534	11538		
REGSAI 053246	1229	10533*											
REINTO 003134	1110*	2310	2317*	2331	2338	2390	2399*	2402*	2403*	2404*	2405*	2406*	2407*
	2408*	2409*	2412	2416*	2417*	2418*	2429	2433	3042	3049*	3050*	3052*	3053*
	3066	3072	3537	3545*	3546*	3547*	3562	3568	3594*	3595*	3596*	3597*	3598*
	3613	3618	3648	3656*	3657*	3658*	3673	3680	3704*	3705*	3706*	3707*	3708*
	3722	3728	3768	3775*	3776*	3777*	3804	3811	3834*	3835*	3836*	3837*	3851
	3855	3923	3930*	3931*	3932*	3946	3953	3977*	3978*	3979*	3980*	3981*	3994
	3998	4046	4062*	4063*	4064*	4077	4083	4107*	4108*	4109*	4110*	4122	4127
	4178	4194*	4195*	4196*	4209	4215	4239*	4240*	4241*	4242*	4254	4259	4302
	4311*	4312*	4325	4331	4391	4537*	4538*	4540*	4541*	4542*	4556	4562	4682
	4683	4690	4792	4793	4796	4797	4805	4916	4917	4920	4921	4928	5094
	5155	5221	5282	5347	5408	5539	5540	5561	5599	5655	5661	5662	5666
	5673	5674	5750	5790	5795	5802	5803	5875	5926	5940	6054	6055	6076
	6112	6267	6272	6279	6280	6335	6366	6439	6500	6677	6689*	6690*	6691*
	6692*	6693*	6694*	6707	6713	6775	6776	6805	6855	6856	6885	6944	6945
	7019	7020	7071	7072	7101	7148	7149	7356	7363*	7364*	7366*	7367*	7368*
	7369*	7370*	7372*	7373*	7374*	7375*	7387	7393	7475	7503	7513*	7514*	7515*
	7516*	7517*	7518*	7529	7535	7614	7624*	7625*	7626*	7627*	7628*	7639	7645
	7720	7730*	7731*	7732*	7733*	7734*	7745	7751	8262	8362	8580	8605	
RELEAS 002034	1091*	4297											
RESVEC= 000010	151*												
RETCL 002060	1101*	4041											
RHAS 001650	1000*	1297	1340	5935	6004*								
RHBA 001626	983*	1663	2058*	2375*	2868	3030*	3244*	4633*	4743*	4865*	4988*	5094*	5221*
	5347*	5469*	5561*	6076*	6186*	6223	6228	6439*	6573*	6667*	7228*	7340*	7476*
	7582*	7693*	8262*	8362*	8551*	8679*							
RHCA 001644	998*	1845	2065*	2382*	3033*	3251*	3531*	3644*	3900*	4383*	4652*	4765*	4887*
	5006*	5102*	5229*	5355*	5474*	5568*	6083*	6194*	6447*	6576*	6657	6671*	7233*
	7345*	7498*	7604*	7710*	8269*	8369*	8543*	8683*	8715*				
RHCC 001670	1008*	3525	3900	7947	8725	8729							
RHCS1 001632	993*	1563	1930	2060*	2305*	2314*	2377*	3036*	3046*	3246*	3524*	3541*	3643*
	3652*	3762*	3772*	3917*	3927*	4041*	4050*	4173*	4182*	4297*	4306*	4410*	5008*
	5010*	5966*	6664*	6676	6686*	6700	6721	7289	7510*	7621*	7727*	8005	8071
	8676*	8689*	8717*	8719*	9087	9125	9232*	9734*					
RHCS2 001630	984*	1278	1341	1401*	1538	1539*	1564*	1588*	1640*	1664*	1688*	1770*	1796*
	1821*	1846*	1867*	2059*	2376*	2396*	2535	2694	3168*	3245*	5015	5944*	5945*
	6088*	6199*	6659*	6660*	6669*	7990*	8009	8072	8681*				
	981*	1233	2056*	2072	2467	2533*	2534*	2548	2549	2555	2562	2587*	2601
RHDB 001622	2611	2647*	2657	2661	2687	2707	3242*	3277	9095				
	995*	1769	2062*	2379*	3031*	3248*	3523*	4381*	4406	4426	4497	4640*	4754*
RHDST 001636	4876*	4995*	5097*	5224*	5350*	5472*	5564*	6079*	6190*	6442*	6574*	6668*	7231*

RHDS1	001654	7343*	7370	7487*	7593*	7699*	8265*	8365*	8548*	8680*	8716*				
RHDT	001656	1002*	2977	3111	3179	7281	8073								
RHEC1	001662	1003*	1402	1404	1421	1483	1487	1489	1937	1939					
RHEC2	001664	1005*	8938												
RHER1	001634	1006*	3107	3166	3175	8932									
		994*	1344	1590*	1687	2061*	2378*	3247*	5928	7613	7632	7654	7719	7738	
		7760	8074	8622	8626	8632	8637	8644	8647	8652	8658				
RHER2	001640	996*	1795	2063*	2380*	3249*	5930								
RHER3	001646	999*	1866	2066*	2383*	3252*	5932								
RHLA	001666	1007*	3597	3707	3837	3981	4110	4242	4312	4425	4495				
RHMR	001652	1001*	1709	2067*	2068*	2304*	2384*	2385*	2928*	2945*	3037*	3103*	3160*	3253*	
		3254*	3274*	3517*	3589*	3639*	3698*	3758*	3829*	3889*	3894*	3895*	3906*	3907*	
		3913*	3972*	4032*	4037*	4038*	4055*	4056*	4102*	4163*	4170*	4171*	4187*	4188*	
		4234*	4293*	4307*	4356*	4405	4583*	5007*	6685*	6739*	6819*	6899*	6974*	7177*	
		7443*	7467*	7468*	7469*	7508	7573*	7574*	7575*	7619	7684*	7685*	7686*	7725	
		8688*	8718*	8986	9231*	9365	9638	9733*	9880	10050					
RHOF	001642	997*	1820	2064*	2381*	3032*	3250*	3918*	4384*	4651*	4764*	4886*	5005*	5098*	
		5225*	5351*	5473*	5565*	6080*	6191*	6443*	6575*	6670*	7232*	7344*	7497*	7603*	
		7709*	8266*	8366*	8554*	8682*									
RHSN	001660	1004*	1476	1486	1488										
RHMC	001624	982*	1639	2057*	2309	2324	2347	2374*	2389	2422	2442	2835	3029*	3041	
		3059	3081	3105	3107	3126	3149	3165	3166	3173	3175	3194	3217	3243*	
		3536	3555	3577	3605	3628	3647	3665	3688	3715	3738	3767	3797	3819	
		3843	3865	3922	3939	3962	3987	4008	4045	4070	4092	4115	4138	4177	
		4202	4224	4247	4269	4301	4318	4340	4390	4549	4571	4631*	4666	4741*	
		4776	4862*	4898	4986*	5093*	5123	5220*	5250	5346*	5376	5468*	5560*	6075*	
		6185*	6438*	6468	6572*	6666*	7227*	7339*	7355	7380	7402	7474*	7502	7522	
		7544	7580*	7691*	7945	7947	8261*	8361*	8549*	8678*					
RKEY1	045262	9323*	9361*	9398											
RKEY2	045264	9324*	9362*	9400											
RMR	= 000004	804*	6690												
RNCTR1	050132	9724*	9736*	9738*											
RNO	051454	10127*	10132*												
RNMAT1	050176	9736*													
RPVEC	001620	971*	1157*	9105	9112*	9131	10166*								
RPVECT	051570	1157	10160*	10166											
RSETR	045260	9322*	9360*	9396											
RSYNC	045254	9320*	9386	9391	9423	9434	9447	9457	9942	9945	9991	9995			
RUNCTR	045024	9222*	9235*	9237*											
RNMAT	045070	9235*													
RO	=2000000	72*	1232*	1235*	1343*	1347*	1387*	1390*	1396*	1435*	1461*	1462	1709*	1712*	
		1713*	1717	1719	1720	1736*	1737*	1738	1741	1742	1913*	1915*	1919*	1924*	
		1944	1947*	1952*	2072*	2077	2078	2093	2103	2115	2126	2136	2146	2156	
		2166	2176	2186	2194	2205	2214	2226	2238	2249	2260	2269	2274	2281	
		2285	2308*	2323*	2337*	2388*	2421*	2432*	2467*	2548*	2550	2554	2579*	2581*	
		2585*	2587	2588*	2599*	2601*	2604*	2607	2609	2623	2645*	2646*	2647	2648	
		2687*	2689*	2707*	2708*	2710	2711	2717	2725	2930*	2932	3040*	3058*	3071*	
		3105*	3108	3125*	3139*	3165*	3167*	3173*	3176	3193*	3207*	3259*	3262*	3277*	
		3284	3295	3306	3320	3335	3346	3356	3366	3376	3386	3396	3406	3415	
		3424	3435	3446	3456	3466	3475	3480	3486	3490	3493	3535*	3554*	3567*	
		3604*	3617*	3646*	3664*	3679*	3714*	3727*	3766*	3796*	3810*	3842*	3854*	3905*	
		3908*	3921*	3938*	3952*	3986*	3997*	4044*	4054*	4057*	4069*	4082*	4114*	4126*	
		4176*	4186*	4189*	4201*	4214*	4246*	4258*	4300*	4317*	4330*	4357*	4388*	4405*	
		4409*	4411*	4412*	4414*	4415*	4417*	4418*	4449*	4450*	4451*	4452*	4478*	4479*	

4480*	4481*	4513*	4514*	4516*	4517*	4527*	4528*	4531*	4532*	4548*	4561*	4584*
4609*	4611*	4632*	4633	4635*	4637*	4681*	4689*	4720*	4722*	4742*	4743	4745*
4747*	4748*	4749*	4751*	4791*	4795*	4804*	4840*	4842*	4863*	4865	4867*	4869*
4870*	4871*	4873*	4915*	4919*	4927*	4964*	4966*	4987*	4988	4990*	4992*	5063*
5064*	5071*	5138*	5139*	5142*	5143*	5144*	5147*	5153*	5190*	5191*	5198*	5265*
5266*	5269*	5270*	5271*	5274*	5280*	5316*	5317*	5324*	5391*	5392*	5395*	5396*
5397*	5400*	5406*	5441*	5463*	5492*	5497	5499	5533*	5538*	5586*	5591*	5598*
5602	5604	5622	5660*	5665*	5667	5669*	5672*	5679*	5684*	5686	5688*	5691*
5722*	5723*	5724*	5725*	5726*	5728*	5733*	5735	5737*	5740*	5748*	5789*	5791
5792*	5794*	5796	5798*	5801*	5808*	5810	5811*	5814*	5816	5818*	5821*	5853*
5855	5856*	5859*	5861	5863*	5866*	5873*	5926*	5928*	5929*	5930*	5931*	5932*
5933*	5940*	5942	5989	5993	6048*	6053*	6101*	6111*	6115	6117	6135	6162*
6266*	6268	6269*	6271*	6273	6275*	6278*	6285*	6287	6288*	6291*	6293	6295*
6298*	6344*	6346	6347*	6350*	6352	6354*	6357*	6364*	6408*	6409*	6416*	6483*
6484*	6487*	6488*	6489*	6492*	6498*	6549*	6567*	6592*	6596	6598	6657*	6658
6675*	6699*	6712*	6740*	6774*	6781*	6782*	6783*	6784*	6785*	6789*	6799*	6820*
6854*	6861*	6862*	6863*	6864*	6865*	6869*	6879*	6900*	6934*	6943*	6953*	6975*
7009*	7018*	7028*	7070*	7077*	7078*	7079*	7080*	7081*	7085*	7095*	7138*	7147*
7157*	7178*	7200*	7222*	7251*	7256	7258	7315*	7334*	7354*	7379*	7392*	7412*
7416	7418	7444*	7475*	7476	7478*	7480*	7481*	7482*	7484*	7501*	7508*	7509*
7521*	7534*	7581*	7582	7584*	7586*	7587*	7588*	7590*	7612*	7619*	7620*	7631*
7644*	7692*	7693	7696*	7718*	7725*	7726*	7737*	7750*	7804*	7806	7843*	7845
7847	7850	7942	7945*	7948	7953*	8039	8040	8041	8051*	8180	8181*	8182
8184	8185	8186	8187*	8236	8227	8228	8235*	8301	8302	8303	8304	8305
8306*	8334*	8409	8410*	8418	8470*	8487	8490*	8493	8516*	8534	8541	8542
8543	8544	8545	8546	8547	8549	8551	8552	8578*	8604*	8618	8662*	8674
8693*	8712	8714	8734*	8822	8839*	8846*	8849*	8850*	8851	8896*	8897*	8899
8901*	8902*	8903*	8905	8967*	8982	8984	9027*	9040	9070*	9095*	9097*	9098*
9225	9311*	9365*	9367*	9368*	9369*	9370*	9372*	9373*	9375*	9376*	9388	9451
9454	9486*	9489*	9491*	9494	9503*	9508*	9511	9520*	9552	9557*	9560*	9561
9570*	9575*	9576	9585*	9600*	9627	9638*	9675*	9727	9846*	9880*	9881*	9883*
9884*	9885	9892*	9894*	9895	9902*	10044	10050*	10052*	10053*	10054*	10055*	10056*
10060*	10061*	10078*	10079*	10081*	10082*	10083*	10084*	10098*	10099*	10100*	10101*	10108*
10109*	10119*	10258	10268*	10272	10288	10289	10302*	10338	10339*	10340	10343*	10479
10483*	10484	10487	10507*	10510*	10569	10570*	10571*	10578*	10579*	10580*	10581*	10582*
10583	10588	10594	10596*	10597	10611*	10703	10704*	10705	10706*	10707*	10708*	10737
10758*												
73*	1233*	1234	1240	1297*	1298*	1299	1303	1340*	1350	1353	1397	1595
1600	1710*	1713	1714	1725*	1726*	1727*	1734*	1737	1738	1740	1747*	1748*
1749*	1912*	1914*	1920*	1945*	1964*	1966	2482	2488*	2494	2688*	2689	2691*
2692	2709*	2713	2756*	2757	2760	2764	2768*	2769	2773	2946*	2958*	3001
3104*	3112	3113*	3172*	3180	3181*	3275*	3276*	3901*	3902*	4385*	4475*	4486*
4509*	4610*	4612*	4648*	4721*	4723*	4761*	4841*	4843*	4883*	4965*	4967*	5002*
5109*	5145*	5148*	5236*	5272*	5275*	5362*	5398*	5401*	5479*	5493*	5497	5500
5508	5559*	5599*	5602	5605	5622	5655*	5656*	5657*	5658*	5659*	5666*	5667*
5685*	5686*	5734*	5735*	5790*	5791*	5795*	5796*	5809*	5810*	5815*	5816*	5854*
5855*	5860*	5861*	5943*	5946	5968	5971	5987*	5989	6074*	6112*	6115	6118
6135	6184*	6187*	6205	6210	6267*	6268*	6272*	6273*	6286*	6287*	6292*	6293*
6345*	6346*	6351*	6352*	6454*	6490*	6493*	6581*	6593*	6596	6599	6610	7238*
7252*	7256	7259	7267	7350*	7413*	7416	7419	7426	7494*	7600*	7706*	7805*
7806	7809	7943	7946*	7948*	7952*	8036	8039*	8042	8044*	8050*	8071*	8078*
8098	8104	8108	8147	8154	8223	8226*	8229	8234*	8274*	8296	8301*	8310
8313	8321	8333*	8374*	8411	8415*	8423	8430	8437	8445*	8448*	8450*	8453*
8455*	8458*	8460*	8465	8469*	8488	8491*	8494*	8496*	8500*	8515*	8542*	8675

R1 =X000001

		8590*	8692*	8823	8828*	8836	8844	8872	8880	8895*	8904*	8905*	8912*	8914
		8930	8966*	8985	8986*	8987*	9006	9014*	9015*	9022*	9023*	9026*	9041	9049*
		9052	9069*	9096*	9099*	9226	9243*	9245*	9246	9247	9248	9249*	9297*	9302*
		9310*	9359	9360	9361	9362	9363	9364	9460*	9461*	9623	9624	9625	9628
		9633*	9636*	9658*	9661*	9665*	9668*	9674*	9677*	9728	9744*	9747*	9748	9749
		9750	9751	9752	9753	9755*	9836*	9845*	9873	9874	9875	9876	9877	9878
		9879*	9908*	9920*	9941*	9942	9946	9954*	9955	9960	9967*	9968	9970	9979*
		9983	9990*	9991	9994	9999*	10001*	10043	10045	10095*	10106*	10118*	10120*	10132
		10133	10134	10154*	10155*	10259	10272*	10273	10277	10301*	10480	10485*	10493*	10495*
		10497*	10500*	10503	10506*	10593	10594*	10599	10603	10605	10610*	10738	10757*	
R2	=%000002	74#	1234*	1341*	1342*	1346*	1608	1611	1711*	1728*	1735*	1750*	1918*	1922
		1927	1934*	1949*	1965*	1968	2070*	2071*	2084*	2087*	2473	2502	2529	2544
		2596	2655	2684	2704	2709	2712	2738	2790*	2791	2796	2802	2807*	2809*
		2813	2817	2992	3107*	3109*	3175*	3177*	3260*	3265*	3271*	3587*	3588*	3696*
		3697*	3827*	3828*	3970*	3971*	4100*	4101*	4232*	4233*	4446*	4453*	4456*	4477*
		4482*	4526*	4529*	5146*	5147	5273*	5274	5399*	5400	5494*	5501	5513*	5600*
		5606	5627*	5707	5942*	5946*	5968	5970	6113*	6119	6140*	6217	6220	6491*
		6492	6594*	6600	6615*	7253*	7260	7272*	7414*	7420	7432*	7944	7947*	7949*
		7951*	8037	8040*	8042*	8043*	8045*	8046*	8049*	8072*	8076*	8077*	8224	8227*
		8229*	8233*	8297	8302*	8310	8314	8321	8332*	8412	8432*	8434*	8435*	8451
		8468*	8489	8492*	8493*	8498*	8514*	8824	8829*	8855	8864	8894*	8898*	8899*
		8911*	8913*	8915	8965*	9042	9050*	9061*	9068*	9149*	9151	9227	9309*	9374*
		9377*	9381*	9384*	9409*	9414	9420*	9442*	9445*	9484	9490*	9525*	9529*	9553
		9559*	9590*	9599*	9624*	9629	9649*	9673*	9729	9844*	9893*	9903*	9917*	9922
		9928*	9950*	9958	9964*	9976*	9981	9987*	10046	10076*	10085*	10087*	10097*	10102*
		10117*	10138*	10143*	10148*	10151*	10260	10271*	10275*	10278	10295*	10286*	10287	10292*
		10300*	10481	10486*	10494*	10496*	10498*	10504	10505*	10739	10756*			
R3	=%000003	75#	1388*	1389*	1393*	1428*	1504	1518	1930*	1931*	1932*	1963*	1996*	2711*
		2722	2727	3106*	3108*	3174*	3176*	4406*	4407*	4408	4419	4431	4501	4519*
		7975*	7979*	7980	7983*	7993	8038	8041*	8044	8048*	8073*	8120	8125	8128
		8160	8165	8225	8228*	8230*	8232*	8298	8303*	8307	8315	8326*	8331*	8413
		8425*	8427*	8428*	8435	8442	8446	8467*	8825	8836*	8837*	8838*	8839	8844*
		8845*	8846	8855*	8856*	8857*	8858	8864*	8865*	8866*	8867	8872*	8873*	8874*
		8875	8880*	8881*	8882*	8883	8964*	9043	9051*	9059*	9067*	9228	9308*	9554
		9565*	9569*	9580*	9584*	9595	9598*	9630	9634*	9635*	9646*	9648*	9654*	9660*
		9664*	9667*	9672*	9730	9843*	9888*	9891*	9898*	9901*	9908	9909	9913	9951*
		9955	9959	10047	10051*	10062	10110*	10116*	10140*	10141	10149	10261	10269*	10270*
		10284*	10287*	10296*	10297*	10299*	10436	10437*	10438	10441*	10442	10446	10448	10450*
		10452*	10650	10659*	10665*	10666*	10669*	10674*	10675*	10676	10685*	10740	10755*	
R4	=%000004	76#	1344*	1345*	1615	1620	1921	2712*	2732	2737	2835*	2836*	2837	2840
		2844	2847*	2848	2852	2868*	2869*	2870	2873	2877	2880*	2881	2885	2951
		2962	3264*	5935*	6003*	7971*	7972	7997*	7998	8004	8005	8009	8011	8074*
		8299	8307*	8308*	8309	8330*	8414	8439*	8441*	8442*	8456	8466*	8826	8963*
		9044	9052*	9053*	9066*	9150	9229	9307*	9631	9642*	9651	9655*	9671*	9731
		9842*	10048	10115*	10651	10653*	10654*	10655*	10656	10657*	10671	10673*	10681*	10684*
		10741	10754*											
R5	=%000005	77#	1279*	1394*	1401	1415	1428	1430	1434*	1540*	1565*	1641*	1665*	1689*
		1771*	1797*	1822*	1847*	1868*	2345*	2346*	2347	2440*	2441*	2442	2549*	2558
		2561	2580*	2582*	2586*	2589*	2600*	2602*	2605*	2630*	2710*	2715	2719	2931*
		2936*	3079*	3080*	3081	3147*	3148*	3149	3166*	3170*	3215*	3216*	3217	3261*
		3266*	3575*	3576*	3577	3626*	3627*	3628	3686*	3687*	3688	3736*	3737*	3738
		3817*	3818*	3819	3863*	3864*	3865	3960*	3961*	3962	4006*	4007*	4008	4090*
		4091*	4092	4136*	4137*	4138	4222*	4223*	4224	4267*	4268*	4269	4338*	4339*
		4340	4447*	4457*	4473*	4508*	4509	4569*	4570*	4571	4625*	4634*	4638*	4735*

F10

2871*	2872*	2925*	2968*	2969*	2970	3022*	3034*	3035*	3036	3236*	3269*	3270*	
3271	3317*	3318*	3319	3424*	3425	3426*	3427	3512*	3525*	3526	3754*	3789*	
3885*	4028*	4159*	4289*	4354*	4376*	4432*	4433*	4434	4502*	4503*	4504	4581*	
4606*	4717*	4837*	4961*	5054*	5061*	5064	5067	5095*	5096*	5097	5140*	5141*	
5142	5181*	5188*	5191	5194	5222*	5223*	5224	5267*	5268*	5269	5307*	5314*	
5317	5320	5348*	5349*	5350	5393*	5394*	5395	5438*	5470*	5471*	5472	5509*	
5510*	5511	5530*	5562*	5563*	5564	5623*	5624*	5625	5651*	5702*	5703*	5705	
5784*	5833*	5834*	5836	5916*	5956*	5957*	5958	5976*	5977*	5978	6012*	6013*	
6014	6045*	6077*	6078*	6079	6136*	6137*	6138	6159*	6188*	6189*	6190	6214*	
6215*	6216	6260*	6313*	6314*	6316	6399*	6406*	6409	6412	6440*	6441*	6442	
6485*	6486*	6487	6543*	6611*	6612*	6613	6650*	6737*	6763*	6817*	6843*	6897*	
6923*	6972*	6998*	7058*	7126*	7175*	7197*	7229*	7230*	7231	7268*	7269*	7270	
7277*	7278*	7279	7312*	7341*	7342*	7343	7427*	7428*	7429	7441*	7462*	7568*	
7679*	7784*	7790*	7798*	7840*	7891*	7897*	7913*	7914	7924	7925*	7942*	7943*	
7944*	7951	7952	7953	8011*	8012*	8013	8036*	8037*	8038*	8048	8049	8050	
8057*	8089	8113*	8114*	8115	8134*	8141	8170*	8180*	8181	8186*	8187	8223*	
8224*	8225*	8232	8233	8234	8263*	8264*	8265	8296*	8297*	8298*	8299*	8300*	
8322*	8323*	8324	8329	8330	8331	8332	8333	8363*	8364*	8365	8409*	8411*	
8412*	8413*	8414*	8466	8467	8468	8469	8470	8487*	8488*	8489*	8514	8515	
8516	8544*	8546*	8548	8674*	8675*	8692	8693	8711*	8733	8822*	8823*	8824*	
8825*	8826*	8827*	8930*	8931*	8932	8962	8963	8964	8965	8966	8967	8985*	
9026	9040*	9041*	9042*	9043*	9044*	9045*	9065	9066	9067	9068	9069	9070	
9087*	9098	9105*	9112	9125*	9131*	9223	9225*	9226*	9227*	9228*	9229*	9230*	
9240*	9241*	9242	9306	9307	9308	9309	9310	9311	9364*	9460	9484*	9499*	
9502*	9503	9516*	9519*	9520	9529	9552*	9553*	9554*	9555*	9597	9598	9599	
9600	9627*	9628*	9629*	9630*	9631*	9639*	9640*	9641	9642	9671	9672	9673	
9674	9675	9725	9727*	9728*	9729*	9730*	9731*	9732*	9741*	9742*	9743	9841	
9842	9843	9844	9845	9846	9878*	9999	10044*	10045*	10046*	10047*	10048*	10049*	
10114	10115	10116	10117	10118	10119	10134*	10154	10202*	10205	10207	10208	10236	
10237	10241*	10258*	10259*	10260*	10261*	10262*	10263*	10264	10267*	10280	10282*	10284	
10294	10296	10298	10299	10300	10301	10302	10304*	10305*	10338*	10339	10340*	10342	
10343	10344*	10347	10349*	10351*	10357	10390*	10391*	10392	10400	10403	10416*	10417*	
10418*	10423*	10436*	10441	10452	10453*	10454*	10455*	10477*	10478*	10479*	10480*	10481*	
10483	10487*	10489	10491	10499*	10500	10502	10503*	10505	10506	10507	10509	10541	
10553*	10556*	10569*	10574*	10593*	10599*	10603*	10610	10611	10642*	10643	10644	10645*	
10650*	10651*	10652*	10658	10683	10684	10685	10686*	10687*	10703*	10704	10737*	10738*	
10739*	10740*	10741*	10742*	10743	10749*	10753	10754	10755	10756	10757	10758	10763*	
SPACE? 057456	1981	11088*											
SPACE.3 057450	1975	1976	1984	1985	1986	11087*							
SRO = 177572	192*												
SR1 = 177574	193*												
SR2 = 177576	194*												
SR3 = 172516	195*												
SSECTR 050360	9867*	9874*											
SSYN 045164	9246*	9258*	9392*										
STACK = 001000	61*	1139	1227	1274	1535	1560	1582	1636	1660	1684	1766	1817	
	1842	1863	1898	2299	2367	2464	2521	2754	2833	2865	2925	3022	3236
	3512	3754	3885	4028	4159	4289	4354	4376	4581	4606	4717	4837	4961
	5054	5181	5307	5438	5530	5651	5784	5916	6045	6159	6260	6399	6543
	6650	6737	6763	6817	6843	6897	6923	6972	6998	7058	7126	7175	7197
	7312	7441	7462	7568	7679								
START 004224	1129	1133*											
STKLMT= 177774	66*												
SMR = 177570	68*	69	1242	1316	1357	1971	2625	3781	5509	5623	6136	6611	7268

	7427	7985	8055	8322	8923	8925	10197*	10211	10213	10219	10226	10537	10544
SM0 = 000001	10548	10551											
SM00 = 000001	119#												
SM01 = 000002	109#	119											
SM02 = 000004	108#	118											
SM03 = 000010	107#	117											
SM04 = 000020	106#	116											
SM05 = 000040	105#	115											
SM06 = 000100	104#	114											
SM07 = 000200	103#	113											
SM08 = 000400	102#	112	2626	2627	5511	5625	6138	6613	7270	7429	8324		
SM09 = 001000	101#	111	2626										
SM1 = 000002	100#	110	7985	8055									
SM10 = 002000	118#												
SM11 = 004000	99#												
SM12 = 010000	98#												
SM13 = 020000	97#												
SM14 = 040000	96#	1242	1316	1357	1971	3781							
SM15 = 100000	95#												
SM2 = 000004	94#												
SM3 = 000010	117#												
SM4 = 000020	116#												
SM5 = 000040	115#												
SM6 = 000100	114#												
SM7 = 000200	113#	8925											
SM8 = 000400	112#												
SM9 = 001000	111#	8923											
TAGDTE 002016	110#												
TBITVE= 000014	1074#												
TDF = 000040	152#												
TESDTE 002014	886#												
TESTAD 037344	1070#	9404	9937	10195#									
TINCNT 041072	7883#												
TKVEC = 000060	8179#	8188	8193										
TMPILL 002010	159#	10381#	10382#										
TOLGAP 050076	1064#	2929#	2932	2934#	2939	2941	2942#						
TOTALA 002006	5443	6551	7202	7317	9634	9711#							
TPVEC = 000064	1062#	1350#	1352#	3259	3404								
TRAPVE= 000034	160#												
TRE = 040000	158#	1144#	1145#										
TRK = 045124	779#	2479	2488	2998	6207	6209	7368	7513					
TRK1 = 004000	9242#	9244#											
TRK10 = 040000	873#												
TRK2 = 010000	876#												
TRK20 = 100000	874#												
TRK4 = 020000	877#												
TRTVEC= 000014	875#												
TSECC 002012	153#												
TSECCG 043222	1066#	9643	10135	10145	10187#								
TSTNM 002020	8792#	8916	9492	9504	9509	9521	9571	9586	9645#	9657#	10137#	10147#	10191#
	1077#	1228#	1275#	1296#	1502#	1536#	1561#	1583#	1637#	1661#	1685#	1707#	1767#
	1793#	1818#	1843#	1864#	1899#	2300#	2368#	2465#	2522#	2578#	2643#	2677#	2753#
	2788#	2832#	2866#	2926#	3023#	3099#	3237#	3513#	3755#	3886#	4029#	4160#	4290#
	4377#	4607#	4718#	4838#	4962#	5055#	5182#	5308#	5435#	5439#	5532#	5652#	5785#

5917*	6047*	6161*	6262*	6401*	6545*	6651*	6764*	6844*	6924*	6999*	7059*	7127*
7198*	7313*	7463*	7569*	7680*	7891	11500	11502	11504	11507	11510	11513	11515
11518	11522	11525	11527	11528	11531	11534	11538	11541	11544	11548	11551	11554
11557	11560	11565										
1208	1225*	7854										

TST1 005346
TST10 010136
TST100 033354
TST101 033476
TST102 033524
TST103 033646
TST104 033674
TST105 033774
TST106 034022
TST107 034122
TST11 010202
TST110 034250
TST111 034354
TST112 034402
TST113 035012
TST114 035542
TST115 035570
TST116 036122
TST117 036446
TST12 010352
TST120 036760
TST13 010416
TST14 010462
TST15 010526
TST16 010714
TST17 010760
TST2 005622
TST20 011024
TST21 011070
TST22 011134
TST23 011200
TST24 011546
TST25 012572
TST26 012734
TST27 013304
TST3 005674
TST30 013450
TST31 013664
TST32 014112
TST33 014224
TST34 014512
TST35 014632
TST36 014776
TST37 015122
TST4 005746
TST40 015246
TST41 015542
TST42 015766
TST43 016356
TST44 017510

1558#
6760#
6816#
6840#
6896#
6920#
6971#
6995#
7055#
1580#
7123#
7174#
7195#
7311#
7440#
7461#
7567#
7678#
1634#
7777#
1658#
1682#
1705#
1763#
1789#
1272#
1815#
1840#
1861#
1897#
2049#
2278
2366#
2463#
1293#
2501
2559
2642#
2660
2733
2771
2816
2850
1301
2883
3021#
3098#
3235#
3484

2298#
2520#
2577#
2675#
2751#
2786#
2830#
2863#
1311#
2923#
3511#

2926	3012	3022#	3023	3090	3099#	3225	3236#	3237	3483	3502	3512#	3513
3745	3754#	3755	3875	3885#	3886	4018	4028#	4029	4149	4159#	4160	4279
4289#	4290	4350	4354#	4364	4376#	4377	4577	4581#	4595	4606#	4607	4672
4695	4706	4717#	4718	4782	4810	4827	4837#	4838	4906	4933	4951	4961#
4962	5045	5054#	5055	5134	5159	5172	5181#	5182	5261	5286	5298	5307#
5308	5387	5412	5424	5434#	5435	5439	5491	5512	5520	5530#	5532	5585
5626	5635	5645#	5652	5754	5766	5777#	5785	5880	5900	5915#	5917	6024
6034	6045#	6047	6139	6148	6159#	6161	6240	6253#	6262	6371	6387	6397#
6401	6479	6518	6530	6540#	6545	6621	6630	6649#	6651	6733	6737#	6747
6761#	6764	6813	6817#	6827	6841#	6844	6893	6897#	6907	6921#	6924	6968
6972#	6982	6996#	6999	7042	7056#	7059	7110	7124#	7127	7171	7175#	7186
7196#	7198	7297	7312#	7313	7437	7441#	7451	7462#	7463	7554	7568#	7569
7665	7679#	7680	7768	7778#								
253#	10357#	10359										
257#	10334	10359										
252#	10355	10359										
1144	10703#											
10710#	10720#	10721#	10722#	10723#	10724#	10725#	10726#	10727#	10728#	10729#	10730#	
10707	10718#											
236#	7795#	7829#	10176	10213	10235#	10240	10244	10536	10559			
10437	10438	10455	10459#									
10724												
10257#	10723											
10334#	10710	10719										
10346	10353	10355#	10356									
10647#	10720											
10646	10649#	10722										
10642#	10721											
10200#												
10709#	10720	10721	10722	10723	10724	10725	10726	10727	10728	10729	10730	
10643#	10647#	10657	10692#									
166#	170	172#	175#	220#	229#	232#	278	1041#	1109#	1110#	1138	1151
1152	1165	1168#	1169	1173	1177	1181	1185	1188#	1189	1192#	1193	1197
1201	1210	1245	1248#	1249	1319	1323	1327	1331	1334#	1335	1363	1366#
1367	1371	1374#	1375	1379	1382#	1410	1413#	1417	1420#	1423	1426#	1464
1467#	1472	1479	1506	1509#	1510	1513#	1514	1520	1523#	3784	3787#	7780
7786	7789#	7855	7859	7887	7893	7900	7903#	7904	7908	7915	7918#	7919
9083	9089	9101	9104#	9107	9113	9117	9121	9127	9133	9700#	9701#	9702#
9703#	9704#	9705#	9707#	9708#	9709#	9710#	9711#	10161	10243	10244	10311#	10359
10363	10366#	10367	10368#	10459#	10460	10461	10516	10559	10615#	10746	10767	11498#
11628#												

STPB 001144
 STPFLG 001151
 STPS 001142
 STRAP 054004
 STRP = 000026
 STRPAD 054024
 STSTNM 001102
 STTYIN 053062
 STYPBN= ##### U
 STYPDS 052140
 STYPE 052364
 STYPEC 052454
 STYPOC 053602
 STYPON 053616
 STYPOS 053556
 SXTSTR 051700
 SSTRP = 000002
 SDFILL 054001
 = 065000

CHECKA	41#	4673	4783	4907	5135	5262	5388	6480							
CHECKB	41#	3494	4394	4643	4756	4878	4997	5104	5231	5357	5475	5569	5919	6084	6195
	6449	6577	6653	7234	7346	7489	7595	7701	8270	8370	8537				
COMMEN	13	162#	2005												
ENDCOM	20	162#	2030												
ERROR	62#	1241	1282	1304	1543	1568	1601	1612	1621	1644	1668	1692	1721	1743	1774
	1800	1825	1850	1871	1962	2081	2282	2349	2443	2474	2483	2495	2503	2530	2545
	2556	2563	2597	2614	2616	2656	2662	2685	2705	2720	2728	2739	2765	2774	2803
	2818	2845	2853	2878	2886	2952	2963	2978	3002	3082	3150	3218	3487	3579	3629
	3689	3739	3821	3866	3963	4009	4093	4139	4225	4270	4341	4435	4505	4572	4668
	4696	4698	4778	4811	4813	4900	4934	4936	5016	5125	5160	5162	5252	5287	5289
	5378	5413	5415	5504	5507	5609	5611	5709	5715	5755	5757	5840	5845	5882	5884
	5951	5960	5973	5980	6008	6018	6025	6122	6124	6211	6221	6229	6320	6325	6332
	6338	6373	6375	6470	6505	6507	6519	6603	6609	6622	6722	7263	7266	7284	7403
	7423	7425	7545	7655	7761	8099	8105	8109	8121	8126	8129	8148	8155	8161	8166
	8199	8585	8587	8611	8613	8628	8639	8649	8659	8730	8936	8942	9010		
ESCAPE	162#														
HCOMPR	41#	6762	6842	7057											
HCOMPW	41#	6922	6997	7125											
MAKECL	41#	4350	4577	6733	6813	6893	6968	7171	7437						
MSG	1221#	1223	1264#	1267	1447#	1450	1493#	1495	1527#	1529	1551#	1554	1573#	1576	1627#
	1630	1651#	1654	1675#	1678	1698#	1701	1756#	1759	1782#	1785	1808#	1811	1833#	1836
	1854#	1857	1888#	1891	2036#	2040	2288#	2291	2357#	2360	2452#	2455	2505#	2508	2568#
	2571	2635#	2638	2666#	2669	2744#	2747	2779#	2782	2823#	2826	2856#	2859	2915#	2918
	3011#	3014	3089#	3092	3224#	3227	3501#	3504	3744#	3747	3874#	3877	4017#	4020	4148#
	4151	4278#	4281	4363#	4366	4594#	4597	4705#	4708	4826#	4829	4950#	4953	5044#	5047
	5171#	5174	5297#	5300	5423#	5426	5519#	5522	5632#	5637	5765#	5768	5898#	5902	6033#
	6036	6147#	6150	6239#	6242	6386#	6389	6529#	6532	6629#	6632	7185#	7188	7296#	7299
	7450#	7453	7553#	7556	7664#	7667	7768#	7770							
MSGA	6747#	6749	6829	7044											
MSGB	6906#	6909	6984	7112											
MULT	162#														
NEWTST	162#	1221	1265	1290	1308	1448	1493	1527	1552	1574	1628	1652	1676	1699	1757
	1783	1809	1834	1855	1889	2038	2289	2358	2453	2506	2569	2636	2667	2745	2780
	2824	2857	2916	3012	3090	3225	3502	3745	3875	4018	4149	4279	4350	4364	4577
	4595	4706	4827	4951	5045	5172	5298	5424	5520	5635	5766	5900	6034	6148	6240
	6387	6530	6630	6733	6747	6813	6827	6893	6907	6968	6982	7042	7110	7171	7186
	7297	7437	7451	7554	7665	7768									
POP	162#	7951	8048	8232	8328	8466	8514	8692	8732	8961	9025	9065	9305	9459	9529
	9597	9671	9840	9998	10113	10153	10298	10505	10753						
PUSH	162#	7941	8035	8222	8295	8408	8411	8486	8673	9710	8821	8985	9039	9225	9364
	9483	9551	9627	9727	9878	10044	10134	10257	10479	10737					
RFORGC	41#	4695	4810	4933	5159	5286	5412	5754	5880	6371					
SAVE	41#	10532													
SAVTST	41#	1228	1275	1296	1502	1536	1561	1583	1637	1661	1685	1707	1767	1793	1818
	1843	1864	1899	2300	2368	2465	2522	2578	2643	2677	1685	1707	1767	1793	1818
	3023	3099	3237	3513	3755	3886	4029	4160	4290	4377	2753	2788	2832	2866	2926
	5182	5308	5435	5439	5532	5652	5785	5917	6047	6161	4607	4718	4838	4962	5055
	6844	6924	6999	7059	7127	7198	7313	7463	7569	7680	6262	6401	6545	6651	6764
SCOPE	63#	1225	1272	1293	1311	1455	1501	1533	1552	1580	1634	1658	1682	1705	1763
	1789	1815	1840	1861	1897	2049	2298	2366	2463	2520	2577	2642	2675	2751	2786
	2830	2863	2923	3021	3098	3235	3511	3753	3884	4027	4158	4288	4353	4375	4580
	4605	4716	4836	4960	5053	5180	5306	5433	5529	5644	5776	5914	6044	6158	6252
	6396	6539	6648	6736	6760	6816	6840	6896	6920	6971	6995	7055	7123	7174	7195

ADD	1998	2346	2441	2934	2942	3080	3148	3216	3576	3627	3687	3737	3818	3864	3961
	4007	4091	4137	4223	4268	4339	4494	4508	4512	4570	6720	7401	7543	7653	7759
	7913	7974	8043	8134	8170	8428	8435	8442	10277	10344	10500	10582	10645	10655	
ASL	5987	10493	10495	10497	10579	10580	10581								
ASLB	10282														
ASR	8859	8868	8876	8884											
BCC	1399	3263	5793	5812	5857	6270	6289	6348	9054	9488	9501	9518	10283		
BOS	5670	5689	5738	5799	5819	5864	5988	6276	6296	6355					
BEG	1208	1301	1354	1358	1403	1405	1443	1505	1598	1610	1618	1718	1739	1923	1925
	1938	1940	1948	1974	1989	2080	2095	2105	2117	2128	2138	2148	2158	2168	2178
	2188	2197	2207	2218	2228	2240	2251	2262	2472	2481	2493	2501	2528	2543	2551
	2559	2595	2608	2628	2649	2660	2683	2703	2714	2716	2724	2762	2771	2799	2816
	2842	2850	2875	2883	2950	2961	2976	3267	3286	3297	3308	3322	3331	3338	3348
	3358	3368	3378	3388	3398	3408	3417	3428	3437	3448	3458	3468	3527	3527	3527
	4485	4499	4667	4777	4899	5014	5124	5251	5377	5498	5512	5603	5626	5706	5708
	5837	5839	5950	5959	5969	5979	6007	6017	6024	6116	6139	6208	6219	6226	6317
	6331	6337	6469	6518	6597	6614	6621	7257	7271	7280	7417	7431	7794	7803	7807
	7844	7986	7988	8003	8056	8094	8116	8311	8325	8424	8431	8438	8619	8621	8625
	8636	8643	8646	8656	8727	8831	8852	8919	8926	8933	8939	9005	9152	9291	9407
	9413	9424	9429	9431	9450	9452	9493	9495	9505	9510	9512	9522	9562	9572	9577
	9587	9644	9652	9886	9896	9910	9921	9943	9956	9969	9980	9992	10063	10136	10212
	10214	10216	10220	10229	10421	10488	10535	10538	10552	10555	10584	10589	10595	10606	10672
BGE	10232														
BGT	7835	10291	10490	10679											
BHI	10218														
BHIS	2693	9002													
BIC	1215	1727	1748	1928	2216	2413	2626	2969	3050	3405	3426	3547	3594	3595	3658
	3704	3705	3777	3834	3835	3895	3932	3977	3978	3979	4038	4064	4107	4108	4171
	4196	4239	4240	4407	4412	4415	4450	4452	4479	4481	4514	4517	4528	4532	5510
	5624	5957	5977	6013	6137	6612	6691	6694	7269	7278	7367	7428	7469	7575	7686
	7832	7995	8000	8012	8114	8323	8448	8453	8458	8845	8898	8904	8913	8931	9015
	9023	9097	9241	9370	9373	9376	9742	10054	10056	10082	10084	10099	10101	10109	10391
	10499	10669													
BIS	1588	1606	1713	1715	1726	1737	1749	2059	2070	2084	2113	2314	2317	2376	2396
	2401	2414	2469	2499	2525	2540	2592	2680	2700	2735	2790	2795	2801	2806	2807
	2811	2958	3035	3046	3052	3113	3181	3245	3270	3276	3318	3541	3545	3546	3587
	3596	3652	3656	3657	3696	3706	3772	3775	3776	3827	3836	3894	3927	3930	3931
	3970	3980	4037	4050	4062	4063	4100	4109	4170	4182	4194	4195	4232	4241	4306
	4307	4311	4410	4411	4414	4417	4449	4451	4478	4480	4513	4516	4527	4531	4537
	4538	4540	4541	5008	5010	5703	5834	5944	6088	6187	6199	6215	6314	6659	6669
	6689	6690	6692	6693	7366	7368	7374	7375	7468	7510	7513	7517	7518	7574	7621
	7627	7628	7685	7727	7733	7734	8007	8013	8450	8455	8460	8681	8689	8719	8837
	8856	8865	8873	8881	8896	8897	8899	8902	8903	8905	9014	9022	9098	9232	9368
	9369	9375	9489	9491	9508	9575	9734	9883	9894	10053	10055	10060	10078	10079	10081
	10083	10098	10100	10108	10285	10286	10674	10675							
BISB	10571														
BIT	1242	1316	1357	1504	1518	1922	1971	2653	2713	2732	2939	2998	3781	5013	5707
	5838	5989	6318	7985	8055	8096	8102	8118	8123	8144	8151	8158	8163	8189	8194
	8423	8430	8437	8923	8925	9006	9151	9388	9451	9454	9494	9511	9561	9576	9885
	9895	10211	10219	10226	10537	10544	10551								
BLOS	10439														
BLT	10274	10290	10352	10492	10680										
BMI	8447	8452	8457	10198	10281										
BNE	1138	1160	1236	1243	1317	1348	1391	1436	1519	1729	1751	1916	1972	1997	2583

	2590	2603	2613	2631	2654	2733	2933	2938	2940	3000	3110	3171	3178	3780	3782
	3909	4058	4190	4454	4458	4483	4488	4520	4530	4613	4639	4672	4724	4753	4782
	4844	4875	4906	4968	4994	5066	5073	5134	5149	5193	5200	5261	5276	5319	5326
	5387	5402	5491	5503	5514	5585	5608	5628	5990	5995	6121	6141	6319	6411	6418
	6479	6494	6591	6602	6616	7250	7262	7273	7422	7433	7486	7592	7698	7846	7848
	7950	7981	8006	8010	8047	8097	8103	8119	8124	8146	8153	8159	8164	8190	8192
	8195	8197	8231	8317	8327	8462	8464	8495	8501	8568	8570	8924	8993	8995	9007
	9060	9062	9100	9238	9289	9296	9378	9380	9385	9389	9405	9421	9446	9455	9526
	9528	9591	9593	9637	9650	9656	9662	9669	9739	9834	9904	9906	9929	9938	9965
	9988	10086	10089	10103	10105	10107	10111	10144	10146	10152	10227	10279	10341	10348	10393
BPL	10398	10406	10426	10443	10449	10545	10572	10598	10670	10752					
BR	10265	10295	10335	10356	10549	10668									
	1129	1161	1166	1170	1174	1178	1182	1186	1190	1194	1198	1202	1211	1237	1246
	1250	1320	1324	1328	1332	1336	1364	1368	1372	1376	1380	1411	1418	1424	1427
	1465	1473	1480	1507	1511	1515	1521	1935	1941	1950	1982	2278	2615	2650	2935
	3006	3268	3333	3484	3529	3785	4448	4510	5505	5610	5710	5841	5991	6123	6321
	6334	6608	7264	7424	7781	7787	7808	7888	7894	7901	7905	7909	7916	7920	8008
	8101	8107	8122	8127	8150	8157	8162	8319	8426	8433	8440	8449	8454	8459	8596
	8630	8641	8651	8840	8906	8927	8937	8996	8998	9017	9056	9084	9090	9102	9108
	9114	9118	9122	9128	9134	9153	9154	9301	9371	9393	9418	9433	9438	9440	9453
	9497	9514	9566	9581	9889	9899	9926	9948	9962	9974	9985	10080	10162	10200	10206
	10209	10222	10225	10276	10293	10337	10354	10445	10501	10514	10577	10601	10608	10646	10661
	10682	10746	10767												
CLC	1724	7978	8848	9568	9583	9890	9900								
CLR	1130	1136	1148	1149	1302	1342	1394	1395	1460	1590	1907	1918	2052	2399	2404
	2405	2407	2417	2418	2533	2552	2581	2585	2598	2606	2645	2688	2726	2929	3049
	3053	3260	3523	3644	3708	4383	4402	4621	4622	4623	4637	4640	4650	4652	4676
	4677	4685	4722	4732	4733	4748	4749	4763	4765	4786	4787	4800	4853	4854	4870
	4871	4885	4887	4910	4911	4923	4976	4977	4978	4992	4995	5004	5006	5071	5085
	5102	5110	5198	5212	5229	5237	5324	5338	5355	5363	5452	5453	5480	5548	5549
	5551	5568	5573	5658	5659	5725	5726	5746	5852	6063	6064	6066	6083	6089	6100
	6173	6174	6176	6194	6200	6304	6305	6343	6416	6430	6447	6455	6554	6557	6558
	6574	6576	6582	6671	6766	6846	7061	7211	7212	7239	7324	7325	7351	7408	7481
	7482	7496	7498	7587	7588	7602	7604	7708	7792	7795	7829	7830	7886	8078	8249
	8250	8252	8269	8347	8348	8350	8369	8415	8416	8427	8434	8441	8498	8556	8683
	8716	8901	8988	9046	9047	9048	9057	9382	9415	9432	9443	9498	9515	9567	9582
	9657	9912	9923	9982	10147	10186	10187	10191	10195	10224	10238	10268	10271	10378	10418
	10419	10485	10486	10570	10659	10750									
CLRB	1352	2408	2793	5546	6061	6171	6555	6556	10223	10297	10450	10510			
CMP	1137	1239	1402	1404	1596	1609	1616	1717	1738	1937	1939	1973	1988	2079	2094
	2104	2116	2127	2137	2147	2157	2167	2177	2187	2206	2217	2227	2239	2250	2261
	2471	2479	2491	2500	2527	2542	2550	2558	2594	2607	2627	2648	2659	2682	2692
	2702	2715	2761	2770	2797	2814	2841	2849	2874	2882	2932	2960	2970	3285	3296
	3307	3321	3337	3347	3357	3367	3377	3387	3397	3416	3427	3436	3447	3457	3467
	3526	4427	4498	5497	5511	5602	5622	5625	5705	5836	5958	5968	5978	6014	6022
	6115	6135	6138	6207	6218	6225	6316	6329	6335	6517	6596	6613	6620	7256	7270
	7279	7416	7429	7806	7845	7847	8002	8005	8009	8092	8115	8310	8321	8324	8620
	8623	8633	8642	8645	8654	8726	8932	8938	9001	9003	9423	9942	9955	9968	9991
	10207	10231	10289	10392	10397	10405	10425	10438							
CMPB	2196	3407	5948	10213	10217	10347	10442	10448	10489	10491					
COM	7066	7134	7983	7994	7996	7999	8001	8838	8857	8866	8874	8882			
DEC	1235	1347	1390	1435	1440	1728	1750	1915	1924	1947	1996	2582	2589	2602	2630
	2936	3109	3170	3177	3266	3908	4057	4189	4453	4455	4457	4482	4484	4486	4519
	4529	4612	4638	4723	4752	4843	4874	4967	4993	5065	5072	5148	5192	5199	5275

	5318	5325	5401	5513	5627	5994	6140	6410	6417	6493	6615	7272	7432	7485	7591
	7697	7802	7833	7949	8045	8046	8191	8196	8230	8326	8461	8463	8494	8500	8994
	9059	9061	9099	9237	9377	9379	9384	9420	9445	9525	9527	9590	9592	9636	9649
	9655	9661	9668	9738	9903	9905	9928	9964	9987	10085	10088	10102	10104	10106	10110
DECB	10143	10151	10422	10578											
EMT	10351	10667	10678												
HALT	62														
	170	3497	4397	4646	4675	4759	4785	4881	4909	5000	5107	5137	5234	5264	5360
	5390	5478	5572	5922	6087	6198	6452	6482	6580	6656	7237	7349	7492	7598	7704
INC	8273	8373	8540	9137	10167	10336	10550	10745	10766						
	1346	1432	1434	1929	1931	1949	2588	2629	2646	2691	2758	2792	2838	2871	3265
	3902	6686	7796	7831	8308	8997	9000	9506	9523	9573	9588	10230	10275	10402	10404
INCB	10424	10540	10673	10681	10751										
IOT	10235	10534													
JMP	63														
JSR	174	176	1162	1257	1355	1359	1384	7801	7810	7854	9915	10396			
	1158	1279	1315	1459	1503	1540	1565	1584	1641	1665	1689	1708	1771	1797	1822
	1847	1868	1901	1952	2051	2096	2106	2118	2129	2139	2149	2159	2169	2179	2189
	2198	2208	2219	2229	2241	2252	2263	2302	2308	2323	2337	2369	2388	2395	2421
	2432	2466	2470	2490	2523	2526	2541	2584	2593	2644	2651	2678	2681	2698	2701
	2755	2789	2834	2867	2927	2944	2948	2959	2985	2996	3025	3040	3058	3071	3102
	3125	3139	3159	3193	3207	3238	3287	3298	3309	3323	3339	3349	3359	3369	3379
	3389	3399	3409	3418	3429	3438	3449	3459	3469	3494	3514	3535	3554	3567	3604
	3617	3637	3646	3664	3679	3714	3727	3756	3766	3796	3810	3842	3854	3887	3911
	3921	3938	3952	3986	3997	4030	4044	4069	4082	4114	4126	4161	4176	4201	4214
	4246	4258	4291	4300	4317	4330	4355	4357	4378	4388	4394	4458	4561	4582	4584
	4614	4625	4643	4653	4673	4681	4689	4725	4735	4756	4766	4783	4791	4795	4804
	4845	4856	4878	4888	4907	4915	4919	4927	4969	4980	4997	5012	5086	5092	5104
	5111	5135	5153	5213	5219	5231	5238	5262	5280	5339	5345	5357	5364	5388	5406
	5441	5456	5463	5467	5475	5481	5489	5533	5538	5552	5558	5569	5574	5586	5591
	5660	5672	5679	5691	5696	5704	5728	5740	5748	5801	5821	5826	5835	5866	5873
	5918	5919	5947	5967	6001	6005	6048	6053	6067	6073	6084	6090	6101	6162	6177
	6183	6195	6201	6263	6278	6298	6306	6315	6357	6364	6431	6437	6449	6456	6480
	6498	6515	6549	6561	6567	6571	6577	6583	6589	6652	6653	6675	6699	6712	6728
	6738	6740	6770	6774	6789	6799	6818	6820	6850	6854	6869	6879	6898	6900	6930
	6934	6943	6953	6973	6975	7005	7009	7018	7028	7065	7070	7085	7095	7133	7138
	7147	7157	7176	7178	7200	7215	7222	7226	7234	7240	7248	7314	7315	7328	7334
	7338	7346	7354	7360	7379	7392	7411	7442	7444	7464	7489	7501	7521	7534	7551
	7570	7595	7612	7631	7644	7661	7681	7701	7718	7737	7750	7767	7850	7976	7977
	7991	8014	8091	8143	8253	8259	8270	8276	8318	8320	8352	8358	8370	8375	8511
	8536	8537	8557	8566	8578	8604	8690	8724	8935	8941	9009	9016	9058	9093	9148
	9243	9249	9297	9302	9383	9387	9395	9397	9399	9401	9403	9411	9422	9444	9448
	9507	9524	9574	9589	9647	9653	9659	9663	9666	9744	9755	9836	9919	9939	9953
MOV	9966	9978	9989	10142	10150	10346	10353	10395	10546						
	1128	1134	1135	1139	1140	1141	1142	1143	1144	1145	1146	1147	1151	1152	1156
	1157	1206	1216	1217	1226	1227	1228	1229	1231	1232	1233	1234	1238	1240	1254
	1258	1260	1273	1274	1275	1277	1278	1294	1296	1297	1298	1299	1303	1312	1340
	1341	1343	1344	1345	1387	1388	1389	1393	1396	1397	1401	1415	1421	1428	1430
	1438	1439	1444	1456	1457	1461	1469	1476	1483	1488	1489	1502	1535	1536	1538
	1539	1560	1561	1563	1564	1582	1583	1595	1599	1600	1605	1607	1608	1611	1615
	1619	1620	1636	1637	1639	1640	1660	1661	1663	1664	1684	1685	1687	1688	1707
	1709	1710	1711	1712	1714	1716	1719	1720	1734	1735	1736	1740	1741	1742	1766
	1767	1769	1770	1792	1793	1795	1796	1817	1818	1820	1821	1842	1843	1845	1846
	1863	1864	1866	1867	1898	1899	1912	1913	1914	1919	1920	1927	1930	1934	1943

1945	1963	1964	1965	1966	1968	1977	1990	2056	2057	2058	2060	2061	2062	2063
2064	2065	2066	2067	2068	2071	2072	2076	2077	2078	2087	2092	2093	2102	2103
2112	2114	2115	2125	2126	2135	2136	2145	2146	2155	2156	2165	2166	2175	2176
2185	2186	2194	2195	2204	2205	2213	2214	2215	2225	2226	2237	2238	2248	2249
2259	2260	2281	2299	2300	2304	2305	2345	2347	2367	2368	2374	2375	2377	2378
2379	2380	2381	2382	2383	2384	2385	2400	2402	2403	2406	2409	2412	2416	2440
2442	2464	2465	2467	2468	2473	2482	2488	2494	2498	2502	2521	2522	2524	2529
2534	2535	2539	2544	2548	2549	2554	2555	2560	2561	2562	2578	2579	2580	2586
2587	2591	2596	2599	2600	2601	2604	2605	2609	2610	2611	2625	2643	2647	2655
2657	2658	2661	2677	2679	2684	2687	2689	2694	2699	2704	2707	2708	2709	2710
2711	2712	2717	2718	2719	2725	2727	2734	2736	2737	2738	2753	2754	2756	2757
2760	2763	2764	2769	2772	2773	2788	2791	2794	2796	2800	2802	2805	2810	2812
2813	2817	2832	2833	2835	2836	2837	2840	2843	2844	2848	2851	2852	2865	2866
2868	2869	2870	2873	2876	2877	2881	2884	2885	2925	2926	2928	2930	2931	2941
2945	2946	2947	2951	2962	2968	2977	2992	3001	3022	3023	3029	3030	3031	3032
3033	3034	3036	3037	3079	3081	3099	3103	3104	3105	3106	3107	3108	3111	3112
3147	3149	3158	3160	3165	3166	3167	3168	3172	3173	3174	3175	3176	3179	3180
3215	3217	3236	3237	3242	3243	3244	3246	3247	3248	3249	3250	3251	3252	3253
3254	3259	3261	3264	3269	3271	3274	3275	3277	3283	3284	3294	3295	3305	3306
3317	3319	3320	3332	3334	3335	3345	3346	3355	3356	3365	3366	3375	3376	3385
3386	3395	3396	3404	3406	3414	3415	3423	3424	3425	3434	3435	3445	3446	3455
3456	3465	3466	3486	3512	3513	3517	3524	3525	3528	3530	3531	3575	3577	3588
3589	3597	3598	3626	3628	3639	3643	3686	3688	3697	3698	3707	3736	3738	3754
3755	3758	3762	3789	3817	3819	3828	3829	3837	3863	3865	3885	3886	3889	3900
3901	3905	3906	3907	3913	3917	3918	3960	3962	3971	3972	3981	4006	4008	4028
4029	4032	4041	4054	4055	4056	4090	4092	4101	4102	4110	4136	4138	4159	4160
4163	4173	4186	4187	4188	4222	4224	4233	4234	4242	4267	4269	4289	4290	4293
4297	4312	4338	4340	4354	4356	4376	4377	4381	4384	4385	4405	4406	4408	4409
4418	4424	4425	4426	4430	4432	4434	4446	4447	4456	4473	4475	4477	4495	4497
4500	4502	4504	4509	4526	4542	4569	4571	4581	4583	4606	4607	4609	4610	4611
4619	4624	4631	4632	4633	4634	4635	4648	4651	4666	4717	4718	4720	4721	4729
4731	4734	4741	4742	4743	4745	4747	4750	4751	4754	4761	4764	4776	4837	4838
4840	4841	4842	4850	4852	4855	4862	4863	4865	4867	4869	4872	4873	4876	4883
4886	4898	4961	4962	4964	4965	4966	4974	4979	4986	4987	4988	4989	4990	5002
5005	5007	5015	5054	5055	5061	5062	5063	5064	5068	5078	5082	5083	5084	5093
5094	5097	5098	5109	5123	5138	5139	5142	5143	5144	5145	5146	5147	5181	5182
5188	5189	5190	5191	5195	5205	5209	5210	5211	5220	5221	5224	5225	5236	5250
5265	5266	5269	5270	5271	5272	5273	5274	5307	5308	5314	5315	5316	5317	5321
5331	5335	5336	5337	5346	5347	5350	5351	5362	5376	5391	5392	5395	5396	5397
5398	5399	5400	5435	5438	5439	5448	5454	5455	5468	5469	5470	5472	5473	5474
5479	5492	5493	5494	5496	5499	5500	5509	5530	5532	5545	5550	5559	5560	5561
5564	5565	5598	5599	5600	5601	5604	5605	5623	5651	5652	5655	5656	5657	5665
5666	5667	5684	5685	5686	5702	5722	5723	5724	5733	5734	5735	5784	5785	5789
5790	5791	5794	5795	5796	5808	5809	5810	5814	5815	5816	5833	5853	5854	5855
5859	5860	5861	5916	5917	5926	5928	5929	5930	5931	5932	5933	5935	5936	5937
5939	5940	5942	5943	5945	5946	5956	5966	5970	5971	5972	5976	6002	6003	6004
6012	6045	6047	6060	6065	6074	6075	6076	6079	6080	6107	6111	6112	6113	6114
6117	6118	6136	6159	6161	6170	6175	6184	6185	6186	6190	6191	6205	6209	6210
6214	6216	6217	6220	6223	6227	6228	6260	6262	6266	6267	6268	6271	6272	6273
6285	6286	6287	6291	6292	6293	6313	6344	6345	6346	6350	6351	6352	6399	6401
6406	6407	6408	6409	6413	6423	6427	6428	6429	6438	6439	6442	6443	6454	6468
6483	6484	6487	6488	6489	6490	6491	6492	6543	6545	6547	6559	6560	6572	6573
6575	6581	6592	6593	6594	6595	6598	6599	6611	6650	6651	6657	6658	6660	6664
6666	6667	6668	6670	6685	6687	6719	6721	6737	6739	6763	6764	6781	6782	6783

6784	6785	6817	6819	6843	6844	6861	6862	6863	6864	6865	6897	6899	6923	6924	
6926	6972	6974	6998	6999	7001	7058	7059	7077	7078	7079	7080	7081	7126	7127	
7129	7175	7177	7197	7198	7207	7213	7214	7227	7228	7229	7231	7232	7233	7238	
7251	7252	7253	7255	7258	7259	7268	7277	7281	7282	7283	7289	7312	7313	7320	
7326	7327	7339	7340	7341	7343	7344	7345	7350	7363	7364	7369	7370	7372	7373	
7400	7402	7412	7413	7414	7415	7418	7419	7427	7441	7443	7462	7463	7467	7474	
7475	7476	7478	7480	7483	7484	7487	7494	7497	7508	7509	7514	7515	7516	7542	
7544	7568	7569	7573	7580	7581	7582	7584	7586	7589	7590	7593	7600	7603	7619	
7620	7624	7625	7626	7652	7654	7679	7680	7684	7691	7692	7693	7695	7696	7699	
7706	7709	7710	7725	7726	7730	7731	7732	7758	7760	7778	7779	7784	7790	7798	
7804	7805	7809	7836	7840	7843	7891	7897	7914	7924	7925	7942	7943	7944	7945	
7946	7947	7948	7951	7952	7953	7970	7971	7972	7973	7975	7984	7990	7993	7997	
7998	8004	8011	8036	8037	8038	8039	8040	8041	8044	8048	8049	8050	8057	8071	
8072	8073	8074	8076	8077	8089	8098	8104	8108	8113	8120	8125	8128	8141	8147	
8154	8160	8165	8180	8181	8182	8184	8185	8186	8187	8188	8193	8198	8223	8224	
8225	8226	8227	8228	8229	8232	8233	8234	8246	8251	8261	8262	8265	8266	8274	
8296	8297	8298	8299	8300	8301	8302	8303	8304	8305	8306	8307	8309	8313	8314	
8322	8329	8330	8331	8332	8333	8344	8349	8361	8362	8365	8366	8374	8409	8410	
8411	8412	8413	8414	8417	8418	8419	8420	8425	8432	8439	8465	8466	8467	8468	
8469	8470	8487	8488	8489	8490	8491	8492	8493	8496	8505	8508	8509	8510	8514	
8515	8516	8534	8541	8542	8543	8548	8549	8551	8552	8554	8622	8626	8627	8632	
8637	8638	8644	8647	8648	8652	8657	8658	8674	8675	8676	8678	8679	8680	8682	
8684	8688	8692	8693	8711	8712	8714	8715	8717	8718	8725	8728	8729	8733	8822	
8823	8824	8825	8826	8827	8828	8829	8836	8839	8844	8846	8855	8858	8864	8867	
8872	8875	8880	8883	8914	8915	8930	8962	8963	8964	8965	8966	8967	8982	8984	
8985	8986	8987	9026	9040	9041	9042	9043	9044	9045	9049	9050	9051	9052	9055	
9063	9064	9065	9066	9067	9068	9069	9070	9087	9095	9096	9105	9112	9125	9131	
9149	9223	9225	9226	9227	9228	9229	9230	9231	9235	9240	9242	9245	9246	9247	
9248	9306	9307	9308	9309	9310	9311	9359	9360	9361	9362	9363	9364	9365	9366	
9367	9372	9374	9381	9386	9390	9391	9392	9394	9396	9398	9400	9402	9408	9409	
9410	9416	9417	9419	9434	9435	9436	9437	9439	9442	9447	9456	9457	9458	9460	
9484	9485	9486	9490	9496	9499	9502	9503	9513	9516	9519	9520	9529	9552	9553	
9554	9555	9556	9557	9559	9560	9563	9570	9578	9585	9595	9597	9598	9599	9600	
9623	9624	9625	9627	9628	9629	9630	9631	9633	9634	9635	9638	9639	9641	9642	
9645	9646	9648	9654	9658	9660	9664	9665	9667	9671	9672	9673	9674	9675	9725	
9727	9728	9729	9730	9731	9732	9733	9736	9741	9743	9747	9748	9749	9750	9751	
9752	9753	9841	9842	9843	9844	9845	9846	9873	9874	9875	9876	9877	9878	9879	
9880	9881	9882	9884	9892	9893	9902	9908	9911	9913	9914	9917	9918	9920	9924	
9925	9941	9944	9945	9946	9947	9950	9951	9952	9954	9959	9960	9961	9967	9970	
9971	9972	9973	9976	9977	9979	9983	9984	9990	9993	9994	9995	9996	9999	10043	
10044	10045	10046	10047	10048	10049	10050	10051	10052	10061	10076	10077	10087	10095	10096	
10097	10114	10115	10116	10117	10118	10119	10132	10133	10134	10137	10138	10140	10141	10148	
10149	10154	10166	10202	10203	10205	10208	10221	10233	10234	10236	10237	10240	10241	10258	
10259	10260	10261	10262	10263	10264	10269	10272	10292	10298	10299	10300	10301	10302	10304	
10305	10338	10339	10343	10349	10379	10380	10381	10382	10384	10407	10416	10417	10427	10436	
10437	10452	10453	10454	10455	10477	10478	10479	10480	10481	10483	10484	10503	10504	10505	
10506	10507	10532	10536	10541	10553	10556	10569	10574	10583	10588	10593	10594	10596	10599	
10603	10610	10611	10642	10650	10651	10652	10658	10665	10683	10684	10685	10686	10687	10703	
10704	10707	10735	10736	10737	10738	10739	10740	10741	10742	10743	10744	10749	10753	10754	
10755	10756	10757	10758	10759	10760	10763									
MOV8	1150	1350	1462	1932	2331	2429	2759	2768	2809	2839	2847	2872	2880	3066	3133
	3201	3562	3613	3673	3722	3804	3851	3946	3994	4077	4122	4209	4254	4325	4556
	5080	5081	5095	5096	5140	5141	5207	5208	5222	5223	5267	5268	5333	5334	5348
	5349	5393	5394	5450	5451	5471	5547	5562	5563	6062	6077	6078	6172	6188	6189

	6425	6426	6440	6441	6485	6486	6707	7209	7210	7230	7322	7323	7342	7387	7529
	7639	7745	8247	8248	8263	8264	8345	8346	8363	8364	8506	8507	8544	8546	10239
	10267	10270	10284	10287	10296	10340	10357	10390	10403	10423	10441	10446	10487	10543	10643
	10644	10647	10648	10649	10653	10656	10657	10676	10706						
NEG	10266	10654													
NOP	3008	4413	4416	4515	5009	5764	5891	6382	6809	6889	6963	7038	7105	7167	7851
	7852	7853	8015	8720	8721	8722	8723	9245	9258	9266	9277	9747	9764	9774	9787
	9799	9807	9820												
RESET	1314	2394	7849	7989											
ROL	1725	1747	5669	5688	5737	5792	5798	5811	5818	5856	5863	6269	6275	6288	6295
	6347	6354	7979	10197	10494	10496	10498	10660	10662	10663	10664	10666			
ROR	1398	3262	8421	8422	8444	8445	8849	8850	8894	8895	8911	8912	9053	9487	9500
	9517	9565	9569	9580	9584	9888	9891	9898	9901						
RTI	7926	8058	8200	10242	10306	10345	10401	10408	10428	10456	10508	10558	10688	10765	
RTS	1283	1544	1569	1645	1669	1693	1775	1801	1826	1851	1872	2286	2352	2447	3084
	3152	3220	3491	3582	3633	3692	3741	3824	3869	3966	4013	4096	4143	4228	4274
	4344	4573	4697	4703	4812	4822	4935	4945	5161	5165	5288	5292	5414	5418	5756
	5762	5883	5889	6374	6380	6506	6510	6723	7405	7547	7657	7763	7954	7982	8016
	8051	8079	8132	8135	8168	8171	8235	8279	8334	8378	8471	8517	8586	8594	8612
	8617	8662	8694	8734	8968	9027	9071	9312	9461	9530	9601	9677	9848	10001	10120
	10155	10358	10385	10613	10708										
SEC	1746	5668	5687	5736	5797	5817	5862	6274	6294	6353	9564	9579	9887	9897	
SUB	1944	4431	4433	4501	4503	5501	5606	6119	6600	7260	7420	8042	8090	8142	8183
	8315	8535	8713	8983	9224	9414	9640	9726	9922	9958	9981	10273	10542		
TRAP	10710	10720	10721	10722	10723	10724	10725	10726	10727	10728	10729				
TST	1159	1207	1442	1486	1487	1921	2269	2274	2285	2612	2623	2632	2722	2949	3330
	3475	3480	3490	3493	3779	4419	4671	4781	4905	5067	5133	5194	5260	5320	5386
	5490	5502	5508	5584	5607	5993	6120	6412	6478	6590	6601	6610	7249	7261	7267
	7421	7426	7793	7980	8316	8446	8451	8456	8567	8569	8618	8830	8851	8916	8992
	9150	9288	9290	9292	9404	9406	9412	9425	9430	9449	9492	9504	9509	9521	9571
	9586	9643	9651	9833	9909	9937	10062	10135	10145	10204	10228	10278	10288	10342	10383
	10400	10420	10502	10509	10548	10554	10605	10671	10705						
TSTB	1300	1353	6006	7987	8545	8547	10215	10280	10294	10334	10355	10597			
.ASCII	275	276	10829	10834	10843	10851	10858	10868	10898	10913	10928	10937	10944	10978	10988
	10997	11024	11042	11056	11062	11064	11073	11087	11091	11106	11120	11140	11168	11186	11206
	11226	11241	11253	11273	11296	11314	11332	11343	11354	11380	11403	11423	11446	11463	11489
.ASCIZ	274	277	1168	1172	1176	1180	1184	1188	1192	1196	1200	1204	1213	1248	1252
	1322	1326	1330	1334	1338	1366	1370	1374	1378	1382	1413	1420	1426	1467	1475
	1482	1509	1513	1517	1523	3787	7783	7789	7855	7890	7896	7903	7907	7911	7918
	7922	9086	9092	9104	9110	9116	9120	9124	9130	9136	10164	10460	10614	10769	10781
	10790	10794	10800	10805	10808	10815	10818	10822	10825	10876	10882	10892	10909	10918	10952
	10960	10968	11001	11011	11032	11050	11082	11088	11099	11113	11130	11152	11163	11177	11196
	11216	11234	11247	11263	11285	11305	11323	11367	11392	11413	11435	11455	11476	11493	
.BLKB	10366	10459													
.BLKM	1041	1109	1110	9700	9701	9702	9703	9704	9705	9707	9708	9709	9710	9711	10311
.BYTE	236	237	242	243	254	255	256	257	1117	1979	1980	1992	1993	7858	10457
	10458	10689	10690	10691	10692	11568	11570	11572	11575	11578	11581	11582	11584	11588	11591
	11593	11595	11598	11601	11604	11606	11609	11612	11615	11618	11621	11623	11627		
.ENABL	1														
.END	11630														
.ENDC	6	17	23	33	40	50	52	53	54	62	148	162	175	196	207
	218	220	223	231	233	258	266	272	273	274	275	278	279	724	726
	764	766	1134	1139	1140	1142	1144	1146	1148	1149	1151	1153	1168	1172	1176
	1180	1184	1188	1192	1196	1200	1204	1213	1222	1223	1224	1225	1226	1227	1248

1252	1266	1267	1271	1272	1273	1274	1291	1292	1293	1294	1295	1309	1310	1311	
1312	1313	1322	1326	1330	1334	1338	1366	1370	1374	1378	1382	1413	1420	1426	
1449	1450	1454	1455	1456	1457	1458	1467	1475	1482	1494	1495	1500	1501	1502	
1509	1513	1517	1523	1528	1529	1532	1533	1534	1553	1554	1557	1558	1559	1575	
1576	1579	1580	1581	1629	1630	1633	1634	1635	1653	1654	1657	1658	1659	1677	
1678	1681	1682	1683	1700	1701	1704	1705	1706	1758	1759	1762	1763	1764	1784	
1785	1788	1789	1790	1810	1811	1814	1815	1816	1835	1836	1839	1840	1841	1856	
1857	1860	1861	1862	1880	1886	1890	1891	1896	1897	1898	1980	1981	1993	1994	
2009	2018	2021	2033	2039	2040	2048	2049	2050	2095	2105	2128	2138	2148	2158	
2168	2178	2188	2207	2251	2262	2290	2291	2297	2298	2299	2359	2360	2365	2366	
2367	2454	2455	2462	2463	2464	2507	2508	2519	2520	2521	2570	2571	2576	2577	
2578	2637	2638	2641	2642	2643	2668	2669	2674	2675	2676	2746	2747	2750	2751	
2752	2781	2782	2785	2786	2787	2825	2826	2829	2830	2831	2858	2859	2862	2863	
2864	2892	2902	2909	2913	2917	2918	2922	2923	2924	3013	3014	3020	3021	3022	
3091	3092	3097	3098	3099	3226	3227	3234	3235	3236	3286	3297	3308	3348	3358	
3368	3378	3388	3398	3417	3458	3468	3503	3504	3510	3511	3512	3746	3747	3752	
3753	3754	3787	3876	3877	3883	3884	3885	4019	4020	4026	4027	4028	4150	4151	
4157	4158	4159	4280	4281	4287	4288	4289	4351	4352	4353	4354	4365	4366	4374	
4375	4376	4578	4579	4580	4581	4596	4597	4604	4605	4606	4707	4708	4715	4716	
4717	4828	4829	4835	4836	4837	4952	4953	4959	4960	4961	5021	5041	5046	5047	
5052	5053	5054	5140	5160	5173	5174	5179	5180	5181	5267	5287	5299	5300	5305	
5306	5307	5393	5413	5425	5426	5432	5433	5434	5492	5513	5521	5522	5528	5529	
5530	5636	5637	5643	5644	5645	5767	5768	5775	5776	5777	5901	5902	5913	5914	
5915	6035	6036	6043	6044	6045	6149	6150	6157	6158	6159	6241	6242	6251	6252	
6253	6388	6389	6395	6396	6397	6485	6505	6531	6532	6538	6539	6540	6631	6632	
6647	6648	6649	6734	6735	6736	6737	6748	6749	6759	6760	6761	6771	6809	6814	
6815	6816	6817	6828	6829	6839	6840	6841	6851	6889	6894	6895	6896	6897	6908	
6909	6919	6920	6921	6931	6963	6969	6970	6971	6972	6983	6984	6994	6995	6996	
7006	7038	7043	7044	7054	7055	7056	7067	7105	7111	7112	7122	7123	7124	7135	
7167	7172	7173	7174	7175	7187	7188	7194	7195	7196	7250	7251	7271	7272	7298	
7299	7310	7311	7312	7438	7439	7440	7441	7452	7453	7460	7461	7462	7555	7556	
7566	7567	7568	7666	7667	7677	7678	7679	7769	7770	7776	7777	7778	7779	7783	
7789	7819	7822	7823	7824	7826	7829	7835	7838	7839	7843	7855	7858	7859	7862	
7881	7890	7896	7903	7907	7911	7918	7922	9086	9092	9104	9110	9116	9120	9124	
9130	9136	9158	9159	9218	9221	9315	9337	9344	9346	9471	9473	9538	9540	9609	
9612	9687	9693	9719	9721	9851	9853	10011	10013	10124	10126	10164	10171	10177	10182	
10197	10199	10210	10213	10214	10215	10217	10219	10226	10230	10235	10240	10243	10244	10245	
10313	10360	10397	10410	10429	10430	10437	10439	10444	10459	10460	10461	10463	10472	10516	
10517	10523	10534	10541	10547	10548	10558	10559	10560	10578	10616	10617	10695	10704	10707	
10709	10719	10720	10721	10722	10723	10724	10725	10726	10727	10728	10729	10731	10743	10753	
10763	10765	10772													
.EQUIV	62	63	65	80	81	110	111	112	113	114	115	116	117	118	119
	138	139	140	141	142	143	144	145	146	147					
.EVEN	1168	1172	1176	1180	1184	1188	1192	1196	1200	1204	1213	1248	1252	1322	1326
	1330	1334	1338	1366	1370	1374	1378	1382	1413	1420	1426	1467	1475	1482	1509
	1513	1517	1523	3787	7783	7789	7890	7896	7903	7907	7911	7918	7922	9086	9092
.IF	9104	9110	9116	9120	9124	9130	9136	10164	10368	10615	10771	11498	11628		
	2	14	20	32	39	50	51	52	53	54	60	120	148	170	196
	207	218	219	222	228	232	258	266	272	273	274	278	723	725	763
	765	1134	1139	1140	1142	1144	1146	1148	1149	1151	1167	1171	1175	1179	1183
	1187	1191	1195	1199	1203	1212	1221	1223	1224	1226	1227	1247	1251	1265	1267
	1271	1273	1274	1290	1292	1294	1295	1308	1310	1312	1313	1321	1325	1329	1333
	1337	1365	1369	1373	1377	1381	1412	1419	1425	1448	1450	1454	1456	1457	1466
	1474	1481	1493	1495	1500	1502	1508	1512	1516	1522	1527	1529	1532	1534	1552

1554	1557	1559	1574	1576	1579	1581	1628	1630	1633	1635	1652	1654	1657	1659	
1676	1678	1681	1683	1699	1701	1704	1706	1757	1759	1762	1764	1783	1785	1788	
1790	1809	1811	1814	1816	1834	1836	1839	1841	1855	1857	1860	1862	1879	1885	
1889	1891	1896	1898	1979	1980	1992	1993	2006	2017	2020	2030	2038	2040	2048	
2050	2094	2095	2104	2105	2127	2128	2137	2138	2147	2148	2157	2158	2167	2168	
2177	2178	2187	2188	2206	2207	2250	2251	2261	2262	2289	2291	2297	2299	2358	
2360	2365	2367	2453	2455	2462	2464	2506	2508	2519	2521	2569	2571	2576	2578	
2636	2638	2641	2643	2667	2669	2674	2676	2745	2747	2750	2752	2780	2782	2785	
2787	2824	2826	2829	2831	2857	2859	2862	2864	2891	2901	2908	2912	2916	2918	
2922	2924	3012	3014	3020	3022	3090	3092	3097	3099	3225	3227	3234	3236	3285	
3286	3296	3297	3307	3308	3347	3348	3357	3358	3367	3368	3377	3378	3387	3388	
3397	3398	3416	3417	3457	3458	3467	3468	3502	3504	3510	3512	3745	3747	3752	
3754	3786	3875	3877	3883	3885	4018	4020	4026	4028	4149	4151	4157	4159	4279	
4281	4287	4289	4350	4352	4354	4364	4366	4374	4376	4577	4579	4581	4595	4597	
4604	4606	4706	4708	4715	4717	4827	4829	4835	4837	4951	4953	4959	4961	5020	
5040	5045	5047	5052	5054	5139	5159	5172	5174	5179	5181	5266	5286	5298	5300	
5305	5307	5392	5412	5424	5426	5432	5434	5491	5492	5512	5513	5520	5522	5528	
5530	5635	5637	5643	5645	5766	5768	5775	5777	5900	5902	5913	5915	6034	6036	
6043	6045	6148	6150	6157	6159	6240	6242	6251	6253	6387	6389	6395	6397	6484	
6504	6530	6532	6538	6540	6630	6632	6647	6649	6733	6735	6737	6747	6749	6759	
6761	6771	6808	6813	6815	6817	6827	6829	6839	6841	6851	6888	6893	6895	6897	
6907	6909	6919	6921	6931	6962	6968	6970	6972	6982	6984	6994	6996	7006	7037	
7042	7044	7054	7056	7066	7104	7110	7112	7122	7124	7134	7166	7171	7173	7175	
7186	7188	7194	7196	7250	7271	7297	7299	7310	7312	7437	7439	7441	7451	7453	
7460	7462	7554	7556	7566	7568	7665	7667	7677	7679	7768	7770	7776	7778	7779	
7782	7788	7818	7822	7823	7824	7825	7826	7828	7834	7837	7839	7843	7854	7855	
7861	7880	7889	7895	7902	7906	7910	7917	7921	9085	9091	9103	9109	9115	9119	
9123	9129	9135	9157	9158	9217	9220	9314	9336	9343	9345	9470	9472	9537	9539	
9608	9611	9686	9692	9718	9720	9850	9852	10010	10012	10123	10125	10163	10170	10176	
10181	10186	10197	10209	10211	10212	10213	10215	10216	10217	10226	10228	10237	10242	10243	
10244	10312	10359	10363	10392	10409	10429	10437	10438	10443	10459	10460	10462	10468	10484	
10516	10522	10532	10537	10544	10546	10547	10548	10551	10558	10559	10577	10593	10616	10694	
10703	10707	10709	10710	10720	10721	10722	10723	10724	10725	10726	10727	10728	10729	10730	
10743	10753	10761	10763	10769											
.IFF	33	40	50	52	53	54	62	220	223	232	258	279	724	726	764
	766	1139	1222	1223	1225	1226	1227	1266	1267	1272	1273	1274	1291	1292	1293
	1294	1295	1309	1310	1311	1312	1313	1449	1450	1455	1456	1457	1494	1495	1501
	1502	1528	1529	1533	1534	1553	1554	1558	1559	1575	1576	1580	1581	1629	1630
	1634	1635	1653	1654	1658	1659	1677	1678	1682	1683	1700	1701	1705	1706	1758
	1759	1763	1764	1784	1785	1789	1790	1810	1811	1815	1816	1835	1836	1840	1841
	1856	1857	1861	1862	1880	1886	1890	1891	1897	1898	1979	1980	1992	1993	2018
	2021	2039	2040	2049	2050	2290	2291	2298	2299	2359	2360	2366	2367	2454	2455
	2463	2464	2507	2508	2520	2521	2570	2571	2577	2578	2637	2638	2642	2643	2668
	2669	2675	2676	2746	2747	2751	2752	2781	2782	2786	2787	2825	2826	2830	2831
	2858	2859	2863	2864	2892	2902	2909	2913	2917	2918	2923	2924	3013	3014	3021
	3022	3091	3092	3098	3099	3226	3227	3235	3236	3503	3504	3511	3512	3746	3747
	3753	3754	3876	3877	3884	3885	4019	4020	4027	4028	4150	4151	4158	4159	4280
	4281	4288	4289	4351	4352	4353	4354	4365	4366	4375	4376	4578	4579	4580	4581
	4596	4597	4605	4606	4707	4708	4716	4717	4828	4829	4836	4837	4952	4953	4960
	4961	5021	5041	5046	5047	5053	5054	5140	5160	5173	5174	5180	5181	5267	5287
	5299	5300	5306	5307	5393	5413	5425	5426	5433	5434	5521	5522	5529	5530	5636
	5637	5644	5645	5767	5768	5776	5777	5901	5902	5914	5915	6035	6036	6044	6045
	6149	6150	6158	6159	6241	6242	6252	6253	6388	6389	6396	6397	6484	6504	6531
	6532	6539	6540	6631	6632	6648	6649	6734	6735	6736	6737	6748	6749	6760	6761

	6808	6814	6815	6816	6817	6828	6829	6840	6841	6888	6894	6895	6896	6897	6908
	6909	6920	6921	6962	6969	6970	6971	6972	6983	6984	6995	6996	7037	7043	7044
	7055	7056	7105	7111	7112	7123	7124	7167	7172	7173	7174	7175	7187	7188	7195
	7196	7298	7299	7311	7312	7438	7439	7440	7441	7452	7453	7461	7462	7555	7556
	7567	7568	7666	7667	7678	7679	7769	7770	7777	7778	7779	7819	7825	7829	7835
	7838	7855	7862	7881	9158	9159	9218	9221	9315	9337	9344	9346	9471	9473	9538
	9540	9609	9612	9687	9693	9719	9721	9851	9853	10011	10013	10124	10126	10171	10210
	10213	10214	10217	10243	10245	10313	10360	10410	10429	10430	10438	10443	10459	10463	10517
.IFT	10522	10537	10558	10559	10560	10577	10593	10617	10695	10704	10731	10763			
	1168	1172	1176	1180	1184	1188	1192	1196	1200	1204	1213	1248	1252	1322	1326
	1330	1334	1338	1366	1370	1374	1378	1382	1413	1420	1426	1467	1475	1482	1509
	1513	1517	1523	3787	5139	5159	5266	5286	5392	5412	6484	6504	6771	6808	6851
	6888	6931	6962	7006	7037	7066	7104	7134	7166	7783	7789	7890	7896	7903	7907
	7911	7918	7922	9086	9092	9104	9110	9116	9120	9124	9130	9136	10164	10225	10416
.IFTF	10489	10509	10516	10547											
	1168	1172	1176	1180	1184	1188	1192	1196	1200	1204	1213	1248	1252	1322	1326
	1330	1334	1338	1366	1370	1374	1378	1382	1413	1420	1426	1467	1475	1482	1509
	1513	1517	1523	3787	7783	7789	7890	7896	7903	7907	7911	7918	7922	9086	9092
.IIF	9104	9110	9116	9120	9124	9130	9136	10164	10223	10410	10485	10493	10515	10546	
	1	6	11	47	48	49	50	53	54	55	56	170	278	1140	1142
	1148	1149	1151	1152	1477	1484	1978	1991	7823	7829	7830	7841	7855	7859	10177
	10178	10179	10180	10181	10182	10224	10225	10240	10243	10244	10359	10363	10368	10452	10460
	10461	10516	10523	10524	10525	10526	10527	10559	10575	10600	10604	10719	10720	10721	10722
.IRP	10723	10724	10725	10726	10727	10728	10729								
	1134	1221	1265	1290	1308	1448	1493	1527	1552	1574	1628	1652	1676	1699	1757
	1783	1809	1834	1855	1889	2038	2289	2358	2453	2506	2569	2636	2667	2745	2780
	2824	2857	2916	3012	3090	3225	3502	3745	3875	4018	4149	4279	4350	4364	4577
	4595	4706	4827	4951	5045	5172	5298	5424	5520	5635	5766	5900	6034	6148	6240
	6387	6530	6630	6733	6747	6813	6827	6893	6907	6968	6982	7042	7110	7171	7186
	7297	7437	7451	7554	7665	7768	7942	7951	8036	8048	8223	8232	8296	8329	8409
	8411	8466	8487	8514	8674	8692	8711	8733	8822	8962	8985	9026	9040	9065	9225
	9306	9364	9460	9484	9529	9552	9597	9627	9671	9727	9841	9878	9999	10044	10114
.LIST	10134	10154	10186	10258	10298	10479	10505	10532	10737	10753					
	1	13	24	41	53	162	170	258	260	261	262	263	264	265	266
	267	268	269	270	271	272	1134	1154	1168	1172	1176	1180	1184	1188	1192
	1196	1200	1204	1213	1221	1226	1228	1248	1252	1265	1273	1275	1290	1294	1296
	1308	1312	1322	1326	1330	1334	1338	1366	1370	1374	1378	1382	1413	1420	1426
	1448	1456	1467	1475	1482	1493	1502	1509	1513	1517	1523	1527	1534	1536	1552
	1559	1561	1574	1581	1583	1628	1635	1637	1652	1659	1661	1676	1683	1685	1699
	1706	1707	1757	1764	1767	1783	1790	1793	1809	1816	1818	1834	1841	1843	1855
	1862	1864	1889	1898	1899	2005	2034	2038	2050	2289	2299	2300	2358	2367	2368
	2453	2464	2465	2506	2521	2522	2563	2578	2636	2643	2667	2676	2677	2745	2752
	2753	2780	2787	2788	2824	2831	2832	2857	2864	2866	2916	2924	2926	3012	3022
	3023	3090	3099	3225	3236	3237	3502	3512	3513	3745	3754	3755	3787	3875	3885
	3886	4018	4028	4029	4149	4159	4160	4279	4289	4290	4350	4354	4364	4376	4377
	4577	4581	4595	4606	4607	4706	4717	4718	4827	4837	4838	4951	4961	4962	5045
	5054	5055	5172	5181	5182	5298	5307	5308	5424	5434	5435	5439	5520	5530	5532
	5635	5645	5652	5766	5777	5785	5900	5915	5917	6034	6045	6047	6148	6159	6161
	6240	6253	6262	6387	6397	6401	6530	6540	6545	6630	6649	6651	6733	6737	6747
	6761	6764	6813	6817	6827	6841	6844	6893	6897	6907	6921	6924	6968	6972	6982
	6996	6999	7042	7056	7059	7110	7124	7127	7171	7175	7186	7196	7198	7297	7312
	7313	7437	7441	7451	7462	7463	7554	7568	7569	7665	7679	7680	7768	7778	7783
	7789	7890	7896	7903	7907	7911	7918	7922	9086	9092	9104	9110	9116	9120	9124
	9130	9136	10164	10181	10429	10709	10710	10719	10720	10721	10722	10723	10724	10725	10726

.MACRO	10727 41 1782 2823 4826 6629	10728 54 1808 2856 4950 6747	10729 222 1833 2915 5044 6906	10730 1221 1854 3011 5171 7185	1264 1888 3089 5297 7296	1447 2036 3224 5423 7450	1493 2288 3501 5519 7553	1527 2357 3744 5632 7664	1551 2452 3874 5765 7768	1573 2505 4017 5898 10710	1627 2568 4148 6033	1651 2635 4278 6147	1675 2666 4363 6239	1698 2744 4594 6386	1756 2779 4705 6529
.MCALL	1 1	162 13	24	41	53	162	170	258	260	261	262	263	264	265	266
.NLIST	267 1196 1308 1448 1559 1706 1862 2453 2753 3023 3886 4577 5054 5635 6240 6761 6996 7313 7789 9130	268 1200 1312 1456 1561 1707 1864 2464 2780 3090 4018 4581 5055 5645 6253 6764 6999 7437 7890 9136	269 1204 1322 1467 1574 1757 1889 2465 2787 3099 4028 4595 5172 5652 6262 6813 7042 7441 7896 10164	270 1213 1326 1475 1581 1764 1898 2506 2788 3225 4029 4606 5181 5766 6387 6817 7056 7451 7903 10181	271 1221 1330 1482 1583 1767 1899 2521 2824 3236 4149 4607 5182 5777 6397 6827 7059 7462 7907 10429	272 1226 1334 1493 1628 1783 2005 2522 2831 3237 4159 4706 5298 5785 6401 6841 7110 7463 7911 10709	277 1134 1228 1338 1502 1635 1790 2034 2569 2832 3502 4160 4717 5307 5900 6530 6844 7124 7554 7918 10710	258 1154 1248 1366 1509 1637 1793 2038 2578 2857 3512 4279 4718 5308 5915 6540 6893 7127 7568 7922 10719	260 1168 1252 1370 1513 1652 1809 2050 2636 2864 3513 4289 4827 5424 5917 6545 6897 7171 7569 9086 10720	261 1172 1265 1374 1517 1659 1816 2050 2643 2866 3513 4290 4837 5434 5917 6545 6907 7175 7569 9092 10721	262 1176 1273 1378 1523 1661 1818 2299 2667 2866 3754 4290 4838 5435 5921 6649 6921 7186 7679 9104 10722	263 1180 1275 1382 1527 1676 1834 2300 2676 2924 3755 4354 4951 5439 6047 6651 6924 7196 7680 9110 10723	264 1184 1290 1413 1534 1683 1841 2358 2677 2926 3787 4364 4961 5520 6148 6733 6968 7198 7768 9116 10724	265 1188 1294 1420 1536 1685 1843 2367 2745 3012 3875 4376 4962 5530 6159 6737 6972 7297 7778 9120 10725	266 1192 1296 1426 1552 1699 1855 2368 2752 3022 3885 4377 5045 5532 6161 6747 6982 7312 7783 9124 10726
.PAGE	10727 57	10728 163	10729 183	10730 222	278	8052	9211	10244	10312	10359	10462	10516	10559	10616	10694
.REPT	10730 14	20	170	260	266	2006	2030								
.SBTTL	41 1290 1834 2824 4577 6034 7110 8019 10314	43 1308 1855 2857 4592 6148 7171 8052 10361	58 1448 1889 2889 4595 6240 7186 8069 10464	164 1493 2038 2916 4706 6387 7297 8082 10518	171 1525 2289 3012 4827 6530 7437 8212 10561	184 1527 2358 3090 4951 6630 7451 8240 10618	224 1552 2450 3225 5045 6733 7554 8284 10696	280 1574 2453 3502 5172 6747 7665 8380 10711	964 1628 2506 3745 5298 6813 7768 8698 10732	1122 1652 2569 3875 5424 6827 7813 8737	1123 1676 2636 4018 5520 6893 7814 9077	1124 1699 2667 4149 5635 6907 7815 9156	1126 1757 2742 4279 5766 6968 7820 10168	1221 1783 2745 4350 5896 6982 7930 10172	1265 1809 2780 4364 5900 7042 7960 10246
.TITLE	1 170	235 263	238 264	239 265	240 266	241 267	244 268	245 269	246 270	247 271	248 1042	249 1043	258 1045	260 1047	261 1048
.WORD	262 1104 1798 4684 5663 5743 6056 6552 9254 10586 11527	1280 1799 4794 5673 5802 6163 7201 9255 10591 11528	1281 1823 4918 5674 5803 6164 7202 9299 10693 11531	1541 1824 4922 5675 5804 6165 7203 9300 10762 11534	1542 1848 5442 5680 5822 6279 7316 9303 10764 11538	1566 1849 5443 5681 5823 6280 7317 9304 11500 11541	1567 1869 5444 5682 5824 6281 7318 9724 11502 11544	1642 1870 5534 5692 5867 6299 7834 9745 11504 11548	1643 2537 5535 5693 5868 6300 7837 9837 11507 11551	1666 2696 5536 5694 5869 6301 8691 9838 11510 11554	1667 2994 5539 5729 6049 6358 9222 10363 11513 11557	1690 3115 5540 5730 6050 6359 9244 10364 11515 11560	1691 3118 5541 5731 6051 6360 9251 10365 11518 11565	1772 3183 5661 5741 6054 6550 9252 10512 11522	1773 3186 5662 5742 6055 6551 9253 10515 11525

ERRORS DETECTED: 0

*DZRPSD,DZRPSD/SOL/CRF+DZRPSD
RUN-TIME: 98 110 16 SECONDS
CORE USED: 23K

