

DH11

MEMORY TEST
MD-11-DZDHB-B

EP-DZDHB-B-DL-A
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IDENTIFICATION

PRODUCT CODE: MAINDEC-11-DZDMB-B-D
PRODUCT NAME: DH11 MEMORY TEST
DATE: JUNE 1976
MAINTAINER: DIAGNOSTIC GROUP
AUTHOR: MICHAEL DAVIS

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DZDHB.PFC

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1. ABSTRACT

THE D411 MEMORY TEST IS A TEST OF THE BYTE COUNT AND
BUS ADDRESS MEMORIES OF THE D411. EACH MEMORY IS
TESTED FOR ADDRESSABILITY AND DATA READ/WRITE
CAPABILITY

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4.3 (CONT'D)

4.3.1.6 TYPE IN THE ADDRESS OF THE RECEIVER INTERRUPT VECTOR FOR THE DH11 TO BE TESTED FOLLOWED BY <CARRIAGE RETURN>

NOTE: WORDS IN ANGLE BRACKETS, I.E. <CARRIAGE RETURN> MEAN THAT THE TELETYPE KEY WITH THE NAMED FUNCTION SHOULD BE STRUCK

IF AN INCORRECT ADDRESS IS ENTERED, THE PROGRAM WILL TYPE "?" AND WILL REPEAT THE SECOND MESSAGE OF 4.3.1.5

4.3.1.7 THE PROGRAM WILL TYPE "CONTROL REGISTER ADDRESS-" AND WAIT FOR AN INPUT FROM THE TELETYPE KEYBOARD

4.3.1.8 TYPE IN THE ADDRESS OF THE SYSTEM CONTROL REGISTER OF THE DH11 TO BE TESTED FOLLOWED BY <CARRIAGE RETURN>

IF AN INCORRECT ADDRESS IS TYPED, THE PROGRAM WILL TYPE "?" AND WILL THEN REPEAT THE MESSAGE OF 4.3.1.7

4.3.1.9 THE PROGRAM WILL TYPE "R" TO INDICATE THAT IT IS ABOUT TO START TESTING, AND THEN TESTING WILL BEGIN

4.3.2 PROGRAM RESTART WITH ALL SWITCHES DOWN

4.3.2.1 PERFORM 4.3.1.2 TO 4.3.1.5

4.3.2.2 THE PROGRAM WILL TYPE "DH11 MEMORY TEST" AND WILL THEN CONTINUE AS DESCRIBED IN 4.3.1.9

4.3.3 PROGRAM RESTART WITH SW00=1

4.3.3.1 LOAD ADDRESS 000200

4.3.3.2 SET SW01=1

4.3.3.3 PRESS START

4.3.3.4 THE PROGRAM WILL PERFORM AS DESCRIBED IN 4.3.1.5 TO 4.3.1.9

4.3.4 PROGRAM RESTART WITH SW01=1

4.3.4.1 LOAD ADDRESS 000200

4.3.4.2 SET SW01=1

4.3.4.3 PRESS START

4.3.4.4 THE PROGRAM WILL TYPE "DH11 MEMORY TEST" AND WILL THEN TYPE "TEST PC-" AND WILL WAIT FOR AN INPUT FROM THE TELETYPE KEYBOARD

4.3.4.5 TYPE IN THE ADDRESS OF THE TEST AT WHICH THE PROGRAM IS TO BE STARTED FOLLOWED BY <CARRIAGE RETURN>

4.3.4.6 THE PROGRAM WILL TYPE R TO INDICATE THAT IT HAS STARTED AND WILL START TESTING AT THE SELECTED TEST.

NOTE: CARE MUST BE TAKEN WHEN THIS FEATURE IS USED, SINCE THERE IS NO PROTECTION AGAINST SELECTING AN ADDRESS THAT IS IN THE MIDDLE OF A TEST

NOTE: IF IT IS DESIRED TO LOOP ON THE TEST THAT IS SELECTED SET SW14=1 BEFORE ENTERING THE TEST ADDRESS

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5. OPERATING PROCEDURE

5.1 OPERATIONAL SWITCH SETTINGS

SW15=1, HALT ON ERROR
SW14=1, LOOP ON CURRENT TEST
SW13=1, SUPPRESS ERROR TYPEOUT
SW11=1, INHIBIT ITERATIONS
SW10=1, ESCAPE TO NEXT TEST ON ERROR
SW09=1, FREEZE VARIABLE PARAMETER IN CURRENT TEST
SW01=1, START PROGRAM AT SELECTED TEST
SW00=1, CHANGE PARAMETERS AT PROGRAM RESTART

5.2 SUBROUTINE ABSTRACTS

5.2.1 TRAPCATCHER (LOCATIONS 000000-000776)

THIS ROUTINE IS USED TO INTERCEPT UNEXPECTED INTERRUPTS AND TRAPS. THE AREA FROM 000000-000776 IS LOADED WITH THE FOLLOWING SEQUENCE

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2
0
4
0
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772
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776
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IF AN UNEXPECTED INTERRUPT OR TRAP OCCURS, THE PROGRAM WILL HALT WITH THE PC 2 GREATER THAN THE ADDRESS TO WHICH THE PROGRAM TRAPPED. THE PROCESSOR STACK MAY BE EXAMINED TO DETERMINE WHERE THE PROGRAM WAS WHEN THE TRAP OR INTERRUPT OCCURED.

5.2.2 START (PROGRAM INITIALIZATION)

THIS ROUTINE INITIALIZES ALL PROGRAM FLAGS AND COUNTERS, TYPES THE PROGRAM TITLE MESSAGE, AND INPUTS THE VECTOR AND CONTROL REGISTER ADDRESSES OF THE DHI1 TO BE TESTED.

5.2.3 BEGIN (PROGRAM START AND RESTART)

THIS ROUTINE IS ENTERED IMMEDIATELY AFTER "START" AND EACH TIME A PROGRAM PASS HAS BEEN COMPLETED. THE ROUTINE SETS UP THE PROCESSOR STACK AND STATUS WORD AND THEN TRANSFERS CONTROL TO THE TEST AT WHICH TESTING WILL BEGIN. IF SW01=0 WHEN THIS ROUTINE IS ENTERED TESTING WILL START AT T1 (TEST 1). IF SW01=1 WHEN THIS ROUTINE IS ENTERED, TESTING WILL START AT THE PC ENTERED FROM THE TELETYPE KEYBOARD.

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5.2.4 EOP (END OF PASS)

THIS ROUTINE IS ENTERED ONCE PER PASS AFTER ALL TESTS HAVE BEEN COMPLETED. THIS ROUTINE TYPES THE MAINDEC IDENTIFICATION CODE OF THE PROGRAM, CLEARS ERROR FLAGS AND UPDATES THE PASS COUNT. IF THE PROGRAM WAS LOADED UNDER ACT11 OR DDP, THE ROUTINE CHECKS FOR RETURN TO THE ACT11 OR DDP MONITOR. IF THE PROGRAM IS NOT UNDER MONITOR CONTROL, THE ROUTINE TRANSFERS TO BEGIN.

5.2.5 SCOPER (SCOPE LOOP AND ITERATION HANDLER)

THIS ROUTINE IS ENTERED EACH TIME A TEST IS COMPLETED. THE ROUTINE CHECKS FOR THE FOLLOWING UPON ENTRY
A) IF SW10=1, THE ROUTINE WILL TRANSFER TO THE NEXT TEST IN SEQUENCE, AFTER CLEARING ERROR FLAGS.
B) IF SW11=1, THE ROUTINE WILL TRANSFER TO THE NEXT TEST SEQUENCE, AFTER CLEARING ERROR FLAGS.
C) IF SW14=1, THE ROUTINE WILL LOOP ON THE CURRENT TEST REGARDLESS OF THE ITERATION COUNT.

IF NONE OF THE ABOVE IS TRUE, THE ROUTINE WILL ADD 1 TO THE COUNT OF TEST ITERATIONS, AND COMPARE THIS VALUE TO THE NUMBER OF ITERATIONS THAT SHOULD BE PERFORMED. IF THESE NUMBERS ARE EQUAL, THE ROUTINE WILL TRANSFER TO THE NEXT TEST IN SEQUENCE. IF THE NUMBERS ARE NOT EQUAL, THE TEST CURRENTLY IN PROGRESS WILL BE REPEATED.

5.2.6 SCOP1R (FREEZE ON CURRENT DATA)

THE CALL TO THIS ROUTINE FOLLOWS IMMEDIATELY AFTER THE CALL TO THE ERROR HANDLER IN THOSE TESTS THAT HAVE VARIABLE PARAMETERS. THIS ROUTINE IS ALWAYS ENTERED IN THOSE TESTS, WHETHER OR NOT AN ERROR OCCURS. IF S-09=1, THE ROUTINE WILL TRANSFER CONTROL BACK TO THE TEST AT A POINT WHICH WILL ALLOW REPEATING THE FUNCTION UNDER TEST CONTINUOUSLY WITH THE SAME DATA. IF THIS OPTION IS SELECTED, THE ROUTINE "SCOPER" IS NEVER ENTERED AND ITERATION COUNTS WILL NOT BE UPDATED.

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5.2.7 ERRORS (ERROR HANDLER)

THIS ROUTINE IS ENTERED UPON ERROR DETECTION ONLY. WITH ALL CONSOLE SWITCHES DOWN, THE ROUTINE PROCEEDS AS FOLLOWS:

- A) THE PC OF THE INSTRUCTION THAT CALLED THE ERROR HANDLER IS ACCESSED THRU THE STACK, AND THEN THE EMT INSTRUCTION ITSELF IS FETCHED. THE 8 LSB OF THE EMT INSTRUCTION ARE THE ERROR CODE. THIS CODE IS USED TO ACCESS A TABLE OF ERROR MESSAGES AND ERROR DATA STORAGE LOCATIONS.
- B) IF THE TEST THAT FAILED DID NOT FAIL PREVIOUSLY DURING THIS PASS, A COMPLETE ERROR REPORT IS MADE. IF THE TEST THAT FAILED FAILED MOR THAT ONCE DURING THE CURRENT PASS, ONLY THE DATA RELATING TO THE FAILUER IS TYPED. IF SW13=1, NO FOR TYPEOUT IS MADE.
- C) THE ROUTINE NOW CHECKS FOR FALT ON ERROR. IF SW15=1 THE PROGRAM WILL HALT WITH THE PC OF THE CALL TO THE ERROR ROUTINE IN RD. IF SW15=0, THE PROGRAM WILL NOT HALT, BUT WILL CHECK FOR ESCAPE TO NEXT TEST.
- D) IF SW10=0, THE ROUTINE WILL RETURN TO THE TEST IN PROGRESS. IF SW10=1, THE ROUTINE WILL ABORT THE CURRENT TEST, AND TRANSFER TO THE NEXT TEST IN SEQUENCE, THRU THE ROUTINE "SCOPE".

5.2.8 TRPSRV (TRAP DECODE AND DISPATCH)

THIS ROUTINE DECODES THE 8 LSB OF THE TRAP INSTRUCTION THAT CAUSED TH PROGRAM INTERRUPT, AND TRANSFERS CONTROL TO THE ROUTINE THRU THE TABLE "TRPT38" USING THE 8 LSB OF THE TRAP INSTRUCTION AS AN OFFSET TO THE POINTER TO THE ROUTINE TO BE ENTERED.

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- 5.3 PROGRAM AND OR OPERATOR ACTION
- 5.3.1 PROGRAM START WITH ALL SWITCHES DOWN
 - 5.3.1.1 REFER TO SECTIONS 4.3.1 AND 4.3.2 FOR INITIAL PROGRAM BEHAVIOR.
 - 5.3.1.2 AFTER "R" HAS BEEN TYPED BY THE PROGRAM, TEST EXECUTION WILL BEGIN. EACH TEST WILL BE REPEATED A SELECTED NUMBER OF ITERATIONS (SEE LISTING FOR EXACT NUMBER FOR EACH TEST) AND THEN THE PROGRAM WILL PROCEED TO THE NEXT TEST.
 - 5.3.1.3 WHEN ALL ITERATIONS HAVE BEEN COMPLETED, THE PROGRAM WILL TYPE "DZDHB" AND THEN RESTART TESTING AT TEST 1 (LOCATION T1 IN THE PROGRAM).
 - 5.3.1.4 IF AN ERROR OCCURS, THE PROGRAM WILL TYPE AN APPROPRIATE ERROR MESSAGE, AND THEN CONTINUE THE TEST IN PROGRESS.
- 5.3.2 PROGRAM START WITH SW00=1
THE PROGRAM WILL PERFORM AS DESCRIBED IN 4.3.1 AND 5.3.1
- 5.3.3 PROGRAM START WITH SW01=1
 - 5.3.3.1 REFER TO SECTION 4.3.4 FOR INITIAL PROGRAM BEHAVIOR
 - 5.3.3.2 TEST EXECUTION WILL START AT THE ADDRESS SPECIFIED AND WILL CONTINUE AS DESCRIBED IN 5.3.1.2
 - 5.3.3.3 AFTER "DZDHB" HAS BEEN TYPED, THE PROGRAM WILL RESUME TESTING AT TEST 1
- 5.3.4 PROGRAM OPERATION WITH SW15=1
SAME AS 5.3.1, EXCEPT THAT IN THE CASE OF AN ERROR, THE PROGRAM WILL HALT AFTER THE ERROR TYPEOUT, AND THE PC+2 OF THE CALL TO THE ERROR ROUTINE WILL BE DISPLAYED IN RO.
- 5.3.5 PROGRAM OPERATION WITH SW13=1
SAME AS 5.3.1 EXCEPT THAT NO ERROR TYPEOUTS WILL OCCUR
- 5.3.6 PROGRAM OPERATION WITH SW11=1
SAME AS 5.3.1 EXCEPT THAT EACH TEST WILL BE REPEATED ONCE ONLY
- 5.3.7 PROGRAM OPERATION WITH SW10=1
SAME AS 5.3.1, EXCEPT THAT IN THE CASE OF AN ERROR THE CURRENT TEST WILL BE ABORTED, AND THE PROGRAM WILL PROCEED TO THE NEXT TEST IN SEQUENCE.

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5. (CONT'D)

5.3.8 PROGRAM OPERATION WITH SW14=1, OR SW09=1

THESE FUNCTIONS ARE NORMALLY USED FOR TROUBLE SHOOTING.
SEE SECTION 6.3 FOR THEIR USE.

6. ERRORS

6.1 ERROR HALTS

THE ERROR MESSAGE FORMAT FOR ALL ERROR TYPEOUTS
IS AS FOLLOWS

PC+2 MESSAGE
HEADER (IF APPLICABLE)
DATA (IF APPLICABLE)

WHERE
PC+2 IS THE ADDRESS OF THE CALL TO THE ERROR HANDLER + 2
MESSAGE IS AN ASCII MESSAGE DESCRIBING (BRIEFLY) THE FAILURE
HEADER IS A DESCRIPTION OF THE DATA TO FOLLOW
DATA IS OCTAL INFORMATION RELATING TO THE CAUSE OF THE FAILURE
IF THE SAME ERROR OCCURS IN A GIVEN TEST ON THE SAME
PASS, AND IF DATA IS ASSOCIATED WITH THAT ERROR, ONLY
DATA IS TYPE ON SUCCEEDING ERROR TYPEOUTS

IF NO DATA IS ASSOCIATED WITH THE ERROR
THE COMPLETE ERROR MESSAGE IS TYPED.

6.1.1 ERROR DESCRIPTIONS

SEE LISTING FOR DETAILS OF ERRORS

6.2 ERROR RECOVERY

6.2.1 SW15=0

IF THE PROGRAM IS RUN WITH SW15=0, NO OPERATOR ACTION IS
REQUIRED TO CONTINUE TESTING

6.2.2 SW15=1

IF THE PROGRAM IS RUN WITH SW15=1, TO CONTINUE TESTING
AFTER THE PROGRAM HAS HALTED, PRESS THE PROCESSOR
CONSOLE CONTINUE SWITCH

6.3 SCOPE LOOPING

6.3.1 TO SCOPE ON A SPECIFIC TEST, SET SW14=1 AND SW13=1
THIS WILL CAUSE THE PROGRAM TO CONTINUOUSLY LOOP ON THE
SAME TEST, AND WILL CAUSE ALL ERROR TYPEOUTS TO BE INHIBITED

6.3.2 TO SCOPE ON A SPECIFIC VALUE OF A PARAMETER WITHIN
A TEST, SET SW09=1 TO FREEZE THE DATA
(SEE LISTING FOR THOSE TESTS THAT INCORPORATE THIS FEATURE)

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9. PROGRAM DESCRIPTION

THE PROGRAM FIRST TESTS THE BUS ADDRESS AND BYTE COUNT MEMORIES FOR ADDRESSABILITY. THE TEST IS PERFORMED IN THE FOLLOWING MANNER:

A) EACH LOCATION OF THE MEMORY TO BE TESTED IS LOADED WITH ITS ADDRESS, DUPLICATED EVERY 4 BITS. THE BINARY CONTENTS OF EACH LOCATION IS SHOWN BELOW

LOCATION	CONTENTS			
00	0000	0000	0000	0000
01	0001	0001	0001	0001
02	0010	0010	0010	0010
03	0011	0011	0011	0011
...
16	1110	1110	1110	1110
17	1111	1111	1111	1111

THE ABOVE PATTERN WAS CHOSEN SINCE THE MEMORY IS COMPOSED OF FOUR (4) CHIPS EACH HAVING A CAPACITY OF 16 WORDS BY FOUR (4) BITS. IF ANY OF THE FOUR CHIPS IS ADDRESSED INCORRECTLY, THE CONTENTS OF THAT CHIP WILL BE INCORRECT AND WILL INDICATION WHAT LOCATION WAS ACTUALLY ADDRESSED.

AFTER THE ABOVE TESTS HAVE BEEN COMPLETED, EACH LOCATION IN BOTH THE BUS ADDRESS AND BYTE COUNT MEMORIES ARE TESTED TO VERIFY THAT ALL BITS CAN BE SET TO 1S AND CLEARED TO 0S

THE NEXT GROUP OF TESTS VERIFY THAT A SELECTED ADDRESS IN EITHER THE BYTE COUNT OR BUS ADDRESS MEMORY CAN BE SET TO A SELECTED VALUE WITHOUT CHANGING THE CONTENTS OF ANY OTHER LOCATION IN THAT MEMORY.

THE NEXT GROUP OF TESTS SETS ALL LOCATIONS IN EITHER THE BYTE COUNT OR BUS ADDRESS MEMORY TO 1S, CLEARS A SELECTED LOCATION TO 0S, AND VERIFY THAT ONLY THE SELECTED LOCATION WAS AFFECTED.

THE FINAL GROUP OF TESTS VERIFIES THAT THE MEMORY EXTENTION BITS OF THE BUS ADDRESS MEMORY CAN BE SET AND CLEARED.

10. LISTING

↑

;DH11 MEMORY TEST
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;STARTING PROCEDURE
;LOAD PROGRAM
;LOAD ADDRESS 000200

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:PRESS START  
:PROGRAM WILL TYPE DH11 MEMORY TEST  
:PROGRAM WILL TYPE "VECTOR ADDRESS-"  
:TYPE IN THE ADDRESS OF THE RECEIVER INTERRUPT VECTOR  
:FOR THE DH11 TO BE TESTED, FOLLOWED BY <CARRIAGE RETURN>  
:PROGRAM WILL TYPE "CONTROL REGISTER ADDRESS-"  
:TYPE IN THE ADDRESS OF THE SYSTEM CONTROL REGISTER  
:FOR THE DH11 TO BE TESTED, FOLLOWED BY <CARRIAGE RETURN>  
:PROGRAM WILL TYPE "R" TO INDICATE THAT TESTING HAS STARTED  
:AT THE END OF A PASS, PROGRAM WILL TYPE " DZD48 "  
:AND THEN RESUM TESTING
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; SWITCH REGISTER OPTIONS

100000
040000
020000
010000
004000
002000
001000
000400
000100
000040
000020
000010
000004
000002
000001

SW15=100000
SW14=40000
SW13=20000
SW12=10000
SW11=4000
SW10=2000
SW09=1000
SW08=400
SW06=100
SW05=40
SW04=20
SW03=10
SW02=4
SW01=2
SW00=1

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:=1, HALT ON ERROR  
:=1, LOOP ON CURRENT TEST  
:=1, INHIBIT ERROR TYPEOUT  
  
:=1, INHIBIT ITERATIONS  
:=1, ESCAPE TO NEXT TEST ON ERROR  
:=1, LOOP WITH CURRENT DATA
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:RESTART PROGRAM AT SELECTED TEST  
:RESELECT VECTOR AND CONTROL REGISTER  
:ADDRESS AFTER PROGRAM RESTART
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000000
 000001
 000002
 000003
 000004
 000005
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 000007

 177570
 177570
 177776
 012606

 005746
 005726
 010046
 012600
 024646
 022626

 100000
 040000
 020000
 010000
 004000
 002000
 001000
 000400
 000200
 000100
 000040
 000020
 000010
 000004
 000002
 000001

;REGISTER DEFINITIONS

RD=%0 ; GENERAL REGISTER
 R1=%1 ; GENERAL REGISTER
 R2=%2 ; GENERAL REGISTER
 R3=%3 ; GENERAL REGISTER
 R4=%4 ; GENERAL REGISTER
 R5=%5 ; GENERAL REGISTER
 SP=%6 ; PROCESSOR STACK POINTER
 PC=%7 ; PROGRAM COUNTER

;LOCATION EQUIVALENCIES

SWR=177570 ; CONSOLE SWITCH REGISTER
 LIGHTS=177570 ; PDP-11/45 DISPLAY REGISTER
 PS=177776 ; PROCESSOR STATUS WORD
 STACK=ENDC00+200 ; START OF PROCESSOR STACK

;INSTRUCTION DEFINITIONS

PUSH1SP=5746 ; DECREMENT PROCESSOR STACK 1 WORD
 POP1SP=5726 ; INCREMENT PROCESSOR STACK 1 WORD
 PUSHRO=10046 ; SAVE R0 ON STACK
 POPRO=12600 ; RESTORE R0 FROM STACK
 PUSH2SP=24646 ; DECREMENT STACK TWICE
 POP2SP=22626 ; INCREMENT STACK TWICE
 .EQUIV EMT,HLT ; BASIC DEFINITION OF ERROR CALL

BIT15=100000
 BIT14=40000
 BIT13=20000
 BIT12=10000
 BIT11=4000
 BIT10=2000
 BIT09=1000
 BIT08=400
 BIT07=200
 BIT06=100
 BIT05=40
 BIT04=20
 BIT03=10
 BIT02=4
 BIT01=2
 BIT00=1

```

573 ;TRAPCATCAER FOR ILLEGAL INTERRUPTS
574 .=0
575 000000 000002 .+2 :UNEXPECTED TRAP TO THIS LOCATION
576 000002 000000 HALT :EXAMINE STACK TO FIND CAUSE
577 000004 000006 .+2 :UNEXPECTED TRAP TO THIS LOCATION
578 000006 000000 HALT :EXAMINE STACK TO FIND CAUSE
579 000010 000012 .+2 :UNEXPECTED TRAP TO THIS LOCATION
580 000012 000000 HALT :EXAMINE STACK TO FIND CAUSE
581 000014 000016 .+2 :UNEXPECTED TRAP TO THIS LOCATION
582 000016 000000 HALT :EXAMINE STACK TO FIND CAUSE
583 000020 000022 .+2 :UNEXPECTED TRAP TO THIS LOCATION
584 000022 000000 HALT :EXAMINE STACK TO FIND CAUSE
585 000024 000026 .+2 :UNEXPECTED TRAP TO THIS LOCATION
586 000026 000000 HALT :EXAMINE STACK TO FIND CAUSE
587 000030 000032 .+2 :UNEXPECTED TRAP TO THIS LOCATION
588 000032 000000 HALT :EXAMINE STACK TO FIND CAUSE
589 000034 000036 .+2 :UNEXPECTED TRAP TO THIS LOCATION
590 000036 000000 HALT :EXAMINE STACK TO FIND CAUSE
591 000040 000042 .+2 :UNEXPECTED TRAP TO THIS LOCATION
592 000042 000000 HALT :EXAMINE STACK TO FIND CAUSE
593 000044 000046 .+2 :UNEXPECTED TRAP TO THIS LOCATION
594 000046 000000 HALT :EXAMINE STACK TO FIND CAUSE
595 000050 000052 .+2 :UNEXPECTED TRAP TO THIS LOCATION
596 000052 000000 HALT :EXAMINE STACK TO FIND CAUSE
597 000054 000056 .+2 :UNEXPECTED TRAP TO THIS LOCATION
598 000056 000000 HALT :EXAMINE STACK TO FIND CAUSE
599 000060 000062 .+2 :UNEXPECTED TRAP TO THIS LOCATION
600 000062 000000 HALT :EXAMINE STACK TO FIND CAUSE
601 000064 000066 .+2 :UNEXPECTED TRAP TO THIS LOCATION
602 000066 000000 HALT :EXAMINE STACK TO FIND CAUSE
603 000070 000072 .+2 :UNEXPECTED TRAP TO THIS LOCATION
604 000072 000000 HALT :EXAMINE STACK TO FIND CAUSE
605 000074 000076 .+2 :UNEXPECTED TRAP TO THIS LOCATION
606 000076 000000 HALT :EXAMINE STACK TO FIND CAUSE
607 000080 000082 .+2 :UNEXPECTED TRAP TO THIS LOCATION
608 000082 000000 HALT :EXAMINE STACK TO FIND CAUSE
609 000100 000102 .+2 :UNEXPECTED TRAP TO THIS LOCATION
610 000102 000000 HALT :EXAMINE STACK TO FIND CAUSE
611 000104 000106 .+2 :UNEXPECTED TRAP TO THIS LOCATION
612 000106 000000 HALT :EXAMINE STACK TO FIND CAUSE
613 000110 000112 .+2 :UNEXPECTED TRAP TO THIS LOCATION
614 000112 000000 HALT :EXAMINE STACK TO FIND CAUSE
615 000114 000116 .+2 :UNEXPECTED TRAP TO THIS LOCATION
616 000116 000000 HALT :EXAMINE STACK TO FIND CAUSE
617 000120 000122 .+2 :UNEXPECTED TRAP TO THIS LOCATION
618 000122 000000 HALT :EXAMINE STACK TO FIND CAUSE
619 000124 000126 .+2 :UNEXPECTED TRAP TO THIS LOCATION
620 000126 000000 HALT :EXAMINE STACK TO FIND CAUSE
621 000130 000132 .+2 :UNEXPECTED TRAP TO THIS LOCATION
622 000132 000000 HALT :EXAMINE STACK TO FIND CAUSE
623 000134 000136 .+2 :UNEXPECTED TRAP TO THIS LOCATION
624 000136 000000 HALT :EXAMINE STACK TO FIND CAUSE
625 000140 000142 .+2 :UNEXPECTED TRAP TO THIS LOCATION
626 000142 000000 HALT :EXAMINE STACK TO FIND CAUSE
627 000144 000146 .+2 :UNEXPECTED TRAP TO THIS LOCATION
628 000146 000000 HALT :EXAMINE STACK TO FIND CAUSE
629 000150 000152 .+2 :UNEXPECTED TRAP TO THIS LOCATION
630 000152 000000 HALT :EXAMINE STACK TO FIND CAUSE

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685	000334	000336	.+2	UNEXPECTED TRAP TO THIS LOCATION
686	000336	000000	HALT	EXAMINE STACK TO FIND CAUSE
687	000340	000342	.+2	UNEXPECTED TRAP TO THIS LOCATION
688	000342	000000	HALT	EXAMINE STACK TO FIND CAUSE
689	000344	000346	.+2	UNEXPECTED TRAP TO THIS LOCATION
690	000346	000000	HALT	EXAMINE STACK TO FIND CAUSE
691	000350	000352	.+2	UNEXPECTED TRAP TO THIS LOCATION
692	000352	000000	HALT	EXAMINE STACK TO FIND CAUSE
693	000354	000356	.+2	UNEXPECTED TRAP TO THIS LOCATION
694	000356	000000	HALT	EXAMINE STACK TO FIND CAUSE
695	000360	000362	.+2	UNEXPECTED TRAP TO THIS LOCATION
696	000362	000000	HALT	EXAMINE STACK TO FIND CAUSE
697	000364	000366	.+2	UNEXPECTED TRAP TO THIS LOCATION
698	000366	000000	HALT	EXAMINE STACK TO FIND CAUSE
699	000370	000372	.+2	UNEXPECTED TRAP TO THIS LOCATION
700	000372	000000	HALT	EXAMINE STACK TO FIND CAUSE
701	000374	000376	.+2	UNEXPECTED TRAP TO THIS LOCATION
702	000376	000000	HALT	EXAMINE STACK TO FIND CAUSE
703	000400	000402	.+2	UNEXPECTED TRAP TO THIS LOCATION
704	000402	000000	HALT	EXAMINE STACK TO FIND CAUSE
705	000404	000406	.+2	UNEXPECTED TRAP TO THIS LOCATION
706	000406	000000	HALT	EXAMINE STACK TO FIND CAUSE
707	000410	000412	.+2	UNEXPECTED TRAP TO THIS LOCATION
708	000412	000000	HALT	EXAMINE STACK TO FIND CAUSE
709	000414	000416	.+2	UNEXPECTED TRAP TO THIS LOCATION
710	000416	000000	HALT	EXAMINE STACK TO FIND CAUSE
711	000420	000422	.+2	UNEXPECTED TRAP TO THIS LOCATION
712	000422	000000	HALT	EXAMINE STACK TO FIND CAUSE
713	000424	000426	.+2	UNEXPECTED TRAP TO THIS LOCATION
714	000426	000000	HALT	EXAMINE STACK TO FIND CAUSE
715	000430	000432	.+2	UNEXPECTED TRAP TO THIS LOCATION
716	000432	000000	HALT	EXAMINE STACK TO FIND CAUSE
717	000434	000436	.+2	UNEXPECTED TRAP TO THIS LOCATION
718	000436	000000	HALT	EXAMINE STACK TO FIND CAUSE
719	000440	000442	.+2	UNEXPECTED TRAP TO THIS LOCATION
720	000442	000000	HALT	EXAMINE STACK TO FIND CAUSE
721	000444	000446	.+2	UNEXPECTED TRAP TO THIS LOCATION
722	000446	000000	HALT	EXAMINE STACK TO FIND CAUSE
723	000450	000452	.+2	UNEXPECTED TRAP TO THIS LOCATION
724	000452	000000	HALT	EXAMINE STACK TO FIND CAUSE
725	000454	000456	.+2	UNEXPECTED TRAP TO THIS LOCATION
726	000456	000000	HALT	EXAMINE STACK TO FIND CAUSE
727	000460	000462	.+2	UNEXPECTED TRAP TO THIS LOCATION
728	000462	000000	HALT	EXAMINE STACK TO FIND CAUSE
729	000464	000466	.+2	UNEXPECTED TRAP TO THIS LOCATION
730	000466	000000	HALT	EXAMINE STACK TO FIND CAUSE
731	000470	000472	.+2	UNEXPECTED TRAP TO THIS LOCATION
732	000472	000000	HALT	EXAMINE STACK TO FIND CAUSE
733	000474	000476	.+2	UNEXPECTED TRAP TO THIS LOCATION
734	000476	000000	HALT	EXAMINE STACK TO FIND CAUSE
735	000500	000502	.+2	UNEXPECTED TRAP TO THIS LOCATION
736	000502	000000	HALT	EXAMINE STACK TO FIND CAUSE
737	000504	000506	.+2	UNEXPECTED TRAP TO THIS LOCATION
738	000506	000000	HALT	EXAMINE STACK TO FIND CAUSE
739	000510	000512	.+2	UNEXPECTED TRAP TO THIS LOCATION
740	000512	000000	HALT	EXAMINE STACK TO FIND CAUSE


```

831                                     ;STANDARD INTERRUPT VECTORS
832
833
834                                     . =24
835 000024 011276 PFAIL ;POWER FAIL HANDLER
836 00026 000340 340 ;SERVICE AT LEVEL 7
837 000030 010142 ERRORS ;ERROR HANDLER
838 000032 000340 340 ;SERVICE AT LEVEL 7
839 000034 010344 TRPSRV ;GENERAL HANDLER DISPATCH SERVICE
840 000036 000340 340 ;SERVICE AT LEVEL 7
841
842 000200 000167 000574 . =200 JMP START ;GO TO START OF PROGRAM
843
844
845
846 ;DEFINITIONS FOR TRAP SUBROUTINE CALLS
847 ;POINTERS TO SUBROUTINES CAN BE FOUND STARTING
848 ;AT LOCATION "TRPTAB"
849
850 104400 SCOPE=TRAP+Y ;SCOPE LOOP AND ITERATION HANDLER
851 104401 TYPE=TRAP+Y ;TELETYPE OUTPUT ROUTINE
852 104402 OCTASC=TRAP+Y ;OCTAL TO ASCII CONVERSION
853 104403 INSTR=TRAP+Y ;INPUT ASCII STRING
854 104404 INSTR=TRAP+Y ;STRING INPUT ERROR
855 104405 PARAP=TRAP+Y ;CONVERT STRING TO OCTAL, CHECK LIMITS
856 104406 SAVOSP=TRAP+Y ;SAVE RD-RS, PC
857 104407 RESOS=TRAP+Y ;RESTORE RD-RS
858 104410 SCOPE1=TRAP+Y ;CHECK FOR FREEZE ON CURRENT DATA
  
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938      :BUS ADDRESS MEMORY ADDRESSING TEST
939      :LOAD EACH LOCATION IN THE BUS ADDRESS MEMORY
940      :WITH THE ADDRESS OF THAT LOCATION.
941      :THE ADDRESS IS REPEATED EVERY 4 BITS
942      :VERIFY THAT EACH LOCATION IN THE BUS ADDRESS MEMORY
943      :WAS ADDRESSED.
945 001274 012767 000340 176474 T1:  MOV    #340,PS      ;DISABLE ALL INTERRUPTS
946 001302 012767 000100 007734      MOV    #100,ICOUNT  ;SET UP FOR 100 ITERATIONS
947 001310 012767 001410 007722      MOV    #4$,ESCAPE  ;SET UP TO ESCAPE TO NEXT TEST
948 001316 012700 000020      MOV    #20,R0      ;SET UP TO ADDRESS 20 (OCTAL)
949      ;LOCATIONS IN THE BUS ADDRESS MEMORY
950 001322 005004      CLR    R4          ;START AT ADDRESS 0
951 001324 000012      CLR    R2
952 001326 010477 007644 18:  MOV    R4,20HSCR  ;SELECT ADDRESS IN BUS ADDRESS
953      ;MEMORY TO BE ADDRESSED
954 001332 010277 007646      MOV    R2,20HBA   ;LOAD MEMORY LOCATION
955 001336 062732 010421      ADD    #10421,R2  ;WITH ITS ADDRESS
956 001342 005074      INC    R4          ;ADVANCE TO NEXT ADDRESS
957 001344 005000      DEC    R0
958 001346 000017      BNE   18
959 001350 000020      MOV    #20,R0
960      ;CONTINUE IF NOT DONE
961      ;SET UP TO CHECK
962 001354 000004      CLR    R4          ;EACH MEMORY ADDRESS
963 001356 000002      CLR    R2          ;START AT ADDRESS 0
964 001360 010477 007612 28:  MOV    R4,20HSCR  ;ADDRESS MEMORY LOCATION
965 001364 017703 007614      MOV    20HBA,R3   ;READ CONTENTS OF MEMORY
966 001370 020203      CMP   R2,R3       ;WAS MEMORY LOCATION LOADED
967      ;WITH ITS ADDRESS
968 001372 001401      BEQ   38
969 001374 104001      HLT   1
970 001376 005204 010421 38:  INC    R4          ;BUS ADDRESS MEMORY ERROR
971 001400 002702      ADD    #10421,R2  ;ADVANCE TO NEXT LOCATION
972 001404 000000      DEC    R0
973 001406 001364 48:  BIT   28
974 001410 104400      SCLE
975      ;BYTE COUNT MEMORY ADDRESSING TEST
976      :LOAD EACH LOCATION IN THE BYTE COUNT MEMORY
977      :WITH THE ADDRESS OF THAT LOCATION.
978      :THE ADDRESS IS REPEATED EVERY 4 BITS
979      :VERIFY THAT EACH LOCATION IN THE BYTE COUNT MEMORY
980      :WAS ADDRESSED.
982 001412 012767 000340 176356 T2:  MOV    #340,PS      ;DISABLE ALL INTERRUPTS
983 001420 012767 000100 007616      MOV    #100,ICOUNT  ;SET UP FOR 100 ITERATIONS
984 001426 012767 001526 007604      MOV    #4$,ESCAPE  ;SET UP TO ESCAPE TO NEXT TEST
985 001434 012700 000020      MOV    #20,R0      ;SET UP TO ADDRESS 20 (OCTAL)
986      ;LOCATIONS IN THE BYTE COUNT MEMORY
987 001440 005004      CLR    R4          ;START AT ADDRESS 0
988 001442 000002      CLR    R2
989 001444 010477 007526 18:  MOV    R4,20HSCR  ;SELECT ADDRESS IN BYTE COUNT
990      ;MEMORY TO BE ADDRESSED
991 001450 010277 007532      MOV    R2,20HBC   ;LOAD MEMORY LOCATION
  
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992	001454	062702	010421			ADD	#10421,R2		;WITH ITS ADDRESS
993	001460	005204				INC	R4		;ADVANCE TO NEXT ADDRESS
994	001462	005300				DEC	R0		
995	001464	001367				BNE	1\$;CONTINUE IF NOT DONE
996	001466	012700	000020			MOV	#20,R0		;SET UP TO CHECK
997									;EACH MEMORY ADDRESS
998	001472	005004				CLR	R4		;START AT ADDRESS 0
999	001474	005002				CLR	R2		
1000	001476	010477	007474		2\$:	MOV	R4,20HSCR		;ADDRESS MEMORY LOCATION
1001	001502	017703	007500			MOV	20HBC,R3		;READ CONTENTS OF MEMORY
1002	001506	020203				CMP	R2,R3		;WAS MEMORY LOCATION LOADED
1003									;WITH ITS ADDRESS
1004	001510	001401				BEQ	3\$		
1005	001512	104002				HLT	2		;BYTE COUNT MEMORY ERROR
1006	001514	005204			3\$:	INC	R4		;ADVANCE TO NEXT LOCATION
1007	001516	062702	010421			ADD	#10421,R2		
1008	001522	005300				DEC	R0		
1009	001524	001364				BNE	2\$;CONTINUE IF NOT DONE
1010	001526	104400			4\$:	SCOPE			
1011									
1012									;BUS ADDRESS MEMORY DATA TEST
1013									;VERIFY THAT ADDRESS 0 OF BUS ADDRESS MEMORY
1014									;CAN BE SET TO 177777 AND CLEARED TO 0
1015									
1016	001530	012767	000340	176240	T3:	MOV	#340,PS		;DISABLE ALL INTERRUPTS
1017	001536	012767	000100	007500		MOV	#100,ICOUNT		;SET UP FOR 100 ITERATIONS
1018	001544	012767	001624	007466		MOV	#2\$,ESCAPE		;SET UP TO ESCAPE TO NEXT TEST
1019	001552	012705	177777			MOV	#177777,R5		;EXPECTED RESULT=177777
1020	001556	012777	000000	007412		MOV	#0,20HSCR		;SELECT LOCATION 0
1021									;OF BUS ADDRESS MEMORY
1022	001564	012777	177777	007412		MOV	#177777,20HBA		;WRITE 177777 INTO MEMORY
1023	001572	017704	007406			MOV	20HBA,R4		;READ CONTENTS OF MEMORY LOCATION
1024	001576	020504				CMP	R5,R4		;COMPARE EXPECTED AND
1025	001600	001401				BEQ	1\$;RECEIVED MEMORY CONTENTS
1026	001602	104003				HLT	3		;BUS ADDRESS MEMORY ERROR
1027	001604	005005			1\$:	CLR	R5		;EXPECTED RESULT AFTER CLEAR=0
1028	001606	012777	177777	007370		BIC	#177777,20HBA		;CLEAR MEMORY LOCATION
1029	001614	017704	007364			MOV	20HBA,R4		;READ CONTENTS OF BUS ADDRESS
1030									;MEMORY ADDRESS 0
1031	001620	001401				BEQ	2\$		
1032	001622	104003				HLT	3		;BUS ADDRESS MEMORY ERROR
1033									;ADDRESS 0 NOT 0, ERROR
1034	001624	104400			2\$:	SCOPE			
1035									
1036									;BUS ADDRESS MEMORY DATA TEST
1037									;VERIFY THAT ADDRESS 1 OF BUS ADDRESS MEMORY
1038									;CAN BE SET TO 177777 AND CLEARED TO 0
1039									
1040	001626	012767	000340	176142	T4:	MOV	#340,PS		;DISABLE ALL INTERRUPTS
1041	001634	012767	000100	007402		MOV	#100,ICOUNT		;SET UP FOR 100 ITERATIONS
1042	001642	012767	001722	007370		MOV	#2\$,ESCAPE		;SET UP TO ESCAPE TO NEXT TEST
1043	001650	012705	177777			MOV	#177777,R5		;EXPECTED RESULT=177777
1044	001654	012777	000001	007314		MOV	#1,20HSCR		;SELECT LOCATION 1
1045									;OF BUS ADDRESS MEMORY
1046	001662	012777	177777	007314		MOV	#177777,20HBA		;WRITE 177777 INTO MEMORY
1047	001670	017704	007310			MOV	20HBA,R4		;READ CONTENTS OF MEMORY LOCATION

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1048	001674	020504				CMP	RS,R4		:COMPARE EXPECTED AND
1049	001676	001401				BEQ	1\$:RECEIVED MEMORY CONTENTS
1050	001730	104003				HLT	3		:BUS ADDRESS MEMORY ERROR
1051	001702	005005			1\$:	CLR	RS		:EXPECTED RESULT AFTER CLEAR=0
1052	001704	042777	177777	007272		BIC	#177777,20HBA		:CLEAR MEMORY LOCATION
1053	001712	017704	007266			MOV	20HBA,R4		:READ CONTENTS OF BUS ADDRESS
1054									:MEMORY ADDRESS 1
1055	001716	001401				BEQ	2\$		
1056	001720	104003				HLT	3		:BUS ADDRESS MEMORY ERROR
1057									:ADDRESS 1 NOT 0, ERROR
1058	001722	104400			2\$:	SCOPE			
1059									
1060									:BUS ADDRESS MEMORY DATA TEST
1061									:VERIFY THAT ADDRESS 2 OF BUS ADDRESS MEMORY
1062									:CAN BE SET TO 177777 AND CLEARED TO 0
1063									
1064	001724	012767	000340	176044	T\$:	MOV	#340,PS		:DISABLE ALL INTERRUPTS
1065	001732	012767	000100	007304		MOV	#100,ICOUNT		:SET UP FOR 100 ITERATIONS
1066	001740	012767	002020	007272		MOV	#2\$,ESCAPE		:SET UP TO ESCAPE TO NEXT TEST
1067	001746	012705	177777			MOV	#177777,RS		:EXPECTED RESULT=177777
1068	001752	012777	000002	007216		MOV	#2,20HSCR		:SELECT LOCATION 2
1069									:OF BUS ADDRESS MEMORY
1070	001760	012777	177777	007216		MOV	#177777,20HBA		:WRITE 177777 INTO MEMORY
1071	001766	017704	007212			MOV	20HBA,R4		:READ CONTENTS OF MEMORY LOCATION
1072	001772	020504				CMP	RS,R4		:COMPARE EXPECTED AND
1073	001774	001401				BEQ	1\$:RECEIVED MEMORY CONTENTS
1074	001776	104003				HLT	3		:BUS ADDRESS MEMORY ERROR
1075	002000	005005			1\$:	CLR	RS		:EXPECTED RESULT AFTER CLEAR=0
1076	002002	042777	177777	007174		BIC	#177777,20HBA		:CLEAR MEMORY LOCATION
1077	002010	017704	007170			MOV	20HBA,R4		:READ CONTENTS OF BUS ADDRESS
1078									:MEMORY ADDRESS 2
1079	002014	001401				BEQ	2\$		
1080	002016	104003				HLT	3		:BUS ADDRESS MEMORY ERROR
1081									:ADDRESS 2 NOT 0, ERROR
1082	002020	104400			2\$:	SCOPE			
1083									
1084									:BUS ADDRESS MEMORY DATA TEST
1085									:VERIFY THAT ADDRESS 3 OF BUS ADDRESS MEMORY
1086									:CAN BE SET TO 177777 AND CLEARED TO 0
1087									
1088	002022	012767	000340	175746	T6:	MOV	#340,PS		:DISABLE ALL INTERRUPTS
1089	002030	012767	000100	007206		MOV	#100,ICOUNT		:SET UP FOR 100 ITERATIONS
1090	002036	012767	002116	007174		MOV	#2\$,ESCAPE		:SET UP TO ESCAPE TO NEXT TEST
1091	002044	012705	177777			MOV	#177777,RS		:EXPECTED RESULT=177777
1092	002050	012777	000003	007120		MOV	#3,20HSCR		:SELECT LOCATION 3
1093									:OF BUS ADDRESS MEMORY
1094	002056	012777	177777	007120		MOV	#177777,20HBA		:WRITE 177777 INTO MEMORY
1095	002064	017704	007114			MOV	20HBA,R4		:READ CONTENTS OF MEMORY LOCATION
1096	002070	020504				CMP	RS,R4		:COMPARE EXPECTED AND
1097	002072	001401				BEQ	1\$:RECEIVED MEMORY CONTENTS
1098	002074	104003				HLT	3		:BUS ADDRESS MEMORY ERROR
1099	002076	005005			1\$:	CLR	RS		:EXPECTED RESULT AFTER CLEAR=0
1100	002100	042777	177777	007076		BIC	#177777,20HBA		:CLEAR MEMORY LOCATION
1101	002106	017704	007072			MOV	20HBA,R4		:READ CONTENTS OF BUS ADDRESS
1102									:MEMORY ADDRESS 3
1103	002112	001401				BEQ	2\$		

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1104 002114 104003          HLT      3          ;BUS ADDRESS MEMORY ERROR
1105                                     ;ADDRESS 3 NOT 0, ERROR
1106 002116 104400          2S:    SCOPE
1107
1108                                     ;BUS ADDRESS MEMORY DATA TEST
1109                                     ;VERIFY THAT ADDRESS 4 OF BUS ADDRESS MEMORY
1110                                     ;CAN BE SET TO 177777 AND CLEARED TO 0
1111
1112 002120 012767 000340 175650 T7:    MOV      #340,PS          ;DISABLE ALL INTERRUPTS
1113 002126 012767 000100 007110      MOV      #100,ICOUNT     ;SET UP FOR 100 ITERATIONS
1114 002134 012767 002214 007076      MOV      #25,ESCAPE     ;SET UP TO ESCAPE TO NEXT TEST
1115 002142 012705 177777              MOV      #177777,R5     ;EXPECTED RESULT=177777
1116 002146 012777 000004 007022      MOV      #4,20HSCR      ;SELECT LOCATION 4
1117                                     ;OF BUS ADDRESS MEMORY
1118 002154 012777 177777 007022      MOV      #177777,20HBA  ;WRITE 177777 INTO MEMORY
1119 002162 017704 007016              MOV      20HBA,R4       ;READ CONTENTS OF MEMORY LOCATION
1120 002166 020504              CMP      R5,R4          ;COMPARE EXPECTED AND
1121 002170 001401              BEQ      1S             ;RECEIVED MEMORY CONTENTS
1122 002172 104003          HLT      3             ;BUS ADDRESS MEMORY ERROR
1123 002174 005005          1S:    CLR      R5          ;EXPECTED RESULT AFTER CLEAR=0
1124 002176 042777 177777 007000      BIC      #177777,20HBA ;CLEAR MEMORY LOCATION
1125 002204 017704 006774              MOV      20HBA,R4       ;READ CONTENTS OF BUS ADDRESS
1126                                     ;MEMORY ADDRESS 4
1127 002210 001401              BEQ      2S             ;BUS ADDRESS MEMORY ERROR
1128 002212 104003          HLT      3             ;ADDRESS 4 NOT 0, ERROR
1129
1130 002214 104400          2S:    SCOPE
1131
1132                                     ;BUS ADDRESS MEMORY DATA TEST
1133                                     ;VERIFY THAT ADDRESS 5 OF BUS ADDRESS MEMORY
1134                                     ;CAN BE SET TO 177777 AND CLEARED TO 0
1135
1136 002216 012767 000340 175552 T10:   MOV      #340,PS          ;DISABLE ALL INTERRUPTS
1137 002224 012767 000100 007012      MOV      #100,ICOUNT     ;SET UP FOR 100 ITERATIONS
1138 002232 012767 002312 007000      MOV      #25,ESCAPE     ;SET UP TO ESCAPE TO NEXT TEST
1139 002240 012705 177777              MOV      #177777,R5     ;EXPECTED RESULT=177777
1140 002244 012777 000005 006724      MOV      #5,20HSCR      ;SELECT LOCATION 5
1141                                     ;OF BUS ADDRESS MEMORY
1142 002252 012777 177777 006724      MOV      #177777,20HBA  ;WRITE 177777 INTO MEMORY
1143 002260 017704 006720              MOV      20HBA,R4       ;READ CONTENTS OF MEMORY LOCATION
1144 002264 020504              CMP      R5,R4          ;COMPARE EXPECTED AND
1145 002266 001401              BEQ      1S             ;RECEIVED MEMORY CONTENTS
1146 002270 104003          HLT      3             ;BUS ADDRESS MEMORY ERROR
1147 002272 005005          1S:    CLR      R5          ;EXPECTED RESULT AFTER CLEAR=0
1148 002274 042777 177777 006702      BIC      #177777,20HBA ;CLEAR MEMORY LOCATION
1149 002302 017704 006676              MOV      20HBA,R4       ;READ CONTENTS OF BUS ADDRESS
1150                                     ;MEMORY ADDRESS 5
1151 002306 001401              BEQ      2S             ;BUS ADDRESS MEMORY ERROR
1152 002310 104003          HLT      3             ;ADDRESS 5 NOT 0, ERROR
1153
1154 002312 104400          2S:    SCOPE
1155
1156                                     ;BUS ADDRESS MEMORY DATA TEST
1157                                     ;VERIFY THAT ADDRESS 6 OF BUS ADDRESS MEMORY
1158                                     ;CAN BE SET TO 177777 AND CLEARED TO 0
1159

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1160	002314	012767	000340	175454	T11:	MOV	#340, PS	:DISABLE ALL INTERRUPTS
1161	002322	012767	000100	006714		MOV	#100, ICOUNT	:SET UP FOR 100 ITERATIONS
1162	002330	012767	002410	006702		MOV	#25, ESCAPE	:SET UP TO ESCAPE TO NEXT TEST
1163	002336	012705	177777			MOV	#177777, R5	:EXPECTED RESULT=177777
1164	002342	012777	000006	006626		MOV	#6, ZDHSCR	:SELECT LOCATION 6
1165								:OF BUS ADDRESS MEMORY
1166	002350	012777	177777	006626		MOV	#177777, ZDHBA	:WRITE 177777 INTO MEMORY
1167	002356	017704	006622			MOV	ZDHBA, R4	:READ CONTENTS OF MEMORY LOCATION
1168	002362	020504				CMP	R5, R4	:COMPARE EXPECTED AND
1169	002364	001401				BEQ	15	:RECEIVED MEMORY CONTENTS
1170	002366	104003				HLT	3	:BUS ADDRESS MEMORY ERROR
1171	002370	005005			15:	CLR	R5	:EXPECTED RESULT AFTER CLEAR=0
1172	002372	042777	177777	006604		BIC	#177777, ZDHBA	:CLEAR MEMORY LOCATION
1173	002400	017704	006600			MOV	ZDHBA, R4	:READ CONTENTS OF BUS ADDRESS
1174								:MEMORY ADDRESS 6
1175	002404	001401				BEQ	25	
1176	002406	104003				HLT	3	:BUS ADDRESS MEMORY ERROR
1177								:ADDRESS 6 NOT 0, ERROR
1178	002410	104400			25:	SCOPE		
1179								
1180								:BUS ADDRESS MEMORY DATA TEST
1181								:VERIFY THAT ADDRESS 7 OF BUS ADDRESS MEMORY
1182								:CAN BE SET TO 177777 AND CLEARED TO 0
1183								
1184	002412	012767	000340	175356	T12:	MOV	#340, PS	:DISABLE ALL INTERRUPTS
1185	002420	012767	000100	006616		MOV	#100, ICOUNT	:SET UP FOR 100 ITERATIONS
1186	002426	012767	002506	006604		MOV	#25, ESCAPE	:SET UP TO ESCAPE TO NEXT TEST
1187	002434	012705	177777			MOV	#177777, R5	:EXPECTED RESULT=177777
1188	002440	012777	000007	006530		MOV	#7, ZDHSCR	:SELECT LOCATION 7
1189								:OF BUS ADDRESS MEMORY
1190	002446	012777	177777	006530		MOV	#177777, ZDHBA	:WRITE 177777 INTO MEMORY
1191	002454	017704	006524			MOV	ZDHBA, R4	:READ CONTENTS OF MEMORY LOCATION
1192	002460	020504				CMP	R5, R4	:COMPARE EXPECTED AND
1193	002462	001401				BEQ	15	:RECEIVED MEMORY CONTENTS
1194	002464	104003				HLT	3	:BUS ADDRESS MEMORY ERROR
1195	002466	005005			15:	CLR	R5	:EXPECTED RESULT AFTER CLEAR=0
1196	002470	042777	177777	006506		BIC	#177777, ZDHBA	:CLEAR MEMORY LOCATION
1197	002476	017704	006502			MOV	ZDHBA, R4	:READ CONTENTS OF BUS ADDRESS
1198								:MEMORY ADDRESS 7
1199	002502	001401				BEQ	25	
1200	002504	104003				HLT	3	:BUS ADDRESS MEMORY ERROR
1201								:ADDRESS 7 NOT 0, ERROR
1202	002506	104400			25:	SCOPE		
1203								
1204								:BUS ADDRESS MEMORY DATA TEST
1205								:VERIFY THAT ADDRESS 10 OF BUS ADDRESS MEMORY
1206								:CAN BE SET TO 177777 AND CLEARED TO 0
1207								
1208	002510	012767	000340	175260	T13:	MOV	#340, PS	:DISABLE ALL INTERRUPTS
1209	002516	012767	000100	006520		MOV	#100, ICOUNT	:SET UP FOR 100 ITERATIONS
1210	002524	012767	002604	006506		MOV	#25, ESCAPE	:SET UP TO ESCAPE TO NEXT TEST
1211	002532	012705	177777			MOV	#177777, R5	:EXPECTED RESULT=177777
1212	002536	012777	000010	006432		MOV	#10, ZDHSCR	:SELECT LOCATION 10
1213								:OF BUS ADDRESS MEMORY
1214	002544	012777	177777	006432		MOV	#177777, ZDHBA	:WRITE 177777 INTO MEMORY
1215	002552	017704	006426			MOV	ZDHBA, R4	:READ CONTENTS OF MEMORY LOCATION

1216	002556	020504				CMP	R5, R4	: COMPARE EXPECTED AND
1217	002560	001401				BEQ	15	: RECEIVED MEMORY CONTENTS
1218	002562	104003				HLT	3	: BUS ADDRESS MEMORY ERROR
1219	002564	005005			18:	CLR	R5	: EXPECTED RESULT AFTER CLEAR=0
1220	002566	042777	177777	006410		BIC	#177777, 20HBA	: CLEAR MEMORY LOCATION
1221	002574	017704	006404			MOV	20HBA, R4	: READ CONTENTS OF BUS ADDRESS
1222								: MEMORY ADDRESS 10
1223	002600	001401				BEQ	25	
1224	002602	104003				HLT	3	: BUS ADDRESS MEMORY ERROR
1225								: ADDRESS 10 NOT 0, ERROR
1226	002604	104400			28:	SCOPE		
1227								
1228								: BUS ADDRESS MEMORY DATA TEST
1229								: VERIFY THAT ADDRESS 11 OF BUS ADDRESS MEMORY
1230								: CAN BE SET TO 177777 AND CLEARED TO 0
1231								
1232	002606	012767	000340	175162	T14:	MOV	#340, PS	: DISABLE ALL INTERRUPTS
1233	002614	012767	000100	006422		MOV	#100, ICOUNT	: SET UP FOR 100 ITERATIONS
1234	002622	012767	002702	006410		MOV	#25, ESCAPE	: SET UP TO ESCAPE TO NEXT TEST
1235	002630	012705	177777			MOV	#177777, R5	: EXPECTED RESULT=177777
1236	002634	012777	000011	006334		MOV	#11, 20HSCR	: SELECT LOCATION 11
1237								: OF BUS ADDRESS MEMORY
1238	002642	012777	177777	006334		MOV	#177777, 20HBA	: WRITE 177777 INTO MEMORY
1239	002650	017704	006330			MOV	20HBA, R4	: READ CONTENTS OF MEMORY LOCATION
1240	002654	020504				CMP	R5, R4	: COMPARE EXPECTED AND
1241	002656	001401				BEQ	18	: RECEIVED MEMORY CONTENTS
1242	002660	104003				HLT	3	: BUS ADDRESS MEMORY ERROR
1243	002662	005005			18:	CLR	R5	: EXPECTED RESULT AFTER CLEAR=0
1244	002664	042777	177777	006312		BIC	#177777, 20HBA	: CLEAR MEMORY LOCATION
1245	002672	017704	006306			MOV	20HBA, R4	: READ CONTENTS OF BUS ADDRESS
1246								: MEMORY ADDRESS 11
1247	002676	001401				BEQ	28	
1248	002700	104003				HLT	3	: BUS ADDRESS MEMORY ERROR
1249								: ADDRESS 11 NOT 0, ERROR
1250	002702	104400			28:	SCOPE		
1251								
1252								: BUS ADDRESS MEMORY DATA TEST
1253								: VERIFY THAT ADDRESS 12 OF BUS ADDRESS MEMORY
1254								: CAN BE SET TO 177777 AND CLEARED TO 0
1255								
1256	002704	012767	000340	175064	T15:	MOV	#340, PS	: DISABLE ALL INTERRUPTS
1257	002712	012767	000100	006324		MOV	#100, ICOUNT	: SET UP FOR 100 ITERATIONS
1258	012720	012767	003000	006312		MOV	#25, ESCAPE	: SET UP TO ESCAPE TO NEXT TEST
1259	002726	012705	177777			MOV	#177777, R5	: EXPECTED RESULT=177777
1260	002732	012777	000012	006236		MOV	#12, 20HSCR	: SELECT LOCATION 12
1261								: OF BUS ADDRESS MEMORY
1262	002740	012777	177777	006236		MOV	#177777, 20HBA	: WRITE 177777 INTO MEMORY
1263	002746	017704	006232			MOV	20HBA, R4	: READ CONTENTS OF MEMORY LOCATION
1264	002752	020504				CMP	R5, R4	: COMPARE EXPECTED AND
1265	002754	001401				BEQ	18	: RECEIVED MEMORY CONTENTS
1266	002756	104003				HLT	3	: BUS ADDRESS MEMORY ERROR
1267	002760	005005			18:	CLR	R5	: EXPECTED RESULT AFTER CLEAR=0
1268	002762	042777	177777	006214		BIC	#177777, 20HBA	: CLEAR MEMORY LOCATION
1269	002770	017704	006210			MOV	20HBA, R4	: READ CONTENTS OF BUS ADDRESS
1270								: MEMORY ADDRESS 12
1271	002774	001401				BEQ	28	

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1272 002776 104003          HLT      3          ;BUS ADDRESS MEMORY ERROR
1273                                     ;ADDRESS 12 NOT 0, ERROR
1274 003000 104400          28:     SCOPE
1275                                     ;BUS ADDRESS MEMORY DATA TEST
1276                                     ;VERIFY THAT ADDRESS 13 OF BUS ADDRESS MEMORY
1277                                     ;CAN BE SET TO 177777 AND CLEARED TO 0
1278
1279
1280 003002 012767 000340 174766 T16:     MOV      #340,PS          ;DISABLE ALL INTERRUPTS
1281 003010 012767 000100 006226     MOV      #100,ICOUNT     ;SET UP FOR 100 ITERATIONS
1282 003016 012767 003076 006214     MOV      #25,ESCAPE     ;SET UP TO ESCAPE TO NEXT TEST
1283 003024 012705 177777             MOV      #177777,RS      ;EXPECTED RESULT=177777
1284 003030 012777 000013 006140     MOV      #13,ZOHSCR     ;SELECT LOCATION 13
1285                                     ;OF BUS ADDRESS MEMORY
1286 003036 012777 177777 006140     MOV      #177777,ZOHBA  ;WRITE 177777 INTO MEMORY
1287 003044 017704 006134             MOV      ZOHBA,R4       ;READ CONTENTS OF MEMORY LOCATION
1288 003050 020504             CMP      R5,R4         ;COMPARE EXPECTED AND
1289 003052 001401             BEQ     18             ;RECEIVED MEMORY CONTENTS
1290 003054 104003             HLT     3             ;BUS ADDRESS MEMORY ERROR
1291 003056 005005          18:     CLR      RS          ;EXPECTED RESULT AFTER CLEAR=0
1292 003060 042777 177777 006116     BIC     #177777,ZOHBA  ;CLEAR MEMORY LOCATION
1293 003066 017704 006112             MOV      ZOHBA,R4     ;READ CONTENTS OF BUS ADDRESS
1294                                     ;MEMORY ADDRESS 13
1295 003072 001401             BEQ     28
1296 003074 104003          HLT     3             ;BUS ADDRESS MEMORY ERROR
1297                                     ;ADDRESS 13 NOT 0, ERROR
1298 003076 104400          28:     SCOPE
1299
1300                                     ;BUS ADDRESS MEMORY DATA TEST
1301                                     ;VERIFY THAT ADDRESS 14 OF BUS ADDRESS MEMORY
1302                                     ;CAN BE SET TO 177777 AND CLEARED TO 0
1303
1304
1305 003100 012767 000340 174670 T17:     MOV      #340,PS          ;DISABLE ALL INTERRUPTS
1306 003106 012767 000100 006130     MOV      #100,ICOUNT     ;SET UP FOR 100 ITERATIONS
1307 003114 012767 003174 006116     MOV      #25,ESCAPE     ;SET UP TO ESCAPE TO NEXT TEST
1308 003122 012705 177777             MOV      #177777,RS      ;EXPECTED RESULT=177777
1309 003126 012777 000014 006042     MOV      #14,ZOHSCR     ;SELECT LOCATION 14
1310                                     ;OF BUS ADDRESS MEMORY
1311 003134 012777 177777 006042     MOV      #177777,ZOHBA  ;WRITE 177777 INTO MEMORY
1312 003142 017704 006036             MOV      ZOHBA,R4       ;READ CONTENTS OF MEMORY LOCATION
1313 003146 020504             CMP      R5,R4         ;COMPARE EXPECTED AND
1314 003150 001401             BEQ     18             ;RECEIVED MEMORY CONTENTS
1315 003152 104003             HLT     3             ;BUS ADDRESS MEMORY ERROR
1316 003154 005005          18:     CLR      RS          ;EXPECTED RESULT AFTER CLEAR=0
1317 003156 042777 177777 006020     BIC     #177777,ZOHBA  ;CLEAR MEMORY LOCATION
1318 003164 017704 006014             MOV      ZOHBA,R4     ;READ CONTENTS OF BUS ADDRESS
1319                                     ;MEMORY ADDRESS 14
1320 003170 001401             BEQ     28
1321 003172 104003          HLT     3             ;BUS ADDRESS MEMORY ERROR
1322                                     ;ADDRESS 14 NOT 0, ERROR
1323 003174 104400          28:     SCOPE
1324
1325                                     ;BUS ADDRESS MEMORY DATA TEST
1326                                     ;VERIFY THAT ADDRESS 15 OF BUS ADDRESS MEMORY
1327                                     ;CAN BE SET TO 177777 AND CLEARED TO 0

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1328 003176 012767 000340 174572 T20: MOV      #340,PS      ;DISABLE ALL INTERRUPTS
1329 003204 012767 000100 006037 MOV      #100,ICOUNT ;SET UP FOR 100 ITERATIONS
1330 003212 012767 003272 006020 MOV      #25,ESCAPE  ;SET UP TO ESCAPE TO NEXT TEST
1331 003220 012705 177777  MOV      #177777,RS  ;EXPECTED RESULT=177777
1332 003224 012777 000015 005744 MOV      #15,ZOHSCR  ;SELECT LOCATION 15
1333                                     ;OF BUS ADDRESS MEMORY
1334 003232 012777 177777 005744 MOV      #177777,ZOHBA ;WRITE 177777 INTO MEMORY
1335 003240 017704 005740  MOV      ZOHBA,R4    ;READ CONTENTS OF MEMORY LOCATION
1336 003244 020504  CMP      R5,R4      ;COMPARE EXPECTED AND
1337 003246 001401  BEQ     15          ;RECEIVED MEMORY CONTENTS
1338 003250 104003  HLT     3          ;BUS ADDRESS MEMORY ERROR
1339 003252 005005  CLR     RS         ;EXPECTED RESULT AFTER CLEAR=0
1340 003254 042777 177777 005722 15:  BIC     #177777,ZOHBA ;CLEAR MEMORY LOCATION
1341 003262 017704 005716  MOV      ZOHBA,R4   ;READ CONTENTS OF BUS ADDRESS
1342                                     ;MEMORY ADDRESS 15
1343 003266 001401  BEQ     25
1344 003270 104003  HLT     3          ;BUS ADDRESS MEMORY ERROR
1345                                     ;ADDRESS 15 NOT 0, ERROR
1346 003272 104400 25:  SCOPE
1347                                     ;BUS ADDRESS MEMORY DATA TEST
1348                                     ;VERIFY THAT ADDRESS 16 OF BUS ADDRESS MEMORY
1349                                     ;CAN BE SET TO 177777 AND CLEARED TO 0
1350
1351
1352 003274 012767 000340 174474 T21: MOV      #37,PS      ;DISABLE ALL INTERRUPTS
1353 003302 012767 000100 005734 MOV      #100,ICOUNT ;SET UP FOR 100 ITERATIONS
1354 003310 012767 003370 005722 MOV      #25,ESCAPE  ;SET UP TO ESCAPE TO NEXT TEST
1355 003316 012705 177777  MOV      #177777,RS  ;EXPECTED RESULT=177777
1356 003322 012777 000016 005646 MOV      #16,ZOHSCR  ;SELECT LOCATION 16
1357                                     ;OF BUS ADDRESS MEMORY
1358 003330 012777 177777 005646 MOV      #177777,ZOHBA ;WRITE 177777 INTO MEMORY
1359 003336 017704 005642  MOV      ZOHBA,R4    ;READ CONTENTS OF MEMORY LOCATION
1360 003342 020504  CMP      R5,R4      ;COMPARE EXPECTED AND
1361 003344 001401  BEQ     15          ;RECEIVED MEMORY CONTENTS
1362 003346 104003  HLT     3          ;BUS ADDRESS MEMORY ERROR
1363 003350 005005  CLR     RS         ;EXPECTED RESULT AFTER CLEAR=0
1364 003352 042777 177777 005624 15:  BIC     #177777,ZOHBA ;CLEAR MEMORY LOCATION
1365 003360 017704 005620  MOV      ZOHBA,R4   ;READ CONTENTS OF BUS ADDRESS
1366                                     ;MEMORY ADDRESS 16
1367 003364 001401  BEQ     25
1368 003366 104003  HLT     3          ;BUS ADDRESS MEMORY ERROR
1369                                     ;ADDRESS 16 NOT 0, ERROR
1370 003370 104400 25:  SCOPE
1371                                     ;BUS ADDRESS MEMORY DATA TEST
1372                                     ;VERIFY THAT ADDRESS 17 OF BUS ADDRESS MEMORY
1373                                     ;CAN BE SET TO 177777 AND CLEARED TO 0
1374
1375
1376 003372 012767 000340 174376 T22: MOV      #340,PS      ;DISABLE ALL INTERRUPTS
1377 003400 012767 000100 005636 MOV      #100,ICOUNT ;SET UP FOR 100 ITERATIONS
1378 003406 012767 003466 005624 MOV      #25,ESCAPE  ;SET UP TO ESCAPE TO NEXT TEST
1379 003414 012705 177777  MOV      #177777,RS  ;EXPECTED RESULT=177777
1380 003420 012777 000017 005550 MOV      #17,ZOHSCR  ;SELECT LOCATION 17
1381                                     ;OF BUS ADDRESS MEMORY
1382 003426 012777 177777 005550 MOV      #177777,ZOHBA ;WRITE 177777 INTO MEMORY
1383 003434 017704 005544  MOV      ZOHBA,R4    ;READ CONTENTS OF MEMORY LOCATION

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1394	003440	020504					CMP	R5,R4	;COMPARE EXPECTED AND
1395	003442	001401					BEQ	15	;RECEIVED MEMORY CONTENTS
1396	003444	104003					HLT	3	;BUS ADDRESS MEMORY ERROR
1397	003446	005005					CLR	R5	;EXPECTED RESULT AFTER CLEAR=0
1398	003450	042777	177777	005526	18:		BIC	#177777,20HBA	;CLEAR MEMORY LOCATION
1399	003456	017704	005522				MOV	20HBA,R4	;READ CONTENTS OF BUS ADDRESS
1400									;MEMORY ADDRESS 17
1401	003462	001401					BEQ	25	
1402	003464	104003					HLT	3	;BUS ADDRESS MEMORY ERROR
1403									;ADDRESS 17 NOT 0, ERROR
1404	003466	104400			25:		SCOPE		
1405									;BYTE COUNT MEMORY DATA TEST
1406									;VERIFY THAT ADDRESS 0 OF BYTE COUNT MEMORY
1407									;CAN BE SET TO 177777 AND CLEARED TO 0
1408	003470	012767	000340	174300	T21:		MOV	#340,PS	;DISABLE ALL INTERRUPTS
1409	003476	012767	000100	005540			MOV	#100,ICOUNT	;SET UP FOR 100 ITERATIONS
1410	003504	012767	003564	005526			MOV	#25,ESCAPE	;SET UP TO ESCAPE TO NEXT TEST
1411	003512	012705	177777				MOV	#177777,R5	;EXPECTED RESULT=177777
1412	003516	012777	000000	005452			MOV	#0,20HSCR	;SELECT LOCATION 0
1413									;OF BYTE COUNT MEMORY
1414	003524	012777	177777	005454			MOV	#177777,20HBC	;WRITE 177777 INTO MEMORY
1415	003532	017704	005450				MOV	20HBC,R4	;READ CONTENTS OF MEMORY LOCATION
1416	003536	020504					CMP	R5,R4	;COMPARE EXPECTED AND
1417	003540	001401					BEQ	15	;RECEIVED MEMORY CONTENTS
1418	003542	104004					HLT	4	;BYTE COUNT MEMORY ERROR
1419	003544	005005					CLR	R5	;EXPECTED RESULT AFTER CLEAR=0
1420	003546	042777	177777	005432	18:		BIC	#177777,20HBC	;CLEAR MEMORY LOCATION
1421	003554	017704	005426				MOV	20HBC,R4	;READ CONTENTS OF BYTE COUNT
1422									;MEMORY ADDRESS 0
1423									;BYTE COUNT MEMORY ERROR
1424									;ADDRESS 0 NOT 0, ERROR
1425	003564	104400			25:		SCOPE		
1426									;BYTE COUNT MEMORY DATA TEST
1427									;VERIFY THAT ADDRESS 1 OF BYTE COUNT MEMORY
1428									;CAN BE SET TO 177777 AND CLEARED TO 0
1429	003566	012767	000340	174202	T24:		MOV	#340,PS	;DISABLE ALL INTERRUPTS
1430	003574	012767	000100	005442			MOV	#100,ICOUNT	;SET UP FOR 100 ITERATIONS
1431	003602	012767	003662	005430			MOV	#25,ESCAPE	;SET UP TO ESCAPE TO NEXT TEST
1432	003610	012705	177777				MOV	#177777,R5	;EXPECTED RESULT=177777
1433	003614	012777	000001	005354			MOV	#1,20HSCR	;SELECT LOCATION 1
1434									;OF BYTE COUNT MEMORY
1435	003622	012777	177777	005356			MOV	#177777,20HBC	;WRITE 177777 INTO MEMORY
1436	003630	017704	005332				MOV	20HBC,R4	;READ CONTENTS OF MEMORY LOCATION
1437	003634	020504					CMP	R5,R4	;COMPARE EXPECTED AND
1438	003636	001401					BEQ	15	;RECEIVED MEMORY CONTENTS
1439	003640	104004					HLT	4	;BYTE COUNT MEMORY ERROR
1440	003642	005005					CLR	R5	;EXPECTED RESULT AFTER CLEAR=0
1441	003644	042777	177777	005334	18:		BIC	#177777,20HBC	;CLEAR MEMORY LOCATION
1442	003652	017704	005330				MOV	20HBC,R4	;READ CONTENTS OF BYTE COUNT
1443									;MEMORY ADDRESS 1
1444	003656	001401					BEQ	25	

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1440 003660 104004 HLT 4 ;BYTE COUNT MEMORY ERROR
1441 ;ADDRESS 1 NOT 0, ERROR
1442 003662 104400 2S: SCOPE
1443
1444 ;BYTE COUNT MEMORY DATA TEST
1445 ;VERIFY THAT ADDRESS 2 OF BYTE COUNT MEMORY
1446 ;CAN BE SET TO 177777 AND CLEARED TO 0
1447
1448 003664 012767 000340 174104 T2S: MOV #340,PS ;DISABLE ALL INTERRUPTS
1449 003672 012767 000100 005344 MOV #100,ICOUNT ;SET UP FOR 100 ITERATIONS
1450 003700 012767 003760 005332 MOV #2S,ESCAPE ;SET UP TO ESCAPE TO NEXT TEST
1451 003706 012705 177777 MOV #177777,RS ;EXPECTED RESULT=177777
1452 003712 012777 000002 005256 MOV #2,ZDHSCR ;SELECT LOCATION 2
1453 ;OF BYTE COUNT MEMORY
1454 003720 012777 177777 005260 MOV #177777,ZDHBC ;WRITE 177777 INTO MEMORY
1455 003726 017704 005254 MOV ZDHBC,R4 ;READ CONTENTS OF MEMORY LOCATION
1456 003732 020504 CMP RS,R4 ;COMPARE EXPECTED AND
1457 003734 001401 BEQ 1S ;RECEIVED MEMORY CONTENTS
1458 003736 104004 HLT 4 ;BYTE COUNT MEMORY ERROR
1459 003740 005005 1S: CLR RS ;EXPECTED RESULT AFTER CLEAR=0
1460 003742 042777 177777 005236 BIC #177777,ZDHBC ;CLEAR MEMORY LOCATION
1461 003750 017704 005232 MOV ZDHBC,R4 ;READ CONTENTS OF BYTE COUNT
1462 ;MEMORY ADDRESS 2
1463 003754 001401 BEQ 2S
1464 003756 104004 HLT 4 ;BYTE COUNT MEMORY ERROR
1465 ;ADDRESS 2 NOT 0, ERROR
1466 003760 104400 2S: SCOPE
1467
1468 ;BYTE COUNT MEMORY DATA TEST
1469 ;VERIFY THAT ADDRESS 3 OF BYTE COUNT MEMORY
1470 ;CAN BE SET TO 177777 AND CLEARED TO 0
1471
1472 003762 012767 000340 174006 T26: MOV #340,PS ;DISABLE ALL INTERRUPTS
1473 003770 012767 000100 005246 MOV #100,ICOUNT ;SET UP FOR 100 ITERATIONS
1474 003776 012767 004056 005234 MOV #2S,ESCAPE ;SET UP TO ESCAPE TO NEXT TEST
1475 004004 012705 177777 MOV #177777,RS ;EXPECTED RESULT=177777
1476 004010 012777 000003 005160 MOV #3,ZDHSCR ;SELECT LOCATION 3
1477 ;OF BYTE COUNT MEMORY
1478 004016 012777 177777 005162 MOV #177777,ZDHBC ;WRITE 177777 INTO MEMORY
1479 004024 017704 005156 MOV ZDHBC,R4 ;READ CONTENTS OF MEMORY LOCATION
1480 004030 020504 CMP RS,R4 ;COMPARE EXPECTED AND
1481 004032 001401 BEQ 1S ;RECEIVED MEMORY CONTENTS
1482 004034 104004 HLT 4 ;BYTE COUNT MEMORY ERROR
1483 004036 005005 1S: CLR RS ;EXPECTED RESULT AFTER CLEAR=0
1484 004040 042777 177777 005140 BIC #177777,ZDHBC ;CLEAR MEMORY LOCATION
1485 004046 017704 005134 MOV ZDHBC,R4 ;READ CONTENTS OF BYTE COUNT
1486 ;MEMORY ADDRESS 3
1487 004052 001401 BEQ 2S
1488 004054 104004 HLT 4 ;BYTE COUNT MEMORY ERROR
1489 ;ADDRESS 3 NOT 0, ERROR
1490 004056 104400 2S: SCOPE
1491
1492 ;BYTE COUNT MEMORY DATA TEST
1493 ;VERIFY THAT ADDRESS 4 OF BYTE COUNT MEMORY
1494 ;CAN BE SET TO 177777 AND CLEARED TO 0
1495

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1496	004060	012767	000340	173710	T27:	MOV	#340,PS	;DISABLE ALL INTERRUPTS
1497	004066	012767	000100	005150		MOV	#100,ICOUNT	;SET UP FOR 100 ITERATIONS
1498	004074	012767	004154	005136		MOV	#25,ESCAPE	;SET UP TO ESCAPE TO NEXT TEST
1499	004102	012705	177777			MOV	#177777,RS	;EXPECTED RESULT=177777
1500	004106	012777	000004	005062		MOV	#4,ZOHSCR	;SELECT LOCATION 4
1501								;OF BYTE COUNT MEMORY
1502	004114	012777	177777	005064		MOV	#177777,ZOHBC	;WRITE 177777 INTO MEMORY
1503	004122	017704	005060			MOV	ZOHBC,R4	;READ CONTENTS OF MEMORY LOCATION
1504	004126	020504				CMP	RS,R4	;COMPARE EXPECTED AND
1505	004130	001401				BEQ	1\$;RECEIVED MEMORY CONTENTS
1506	004134	104004				HLT	4	;BYTE COUNT MEMORY ERROR
1507	004138	005005			1\$:	CLR	RS	;EXPECTED RESULT AFTER CLEAR=0
1508	004136	012777	177777	005042		BIC	#177777,ZOHBC	;CLEAR MEMORY LOCATION
1509	004144	017704	005036			MOV	ZOHBC,R4	;READ CONTENTS OF BYTE COUNT
1510								;MEMORY ADDRESS 4
1511	004150	001401				BEQ	2\$	
1512	004152	104004				HLT	4	;BYTE COUNT MEMORY ERROR
1513								;ADDRESS 4 NOT 0, ERROR
1514	004154	104400			2\$:	SCOPE		
1515								
1516								
1517								
1518								
1519								
1520	004156	012767	000340	173612	T30:	MOV	#340,PS	;DISABLE ALL INTERRUPTS
1521	004164	012767	000100	005052		MOV	#100,ICOUNT	;SET UP FOR 100 ITERATIONS
1522	004172	012767	004252	005040		MOV	#25,ESCAPE	;SET UP TO ESCAPE TO NEXT TEST
1523	004200	012705	177777			MOV	#177777,RS	;EXPECTED RESULT=177777
1524	004204	012777	000005	004764		MOV	#5,ZOHSCR	;SELECT LOCATION 5
1525								;OF BYTE COUNT MEMORY
1526	004212	012777	177777	004766		MOV	#177777,ZOHBC	;WRITE 177777 INTO MEMORY
1527	004220	017704	004762			MOV	ZOHBC,R4	;READ CONTENTS OF MEMORY LOCATION
1528	004224	020504				CMP	RS,R4	;COMPARE EXPECTED AND
1529	004226	001401				BEQ	1\$;RECEIVED MEMORY CONTENTS
1530	004230	104004				HLT	4	;BYTE COUNT MEMORY ERROR
1531	004232	005005			1\$:	CLR	RS	;EXPECTED RESULT AFTER CLEAR=0
1532	004234	012777	177777	004744		BIC	#177777,ZOHBC	;CLEAR MEMORY LOCATION
1533	004242	017704	004740			MOV	ZOHBC,R4	;READ CONTENTS OF BYTE COUNT
1534								;MEMORY ADDRESS 5
1535	004246	001401				BEQ	2\$	
1536	004250	104004				HLT	4	;BYTE COUNT MEMORY ERROR
1537								;ADDRESS 5 NOT 0, ERROR
1538	004252	104400			2\$:	SCOPE		
1539								
1540								
1541								
1542								
1543								
1544	004254	012767	000340	173514	T31:	MOV	#340,PS	;DISABLE ALL INTERRUPTS
1545	004262	012767	000100	004754		MOV	#100,ICOUNT	;SET UP FOR 100 ITERATIONS
1546	004270	012767	004300	004742		MOV	#25,ESCAPE	;SET UP TO ESCAPE TO NEXT TEST
1547	004276	012705	177777			MOV	#177777,RS	;EXPECTED RESULT=177777
1548	004302	012777	000006	004666		MOV	#6,ZOHSCR	;SELECT LOCATION 6
1549								;OF BYTE COUNT MEMORY
1550	004310	012777	177777	004670		MOV	#177777,ZOHBC	;WRITE 177777 INTO MEMORY
1551	004316	017704	004664			MOV	ZOHBC,R4	;READ CONTENTS OF MEMORY LOCATION

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1552 004322 020504      CMP      R5,R4      ;COMPARE EXPECTED AND
1553 004324 001401      BEQ      15        ;RECEIVED MEMORY CONTENTS
1554 004326 104004      HLT      4          ;BYTE COUNT MEMORY ERROR
1555 004330 005005      15:     CLR      R5      ;EXPECTED RESULT AFTER CLEAR=0
1556 004332 042777 177777 004646      BIC      #177777,20HBC ;CLEAR MEMORY LOCATION
1557 004340 017704 004642      MOV      20HBC,R4   ;READ CONTENTS OF BYTE COUNT
1558                                ;MEMORY ADDRESS 6
1559 004344 001401      BEQ      25
1560 004346 104004      HLT      4          ;BYTE COUNT MEMORY ERROR
1561                                ;ADDRESS 6 NOT 0, ERROR
1562 004350 104400      25:     SCOPE
1563
1564                                ;BYTE COUNT MEMORY DATA TEST
1565                                ;VERIFY THAT ADDRESS 7 OF BYTE COUNT MEMORY
1566                                ;CAN BE SET TO 177777 AND CLEARED TO 0
1567
1568 004352 012767 000340 173416      T32:    MOV      #340,PS   ;DISABLE ALL INTERRUPTS
1569 004360 012767 000100 004656      MOV      #100,ICOUNT ;SET UP FOR 100 ITERATIONS
1570 004366 012767 004446 004644      MOV      #25,ESCAPE  ;SET UP TO ESCAPE TO NEXT TEST
1571 004374 012705 177777      MOV      #177777,R5  ;EXPECTED RESULT=177777
1572 004400 012777 000007 004570      MOV      #7,20HSCR  ;SELECT LOCATION 7
1573                                ;OF BYTE COUNT MEMORY
1574 004406 012777 177777 004572      MOV      #177777,20HBC ;WRITE 177777 INTO MEMORY
1575 004414 017704 004566      MOV      20HBC,R4   ;READ CONTENTS OF MEMORY LOCATION
1576 004420 020504      CMP      R5,R4      ;COMPARE EXPECTED AND
1577 004422 001401      BEQ      15        ;RECEIVED MEMORY CONTENTS
1578 004424 104004      HLT      4          ;BYTE COUNT MEMORY ERROR
1579 004426 005005      15:     CLR      R5      ;EXPECTED RESULT AFTER CLEAR=0
1580 004430 042777 177777 004550      BIC      #177777,20HBC ;CLEAR MEMORY LOCATION
1581 004436 017704 004544      MOV      20HBC,R4   ;READ CONTENTS OF BYTE COUNT
1582                                ;MEMORY ADDRESS 7
1583 004442 001401      BEQ      25
1584 004444 104004      HLT      4          ;BYTE COUNT MEMORY ERROR
1585                                ;ADDRESS 7 NOT 0, ERROR
1586 004446 104400      25:     SCOPE
1587
1588                                ;BYTE COUNT MEMORY DATA TEST
1589                                ;VERIFY THAT ADDRESS 10 OF BYTE COUNT MEMORY
1590                                ;CAN BE SET TO 177777 AND CLEARED TO 0
1591
1592 004450 012767 000340 173320      T33:    MOV      #340,PS   ;DISABLE ALL INTERRUPTS
1593 004456 012767 000100 004560      MOV      #100,ICOUNT ;SET UP FOR 100 ITERATIONS
1594 004464 012767 004544 004546      MOV      #25,ESCAPE  ;SET UP TO ESCAPE TO NEXT TEST
1595 004472 012705 177777      MOV      #177777,R5  ;EXPECTED RESULT=177777
1596 004476 012777 000010 004472      MOV      #10,20HSCR  ;SELECT LOCATION 10
1597                                ;OF BYTE COUNT MEMORY
1598 004504 012777 177777 004474      MOV      #177777,20HBC ;WRITE 177777 INTO MEMORY
1599 004512 017704 004470      MOV      20HBC,R4   ;READ CONTENTS OF MEMORY LOCATION
1600 004516 020504      CMP      R5,R4      ;COMPARE EXPECTED AND
1601 004520 001401      BEQ      15        ;RECEIVED MEMORY CONTENTS
1602 004522 104004      HLT      4          ;BYTE COUNT MEMORY ERROR
1603 004524 005005      15:     CLR      R5      ;EXPECTED RESULT AFTER CLEAR=0
1604 004526 042777 177777 004452      BIC      #177777,20HBC ;CLEAR MEMORY LOCATION
1605 004534 017704 004446      MOV      20HBC,R4   ;READ CONTENTS OF BYTE COUNT
1606                                ;MEMORY ADDRESS 10
1607 004540 001401      BEQ      25

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1608 004542 104004          HLT      4          ; BYTE COUNT MEMORY ERROR
1609                                     ; ADDRESS 10 NOT 0, ERROR
1610 004544 104400          2S:    SCOPE
1611
1612                                     ; BYTE COUNT MEMORY DATA TEST
1613                                     ; VERIFY THAT ADDRESS 11 OF BYTE COUNT MEMORY
1614                                     ; CAN BE SET TO 177777 AND CLEARED TO 0
1615
1616 004546 012767 000340 173222 T34:    MOV      #340,PS          ; DISABLE ALL INTERRUPTS
1617 004554 012767 000100 004462      MOV      #100,ICOUNT     ; SET UP FOR 100 ITERATIONS
1618 004562 012767 004642 004450      MOV      #25,ESCAPE     ; SET UP TO ESCAPE TO NEXT TEST
1619 004570 012705 177777          MOV      #177777,R5     ; EXPECTED RESULT=177777
1620 004574 012777 000011 004374      MOV      #11,20HSCR     ; SELECT LOCATION 11
1621                                     ; OF BYTE COUNT MEMORY
1622 004602 012777 177777 004376      MOV      #177777,20HBC  ; WRITE 177777 INTO MEMORY
1623 004610 017704 004372          MOV      20HBC,R4       ; READ CONTENTS OF MEMORY LOCATION
1624 004614 020504          CMP      R5,R4          ; COMPARE EXPECTED AND
1625 004616 001401          BEQ     1S              ; RECEIVED MEMORY CONTENTS
1626 004620 104004          HLT     4              ; BYTE COUNT MEMORY ERROR
1627 004622 005005          CLR     R5              ; EXPECTED RESULT AFTER CLEAR=0
1628 004624 042777 177777 004354      BIC     #177777,20HBC   ; CLEAR MEMORY LOCATION
1629 004632 017704 004350          MOV      20HBC,R4       ; READ CONTENTS OF BYTE COUNT
1630                                     ; MEMORY ADDRESS 11
1631 004636 001401          BEQ     2S              ;
1632 004640 104004          HLT     4              ; BYTE COUNT MEMORY ERROR
1633                                     ; ADDRESS 11 NOT 0, ERROR
1634 004642 104400          2S:    SCOPE
1635
1636                                     ; BYTE COUNT MEMORY DATA TEST
1637                                     ; VERIFY THAT ADDRESS 12 OF BYTE COUNT MEMORY
1638                                     ; CAN BE SET TO 177777 AND CLEARED TO 0
1639
1640 004644 012767 000340 173124 T35:    MOV      #340,PS          ; DISABLE ALL INTERRUPTS
1641 004652 012767 000100 004364      MOV      #100,ICOUNT     ; SET UP FOR 100 ITERATIONS
1642 004660 012767 004740 004352      MOV      #25,ESCAPE     ; SET UP TO ESCAPE TO NEXT TEST
1643 004666 012705 177777          MOV      #177777,R5     ; EXPECTED RESULT=177777
1644 004672 012777 000012 004276      MOV      #12,20HSCR     ; SELECT LOCATION 12
1645                                     ; OF BYTE COUNT MEMORY
1646 004700 012777 177777 004300      MOV      #177777,20HBC  ; WRITE 177777 INTO MEMORY
1647 004706 017704 004274          MOV      20HBC,R4       ; READ CONTENTS OF MEMORY LOCATION
1648 004712 020504          CMP      R5,R4          ; COMPARE EXPECTED AND
1649 004714 001401          BEQ     1S              ; RECEIVED MEMORY CONTENTS
1650 004716 104004          HLT     4              ; BYTE COUNT MEMORY ERROR
1651 004720 005005          CLR     R5              ; EXPECTED RESULT AFTER CLEAR=0
1652 004722 042777 177777 004256      BIC     #177777,20HBC   ; CLEAR MEMORY LOCATION
1653 004730 017704 004252          MOV      20HBC,R4       ; READ CONTENTS OF BYTE COUNT
1654                                     ; MEMORY ADDRESS 12
1655 004734 001401          BEQ     2S              ;
1656 004736 104004          HLT     4              ; BYTE COUNT MEMORY ERROR
1657                                     ; ADDRESS 12 NOT 0, ERROR
1658 004740 104400          2S:    SCOPE
1659
1660                                     ; BYTE COUNT MEMORY DATA TEST
1661                                     ; VERIFY THAT ADDRESS 13 OF BYTE COUNT MEMORY
1662                                     ; CAN BE SET TO 177777 AND CLEARED TO 0
1663

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1664	004742	012767	000340	173026	T36:	MOV	#340,PS	:DISABLE ALL INTERRUPTS
1665	004750	012767	000100	004266		MOV	#100,ICOUNT	:SET UP FOR 100 ITERATIONS
1666	004756	012767	005036	004254		MOV	#2\$,ESCAPE	:SET UP TO ESCAPE TO NEXT TEST
1667	004764	012705	177777			MOV	#177777,RS	:EXPECTED RESULT=177777
1668	004770	012777	000013	004200		MOV	#13,2DHSCR	:SELECT LOCATION 13
1669								:OF BYTE COUNT MEMORY
1670	004776	012777	177777	004202		MOV	#177777,2DHBC	:WRITE 177777 INTO MEMORY
1671	005004	017704	004176			MOV	2DHBC,R4	:READ CONTENTS OF MEMORY LOCATION
1672	005010	020504				CMP	RS,R4	:COMPARE EXPECTED AND
1673	005012	001401				BEQ	1\$:RECEIVED MEMORY CONTENTS
1674	005014	104004				HLT	4	:BYTE COUNT MEMORY ERROR
1675	005016	005005			1\$:	CLR	RS	:EXPECTED RESULT AFTER CLEAR=0
1676	005020	042777	177777	004160		BIC	#177777,2DHBC	:CLEAR MEMORY LOCATION
1677	005026	017704	004154			MOV	2DHBC,R4	:READ CONTENTS OF BYTE COUNT
1678								:MEMORY ADDRESS 13
1679	005032	001401				BEQ	2\$	
1680	005034	104004				HLT	4	:BYTE COUNT MEMORY ERROR
1681								:ADDRESS 13 NOT 0, ERROR
1682	005036	104400			2\$:	SCOPE		
1683								
1684								:BYTE COUNT MEMORY DATA TEST
1685								:VERIFY THAT ADDRESS 14 OF BYTE COUNT MEMORY
1686								:CAN BE SET TO 177777 AND CLEARED TO 0
1687								
1688	005040	012767	000340	172730	T37:	MOV	#340,PS	:DISABLE ALL INTERRUPTS
1689	005046	012767	000100	004170		MOV	#100,ICOUNT	:SET UP FOR 100 ITERATIONS
1690	005054	012767	005134	004156		MOV	#2\$,ESCAPE	:SET UP TO ESCAPE TO NEXT TEST
1691	005062	012705	177777			MOV	#177777,RS	:EXPECTED RESULT=177777
1692	005066	012777	000014	004102		MOV	#14,2DHSCR	:SELECT LOCATION 14
1693								:OF BYTE COUNT MEMORY
1694	005074	012777	177777	004104		MOV	#177777,2DHBC	:WRITE 177777 INTO MEMORY
1695	005102	017704	004100			MOV	2DHBC,R4	:READ CONTENTS OF MEMORY LOCATION
1696	005106	020504				CMP	RS,R4	:COMPARE EXPECTED AND
1697	005110	001401				BEQ	1\$:RECEIVED MEMORY CONTENTS
1698	005112	104004				HLT	4	:BYTE COUNT MEMORY ERROR
1699	005114	005005			1\$:	CLR	RS	:EXPECTED RESULT AFTER CLEAR=0
1700	005116	042777	177777	004062		BIC	#177777,2DHBC	:CLEAR MEMORY LOCATION
1701	005124	017704	004056			MOV	2DHBC,R4	:READ CONTENTS OF BYTE COUNT
1702								:MEMORY ADDRESS 14
1703	005130	001401				BEQ	2\$	
1704	005132	104004				HLT	4	:BYTE COUNT MEMORY ERROR
1705								:ADDRESS 14 NOT 0, ERROR
1706	005134	104400			2\$:	SCOPE		
1707								
1708								:BYTE COUNT MEMORY DATA TEST
1709								:VERIFY THAT ADDRESS 15 OF BYTE COUNT MEMORY
1710								:CAN BE SET TO 177777 AND CLEARED TO 0
1711								
1712	005136	012767	000340	172632	T40:	MOV	#340,PS	:DISABLE ALL INTERRUPTS
1713	005144	012767	000100	004072		MOV	#100,ICOUNT	:SET UP FOR 100 ITERATIONS
1714	005152	012767	005232	004060		MOV	#2\$,ESCAPE	:SET UP TO ESCAPE TO NEXT TEST
1715	005160	012705	177777			MOV	#177777,RS	:EXPECTED RESULT=177777
1716	005164	012777	000015	004004		MOV	#15,2DHSCR	:SELECT LOCATION 15
1717								:OF BYTE COUNT MEMORY
1718	005172	012777	177777	004006		MOV	#177777,2DHBC	:WRITE 177777 INTO MEMORY
1719	005200	017704	004002			MOV	2DHBC,R4	:READ CONTENTS OF MEMORY LOCATION

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1720	005204	020504				CMP	R5,R4		:COMPARE EXPECTED AND
1721	005206	001401				BEQ	15		:RECEIVED MEMORY CONTENTS
1722	005210	104004				HLT	4		:BYTE COUNT MEMORY ERROR
1723	005212	005005			15:	CLR	R5		:EXPECTED RESULT AFTER CLEAR=0
1724	005214	042777	177777	003764		BIC	#177777,20HBC		:CLEAR MEMORY LOCATION
1725	005222	017704	003760			MOV	20HBC,R4		:READ CONTENTS OF BYTE COUNT
1726									:MEMORY ADDRESS 15
1727	005226	001401				BEQ	25		
1728	005230	104004				HLT	4		:BYTE COUNT MEMORY ERROR
1729									:ADDRESS 15 NOT 0, ERROR
1730	005232	104400			25:	SCOPE			
1731									
1732									:BYTE COUNT MEMORY DATA TEST
1733									:VERIFY THAT ADDRESS 16 OF BYTE COUNT MEMORY
1734									:CAN BE SET TO 177777 AND CLEARED TO 0
1735									
1736	005234	012767	000340	172534	T41:	MOV	#340,PS		:DISABLE ALL INTERRUPTS
1737	005242	012767	000100	003774		MOV	#100,ICOUNT		:SET UP FOR 100 ITERATIONS
1738	005250	012767	005330	003762		MOV	#25,ESCAPE		:SET UP TO ESCAPE TO NEXT TEST
1739	005256	012705	177777			MOV	#177777,R5		:EXPECTED RESULT=177777
1740	005262	012777	000016	003706		MOV	#16,20HSCR		:SELECT LOCATION 16
1741									:OF BYTE COUNT MEMORY
1742	005270	012777	177777	003710		MOV	#177777,20HBC		:WRITE 177777 INTO MEMORY
1743	005276	017704	003704			MOV	20HBC,R4		:READ CONTENTS OF MEMORY LOCATION
1744	005302	020504				CMP	R5,R4		:COMPARE EXPECTED AND
1745	005304	001401				BEQ	15		:RECEIVED MEMORY CONTENTS
1746	005306	104004				HLT	4		:BYTE COUNT MEMORY ERROR
1747	005310	005005			15:	CLR	R5		:EXPECTED RESULT AFTER CLEAR=0
1748	005312	042777	177777	003666		BIC	#177777,20HBC		:CLEAR MEMORY LOCATION
1749	005320	017704	003662			MOV	20HBC,R4		:READ CONTENTS OF BYTE COUNT
1750									:MEMORY ADDRESS 16
1751	005324	001401				BEQ	25		
1752	005326	104004				HLT	4		:BYTE COUNT MEMORY ERROR
1753									:ADDRESS 16 NOT 0, ERROR
1754	005330	104400			25:	SCOPE			
1755									
1756									:BYTE COUNT MEMORY DATA TEST
1757									:VERIFY THAT ADDRESS 17 OF BYTE COUNT MEMORY
1758									:CAN BE SET TO 177777 AND CLEARED TO 0
1759									
1760	005332	012767	000340	172436	T42:	MOV	#340,PS		:DISABLE ALL INTERRUPTS
1761	005340	012767	000100	003676		MOV	#100,ICOUNT		:SET UP FOR 100 ITERATIONS
1762	005346	012767	005426	003664		MOV	#25,ESCAPE		:SET UP TO ESCAPE TO NEXT TEST
1763	005354	012705	177777			MOV	#177777,R5		:EXPECTED RESULT=177777
1764	005360	012777	000017	003610		MOV	#17,20HSCR		:SELECT LOCATION 17
1765									:OF BYTE COUNT MEMORY
1766	005366	012777	177777	003612		MOV	#177777,20HBC		:WRITE 177777 INTO MEMORY
1767	005374	017704	003606			MOV	20HBC,R4		:READ CONTENTS OF MEMORY LOCATION
1768	005400	020504				CMP	R5,R4		:COMPARE EXPECTED AND
1769	005402	001401				BEQ	15		:RECEIVED MEMORY CONTENTS
1770	005404	104004				HLT	4		:BYTE COUNT MEMORY ERROR
1771	005406	005005			15:	CLR	R5		:EXPECTED RESULT AFTER CLEAR=0
1772	005410	042777	177777	003570		BIC	#177777,20HBC		:CLEAR MEMORY LOCATION
1773	005416	017704	003564			MOV	20HBC,R4		:READ CONTENTS OF BYTE COUNT
1774									:MEMORY ADDRESS 17
1775	005422	001401				BEQ	25		

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1776	005424	104004			HLT	4		; BYTE COUNT MEMORY ERROR
1777								; ADDRESS 17 NOT 0, ERROR
1778	005426	104400			2S:	SCOPE		
1779								
1780								; BUS ADDRESS MEMORY TEST
1781								; CLEAR ALL LOCATIONS IN BUS ADDRESS MEMORY
1782								; SET SELECTED LOCATION TO VALUE 177777
1783								; VERIFY THAT SELECTED LOCATION WAS SET
1784								TO 177777.
1785								; VERIFY THAT NO OTHER LOCATION WAS MODIFIED.
1786								
1787	005430	012767	000340	172398	7S:	MOV	#340,PS	; DISABLE ALL INTERRUPTS
1788	005436	012767	000100	003500		MOV	#100,ICOUNT	; SET UP FOR 100 ITERATIONS
1789	005444	012767	005602	003502		MOV	#6\$,ESCAPE	; SET UP TO ESCAPE TO NEXT TEST
1790	005452	012767	005476	003504		MOV	#2\$,FREEZ1	; SET UP TO LOOP WITH DATA
1791	005460	012700	000020	003506		MOV	#20,R0	; SET UP TO TEST 20(OCTAL)
1792								LOCATIONS IN BUS ADDRESS MEMORY
1793	005464	005003				CLR	R3	; FIRST LOCATION TO BE
1794								WRITTEN INTO IS 0
1795	005466	012701	000020		1S:	MOV	#20,R1	; SET UP TO CLEAR 20 (OCTAL)
1796								LOCATIONS IN BUS ADDRESS MEMORY
1797	005472	005077	003500			CLR	2DHSCR	; START AT LOCATION 0
1798	005476	005077	003502		2S:	CLR	2DHBA	; CLEAR LOCATION IN
1799								BUS ADDRESS MEMORY
1800	005502	005277	003470			INC	2DHSCR	; ADVANCE TO NEXT LOCATION
1801	005506	005301				DEC	R1	; CONTINUE CLEARING
1802	005510	001372				BNE	2\$; IF NOT DONE
1803	005512	010377	003460			MOV	R3,2DHSCR	; SELECT ADDRESS TO BE TESTED
1804	005516	012777	177777	003460		MOV	#177777,2DHBA	; WRITE 177777 INTO LOCATION
1805	005524	005077	003446			CLR	2DHSCR	; ADDRESS LOCATION 0
1806	005530	012701	000020			MOV	#20,R1	; SET UP TO CHECK ALL ADDRESSES
1807								IN BUS ADDRESS MEMORY
1808	005534	012705	177777		3S:	MOV	#177777,R5	; 177777=EXPECTED RESULT
1809								; IF ADDRESS READ IS LOCATION
1810								WRITTEN INTO
1811	005540	017704	003440			MOV	2DHBA,R4	; READ MEMORY LOCATION
1812	005544	027703	003426			CMP	2DHSCR,R3	; IF LINE NUMBER=ADDRESS
1813								OF LOCATION WRITTEN INTO
1814								EXPECTED CONTENTS=177777
1815	005550	001401				BEQ	4\$	
1816	005552	005005				CLR	R5	; OTHERWISE, EXPECTED RESULTS=0
1817	005554	020504			4S:	CMP	R5,R4	; DOES MEMORY LOCATION CONTAIN
1818	005556	001401				BEQ	5\$	EXPECTED RESULT
1819	005560	104003				HLT	3	; BUS ADDRESS MEMORY ERROR
1820	005562	104410			5S:	SCOPE 1		; CHECK FOR LOOP WITH CURRENT DATA
1821	005564	005277	003406			INC	2DHSCR	; CHECK CONTENTS OF NEXT LOCATION
1822	005570	005301				DEC	R1	
1823	005572	001360				BNE	3\$	
1824	005574	005203				INC	R3	; NEXT ADDRESS TO BE WRITTEN
1825	005576	005300				DEC	R0	
1826	005500	001332				BNE	1\$	
1827	005602	104400			6S:	SCOPE		; CHECK FOR ITERATIONS, LOOP
1828								
1829								; BUS ADDRESS MEMORY TEST
1830								; CLEAR ALL LOCATIONS IN BUS ADDRESS MEMORY
1831								; SET SELECTED LOCATION TO VALUE 125252

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1832                                     ;VERIFY THAT SELECTED LOCATION WAS SET
1833                                     ;TO 125252.
1834                                     ;VERIFY THAT NO OTHER LOCATION WAS MODIFIED.
1835
1836 005604 012767 000340 172164 T44: MOV #340,PS ;DISABLE ALL INTERRUPTS
1837 005612 012767 000100 003424 MOV #100,ICOUNT ;SET UP FOR 100 ITERATIONS
1838 005620 012767 005756 003412 MOV #6$,ESCAPE ;SET UP TO ESCAPE TO NEXT TEST
1839 005626 012767 005652 003406 MOV #2$,FREEZ1 ;SET UP TO LOOP WITH DATA
1840 005634 012700 000020 MOV #20,R0 ;SET UP TO TEST 20(OCTAL)
1841                                     ;LOCATIONS IN BUS ADDRESS MEMORY
1842 005640 005003 CLR R3 ;FIRST LOCATION TO BE
1843                                     ;WRITTEN INTO IS 0
1844 005642 012701 000020 15: MOV #20,R1 ;SET UP TO CLEAR 20 (OCTAL);
1845                                     ;LOCATIONS IN BUS ADDRESS MEMORY
1846 005646 005077 003324 CLR 20HSCR ;START AT LOCATION 0
1847 005652 005077 003326 25: CLR 20HBA ;CLEAR LOCATION IN
1848                                     ;BUS ADDRESS MEMORY
1849 005656 005277 003314 INC 20HSCR ;ADVANCE TO NEXT LOCATION
1850 005662 005301 DEC R1 ;CONTINUE CLEARING
1851 005664 001372 BNE 25 ;IF NOT DONE
1852 005666 010377 003304 MOV R3,20HSCR ;SELECT ADDRESS TO BE TESTED
1853 005672 012777 125252 003304 MOV #125252,20HBA ;WRITE 125252 INTO LOCATION
1854 005700 005077 003272 CLR 20HSCR ;ADDRESS LOCATION 0
1855 005704 012701 000020 MOV #20,R1 ;SET UP TO CHECK ALL ADDRESSES
1856                                     ;IN BUS ADDRESS MEMORY
1857 005710 012705 125252 35: MOV #125252,R5 ;125252=EXPECTED RESULT
1858                                     ;IF ADDRESS READ IS LOCATION
1859                                     ;WRITTEN INTO
1860 005714 017704 003264 MOV 20HBA,R4 ;READ MEMORY LOCATION
1861 005720 027703 003252 CMP 20HSCR,R3 ;IF LINE NUMBER=ADDRESS
1862                                     ;OF LOCATION WRITTEN INTO
1863                                     ;EXPECTED CONTENTS=125252
1864 005724 001401 BEQ 45
1865 005726 005005 CLR R5 ;OTHERWISE, EXPECTED RESULTS=0
1866 005730 020504 45: CMP R5,R4 ;DOES MEMORY LOCATION CONTAIN
1867 005732 001401 BEQ 55 ;EXPECTED RESULT
1868 005734 104003 HLT 3 ;BUS ADDRESS MEMORY ERROR
1869 005736 104410 55: SCOPE1 ;CHECK FOR LOOP WITH CURRENT DATA
1870 005740 005277 003232 INC 20HSCR ;CHECK CONTENTS OF NEXT LOCATION
1871 005744 005301 DEC R1
1872 005746 001360 BNE 35
1873 005750 005203 INC R3 ;NEXT ADDRESS TO BE WRITTEN
1874 005752 005300 DEC R0
1875 005754 001332 BNE 15
1876 005756 104400 65: SCOPE ;CHECK FOR ITERATIONS, LOOP
1877
1878 ;BUS ADDRESS MEMORY TEST
1879 ;CLEAR ALL LOCATIONS IN BUS ADDRESS MEMORY
1880 ;SET SELECTED LOCATION TO VALUE 52525
1881 ;VERIFY THAT SELECTED LOCATION WAS SET
1882 ;TO 52525.
1883 ;VERIFY THAT NO OTHER LOCATION WAS MODIFIED.
1884
1885 005760 012767 000340 172010 T45: MOV #340,PS ;DISABLE ALL INTERRUPTS
1886 005766 012767 000100 003250 MOV #100,ICOUNT ;SET UP FOR 100 ITERATIONS
1887 005774 012767 006132 003236 MOV #6$,ESCAPE ;SET UP TO ESCAPE TO NEXT TEST

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1888	006002	012767	006026	003232		MOV	#28, FREEZ1	: SET UP TO LOOP WITH DATA
1889	006010	012700	000020			MOV	#20, R0	: SET UP TO TEST 20(OCTAL)
1890								: LOCATIONS IN BUS ADDRESS MEMORY
1891	006014	005003				CLR	R3	: FIRST LOCATION TO BE
1892								: WRITTEN INTO IS 0
1893	006016	012701	000020		15:	MOV	#20, R1	: SET UP TO CLEAR 20 (OCTAL)
1894								: LOCATIONS IN BUS ADDRESS MEMORY
1895	006022	005077	003150			CLR	20HSCR	: START AT LOCATION 0
1896	006026	005077	003152		25:	CLR	20HBA	: CLEAR LOCATION IN
1897								: BUS ADDRESS MEMORY
1898	006032	005277	003140			INC	20HSCR	: ADVANCE TO NEXT LOCATION
1899	006036	005301				DEC	R1	: CONTINUE CLEARING
1900	006040	001372				BNE	25	: IF NOT DONE
1901	006042	010377	003130			MOV	R3, 20HSCR	: SELECT ADDRESS TO BE TESTED
1902	006046	012777	052525	003130		MOV	#52525, 20HBA	: WRITE 52525 INTO LOCATION
1903	006054	005077	003115			CLR	20HSCR	: ADDRESS LOCATION 0
1904	006060	012701	000020			MOV	#20, R1	: SET UP TO CHECK ALL ADDRESSES
1905								: IN BUS ADDRESS MEMORY
1906	006064	012705	052525		35:	MOV	#52525, R5	: 52525=EXPECTED RESULT
1907								: IF ADDRESS READ IS LOCATION
1908								: WRITTEN INTO
1909	006070	017704	003110			MOV	20HBA, R4	: READ MEMORY LOCATION
1910	006074	027703	003076			CMP	20HSCR, R3	: IF LINE NUMBER=ADDRESS
1911								: OF LOCATION WRITTEN INTO
1912								: EXPECTED CONTENTS=52525
1913	006100	001401				BEG	45	
1914	006102	005005				CLR	R5	: OTHERWISE, EXPECTED RESULTS=0
1915	006104	020504			45:	CMP	R5, R4	: DOES MEMORY LOCATION CONTAIN
1916	006106	001401				BEG	55	: EXPECTED RESULT
1917	006110	104003				HLT	3	: BUS ADDRESS MEMORY ERROR
1918	006112	104410			55:	SCOPE 1		: CHECK FOR LOOP WITH CURRENT DATA
1919	006114	005277	003056			INC	20HSCR	: CHECK CONTENTS OF NEXT LOCATION
1920	006120	005301				DEC	R1	
1921	006122	001360				BNE	J5	
1922	006124	005203				INC	R3	: NEXT ADDRESS TO BE WRITTEN
1923	006126	005300				DEC	R0	
1924	006130	001332				BNE	15	
1925	006132	104400			65:	SCOPE		: CHECK FOR ITERATIONS, LOOP
1926								
1927								: BYTE COUNT MEMORY TEST
1928								: CLEAR ALL LOCATIONS IN BYTE COUNT MEMORY
1929								: SET SELECTED LOCATION TO VALUE 177777
1930								: VERIFY THAT SELECTED LOCATION WAS SET
1931								: TO 177777.
1932								: VERIFY THAT NO OTHER LOCATION WAS MODIFIED.
1933								
1934	006134	012767	000340	171634	T46:	MOV	#340, PS	: DISABLE ALL INTERRUPTS
1935	006142	012767	000100	003074		MOV	#100, ICOUNT	: SET UP FOR 100 ITERATIONS
1936	006150	012767	006306	003062		MOV	#65, ESCAPE	: SET UP TO ESCAPE TO NEXT TEST
1937	006156	012767	006202	003056		MOV	#28, FREEZ1	: SET UP TO LOOP WITH DATA
1938	006164	012700	000020			MOV	#20, R0	: SET UP TO TEST 20(OCTAL)
1939								: LOCATIONS IN BYTE COUNT MEMORY
1940	006170	005003				CLR	R3	: FIRST LOCATION TO BE
1941								: WRITTEN INTO IS 0
1942	006172	012701	000020		15:	MOV	#20, R1	: SET UP TO CLEAR 20 (OCTAL)
1943								: LOCATIONS IN BYTE COUNT MEMORY

1944	006176	005077	002774		CLR	20HSCR	: START AT LOCATION 0	
1945	006202	005077	003000	25:	CLR	20HBC	: CLEAR LOCATION IN	
1946							: BYTE COUNT MEMORY	
1947	006206	005277	002764		INC	20HSCR	: ADVANCE TO NEXT LOCATION	
1948	006212	005301			DEC	R1	: CONTINUE CLEARING	
1949	006214	001372			BNE	25	: IF NOT DONE	
1950	006216	010377	002754		MOV	R3, 20HSCR	: SELECT ADDRESS TO BE TESTED	
1951	006222	012777	177777	002756	MOV	0177777, 20HBC	: WRITE 177777 INTO LOCATION	
1952	006230	005077	002742		CLR	20HSCR	: ADDRESS LOCATION 0	
1953	006234	012701	000020		MOV	#20, R1	: SET UP TO CHECK ALL ADDRESSES	
1954							: IN BYTE COUNT MEMORY	
1955	006240	012705	177777	35:	MOV	0177777, R5	: 177777=EXPECTED RESULT	
1956							: IF ADDRESS READ IS LOCATION	
1957							: WRITTEN INTO	
1958	006244	017704	002736		MOV	20HBC, R4	: READ MEMORY LOCATION	
1959	006250	027703	002722		CMP	20HSCR, R3	: IF LINE NUMBER=ADDRESS	
1960							: OF LOCATION WRITTEN INTO	
1961							: EXPECTED CONTENTS=177777	
1962	006254	001401			BEG	45		
1963	006256	005305			CLR	R5	: OTHERWISE, EXPECTED RESULTS=0	
1964	006260	020504		45:	CMP	R5, R4	: DOES MEMORY LOCATION CONTAIN	
1965	006262	001401			BEG	55	: EXPECTED RESULT	
1966	006264	104004			HLT	4	: BYTE COUNT MEMORY ERROR	
1967	006266	104410		55:	SCOPE 1		: CHECK FOR LOOP WITH CURRENT DATA	
1968	006270	005277	002702		INC	20HSCR	: CHECK CONTENTS OF NEXT LOCATION	
1969	006274	005301			DEC	R1		
1970	006276	001360			BNE	35		
1971	006300	005203			INC	R3	: NEXT ADDRESS TO BE WRITTEN	
1972	006302	005300			DEC	R0		
1973	006304	001332			BNE	15		
1974	006306	104400		65:	SCOPE		: CHECK FOR ITERATIONS, LOOP	
1975								
1976							: BYTE COUNT MEMORY TEST	
1977							: CLEAR ALL LOCATIONS IN BYTE COUNT MEMORY	
1978							: SET SELECTED LOCATION TO VALUE 125252	
1979							: VERIFY THAT SELECTED LOCATION WAS SET	
1980							: TO 125252.	
1981							: VERIFY THAT NO OTHER LOCATION WAS MODIFIED.	
1982								
1983	006310	012767	000340	171460	T47:	MOV	#340, PS	: DISABLE ALL INTERRUPTS
1984	006316	012767	000100	002720		MOV	#100, ICOUNT	: SET UP FOR 100 ITERATIONS
1985	006324	012757	006462	002706		MOV	#65, ESCAPE	: SET UP TO ESCAPE TO NEXT TEST
1986	006332	012767	006356	002702		MOV	#25, FREEZ1	: SET UP TO LOOP WITH DATA
1987	006340	012700	000020			MOV	#20, R0	: SET UP TO TEST 20(OCTAL)
1988							: LOCATIONS IN BYTE COUNT MEMORY	
1989	006344	005003			CLR	R3	: FIRST LOCATION TO BE	
1990							: WRITTEN INTO IS 0	
1991	006346	012701	000020		15:	MOV	#20, R1	: SET UP TO CLEAR 20 (OCTAL)
1992							: LOCATIONS IN BYTE COUNT MEMORY	
1993	006352	005077	002620		CLR	20HSCR	: START AT LOCATION 0	
1994	006356	005077	002624	25:	CLR	20HBC	: CLEAR LOCATION IN	
1995							: BYTE COUNT MEMORY	
1996	006362	005277	002610		INC	20HSCR	: ADVANCE TO NEXT LOCATION	
1997	006366	005301			DEC	R1	: CONTINUE CLEARING	
1998	006370	001372			BNE	25	: IF NOT DONE	
1999	006372	010377	002600		MOV	R3, 20HSCR	: SELECT ADDRESS TO BE TESTED	

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2000 006376 012777 125252 002602      MOV      #125252,20HBC      ;WRITE 125252 INTO LOCATION
2001 006404 005077 002566              CLR      20HSCR           ;ADDRESS LOCATION 0
2002 006410 012701 000020              MOV      #20,R1          ;SET UP TO CHECK ALL ADDRESSES
2003                                ;IN BYTE COUNT MEMORY
2004 006414 012705 125252      38:     MOV      #125252,R5    ;125252=EXPECTED RESULT
2005                                ;IF ADDRESS READ IS LOCATION
2006                                ;WRITTEN INTO
2007 006420 017704 002562      MOV      20HBC,R4        ;READ MEMORY LOCATION
2008 006424 027703 002546      CMP      20HSCR,R3       ;IF LINE NUMBER=ADDRESS
2009                                ;OF LOCATION WRITTEN INTO
2010                                ;EXPECTED CONTENTS=125252
2011 006430 001401              BEQ      48              ;OTHERWISE, EXPECTED RESULTS=0
2012 006432 005005              CLR      R5              ;DOES MEMORY LOCATION CONTAIN
2013 006434 020504      48:     CMP      R5,R4          ;EXPECTED RESULT
2014 006436 001401              BEQ      58              ;EXPECTED RESULT
2015 006440 104004              HLT                     ;BYTE COUNT MEMORY ERROR
2016 006442 104410      58:     SCOPE1             ;CHECK FOR LOOP WITH CURRENT DATA
2017 006444 005277 002526      INC      20HSCR          ;CHECK CONTENTS OF NEXT LOCATION
2018 006450 005301              DEC      R1
2019 006452 001360              BNE     38
2020 006454 005203              INC      R3              ;NEXT ADDRESS TO BE WRITTEN
2021 006456 005300              DEC      R0
2022 006460 001332              BNE     18
2023 006462 104400      68:     SCOPE              ;CHECK FOR ITERATIONS, LOOP

;BYTE COUNT MEMORY TEST
;CLEAR ALL LOCATIONS IN BYTE COUNT MEMORY
;SET SELECTED LOCATION TO VALUE 52525
;VERIFY THAT SELECTED LOCATION WAS SET
;TO 52525.
;VERIFY THAT NO OTHER LOCATION WAS MODIFIED.

2032 006464 012767 00034C 171304 T50:  MOV      #340,PS        ;DISABLE ALL INTERRUPTS
2033 006472 012767 000100 002544  MOV      #100,ICOUNT    ;SET UP FOR 100 ITERATIONS
2034 006500 012767 006636 002532  MOV      #68,ESCAPE     ;SET UP TO ESCAPE TO NEXT TEST
2035 006506 012767 006532 002526  MOV      #28,FREEZ1     ;SET UP TO LOOP WITH DATA
2036 006514 012700 000020              MOV      #20,R0        ;SET UP TO TEST 20(OCTAL)
2037                                ;LOCATIONS IN BYTE COUNT MEMORY
2038 006520 005003              CLR      R3            ;FIRST LOCATION TO BE
2039                                ;WRITTEN INTO IS 0
2040 005522 012701 000020      18:     MOV      #20,R1        ;SET UP TO CLEAR 20 (OCTAL)
2041                                ;LOCATIONS IN BYTE COUNT MEMORY
2042 006526 005077 002444              CLR      20HSCR        ;START AT LOCATION 0
2043 006532 005077 002450      28:     CLR      20HBC        ;CLEAR LOCATION IN
2044                                ;BYTE COUNT MEMORY
2045 006536 005277 002434              INC      20HSCR        ;ADVANCE TO NEXT LOCATION
2046 006542 005301              DEC      R1            ;CONTINUE CLEARING
2047 006544 001372              BNE     28             ;IF NOT DONE
2048 006546 010377 002424              MOV      R3,20HSCR     ;SELECT ADDRESS TO BE TESTED
2049 006552 012777 052525 002426  MOV      #52525,20HBC  ;WRITE 52525 INTO LOCATION
2050 006560 005077 002412              CLR      20HSCR        ;ADDRESS LOCATION 0
2051 006564 012701 000020              MOV      #20,R1        ;SET UP TO CHECK ALL ADDRESSES
2052                                ;IN BYTE COUNT MEMORY
2053 006570 012705 052525      38:     MOV      #52525,R5    ;52525=EXPECTED RESULT
2054                                ;IF ADDRESS READ IS LOCATION
2055                                ;WRITTEN INTO

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2056	006574	017704	002406		MOV	20HBC,R4	:READ MEMORY LOCATION	
2057	006600	027703	002372		CMP	20HSCR,R3	:IF LINE NUMBER=ADDRESS	
2058							:OF LOCATION WRITTEN INTO	
2059							:EXPECTED CONTENTS=52525	
2060	006604	001401			BEQ	48		
2061	006606	005005			CLR	R5	:OTHERWISE, EXPECTED RESULTS=0	
2062	006610	020504		48:	CMP	R5,R4	:DOES MEMORY LOCATION CONTAIN	
2063	006612	001401			BEQ	58	:EXPECTED RESULT	
2064	006614	104004			HLT	4	:BYTE COUNT MEMORY ERROR	
2065	006616	104410		58:	SCOPE1		:CHECK FOR LOOP WITH CURRENT DATA	
2066	006620	005277	002352		INC	20HSCR	:CHECK CONTENTS OF NEXT LOCATION	
2067	006624	005301			DEC	R1		
2068	006626	001360			BNE	38		
2069	006630	005203			INC	R3	:NEXT ADDRESS TO BE WRITTEN	
2070	006632	005300			DEC	R0		
2071	006634	001332			BNE	18		
2072	006636	104400		68:	SCOPE		:CHECK FOR ITERATIONS, LOOP	
2073								
2074							:BUS ADDRESS MEMORY TEST	
2075							:SET ALL LOCATIONS IN BUS ADDRESS MEMORY TO 177777	
2076							:SET SELECTED LOCATION TO VALUE 0	
2077							:VERIFY THAT SELECTED LOCATION WAS SET	
2078							:TO 0.	
2079							:VERIFY THAT NO OTHER LOCATION WAS MODIFIED.	
2080								
2081	006640	012767	000340	171130	TS1:	MOV	#340,PS	:DISABLE ALL INTERRUPTS
2082	006646	012767	000100	002370		MOV	#100,ICOUNT	:SET UP FOR 100 ITERATIONS
2083	006654	012767	007014	002356		MOV	#68,ESCAPE	:SET UP TO ESCAPE TO NEXT TEST
2084	006662	012767	006706	002352		MOV	#28,FREEZ1	:SET UP TO LOOP WITH DATA
2085	006670	012700	000020			MOV	#20,R0	:SET UP TO TEST 20(OCTAL)
2086								:LOCATIONS IN BUS ADDRESS MEMORY
2087	006674	005003				CLR	R3	:FIRST LOCATION TO BE
2088								:WRITTEN INTO IS 0
2089	006676	012701	000020		18:	MOV	#20,R1	:SET UP TO SET 20(OCTAL)
2090								:LOCATIONS IN BUS ADDRESS MEMORY TO 177777
2091	006702	005077	002270			CLR	20HSCR	:START AT LOCATION 0
2092	006706	012777	177777	002270	28:	MOV	#177777,20HBA	:SET LOCATION IN
2093								:BUS ADDRESS MEMORY
2094	006714	005277	002256			INC	20HSCR	:ADVANCE TO NEXT LOCATION
2095	006720	005301				DEC	R1	:CONTINUE SETTING
2096	006722	001371				BNE	28	:IF NOT DONE
2097	006724	010377	002246			MOV	R3,20HSCR	:SELECT ADDRESS TO BE TESTED
2098	006730	012777	000000	002246		MOV	#0,20HBA	:WRITE 0 INTO LOCATION
2099	006736	005077	002234			CLR	20HSCR	:ADDRESS LOCATION 0
2100	006742	012701	000020			MOV	#20,R1	:SET UP TO CHECK ALL ADDRESSES
2101								:IN BUS ADDRESS MEMORY
2102	006746	012705	000000		38:	MOV	#0,R5	:0=EXPECTED RESULT
2103								:IF ADDRESS READ IS LOCATION
2104								:WRITTEN INTO
2105	006752	017704	002226			MOV	20HBA,R4	:READ MEMORY LOCATION
2106	006756	027703	002214			CMP	20HSCR,R3	:IF LINE NUMBER=ADDRESS
2107								:OF LOCATION WRITTEN INTO
2108								:EXPECTED CONTENTS=0
2109	006762	001401				BEQ	48	
2110	006764	005105				COM	R5	:OTHERWISE, EXPECTED RESULTS=177777
2111	006766	020504			48:	CMP	R5,R4	:DOES MEMORY LOCATION CONTAIN

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006770 001401 BEQ 55 ; EXPECTED RESULT
006772 104003 HLT 3 ; BUS ADDRESS MEMORY ERROR
006774 104410 55: SCOPE1 ; CHECK FOR LOOP WITH CURRENT DATA
006776 005277 002174 INC 20HSCR ; CHECK CONTENTS OF NEXT LOCATION
007002 005301 DEC R1
007004 001360 BNE R3
007006 005203 INC R3 ; NEXT ADDRESS TO BE WRITTEN
007010 005300 DEC R3
007012 001331 BNE R3
007014 104400 65: SCOPE ; CHECK FOR ITERATIONS, LOOP

; BYTE COUNT MEMORY TEST
; SET ALL LOCATIONS IN BYTE COUNT MEMORY TO 177777
; SET SELECTED LOCATION TO VALUE 0
; VERIFY THAT SELECTED LOCATION WAS SET
; TO 0.
; VERIFY THAT NO OTHER LOCATION WAS MODIFIED.

007016 012767 000340 170752 T52: MOV #340,PS ; DISABLE ALL INTERRUPTS
007024 012767 000100 002212 MOV #100,ICOUNT ; SET UP FOR 100 ITERATIONS
007032 012767 007172 002200 MOV #68,ESCAPE ; SET UP TO ESCAPE TO NEXT TEST
007040 012767 007064 002174 MOV #28,FREEZ1 ; SET UP TO LOOP WITH DATA
007046 012700 000020 MOV #20,R0 ; SET UP TO TEST 20(OCTAL)
; LOCATIONS IN BYTE COUNT MEMORY
; FIRST LOCATION TO BE
; WRITTEN INTO IS 0
007052 005003 CLR R3 ; SET UP TO SET 20 (OCTAL)
; LOCATIONS IN BYTE COUNT MEMORY TO 177777
007054 012701 000020 15: MOV #20,R1 ; START AT LOCATION 0
007060 005077 002112 002114 25: CLR 20HSCR ; SET LOCATION IN
007064 012777 177777 MOV #177777,20HBC ; BYTE COUNT MEMORY
; ADVANCE TO NEXT LOCATION
; CONTINUE SETTING
; IF NOT DONE
007072 005277 002100 INC 20HSCR ; SELECT ADDRESS TO BE TESTED
007076 005301 DEC R1 ; WRITE 0 INTO LOCATION
007100 001371 BNE R3 ; ADDRESS LOCATION 0
007102 012777 002070 002072 MOV R3,20HSCR ; SET UP TO CHECK ALL ADDRESSES
007106 012777 000000 MOV #0,20HBC ; IN BYTE COUNT MEMORY
007114 005077 002056 35: CLR 20HSCR ; 0=EXPECTED RESULT
007120 012701 000020 MOV #20,R5 ; IF ADDRESS READ IS LOCATION
; WRITTEN INTO
; READ MEMORY LOCATION
; IF LINE NUMBER=ADDRESS
; OF LOCATION WRITTEN INTO
; EXPECTED CONTENTS=0

007124 012705 000000 MOV #0,R5 ; OTHERWISE, EXPECTED RESULTS=177777
; DOES MEMORY LOCATION CONTAIN
; EXPECTED RESULT
; BYTE COUNT MEMORY ERROR
007130 017704 002052 MOV 20HBC,R4 ; CHECK FOR LOOP WITH CURRENT DATA
007134 027703 002036 CMP 20HSCR,R3 ; CHECK CONTENTS OF NEXT LOCATION

007140 001401 BEQ 45
007142 005105 COM R5
007144 020504 45: CMP R5,R4 ; OTHERWISE, EXPECTED RESULTS=177777
007146 001401 BEQ 55 ; DOES MEMORY LOCATION CONTAIN
007150 104004 HLT 4 ; EXPECTED RESULT
007152 104410 55: SCOPE1 ; BYTE COUNT MEMORY ERROR
007154 005277 002016 INC 20HSCR ; CHECK FOR LOOP WITH CURRENT DATA
007160 005301 DEC R1 ; CHECK CONTENTS OF NEXT LOCATION
007162 001360 BNE R3
007164 005203 INC R3 ; NEXT ADDRESS TO BE WRITTEN

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2168 007166 005300          DEC      R0
2169 007170 001331          BNE     18
2170 007172 104400          65:    SCOPE                                ;CHECK FOR ITERATIONS, LOOP
2171
2172          ;MEMORY EXTENSION MEMORY TEST
2173          ;VERIFY THAT LOW AND HIGH ORDER MEMORY EXTENSION BIT CAN BE
2174          ;SET AND CLEARED IN SELECTED MEMORY EXTENSION MEMORY LOCATION
2175 007174 012767 000340 170574 T53:  MOV     #340,PS          ;DISABLE ALL INTERRUPTS
2176 007202 012767 000100 001334      MOV     #100,ICOUNT     ;SET UP FOR 100 ITERATIONS
2177 007210 012767 007364 001322      MOV     #65,ESCAPE     ;SET UP TO ESCAPE TO NEXT TEST
2178 007216 012767 007242 002016      MOV     #25,FREEZI     ;SET UP TO LOOP WITH DATA
2179 007224 012700 000020      MOV     #20,R0         ;SET UP TO TEST 20(OCTAL)
2180
2181 007230 005003          CLR     R3             ;LOCATIONS IN MEMORY EXTENSION MEMORY
2182
2183 007232 012701 000020          15:    MOV     #20,R1       ;FIRST LOCATION TO BE
2184
2185 007236 005077 001734          CLR     20HSCR        ;WRITTEN INTO IS 0
2186 007242 042777 000060 001726 25:    BIC     #60,20HSCR    ;SET UP TO CLEAR 20 (OCTAL)
2187 007250 012777 000000 001726      MOV     #0,20HBA      ;LOCATIONS IN MEMORY EXTENSION MEMORY
2188 007256 005277 001714          INC     20HSCR        ;START AT LOCATION 0
2189 007262 005301          DEC     R1            ;CLEAR LOCATION IN
2190 007264 001366          BNE     25           ;MEMORY EXTENSION MEMORY
2191 007266 010377 001704          MOV     R3,20HSCR    ;ADVANCE TO NEXT LOCATION
2192 007272 052777 000060 001676 25:    BIS     #60,20HSCR    ;CONTINUE CLEARING
2193 007300 012777 000000 001676      MOV     #0,20HBA      ;IF NOT DONE
2194 007306 005077 001664          CLR     20HSCR        ;SELECT ADDRESS TO BE TESTED
2195 007312 012701 000020          MOV     #20,R1       ;WRITE LOW AND HIGH INTO LOCATION
2196
2197 007316 012765 000300          35:    MOV     #300,R5      ;LOAD ADDRESS
2198
2199 007322 017704 001666          MOV     20HSSR,R4    ;ADDRESS LOCATION 0
2200 007326 027703 001644          CMP     20HSCR,R3    ;SET UP TO CHECK ALL ADDRESSES
2201
2202          ;IN MEMORY EXTENSION MEMORY
2203          ;IF ADDRESS READ IS LOCATION
2204          ;WRITTEN INTO
2205          ;READ MEMORY LOCATION
2206          ;IF LINE NUMBER=ADDRESS
2207          ;OF LOCATION WRITTEN INTO
2208          ;EXPECTED CONTENTS=LOW AND HIGH
2209
2210 007332 001401          BEQ     45
2211 007334 005006          CLR     20HSCR
2212 007336 020504          45:    CMP     20HSCR,R4    ;OTHERWISE, EXPECTED RESULTS=0
2213 007340 001401          BEQ     55           ;DOES MEMORY LOCATION CONTAIN
2214 007342 104006          HLT     5            ;EXPECTED RESULT
2215 007344 104410          55:    SCOPE1          ;MEMORY EXTENSION DATA ERROR
2216 007346 005277 001624          INC     20HSCR        ;CHECK FOR LOOP WITH CURRENT DATA
2217 007348 005301          DEC     R1            ;CHECK CONTENTS OF NEXT LOCATION
2218 007350 001366          BNE     65
2219 007352 005277 001624          INC     20HSCR
2220 007354 005301          DEC     R1
2221 007356 001366          INC     20HSCR
2222 007358 005301          DEC     R1
2223 007360 001366          BNE     65
2224 007362 104400          65:    SCOPE                                ;NEXT ADDRESS TO BE WRITTEN
2225
2226          ;CHECK FOR ITERATIONS, LOOP
2227
2228          ;MEMORY EXTENSION MEMORY TEST
2229          ;VERIFY THAT LOW ORDER MEMORY EXTENSION BIT CAN BE
2230          ;SET AND CLEARED IN SELECTED MEMORY EXTENSION MEMORY LOCATION
2231 007366 012767 000340 170402 T54:  MOV     #340,PS          ;DISABLE ALL INTERRUPTS
2232 007374 012767 000100 001642      MOV     #100,ICOUNT     ;SET UP FOR 100 ITERATIONS
2233 007402 012767 007556 001630      MOV     #65,ESCAPE     ;SET UP TO ESCAPE TO NEXT TEST

```

```

2224 007410 012767 007434 001624      MOV      #25,FREEZ1
2225 007416 012700 000020                MOV      #20,R0
2226 007422 005003                CLR      R3
2227 007424 012701 000020      15:    MOV      #20,R1
2228 007430 005077 001542      CLR      20HSCR
2229 007434 042777 000060 001534 25:    BIC      #60,20HSCR
2230 007442 012777 000000 001534      MOV      #0,20HBA
2231 007450 005277 001522      INC      20HSCR
2232 007454 005301      DEC      R1
2233 007458 001366      BNE      25
2234 007460 010377 001512      MOV      R3,20HSCR
2235 007464 052777 000020 001504      BIS      #20,20HSCR
2236 007472 012777 000000 001504      MOV      #0,20HBA
2237 007500 005077 001472      CLR      20HSCR
2238 007504 012701 000020      MOV      #20,R1
2239 007510 012705 000100      35:    MOV      #100,R5
2240 007514 017704 001474      MOV      20H55R,R4
2241 007520 027703 001452      CMP      20HSCR,R3
2242 007524 001401      AND     45
2243 007526 005005      CLR     R5
2244 007530 020504      45:    CMP      R5,R4
2245 007532 001401      BEQ     55
2246 007534 104005      HLT
2247 007536 104410      55:    SCOPE1
2248 007538 005277 001432      INC     20HSCR
2249 007540 005301      DEC     R1
2250 007542 001366      BNE     35
2251 007544 005203      INC     R3
2252 007546 005300      DEC     R0
2253 007548 001323      BNE     15
2254 007550 104400      65:    SCOPE
2255
2256
2257
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2267 007560 012767 000340 170210 T55:  MOV     #340,PS
2268 007566 012767 000100 001450      MOV     #100,ICOUNT
2269 007574 012767 007750 001436      MOV     #65,ESCAPE
2270 007602 012767 007626 001432      MOV     #25,FREEZ1
2271 007610 012700 000020      MOV     #20,R0
2272
2273 007614 005003                CLR     R3
2274
2275 007616 012701 000020      15:    MOV     #20,R1
2276
2277 007622 005077 001350      CLR     20HSCR
2278 007626 042777 000060 001342 25:    BIC     #60,20HSCR
2279 007634 012777 000000 001342      MOV     #0,20HBA

```

```

;SET UP TO LOOP WITH DATA
;SET UP TO TEST 20(OCTAL)
;LOCATIONS IN MEMORY EXTENSION MEMORY
;FIRST LOCATION TO BE
;WRITTEN INTO IS 0
;SET UP TO CLEAR 20 (OCTAL)
;LOCATIONS IN MEMORY EXTENSION MEMORY
;START AT LOCATION 0
;CLEAR LOCATION IN
;MEMORY EXTENSION MEMORY
;ADVANCE TO NEXT LOCATION
;CONTINUE CLEARING
;IF NOT DONE
;SELECT ADDRESS TO BE TESTED
;WRITE LOW INTO LOCATION
;LOAD ADDRESS
;ADDRESS LOCATION 0
;SET UP TO CHECK ALL ADDRESSES
;IN MEMORY EXTENSION MEMORY
;LOW=EXPECTED RESULT
;IF ADDRESS READ IS LOCATION
;WRITTEN INTO
;READ MEMORY LOCATION
;IF LINE NUMBER=ADDRESS
;OF LOCATION WRITTEN INTO
;EXPECTED CONTENTS=LOW
;OTHERWISE, EXPECTED RESULTS=0
;DOES MEMORY LOCATION CONTAIN
;EXPECTED RESULT
;MEMORY EXTENSION DATA ERROR
;CHECK FOR LOOP WITH CURRENT DATA
;CHECK CONTENTS OF NEXT LOCATION
;NEXT ADDRESS TO BE WRITTEN
;CHECK FOR ITERATIONS, LOOP
;MEMORY EXTENSION MEMORY TEST
;VERIFY THAT HIGH ORDER MEMORY EXTENSION BIT CAN BE
;SET AND CLEARED IN SELECTED MEMORY EXTENSION MEMORY LOCATION
;DISABLE ALL INTERRUPTS
;SET UP FOR 100 ITERATIONS
;SET UP TO ESCAPE TO NEXT TEST
;SET UP TO LOOP WITH DATA
;SET UP TO TEST 20(OCTAL)
;LOCATIONS IN MEMORY EXTENSION MEMORY
;FIRST LOCATION TO BE
;WRITTEN INTO IS 0
;SET UP TO CLEAR 20 (OCTAL)
;LOCATIONS IN MEMORY EXTENSION MEMORY
;START AT LOCATION 0
;CLEAR LOCATION IN
;MEMORY EXTENSION MEMORY

```

2280	007642	005277	001330		INC	20HSCR		: ADVANCE TO NEXT LOCATION
2281	007646	005301			DEC	R1		: CONTINUE CLEARING
2282	007650	001366			BNE	23		: IF NOT DONE
2283	007652	010377	001320		MOV	R3, 20HSCR		: SELECT ADDRESS TO BE TESTED
2284	007656	052777	000040	001312	BIS	940, 20HSCR		: WRITE HIGH INTO LOCATION
2285	007664	012777	000000	001312	MOV	80, 20HBA		: LOAD ADDRESS
2286	007672	005077	001300		CLR	20HSCR		: ADDRESS LOCATION 0
2287	007676	012701	000020		MOV	820, R1		: SET UP TO CHECK ALL ADDRESSES
2288								: IN MEMORY EXTENSION MEMORY
2289	007702	012705	000200		MOV	8200, R5		: HIGH=EXPECTED RESULT
2290				35:				: IF ADDRESS READ IS LOCATION
2291								: WRITTEN INTO
2292	007706	017704	001302		MOV	20HSSR, R4		: READ MEMORY LOCATION
2293	007712	027703	001260		CMP	20HSCR, R3		: IF LINE NUMBER=ADDRESS
2294								: OF LOCATION WRITTEN INTO
2295								: EXPECTED CONTENTS=HIGH
2296	007716	001401			BEQ	45		
2297	007720	005705			CLR	R5		: OTHERWISE, EXPECTED RESULTS=0
2298	007722	023504			CMP	R5, R4		: DOES MEMORY LOCATION CONTAIN
2299	007724	001401		45:	BEQ	55		: EXPECTED RESULT
2300	007726	104005			HLT	5		: MEMORY EXTENSION DATA ERROR
2301	007730	104410			SCOPE1			: CHECK FOR LOOP WITH CURRENT DATA
2302	007732	005277	001240	55:	INC	20HSCR		: CHECK CONTENTS OF NEXT LOCATION
2303	007736	005301			DEC	R1		
2304	007740	001360			BNE	35		
2305	007742	005203			INC	R3		: NEXT ADDRESS TO BE WRITTEN
2306	007744	005300			DEC	R0		
2307	007746	001323			BNE	15		
2308	007750	104400		65:	SCOPE			: CHECK FOR ITERATIONS, LOOP

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```

;END OF PASS
;TYPE NAME OF TEST
;UPDATE PASS COUNT
;CHECK FOR EXIT TO ACT-11
;RESTART TEST

EOP:  TYPE NAME OF TEST
      MEPASS
      CLR LAST
      CLR ERRFLG
      INC PASCNT
      MOV PASCNT,LIGHTS
      MOV 2042,R1
      BEQ RESTRT
      RESET
      LOGICAL: JSR PC,(R1)
      NOP
      NOP
      NOP
      RESTRT: JMP BEGIN

;CHECK FOR LOOP ON CURRENT TEST
;CHECK FOR ITERATION SUPPRESSION

SCOPE: BIT #SW10,SWR
      BNE 4$
1$: BIT #SW14,SWR
      BNE 3$
      BIT #SW11,SWR
      BNE 2$
      INC LPCNT
      CMP LPCNT,ICOUNT
      BNE 3$
2$: CLR LPCNT
      CLR ERRFLG
      MOV (SP),RETURN
3$: MOV RETURN,(SP)
      RTI
4$: TST ERRFLG
      BEQ 1$
      BR 2$

;CHECK FOR FREEZE ON CURRENT DATA

SCOPE: BIT #SW09,SWR
      BEQ 1$
      MOV FREEZ1,(SP)
1$: RTI

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010149 032767 020000 167420
010150 001051
010151 021667 001116
010152 001404
010153 011667 001110
010154 005067 001040
010170 104406
010172 011605
010174 162705 000002
010200 011504
010 02 006304
010 04 006304
010 06 042704 177001
010212 062704 011752
010216 012467 000034
010 02 011467 000042
010 06 005767 000776
010232 001403
010234 005767 000030
010240 001007
010242 104402
010244 010336
010246 012767 000001 000754
010254 104401
010256 000000
010260 005767 000004
010264 001402
010266 104402
010270 000000
010272 104407
010274 005767 167270
010300 100005
010302 010046
010304 016600 000002
010310 000000
010312 012600
010314 00 067 000714
010320 032767 002000 167242
010326 001402
010330 016716 000704
010334 000002
010336 000001
010340 006 002
010372 011266

;ERROR HANDLER
ERRORS: BIT #SW13,SWR
BNE HALTS
CMP (SP),LAST
BEQ IS
MOV (SP),LAST
CLR ERRFLG
IS: SAVOSP
MOV (SP),RS
SUB #2,RS
MOV (RS),R4
ASL R4
LSL R4
BIC #177001,R4
ADD #ERRTAB,R4
MOV (R4)+,ERRMSG
MOV (R4),DATABP
TST ERRFLG
BEQ TYPMSG
TST DATABP
BNE TYPDAT
TYPMSG: OCTASC
ERTAB0
MOV #1,ERRFLG
ERRMSG: 0
TYPDAT: TST DATABP
BEQ RESREG
OCTASC
DATABP: 0
RESREG: RESOS
HALTS: TST SWR
BPL EXITER
PUSHRO
MOV 2(SP),RO
HALT
POPPO
EXITER: INC ERRCNT
BIT #SW10,SWR
BEQ IS
MOV ESCAPE,(SP)
IS: RTI
ERTAB0: 1
.BYTE 6,2
SAVPC

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01034E 011646
01034E 162716 000002
010350 017616 000000
010356 006316
010360 042716 177001
010364 062716 011672
010370 017616 000000
010374 000136

010376 016667 000004 000662

010404 010567 000652
010410 010467 000644
010414 010367 000636
010420 010267 000630
010424 010167 000622
010430 010067 000614
010434 000002

010436 016700 000606
010442 016701 000604
010444 016702 000602
010448 016703 000600
010450 016704 000576
010452 016705 000574
010466 000002

```
: TRAP DISPATCH SERVICE  
: ARGUMENT OF TRAP IS EXTRACTED  
: AND USED AS OFFSET TO OBTAIN POINTER  
: TO SELECTED SUBROUTINE  
  
TRPSRV: MOV      (SP), -(SP)      ; GET PC OF RETURN  
        SUB      #2, (SP)       ; =PC OF TRAP  
        MOV      2(SP), (SP)    ; GET TRP  
TRPOK:  RSL      (SP)          ; MULTIPLY TRAP ARG BY 2  
        BIC      #177001, (SP) ; CLEAR UNWANTED BITS  
        ADD      #TRPTAG, (SP)  ; POINTER TO SUBROUTINE ADDRESS  
        MOV      2(SP), (SP)    ; SUBROUTINE ADDRESS  
        JMP      2(SP)+         ; GO TO SUBROUTINE  
  
; SAVE PC OF TEST THAT FAILED AND RO-R5  
SV0SP:  MOV      4(SP), SAVPC  
  
; SAVE RO-R5  
SV05:   MOV      R5, SAVR5  
        MOV      R4, SAVR4  
        MOV      R3, SAVR3  
        MOV      R2, SAVR2  
        MOV      R1, SAVR1  
        MOV      R0, SAVR0  
        RTI  
; RESTORE RO-R5  
RS05:   MOV      SAVR0, R0  
        MOV      SAVR1, R1  
        MOV      SAVR2, R2  
        MOV      SAVR3, R3  
        MOV      SAVR4, R4  
        MOV      SAVR5, R5  
        RTI
```

```

;TELETYPE OUTPUT ROUTINE
2443
2444
2445
2446 010470 017605 000000
2447 010474 062716 000002
2448 010500 105777 000466
2449 010504 100375
2450 010506 105715
2451 010510 001001
2452 010512 000002
2453 010514 112577 000454
2454 010520 000767
;ASCII STRING INPUT ROUTINE
2458 010522 017667 000000 000006 INSTRG: MOV @ (SP), MSG
2459 010530 062716 000002 INSTR1: ADD @2, (SP)
2460 010534 104401 INSTR1: TYPE
2461 010536 000000 MSG: 0
2462 010540 012704 011714 MOV @INBUF, R4
2463 010544 012703 000007 MOV #7, R3
2464 010550 105777 000412 1$: TSTB @TKCSR
2465 010554 100375 BPL 1$
2466 010556 117714 000406 MOVB @TKDBR, (R4)
2467 010558 142714 000200 BICB #200, (R4)
2468 010566 122427 000015 CMPB (R4)+, #15
2469 010572 001413 BEQ INSTR2
2470 010574 117777 000370 000372 2$: MOVB @TKDBR, @TPDBR
2471 010602 105777 000364 TSTB @TPCSR
2472 010606 100375 BPL 2$
2473 010610 005303 DEC R3
2474 010612 001356 BNE 1$
2475 010614 104401 INSTR1: TYPE
2476 010616 011547 INSTR1: MGH
2477 010620 000745 BR INSTR1
2478 010622 000002 INSTR2: RTI
  
```

2579			
2580			
2581			
2582	010624	011605	
2583	010626	012567	000146
2584	010632	012567	000144
2585	010636	012567	000142
2586	010642	112567	000140
2587	010646	112567	000135
2588	010652	010516	
2589	010654	005005	
2590	010656	012704	011714
2591	010662	122714	000015
2592	010666	001420	
2593	010670	121427	000060
2594	010674	002415	
2595	010676	121427	000067
2596	010702	003012	
2597	010704	142714	000060
2598	010710	152405	
2599	010712	122714	000015
2500	010716	001406	
2501	010720	006305	
2502	010722	006305	
2503	010724	006305	
2504	010726	000760	
2505	010730	104404	
2506	010732	000750	
2507			
2508			
2509			
2510	010734	020567	000042
2511	010740	101373	
2512	010742	020567	000032
2513	010746	103770	
2514	010750	136705	000032
2515	010754	001365	
2516			
2517			
2518			
2519	010756	016704	000022
2520	010762	010524	
2521	010764	062705	000002
2522	010770	105367	000013
2523	010774	001372	
2524	010776	000002	
2525	011000	000000	
2526	011002	000000	
2527	011004	000000	
2528	011006	000000	
2529		011007	

;CONVERT ASCII STRING TO OCTAL

```

PARAMS: MOV (SP),R5
          MOV (R5)+,LOLIM
          MOV (R5)+,HILIM
          MOV (R5)+,DEVAOR
          MOV (R5)+,LOBITS
          MOV (R5)+,ADRCNT
          MOV R5,(SP)
PARAM1: CLR R5
          MOV #INBUF,R4
          CMPB #15,(R4)
          BEQ PARERR
IS:      CMPB (R4),#60
          BLT PARERR
          CMPB (R4),#67
          BGT PARERR
          BICB #60,(R4)
          BISB (R4)+,R5
          CMPB #15,(R4)
          BEQ LIMITS
          ASL R5
          ASL R5
          ASL R5
          BR IS
PARERR: INSTER
          BR PARAM1
  
```

;TEST TO SEE IF NUMBER IS WITHIN LIMITS

```

LIMITS: CMP R5,HILIM
          BHI PARERR
          CMP R5,LOLIM
          BLO PARERR
          BITB LOBITS,R5
          BNE PARERR
  
```

;STORE NUMBER AT SPECIFIED ADDRESS

```

IS:      MOV DEVAOR,R4
          MOV R5,(R4)+
          ADD #2,R5
          DECB ADRCNT
          BNE IS
          RTI
  
```

```

LOLIM: 0
HILIM: 0
DEVAOR: 0
LOBITS: 0
ADRCNT=LOBITS+1
  
```

```

2530                                     ;CONVERT OCTAL NUMBER TO ASCII AND OUTPUT TO TELEPRINTER
2531
2532
2533 011010 104401          OCTASN: TYPE
2534 011012 011553          MCRLF
2535 011014 017601          MOV      2(SP),R1
2536 011020 062716 000000  ADD      #2(SP)
2537 011024 012167 000130  MOV      (R1)+,WRDCNT
2538 011030 112167 000126 15:  MOVB   (R1)+,CHRCNT
2539 011034 112167 000123  MOVB   (R1)+,SPACNT
2540 011040 013167 000120  MOV      2(R1)+,BINWRD
2541 011044 016704 000114 25:  MOV      BINWRD,R4
2542 011050 116705 000106  MOVB   CHRCNT,R5
2543 011054 012700 011726  MOV      #TEMP,R0
2544 011060 010403 35:  MOV      R4,R3
2545 011062 042703 177770  BIC     #177770,R3
2546 011066 062703 000260  ADD     #260,R3
2547 011072 110320  MOVB   R3,(R0)+
2548 011074 006204  ASR    R4
2549 011076 006204  ASR    R4
2550 011100 006204  ASR    R4
2551 011102 005305  DEC    R5
2552 011104 001365  BNE    35
2553 011106 012703 011740  MOV     #MODATA,R3
2554 011112 114023 45:  MOVB   -(R0),(R3)+
2555 011114 105367 000042  DECB   CHRCNT
2556 011120 001374  BNE    45
2557 011122 105767 000035  TSTB   SPACNT
2558 011126 001405  BEQ    65
2559 011130 112723 000240 55:  MOVB   #240,(R3)+
2560 011134 105367 000023  DECB   SPACNT
2561 011140 001373  BNE    55
2562 011142 105013 65:  CLRB   (R3)
2563 011144 104401  TYPE
2564 011146 011740  MOVA   MODATA
2565 011150 005367 000004  DEC    WRDCNT
2566 011154 001325  BNE    15
2567 011156 000002  RTI
2568 011160 000000  WRDCNT: 0
2569 011162 000000  CHRCNT: 0
2570 011163 011163  SPACNT=CHRCNT+1
2571 011164 000000  BINWRD: 0
  
```

```

011166 177560
011170 177562
011172 177564
011174 177566
011176 000000
011200 000000
011202 000000
011204 000000
011206 000000
011210 000000
011212 000000
011214 000000
011216 000000
011220 000000
011222 000000
011224 000000
011226 000000
011228 000000
011230 000000
011232 000000
011234 000000
011236 000000
011238 000000
011240 000000
011242 000000
011244 000000
011246 000000
011248 000000
011250 000000
011252 000000
011254 000000
011256 000000
011258 000000
011260 000000
011262 000000
011264 000000
011266 000000
011268 000000
011270 000000
011272 000000
011274 000000

```

: INDIRECT POINTERS

```

TKCSR: 177560
TKDBP: 177562
TPCSR: 177564
TPDBP: 177566
UNSCR: 000000
DHRNC: 000000
DMLPR: 000000
DMSR: 000000
DMSR: 000000
DMSR: 000000
DMSR: 000000
DMSR: 000000
DMSR: 000000
DMSR: 000000
DMSR: 000000
DMSR: 000000
DMSR: 000000
DMSR: 000000
DMSR: 000000
DMSR: 000000
DMSR: 000000
DMSR: 000000
DMSR: 000000

```

: PROGRAM VARIABLES

```

ERRFLG: 000000
PASSCNT: 000000
ERRCNT: 000000
RETURN: 000000
ESCAPE: 000000
FREEZ1: 000000
ICOUNT: 000000
LICNT: 000000
SAVR0: 000000
SAVR1: 000000
SAVR2: 000000
SAVR3: 000000
SAVR4: 000000
SAVR5: 000000
SAVR6: 000000
SAVR7: 000000
SAVR8: 000000
SAVR9: 000000
SAVSP: 000000
SAVPC: 000000
INIPLG: 000000
STFLG: 000000
LAST: 000000

```

```

: ERROR FLAG
: PASS COUNT
: ERROR COUNT
: SCOPE RETURN ADDRESS FOR TEST LOOPING
: ADDRESS FOR ERROR ESCAPE
: DATA LOOPING RETURN ADDRESS
: ITERATION COUNT FOR TEST IN PROGRESS
: NUMBER OF ITERATIONS THIS TEST
: R0 SAVE AREA
: R1 SAVE AREA
: R2 SAVE AREA
: R3 SAVE AREA
: R4 SAVE AREA
: R5 SAVE AREA
: STACK POINTER SAVE AREA
: CALLING ROUTINE SAVE AREA
: PROGRAM INITIALIZATION FLAG
: PROGRAM START FLAG
: LAST ERROR PC

```



```

011146 005016 042012 030510
011147 005016 042012 047515
011148 005016 042012 051505
011149 005016 042012 041505
011150 005016 042012 042101
011151 005016 042012 026523
011152 005016 042012 047117
011153 005016 042012 051040
011154 005016 042012 042524
011155 005016 042012 051104
011156 005016 042012 000000
011157 005016 042012 000000
011158 020040 047520 042527
011159 020123 040506 046111
011160 051123 026105 050040
011161 047522 051137 046501
011162 051046 051505 040524
011163 052122 040440 020124
011164 042524 052123 044440
011165 020116 051120 043517
011166 042522 051523 000000
011167 015012 042012 042132
011168 000000 000000 000000
011169 051012 000000 000000
011170 052012 051505 051505
011171 041520 000055 000055

```

```

MTITLE: .ASCIZ (15)(12)(12)/DH11 MEMORY TEST /(15)(12)
MVECTOR: .ASCIZ (15)(12)/VECTOR ADDRESS-/
MREGAD: .ASCIZ (15)(12)/CONTROL REGISTER ADDRESS-/
MOM: .ASCIZ / ?/
MCRLF: .ASCIZ (15)(12)
MPFAIL: .ASCIZ / POWER FAILURE, PROGRAM RESTART AT TEST IN PROGRESS/
MEPASS: .ASCIZ (15)(12)/DZDHB/
MR: .ASCIZ (15)(12)/R/
MTSTPC: .ASCIZ (15)(12)/TEST PC-/

```

;TABLE OF POINTERS FOR TRAP DECODING

```

TRPTAB: SCOPER
        TYPE 2
        OCTASN
        INSTAG
        INSTRE
        PARAMS
        SVOSP
        RSOS
        SCOP1R

```

;BUFFERS FOR INPUT-OUTPUT

```

INBUF: 0
.=.+10
TEMP: 0
.=.+10
MDATA: 0
.=.+10

```

;TABLE OF POINTERS TO ERROR MESSAGES AND DATA

```

ERRTAB:
0
0

```


2708	011756	012002						
2709	011760	012340						
2710	011764	012340						
2711	011764	012340						
2712	011766	012340						
2713	011770	012340						
2714	011772	012340						
2715	011772	012340						
2716	011772	012340						
2717	011772	012340						
2718	011772	012340						
2719	011772	012340						
2720	011772	012340						
2721	011772	012340						
2722	011772	012340						
2723	011772	012340						
2724	011772	012340						
2725	011772	012340						
2726	011772	012340						
2727	011772	012340						
2728	011772	012340						
2729	011772	012340						
2730	011772	012340						
2731	011772	012340						
2732	011772	012340						
2733	011772	012340						
2734	011772	012340						
2735	011772	012340						
2736	011772	012340						
2737	011772	012340						
2738	011772	012340						
2739	011772	012340						
2740	011772	012340						
2741	011772	012340						
2742	011772	012340						
2743	011772	012340						
2744	011772	012340						
2745	011772	012340						
2746	011772	012340						
2747	011772	012340						
2748	011772	012340						
2749	011772	012340						
2750	011772	012340						
2751	011772	012340						
2752	011772	012340						
2753	011772	012340						
2754	011772	012340						
2755	011772	012340						
2756	011772	012340						
2757	011772	012340						
2758	011772	012340						
2759	012340	000003						
2760	012342	000	002					
2761	012344	011254						
2762	012346	000	002					
2763	012350	011256						

EM1
 DT1
 EM2
 DT1
 EM3
 DT1
 DT2
 DT3

EM1: .ASCIZ /BUS ADDRESS MEMORY ERROR/<15><12>/EXP REC ADDRESS/

EM2: .ASCIZ /BYTE COUNT MEMORY ERROR/<15><12>/EXP REC ADDRESS/

EM3: .ASCIZ /BUS ADDRESS MEMORY ERROR/<15><12>/EXP REC/

EM4: .ASCIZ /BYTE COUNT MEMORY ERROR/<15><12>/EXP REC/

EM5: .ASCIZ /MEMORY EXTENSION ERROR/<15><12>/EXP REC ADDRESS/

.EVEN
 DT1: 3
 .BYTE 6 2
 SAVR2
 .BYTE 6 2
 SAVR3

2764	012352	002	000	.BYTE	2,0
2765	012354	011260			SAVR4
2766	012357	000002		DT2:	2,0
2767	012360	006	002	.BYTE	6,2
2768	012362	011262			SAVR5
2769	012364	006	002	.BYTE	6,2
2770	012366	011260			SAVR4
2771	012370	000003		DT3:	3,0
2772	012372	006	002	.BYTE	6,2
2773	012374	011262			SAVR5
2774	012376	006	002	.BYTE	6,2
2775	012400	011260			SAVR4
2776	012402	002	000	.BYTE	2,0
2777	012404	011256			SAVR3
2778	012406	000000		ENDCOD:	0
2779		000001		.END	

VEC2	001070	880	883											
WROCNT	011160	2537	2565	2568										
X	= 000000	1												
XADRS	= 000020	1011	1035	1059	1083	1107	1131	1155	1179	1203	1227	1251	1275	1299
		1323	1347	1371	1395	1419	1443	1467	1491	1515	1539	1563	1587	1611
		1635	1659	1683	1707	1731	1755	1779						
XCADRS	= 000020	1011	1035	1059	1083	1107	1131	1155	1179	1203	1227	1251	1275	1299
		1323	1347	1371	1395	1419	1443	1467	1491	1515	1539	1563	1587	1611
		1635	1659	1683	1707	1731	1755	1779						
XN	= 000056	1	945	948	982	985	1016	1019	1040	1043	1064	1067	1088	1091
		1112	1115	1136	1139	1160	1163	1184	1187	1208	1211	1232	1235	1256
		1259	1280	1283	1304	1307	1328	1331	1352	1355	1376	1379	1400	1403
		1424	1427	1448	1451	1472	1475	1496	1499	1520	1523	1544	1547	1568
		1571	1592	1595	1616	1619	1640	1643	1664	1667	1688	1691	1712	1715
		1736	1739	1760	1763	1787	1791	1836	1840	1885	1889	1934	1938	1983
		1987	2032	2036	2081	2085	2130	2134	2175	2179	2221	2225	2267	2271
Y	= 000011	1	850	851	852	853	854	855	856	857	858	859		
	= 012410	574	575	577	579	531	523	525	587	589	591	593	595	597
		599	601	603	605	607	609	611	613	615	617	619	621	623
		625	627	629	631	633	635	637	639	641	643	645	647	649
		651	653	655	657	659	661	663	665	667	669	671	673	675
		677	679	681	683	685	687	689	691	693	695	697	699	701
		703	705	707	709	711	713	715	717	719	721	723	725	727
		729	731	733	735	737	739	741	743	745	747	749	751	753
		755	757	759	761	763	765	767	769	771	773	775	777	779
		781	783	785	787	789	791	793	795	797	799	801	803	805
		807	809	811	813	815	817	819	821	823	825	827	829	834
		841	859	2625	2641	2697	2699	2701						

.EVEN	2758														
.IF	878	880	908	948	985	1019	1043	1067	1091	1115	1139	1163	1187	1211	1235
	1259	1263	1307	1331	1355	1379	1403	1427	1451	1475	1499	1523	1547	1571	1595
	1619	1643	1667	1691	1715	1739	1763	1790	1839	1888	1937	1986	2035	2084	2133
.IFF	2178	2224	2270												
.IIF	887	881	869	1005	1026	1027	1032	1033	1050	1051	1056	1057	1074	1075	1080
	1081	1098	1099	1104	1105	1122	1123	1128	1129	1146	1147	1152	1153	1170	1171
	1176	1177	1194	1195	1200	1201	1218	1219	1224	1225	1242	1243	1248	1249	1266
	1267	1272	1273	1280	1291	1296	1297	1314	1315	1320	1321	1338	1339	1344	1345
	1362	1363	1368	1369	1386	1387	1392	1393	1410	1416	1434	1440	1458	1464	1482
	1488	1506	1512	1530	1536	1554	1560	1578	1584	1602	1608	1626	1632	1650	1656
	1674	1680	1698	1704	1722	1728	1746	1752	1770	1776	1819	1820	1868	1869	1917
.IRP	1918	196	2015	2064	2113	2114	2162								
.LIST	2578	2612													
	1	488	507	851	852	853	854	855	856	857	858	859	936	948	985
	1011	1019	1035	1043	1059	1067	1083	1091	1107	1115	1131	1139	1155	1163	1179
	1187	1203	1211	1227	1235	1251	1259	1275	1283	1299	1307	1323	1331	1347	1355
	1371	1379	1395	1403	1419	1427	1443	1451	1467	1475	1491	1499	1515	1523	1539
	1547	1563	1571	1587	1595	1611	1619	1635	1643	1659	1667	1683	1691	1707	1715
	1731	1739	1755	1763	1779	1791	1840	1889	1938	1987	2036	2085	2134	2179	2225
.MACRO	2271														
.MLIST	1	859	936												
	1	488	507	851	852	853	854	855	856	857	858	859	936	948	985
	1011	1019	1035	1043	1059	1067	1083	1091	1107	1115	1131	1139	1155	1163	1179
	1187	1203	1211	1227	1235	1251	1259	1275	1283	1299	1307	1323	1331	1347	1355
	1371	1379	1395	1403	1419	1427	1443	1451	1467	1475	1491	1499	1515	1523	1539
	1547	1563	1571	1587	1595	1611	1619	1635	1643	1659	1667	1683	1691	1707	1715
	1731	1739	1755	1763	1779	1791	1840	1889	1938	1987	2036	2085	2134	2179	2225
.PAGE	2271														
.REM	526	573	831	859	2360	2407	2443	2479	2530	2572	2612	2652			
.REPT	575	1011	1395												
.TITLE	507														

ERRORS DETECTED: 0
 DEFAULT GLOBALS GENERATED: 0

#DZDHB DZDHB SEQ/SOL/CRF/PAGNUM=DSKZ:UTIL2.P11,DSKM:DZDHB.PFC
 RUN-TIME: 13 22 3 SECONDS
 RUN-TIME RATIO: 108/40=2.6
 CORE USED: 12K (23 PAGES)

