

MRV11-BA

LSI-11-UV PROM-RAM TEST
MD-11-DVMRA-A

EP-DVMRA-A-DL-A
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This section contains a grid of 48 small tables, arranged in 8 rows and 6 columns. Each table appears to be a test result or data sheet, containing various columns of text and numbers. The text is too small to read clearly, but the layout suggests a systematic collection of test data for the MRV11-BA component.

LSI-11-UV PROM-RAM TEST
MD-11-DVMRA-A

B01

EOF100M9RASE0

00010000

770526

PDP10 411

HDR10VMRASE0

00010000

770526

.REM %

IDENTIFICATION

PRODUCT CODE: MAINDEC-11-DVMRA-A-D
PRODUCT NAME: LSI11 UV PROM-RAM (MRV11-BA) TEST.
DATE: APRIL 1977
MAINTAINER: DIAGNOSTIC GROUP
AUTHOR: GUS PASQUANTONIO

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1.0 ABSTRACT

THIS PROGRAM CONSISTS OF A SERIES OF ROUTINES DESIGNED TO TEST THE MRV11-BA UV PROM-RAM OPTION ON AN LSI11 OR 11/03 SYSTEM. THE PROGRAM ALSO PROVIDES OPTIONAL MEMORY DUMP ROUTINES WHICH WILL OUTPUT THE CONTENTS OF ANY GIVEN MEMORY RANGE, IN EITHER OCTAL OR BINARY FORMAT, OR OUTPUT A 32K MEMORY MAP. SEE 7.0 FOR DETAILS.

THE PROGRAM WILL RUN UNDER ACT, APT, AND/OR XXDP WITH THE FOLLOWING RESTRICTIONS (SEE 4.3):

1. THE 256 WORD RAM ADDRESS JUMPERS MUST BE SET TO THE DEFAULT ADDRESS 020000.
2. THE 4K PROM ADDRESS JUMPERS MUST BE SET TO THE DEFAULT ADDRESS 140000, AND THE PROM SIZE JUMPERS SET FOR 4K.

2.0 REQUIREMENTS

2.1 EQUIPMENT REQUIRED:

1. LSI11 OR 11/03 WITH 4K READ/WRITE MEMORY.
2. CONSOLE TERMINAL (LA36, OR TTY).
3. MRV11-BA UV PROM-RAM.

2.2 MEMORY REQUIRED:

1. 4K READ/WRITE FOR PROGRAM STORAGE AND EXECUTION.
2. 256 WORD RAM (STANDARD IN MRV11-BA).
3. 1K TO 4K UV PROM (OPTIONAL IN MRV11-BA).
4. SPECIAL 4K TEST PROM (IN-HOUSE ENGINEERING USE ONLY).

3.0 LOADING PROCEDURE

USE STANDARD LOAD PROCEDURE FOR PDP-11 ABSOLUTE BINARY FORMATTED TAPE.

4.0 STARTING PROCEDURE

FIVE START-RESTART ADDRESS ARE PROVIDED:

00200 = INITIAL START. INITIALIZE PROM-RAM ADDRESSES.
00204 = RESTART. RE-USE OLD PROM-RAM ADDRESSES.
00210 = OCTAL MEMORY DUMP START (SEE 7.1).
00214 = BINARY MEMORY DUMP START (SEE 7.1).
00220 = MEMORY MAP DUMP START (SEE 7.2).

STARTING PROCEDURE (CONT'D)

4.1 UPON INITIAL START THE PROGRAM WILL IDENTIFY ITSELF, TYPE THE CURRENT SWITCH REGISTER CONTENTS (SEE 5.0), AND BEGIN A BRIEF CONVERSATION IN WHICH THE RAM ADDRESS, PROM SIZE, AND PROM ADDRESS ARE ESTABLISHED. RESPOND TO EACH QUERY AS FOLLOWS:

"SWR = 00000 NEW = "
ENTER THE DESIRED SWITCH REGISTER CONTENTS AND TERMINATE WITH <CR>. SEE 5.0 FOR DETAILS.

"RAM ADDRESS = "
ENTER THE OCTAL STARTING ADDRESS OF THE 256 WORD RAM, AND TERMINATE WITH <CR>. ADDRESS MUST BE ON A 256 WORD (1000 OCTAL BYTE) BOUNDARY. IF <CR> ONLY, THE DEFAULT ADDRESS 020000 IS USED.

"PROM SIZE = "
ENTER THE PROM SIZE AND TERMINATE WITH <CR>, AS FOLLOWS:
1 (OR 1024) = 1K OPTIONAL PROM INSTALLED.
2 (OR 2048) = 2K OPTIONAL PROM INSTALLED.
3 (OR 3072) = 3K OPTIONAL PROM INSTALLED.
4 (OR 4096) = 4K OPTIONAL PROM INSTALLED.
X = SPECIAL 4K TEST PROM INSTALLED.
(IN-HOUSE ENGINEERING USE ONLY)
IF <CR> ONLY, THE DEFAULT SIZE 4K IS USED.

"PROM ADDRESS = "
ENTER THE OCTAL STARTING ADDRESS OF THE 4K PROM, AND TERMINATE WITH <CR>. THE ADDRESS MUST BE ON A 1K (4000 OCTAL BYTE) BOUNDARY. IF <CR> ONLY, THE DEFAULT ADDRESS 140000 IS USED.

ALL INPUT DATA ARE TESTED FOR VALIDITY (ON PROPER BOUNDARY AND NOT NON-EXISTANT ADDRESS) BEFORE PROCEEDING TO THE NEXT QUERY. WHEN AN INVALID INPUT IS MADE, THAT INPUT REQUEST WILL BE REPEATED.

NOTE: VALID PROM ENTRIES MUST BE MADE EVEN THOUGH THERE MAY BE NO PROM PHYSICALLY INSTALLED.
THE PROM TESTS (11 AND 12) WILL RUN WITH OR WITHOUT PROM CHIPS INSTALLED.

4.2 SUBSEQUENT RE-STARTS FROM LOC 204 WILL OMIT THE INITIAL DIALOGUE AND USE THE SIZE AND ADDRESS PARAMETERS OBTAINED AT THE LAST INITIALIZATION.

STARTING PROCEDURE (CONT'D)

4.3 THE FOLLOWING PROM-RAM PARAMETERS ARE ASSUMED BY THE PROGRAM UNLESS OTHERWISE ESTABLISHED AS IN 4.1 ABOVE:

1. THE FIRST RAM ADDRESS IS 020000.
2. THE FIRST PROM ADDRESS IS 140000.
3. THE PROM SIZE IS 4K.

THESE DEFAULT VALUES MAY BE ALTERED IF NECESSARY TO MEET SOME SPECIFIC CONFIGURATION. CHANGE THE FOLLOWING MEMORY LOCATIONS TO CONFORM TO YOUR PARTICULAR SYSTEM:

| <u>LOC</u> | <u>TAG</u> | <u>CONTENT</u> | <u>COMMENT</u> |
|------------|------------|----------------|-------------------------|
| 1250 | SBASE: | 020000 | :DEFAULT RAM ADDRESS. |
| 1254 | SCDW1: | 140000 | :DEFAULT PROM ADDRESS. |
| 1256 | SCDW2: | 000004 | :DEFAULT PROM SIZE (K). |

5.0 SWITCH REGISTER OPTIONS AND CONTROL.

THE FOLLOWING SWITCH OPTIONS ARE PROVIDED:

| <u>SWITCH</u> | <u>OCTAL</u> | <u>FUNCTION</u> |
|---------------|--------------|--------------------------|
| SWR15=1 | 100000 | HALT ON ERROR. |
| SWR14=1 | 040000 | LOOP ON TEST. |
| SWR13=1 | 020000 | INHIBIT ERROR TYPEOUTS |
| SWR12=1 | | NOT USED. |
| SWR11=1 | 004000 | INHIBIT ITERATIONS. |
| SWR10=1 | 002000 | BELL ON ERROR. |
| SWR09=1 | 001000 | LOOP ON ERROR. |
| SWR08=1 | 000400 | LOOP ON TEST SWR <7:0> |
| SWR<7:0> | 0004XX | TEST 8 FOR SWR 8 OPTION. |

THE LSI11 USES A DEDICATED MEMORY LOCATION (00176) AS A SOFTWARE SWITCH REGISTER. THIS LOCATION FUNCTIONS THE SAME AS THE HARDWARE SWITCH REGISTER IN OTHER PDP-11'S. THE SOFT SWITCH REGISTER IS DISPLAYED AND/OR CHANGED AT INITIAL START TIME, AND THEREAFTER MAY BE ALTERED BY TYPING "CNTRL G" <↑G>. THE PROGRAM WILL TYPE THE CURRENT SWR CONTENTS. ENTER THE NEW VALUE, IF ANY, AND TERMINATE WITH "CARRIAGE RETURN" <CR>.

6.0 ERROR REPORTING

ANY ERRORS ENCOUNTERED ARE REPORTED ON THE CONSOLE TTY.
EACH ERROR SIGNATURE IS CONSISTENT WITH THE FOLLOWING
EXAMPLES;

```
RAM READ-WRITE ERROR.  
TEST#  ERRPC  ADDR  GOOD  BAD  
0000NN XXXXXX YYYYYY 177777 000000
```

```
PROM-RAM COPY ERROR.  
TEST#  ERRPC  PRMADD  PRMDAT  RAMADD  RANDAT  
0000NN XXXXXX 140000 010000 023000 000000
```

TESTING CONTINUES FOLLOWING ANY ERROR UNLESS THE
LOOP (SWR 9) OR HALT (SWR 15) OPTIONS ARE SELECTED.

IF AN UNFORSEEN BUS TIME-OUT TRAP OCCURS AT ANY TIME,
AN APPROPRIATE ERROR MESSAGE WILL BE OUTPUT, AND THE
PROCESSOR WILL HALT AT LOC "STOP" (SEE LISTING).

THE FOLLOWING KEY WORDS ARE USED TO IDENTIFY THE
VARIOUS DATA WORDS THAT MAKE UP AN ERROR
SIGNATURE:

ADDR THE WORD ADDRESS CURRENTLY UNDER TEST.
ADDR-2 THE NEXT LOWER WORD ADDRESS (TESTS 5 AND 6).
ADDR+2 THE NEXT HIGHER WORD ADDRESS (TESTS 5 AND 6).
BAD THE BAD (FAILING) DATA WORD.
BUFDAT THE CONTENTS OF AN INTERNAL BUFFER COPY OF RAM
LOCATION "ADDR" (TEST 4).
BYTE THE FAILING BYTE ADDRESS (TEST 10).
DATIS THE CONTENTS OF PROM LOC "ADDR" AFTER A WRITE
ATTEMPT (TEST 12).
DATWAS THE CONTENTS OF PROM LOC "ADDR" BEFORE A WRITE
ATTEMPT (TEST 12).
ERRPC THE PC AT WHICH THE ERROR CALL OCCURRED.
GOOD THE GOOD (EXPECTED) DATA WORD.
PATRN THE CURRENT TEST BIT PATTERN (TESTS 5 AND 6).
PRMADD THE CURRENT PROM ADDRESS (TEST 11).
PRMDAT THE CONTENTS OF "PRMADD".
RAMADD THE CURRENT RAM ADDRESS (TEST 11).
RANDAT THE CONTENTS OF "RAMADD" (TESTS 4 AND 11).
TEST# THE CURRENT TEST NUMBER.
TRAPPC THE PC AT WHICH A BUS-ERROR OCCURRED.
TRAPPS THE PROCESSOR STATUS WHEN BUS-ERROR OCCURRED.

7.0 MISCELLANEOUS

TWO MEMORY DUMP ROUTINES ARE INCLUDED IN THE PROM-RAM TEST FOR CONVENIENCE. THESE ROUTINES WILL DUMP THE CONTENTS OF ANY GIVEN MEMORY RANGE, OR DUMP A 32K MEMORY MAP ON THE CONSOLE TTY.

7.1 MEMORY DATA DUMP.

THIS ROUTINE IS STARTED FROM LOC 00210 (FOR OCTAL OUTPUT) OR 00214 (FOR BINARY OUTPUT). THE OPERATOR WILL BE ASKED TO INPUT THE STARTING ADDRESS AND THE WORD COUNT OF THE RANGE TO BE DUMPED. ODD ADDRESS INPUTS ARE ACCEPTED, BUT THE DUMP RANGE WILL START AT THAT ADDRESS -1. WORD COUNT INPUT MUST BE GREATER THAN ZERO. THE DUMP RANGE IS CHECKED FOR VALIDITY, AND IF OK, THAT RANGE WILL BE TYPED ON THE TTY, 4 WORDS/LINE OCTAL, OR 2 WORDS/LINE BINARY. IF THE DUMP RANGE IS INVALID (NON-EXISTANT ADDRESS OR WORD COUNT = 0) THE STARTING ADDRESS AND WORD COUNT QUERIES WILL BE REPEATED.

ON OUTPUT, EACH DATA SET IS PRECEDED BY THE ADDRESS OF THE FIRST WORD OF THE SET. I.E.

```
0      000000  111111  022222  133333
10     044444  155555  066666  177777
      OR
0      0000000000000000  1001001001001001
4      0010010010010010  0011011011011011
      ETC.
```

7.2 MEMORY MAP DUMP.

NORMALLY A 32K MEMORY MAP IS OUTPUT AS AN INTEGRAL PART OF TEST #1. FOR MAINTENANCE PURPOSES, A START FROM LOC 00220 WILL FORCE A SINGLE PASS ENTRY TO TEST #1, OUTPUT THE RESULTING MAP, AND HALT. SEE 9.1 FOR A DESCRIPTION OF THE MAP FORMAT.

8.0 EXECUTION TIME AND END PASS

THE FIRST PASS THRU THE PROGRAM IS A "QUICK VERIFY" WITH NO ITERATIONS. THEREAFTER, EACH TEST IS REPEATED 100 OCTAL TIMES. EXECUTION TIME WILL VARY FROM 70 TO 150 SECONDS PER PASS DEPENDING ON PROM SIZE. THE PROGRAM WILL TYPE "END PASS # X" AT THE CONCLUSION OF EACH PASS.

9.0 TEST DESCRIPTION

AFTER INITIALIZATION, THE TEST SEQUENCE IS CONDUCTED IN THE FOLLOWING ORDER. EACH TEST IS REPEATED 100 OCTAL TIMES BEFORE PROCEEDING TO THE NEXT. ANY ERRORS OR DISCREPANCIES ARE REPORTED ON THE CONSOLE TTY AS DESCRIBED IN 6.0 ABOVE.

9.1 TEST 1. 32K MEMORY MAP TEST.

FOR THIS TEST A MEMORY MAP IS CONSTRUCTED BY TESTING MEMORY FROM 0 THRU 32K, IN 256 WORD INCREMENTS. TESTING IS DONE VIA A READ/WRITE SEQUENCE ON THE FIRST WORD OF EACH 256 SEGMENT. THE MAP THUS OBTAINED DESCRIBES EACH 256 WORD SEGMENT AS NON-EXISTANT, READ-ONLY, OR READ/WRITE MEMORY. THE MAP INVOLVES THE USE OF 2 WORDS FOR EACH 4K BANK (16. WORDS). ON THE INITIAL PASS EACH MAP PAIR IS TRANSLATED INTO A 16 CHARACTER STRING COMPOSED OF THE CHARACTERS (R) FOR READ ONLY, (W) FOR READ/WRITE, AND (-) FOR NON-EXISTANT, AND OUTPUT AS SHOWN BELOW. ON SUBSEQUENT PASSES, A NEW MAP IS CONSTRUCTED AND COMPARED AGAINST THE ORIGINAL (REFERENCE) MAP. ANY DIFFERENCES ARE INTERPRETED AS ERRORS AND REPORTED ACCORDINGLY. NORMALLY THE MAP IS PRINTED ONLY ONCE, UPON INITIAL START FROM LOC 00200, AND NOT AT ALL IF RUNNING UNDER ACT11, APT11, OR XDP.

A TYPICAL MAP FOR A 4K LSI-11, WITH MRV11-8A, AND 4K PROM MIGHT APPEAR AS FOLLOWS (COMMENTS ADDED):

| | |
|----------------|--------------------------|
| MMMMMMMMMMMMMM | BANK 0 IS R/W MEMORY. |
| W----- | RAM AT DEFAULT 020000 |
| ----- | BANKS 2-5 ARE NEXT |
| ----- | |
| ----- | |
| ----- | |
| RRRRRRRRRRRRRR | PROM AT DEFAULT 140000 |
| ---R---R--- | BANK 7 (I/O PAGE) WITH |
| | BOOTSTRAP ROM AT 165000, |
| | AND 173000. |

NOTE THAT EACH CHARACTER IN A LINE REPRESENTS A 256 WORD (1000 OCTAL BYTE) SEGMENT OF THAT BANK, STARTING AT ADDRESS X+00000 (LEFTMOST CHAR), AND ENDING AT ADDRESS X+17000 (RIGHTMOST CHAR), WHERE X = THE BANK BITS FOR THAT BANK (00, 02, 04, 06, 10, 12, 14, OR 16).

IF A MAP COMPARE ERROR OCCURS, THE MAP PAIR FOR THE FAILING BANK ARE TRANSLATED AS ABOVE AND THE RESULTING MAP LINE IS OUTPUT ON THE TTY. ONE SHOULD ALWAYS SAVE A COPY OF THE 1ST PASS MAP FOR REFERENCE.

9.2 TEST 2. RAM ONES AND ZEROS TEST.

THIS TEST VERIFIES THAT THE RAM CAN HOLD ALL ONES, AND THEN ALL ZEROS. THE RAM IS FILLED WITH THE APPROPRIATE DATA PATTERN, THEN EACH LOCATION IS READ AND VERIFIED.

9.3 TEST 3. RAM BASIC ADDRESS TEST.

THE RAM IS FILLED WITH AN ADDRESS PATTERN WHERE EACH LOCATION CONTAINS ITS OWN ADDRESS WORD. EACH ADDRESS IS THEN READ AND VERIFIED.

9.4 TEST 4. RAM FAST READ TEST.

THIS TEST IS SIMILAR TO THE ADDRESS TEST ABOVE, IN THAT THE DATA PATTERN USED IS AN ADDRESS PATTERN. HOWEVER, IN THIS CASE THE ENTIRE RAM IS COPIED INTO AN INTERNAL BUFFER USING 256 CONSECUTIVE "MOV" INSTRUCTIONS. THE INTENT HERE IS TO HIT THE RAM AS FAST AS POSSIBLE. THE CONTENTS OF THE RAM ARE THEN COMPARED TO THAT OF THE INTERNAL BUFFER.

9.5 TEST 5. RAM SLIDING 0 BIT TEST.

IN THIS TEST THE RAM IS FILLED WITH 1'S, THEN A WORD CONTAINING A SINGLE 0 BIT IS WRITTEN, BEGINNING WITH BIT 0 OF WORD 0, AND ENDING WITH BIT 15 OF WORD 255. AS EACH 0 BIT IS WRITTEN, THAT LOCATION (N) IS VERIFIED, AND THE TWO ADJACENT LOCATIONS (N-2 AND N+2) ARE TESTED TO VERIFY THAT THEY WERE UNAFFECTED BY THE WRITING INTO LOCATION N.

9.6 TEST 6. RAM SLIDING 1 BIT TEST.

SAME AS TEST 4 ABOVE, EXCEPT THAT THE RAM IS INITIALIZED WITH 0'S, AND A WORD CONTAINING A SINGLE 1 BIT IS WRITTEN.

9.7 TEST 7. RAM ALTERNATE BIT/WORD TEST.

THE RAM IS FILLED WITH 052525, 125252, 052525, 125252, ETC., AND VERIFIED. THE PATTERN IS THEN COMPLIMENTED 125252, 052525, 125252, 052525, ETC., AND VERIFIED AGAIN.

9.8 TEST 10. RAM WRITE BYTE TEST.

THE RAM IS FILLED WITH 1'S, AND A LO BYTE 0 IS WRITTEN INTO EACH WORD. EACH ADDRESS IS THEN CHECKED TO SEE THAT THE HI BYTE = -1, AND THE LO BYTE = 0.

REFILL WITH 1'S, AND WRITE AND CHECK A HI BYTE 0.
REFILL WITH 0'S, AND WRITE AND CHECK A LO BYTE -1.
REFILL WITH 0'S, AND WRITE AND CHECK A HI BYTE -1.

9.9 TEST 11. PROM TO RAM COPY TEST.

THE CONTENTS OF THE PROM IS COPIED INTO THE RAM, IN 256 WORD PACKETS. THEN THE RAM DATA IS COMPARED WITH THE PROM DATA. THE COPY-COMPARE CONTINUES UNTIL THE PROM IS EXHAUSTED.

9.10 TEST 12. PROM WRITE-TRAP TEST.

THIS TEST CHECKS THAT ANY ATTEMPT TO WRITE INTO THE PROM RESULTS IN A "BUS ERROR TRAP". ALL PROM ADDRESSES AS DETERMINED BY THE "PROM SIZE", ARE TESTED.

9.11 TEST 13. PROM DATA PATTERN TEST
(IN-HOUSE ENGINEERING USE ONLY)

TEST 13 IS INCLUDED FOR ENGINEERING PURPOSES ONLY, AND REQUIRES THAT A SPECIAL 4K PROGRAMMED PROM CHIP SET BE INSTALLED. ENTRY INTO THE TEST IS ONLY MADE IF THE "X" RESPONSE WAS GIVEN TO THE "PROM SIZE" QUERY DURING INITIALIZATION.

THE TEST READS EACH PROM LOCATION AND VERIFIES THAT THE FOLLOWING BINARY COUNT PATTERN IS READ:

1ST K 010000 THRU 011777
2ND K 022000 THRU 023777
3RD K 044000 THRU 045777
4TH K 106000 THRU 107777

10.0 PROGRAM LISTING FOLLOWS:

x

487
488
489
490
491
492
493
494
495
496
497
498
499
500
501
502
503
504
505
506
507
508
509
510
511
512
513
514
515

000001
160000

167400
020000
140000
000004

```

:TITLE LSI-11 UVPROM-RAM TEST. MD-11-DVMRA-A.
:*COPYRIGHT (C) 1977
:*DIGITAL EQUIPMENT CORP.
:*MAYNARD, MASS. 01754
:*
:*PROGRAM BY GUS PASQUANTONIO
:*
:*THIS PROGRAM WAS ASSEMBLED USING THE PDP-11 MAINDEC SYSMAC
:*PACKAGE (MAINDEC-11-DZQAC-C2), SEPT 14, 1976.
:*
$TN=1
$SWR=160000 ;;HALT ON ERROR, LOOP ON TEST, INHIBIT ERROR TYP0UT

$SWR=167400 ;REDEFINE SWITCHES USED.
ABASE=020000 ;DEFAULT RAM ADDRESS
ACDW1=140000 ;DEFAULT PROM ADDRESS
ACDW2=4 ;DEFAULT PROM SIZE IN K.

.SBTTL OPERATIONAL SWITCH SETTINGS
:*
:* SWITCH USE
:* -----
:* 15 HALT ON ERROR
:* 14 LOOP ON TEST
:* 13 INHIBIT ERROR TYPEOUTS
:* 11 INHIBIT ITERATIONS
:* 10 BELL ON ERROR
:* 9 LOOP ON ERROR
:* 8 LOOP ON TEST IN SWR<7:0>

```

516
517
518
519
520
521
522
523
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531
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562
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564
565
566
567
568
569

000000

000174 000174
000176 000000

000004 000004 000000

000100 000100 000200 000002

000200 000200
000204 000137 002620

000210 000137 007174
000214 000137 007276
000220 000137 007124

001100

000011
000012
000015
000200
177776

177774
177772
177570
177570

000000
000001
000002
000003
000004
000005
000006
000007
000006
000007

```
.SBTTL TRAP CATCHER

;#ALL UNUSED LOCATIONS FROM 4 - 776 CONTAIN A "+2,HALT"
;#SEQUENCE TO CATCH ILLEGAL TRAPS AND INTERRUPTS
;#LOCATION 0 CONTAINS 0 TO CATCH IMPROPERLY LOADED VECTORS

DISPREG: .WORD 0      ;;SOFTWARE DISPLAY REGISTER
SWREG:   .WORD 0      ;;SOFTWARE SWITCH REGISTER

        .=4
        .WORD TIMEOUT,0 ;BUS ERROR PROCESSER.

        .=100
        .WORD 104,200,2 ;IGNORE ANY "BEVNT" INTERRUPTS

        .=200
        JMP @#START      ;INITIAL START AND INIT.
        JMP @#RSTRT     ;RESTART. USE SAME PROM/RAM
                        ;PARAMETERS. NO MAP OUTPUT.
        JMP @#ODUMP     ;OCTAL DUMP START
        JMP @#BDUMP     ;BINARY DUMP START
        JMP @#MDUMP     ;MAP DUMP START

.SBTTL BASIC DEFINITIONS

;#INITIAL ADDRESS OF THE STACK POINTER *** 1100 ***
STACK= 1100
.EQUIV EMT,ERROR      ;;BASIC DEFINITION OF ERROR CALL
.EQUIV IOT,SCOPE     ;;BASIC DEFINITION OF SCOPE CALL

;#MISCELLANEOUS DEFINITIONS
HT= 11                ;CODE FOR HORIZONTAL TAB
LF= 12                ;CODE FOR LINE FEED
CR= 15                ;CODE FOR CARRIAGE RETURN
CRLF= 200             ;CODE FOR CARRIAGE RETURN-LINE FEED
PS= 177776           ;PROCESSOR STATUS WORD
.EQUIV PS,PSW
STKLMT= 177774       ;STACK LIMIT REGISTER
PIRQ= 177772         ;PROGRAM INTERRUPT REQUEST REGISTER
DSWR= 177570         ;HARDWARE SWITCH REGISTER
DDISP= 177570        ;HARDWARE DISPLAY REGISTER

;#GENERAL PURPOSE REGISTER DEFINITIONS
R0= %0                ;GENERAL REGISTER
R1= %1                ;GENERAL REGISTER
R2= %2                ;GENERAL REGISTER
R3= %3                ;GENERAL REGISTER
R4= %4                ;GENERAL REGISTER
R5= %5                ;GENERAL REGISTER
R6= %6                ;GENERAL REGISTER
R7= %7                ;GENERAL REGISTER
SP= %6                ;STACK POINTER
PC= %7                ;PROGRAM COUNTER
```

570
571
572 000000
573 000040
574 000100
575 000140
576 000200
577 000240
578 000300
579 000340
580
581
582 100000
583 040000
584 020000
585 010000
586 004000
587 002000
588 001000
589 000400
590 000200
591 000100
592 000040
593 000020
594 000010
595 000004
596 000002
597 000001
598
599
600
601
602
603
604
605
606
607
608
609
610 100000
611 040000
612 020000
613 010000
614 004000
615 002000
616 001000
617 000400
618 000200
619 000100
620 000040
621 000020
622 000010
623 000004

.:#PRIORITY LEVEL DEFINITIONS

| | | | |
|------|-----|------------------|---|
| PR0= | 0 | .:PRIORITY LEVEL | 0 |
| PR1= | 40 | .:PRIORITY LEVEL | 1 |
| PR2= | 100 | .:PRIORITY LEVEL | 2 |
| PR3= | 140 | .:PRIORITY LEVEL | 3 |
| PR4= | 200 | .:PRIORITY LEVEL | 4 |
| PR5= | 240 | .:PRIORITY LEVEL | 5 |
| PR6= | 300 | .:PRIORITY LEVEL | 6 |
| PR7= | 340 | .:PRIORITY LEVEL | 7 |

.:#"SWITCH REGISTER" SWITCH DEFINITIONS

| | |
|-------|--------|
| SW15= | 100000 |
| SW14= | 40000 |
| SW13= | 20000 |
| SW12= | 10000 |
| SW11= | 4000 |
| SW10= | 2000 |
| SW09= | 1000 |
| SW08= | 400 |
| SW07= | 200 |
| SW06= | 100 |
| SW05= | 40 |
| SW04= | 20 |
| SW03= | 10 |
| SW02= | 4 |
| SW01= | 2 |
| SW00= | 1 |

.EQUIV SW09,SW14
.EQUIV SW08,SW13
.EQUIV SW07,SW12
.EQUIV SW06,SW11
.EQUIV SW05,SW10
.EQUIV SW04,SW09
.EQUIV SW03,SW08
.EQUIV SW02,SW07
.EQUIV SW01,SW06
.EQUIV SW00,SW05

.:#DATA BIT DEFINITIONS (BIT00 TO BIT15)

| | |
|--------|--------|
| BIT15= | 100000 |
| BIT14= | 40000 |
| BIT13= | 20000 |
| BIT12= | 10000 |
| BIT11= | 4000 |
| BIT10= | 2000 |
| BIT09= | 1000 |
| BIT08= | 400 |
| BIT07= | 200 |
| BIT06= | 100 |
| BIT05= | 40 |
| BIT04= | 20 |
| BIT03= | 10 |
| BIT02= | 4 |

624 000002
625 000001
626
627
628
629
630
631
632
633
634
635
636
637
638 000004
639 000010
640 000014
641 000014
642 000014
643 000020
644 000024
645 000030
646 000034
647 000060
648 000064
649 000240
650
651
652
653
654
655 000224
656 000046
657 000046 006626
658 000052
659 000052 000000
660 000224
661 001000
662
663
664
665
666
667 001000
668 000024
669 000024 000200
670 000044
671 000044 001000
672 001000
673
674
675
676
677 001000

BIT01= 2
BIT00= 1
.EQUIV BIT09,BIT9
.EQUIV BIT08,BIT8
.EQUIV BIT07,BIT7
.EQUIV BIT06,BIT6
.EQUIV BIT05,BIT5
.EQUIV BIT04,BIT4
.EQUIV BIT03,BIT3
.EQUIV BIT02,BIT2
.EQUIV BIT01,BIT1
.EQUIV BIT00,BIT0

.*BASIC "CPU" TRAP VECTOR ADDRESSES
ERRVEC= 4 ; TIME OUT AND OTHER ERRORS
RESVEC= 10 ; RESERVED AND ILLEGAL INSTRUCTIONS
TBITVEC= 14 ; "T" BIT
TRTVEC= 14 ; TRACE TRAP
BPTVEC= 14 ; BREAKPOINT TRAP (BPT)
IOTVEC= 20 ; INPUT/OUTPUT TRAP (IOT) **SCOPE**
PWRVEC= 24 ; POWER FAIL
EMTVEC= 30 ; EMULATOR TRAP (EMT) **ERROR**
TRAPVEC= 34 ; "TRAP" TRAP
TKVEC= 60 ; TTY KEYBOARD VECTOR
TPVEC= 64 ; TTY PRINTER VECTOR
PIRQVEC= 240 ; PROGRAM INTERRUPT REQUEST VECTOR

.SBTTL ACT11 HOOKS

;HOOKS REQUIRED BY ACT11
\$SVPC=.; ;SAVE PC
=46 ;;1)SET LOC.46 TO ADDRESS OF SENDAD IN .SEOP
SENDAD
=52 ;;2)SET LOC.52 TO ZERO
.WORD 0 ;; RESTORE PC
=\$SVPC
=1000

.SBTTL APT PARAMETER BLOCK

;SET LOCATIONS 24 AND 44 AS REQUIRED FOR APT

.\$X=.; ;SAVE CURRENT LOCATION
=24 ;;SET POWER FAIL TO POINT TO START OF PROGRAM
200 ;;FOR APT START UP
=44 ;;POINT TO APT INDIRECT ADDRESS PNTR.
\$APTHDR ;;POINT TO APT HEADER BLOCK
=.\$X ;;RESET LOCATION COUNTER

;SETUP APT PARAMETER BLOCK AS DEFINED IN THE APT-POP11 DIAGNOSTIC
;INTERFACE SPEC.

\$APTHD:

678 001000 000000
679 001002 001174
680 001004 000055
681 001006 000005
682 001010 000005
683 001012 000032

SHIBTS: .WORD 0
SMBADR: .WORD SMAIL
STSTM: .WORD 45.
SPASTM: .WORD 5.
SUNITH: .WORD 5.
.WORD SETEND-SMAIL/2 ;;LENGTH MAILBOX-ETABLE(WORDS)

;; TWO HIGH BITS OF 18 BIT MAILBOX ADDR.
;; ADDRESS OF APT MAILBOX (BITS 0-15)
;; RUN TIME OF LONGEST TEST
;; RUN TIME IN SECS. OF 1ST PASS ON 1 UNIT (QUICK VERIFY)
;; ADDITIONAL RUN TIME (SECS) OF A PASS FOR EACH ADDITIONAL UNIT

.SBTTL COMMON TAGS

: THIS TABLE CONTAINS VARIOUS COMMON STORAGE LOCATIONS
: USED IN THE PROGRAM.

684
685
686
687
688
689
690 001100
691 001100
692 001100 000000
693 001102 000
694 001103 000
695 001104 000000
696 001106 000000
697 001110 000000
698 001112 000000
699 001114 000
700 001115 001
701 001116 000000
702 001120 000000
703 001122 000000
704 001124 000000
705 001126 000000
706 001130 000000
707 001132 000000
708 001134 000
709 001135 000
710 001136 000000
711 001140 177570
712 001142 177570
713 001144 177560
714 001146 177562
715 001150 177564
716 001152 177566
717 001154 000
718 001155 002
719 001156 012
720 001157 000
721 001160 000000
722 001162 000000
723 001164 177607 000377
724 001170 077
725 001171 015
726 001172 000012
727
728
729
730
731
732 001174
733 001174 000000
734 001176 000000
735 001200 000000
736 001202 000000
737 001204 000000

SCNTAG: . =1100
: START OF COMMON TAGS
STSTNM: .WORD 0 : CONTAINS THE TEST NUMBER
SERFLG: .BYTE 0 : CONTAINS ERROR FLAG
SICNT: .WORD 0 : CONTAINS SUBTEST ITERATION COUNT
SLPADR: .WORD 0 : CONTAINS SCOPE LOOP ADDRESS
SLPERR: .WORD 0 : CONTAINS SCOPE RETURN FOR ERRORS
SERTTL: .WORD 0 : CONTAINS TOTAL ERRORS DETECTED
SITEMB: .BYTE 0 : CONTAINS ITEM CONTROL BYTE
SERMAX: .BYTE 1 : CONTAINS MAX. ERRORS PER TEST
SERRPC: .WORD 0 : CONTAINS PC OF LAST ERROR INSTRUCTION
SGDADR: .WORD 0 : CONTAINS ADDRESS OF 'GOOD' DATA
SBDADR: .WORD 0 : CONTAINS ADDRESS OF 'BAD' DATA
SGDDAT: .WORD 0 : CONTAINS 'GOOD' DATA
SBDAT: .WORD 0 : CONTAINS 'BAD' DATA
: RESERVED--NOT TO BE USED
SAUTOB: .BYTE 0 : AUTOMATIC MODE INDICATOR
SINTAG: .BYTE 0 : INTERRUPT MODE INDICATOR
SWR: .WORD DSWR : ADDRESS OF SWITCH REGISTER
DISPLAY: .WORD DDISP : ADDRESS OF DISPLAY REGISTER
STKS: 177560 : TTY KBD STATUS
STKB: 177562 : TTY KBD BUFFER
STPS: 177564 : TTY PRINTER STATUS REG. ADDRESS
STPB: 177566 : TTY PRINTER BUFFER REG. ADDRESS
SNLL: .BYTE 0 : CONTAINS NULL CHARACTER FOR FILLS
SFILLS: .BYTE 2 : CONTAINS # OF FILLER CHARACTERS REQUIRED
SFILLC: .BYTE 12 : INSERT FILL CHARS. AFTER A "LINE FEED"
STPFLG: .BYTE 0 : "TERMINAL AVAILABLE" FLAG (BIT<07>=0=YES)
STIMES: 0 : MAX. NUMBER OF ITERATIONS
SESCAPE: 0 : ESCAPE ON ERROR ADDRESS
SBELL: .ASCIZ <207><377><377> : CODE FOR BELL
SQUES: .ASCII /?/ : QUESTION MARK
SCRLF: .ASCII <15> : CARRIAGE RETURN
SLF: .ASCIZ <12> : LINE FEED

.SBTTL APT MAILBOX-ETABLE

.EVEN
SMAIL: : APT MAILBOX
SMSGTY: .WORD AMSGTY : MESSAGE TYPE CODE
SFATAL: .WORD AFATAL : FATAL ERROR NUMBER
STESTN: .WORD ATESTN : TEST NUMBER
SPASS: .WORD APASS : PASS COUNT
SDEVCT: .WORD ADEVCT : DEVICE COUNT

| | | | | | |
|-----|--------|--------|---------------|--------|---|
| 738 | 001206 | 000000 | SUNIT: .WORD | RUNIT | :: I/O UNIT NUMBER |
| 739 | 001210 | 000000 | SMSGAD: .WORD | RMSGAD | :: MESSAGE ADDRESS |
| 740 | 001212 | 000000 | SMSGLG: .WORD | RMSGLG | :: MESSAGE LENGTH |
| 741 | 001214 | | SETABLE: | | :: APT ENVIRONMENT TABLE |
| 742 | 001214 | 000 | SENV: .BYTE | RENVM | :: ENVIRONMENT BYTE |
| 743 | 001215 | 000 | SENVH: .BYTE | RENVMH | :: ENVIRONMENT MODE BITS |
| 744 | 001216 | 000000 | SSWREG: .WORD | RSWREG | :: APT SWITCH REGISTER |
| 745 | 001220 | 000000 | SUSMR: .WORD | RUSMR | :: USER SWITCHES |
| 746 | 001222 | 000000 | SCPUOP: .WORD | ACPUOP | :: CPU TYPE, OPTIONS |
| 747 | | | :: | | BITS 15-11=CPU TYPE |
| 748 | | | :: | | 11/04=01, 11/05=02, 11/20=03, 11/40=04, 11/45=05 |
| 749 | | | :: | | 11/70=06, P00=07, Q=10 |
| 750 | | | :: | | BIT 10=REAL TIME CLOCK |
| 751 | | | :: | | BIT 9=FLOATING POINT PROCESSOR |
| 752 | | | :: | | BIT 8=MEMORY MANAGEMENT |
| 753 | 001224 | 000 | SMAMS1: .BYTE | AMAMS1 | :: HIGH ADDRESS, M.S. BYTE |
| 754 | 001225 | 000 | SMTYP1: .BYTE | AMTYP1 | :: MEM. TYPE, BLK#1 |
| 755 | | | :: | | MEM. TYPE BYTE -- (HIGH BYTE) |
| 756 | | | :: | | 900 NSEC CORE=001 |
| 757 | | | :: | | 300 NSEC BIPOLAR=002 |
| 758 | | | :: | | 500 NSEC NOS=003 |
| 759 | 001226 | 000000 | SMADR1: .WORD | AMADR1 | :: HIGH ADDRESS, BLK#1 |
| 760 | | | :: | | MEM. LAST ADDR. =3 BYTES, THIS WORD AND LOW OF "TYPE" ABOVE |
| 761 | 001230 | 000 | SMAMS2: .BYTE | AMAMS2 | :: HIGH ADDRESS, M.S. BYTE |
| 762 | 001231 | 000 | SMTYP2: .BYTE | AMTYP2 | :: MEM. TYPE, BLK#2 |
| 763 | 001232 | 000000 | SMADR2: .WORD | AMADR2 | :: MEM. LAST ADDRESS, BLK#2 |
| 764 | 001234 | 000 | SMAMS3: .BYTE | AMAMS3 | :: HIGH ADDRESS, M.S. BYTE |
| 765 | 001235 | 000 | SMTYP3: .BYTE | AMTYP3 | :: MEM. TYPE, BLK#3 |
| 766 | 001236 | 000000 | SMADR3: .WORD | AMADR3 | :: MEM. LAST ADDRESS, BLK#3 |
| 767 | 001240 | 000 | SMAMS4: .BYTE | AMAMS4 | :: HIGH ADDRESS, M.S. BYTE |
| 768 | 001241 | 000 | SMTYP4: .BYTE | AMTYP4 | :: MEM. TYPE, BLK#4 |
| 769 | 001242 | 000000 | SMADR4: .WORD | AMADR4 | :: MEM. LAST ADDRESS, BLK#4 |
| 770 | 001244 | 000000 | SVECT1: .WORD | AVECT1 | :: INTERRUPT VECTOR#1, BUS PRIORITY#1 |
| 771 | 001246 | 000000 | SVECT2: .WORD | AVECT2 | :: INTERRUPT VECTOR#2, BUS PRIORITY#2 |
| 772 | 001250 | 020000 | SBASE: .WORD | ABASE | :: BASE ADDRESS OF EQUIPMENT UNDER TEST |
| 773 | 001252 | 000000 | SDEVH: .WORD | ADEVH | :: DEVICE MAP |
| 774 | 001254 | 140000 | SCDW1: .WORD | ACDW1 | :: CONTROLLER DESCRIPTION WORD#1 |
| 775 | 001256 | 000004 | SCDW2: .WORD | ACDW2 | :: CONTROLLER DESCRIPTION WORD#2 |
| 776 | 001260 | | SETEND: | | |
| 777 | | | .MEXIT | | |

.SBTTL ERROR POINTER TABLE

;;THIS TABLE CONTAINS THE INFORMATION FOR EACH ERROR THAT CAN OCCUR.
;;THE INFORMATION IS OBTAINED BY USING THE INDEX NUMBER FOUND IN
;;LOCATION SITEMB. THIS NUMBER INDICATES WHICH ITEM IN THE TABLE IS PERTINENT.
;;NOTE1: IF SITEMB IS 0 THE ONLY PERTINENT DATA IS (SERRPC).
;;NOTE2: EACH ITEM IN THE TABLE CONTAINS 4 POINTERS EXPLAINED AS FOLLOWS:

;; EM ;;POINTS TO THE ERROR MESSAGE
;; DH ;;POINTS TO THE DATA HEADER
;; DT ;;POINTS TO THE DATA
;; DF ;;POINTS TO THE DATA FORMAT

778
779
780
781
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783
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827
828
829
830

| Index | SITEMB | EM | DH | DT | DF | Description |
|-------|--------|--------|--------|--------|----|--|
| 792 | 001260 | | | | | SERRTB: |
| 793 | | | | | | ;ITEM1 |
| 794 | 001260 | 013436 | 013464 | 013516 | | EM1,DH1,DT1,DF ;READ-WRITE ERROR |
| 795 | 001266 | 015110 | | | | |
| 796 | | | | | | ;ITEM2 |
| 797 | 001270 | 013532 | 013555 | 013610 | | EM2,DH2,DT2,DF ;BASIC ADDRESS TEST. |
| 798 | 001276 | 015110 | | | | |
| 799 | | | | | | ;ITEM3 |
| 800 | 001300 | 013624 | 013651 | 013716 | | EM3,DH3,DT3,DF ;FAST READ TEST. |
| 801 | 001306 | 015110 | | | | |
| 802 | | | | | | ;ITEM4 |
| 803 | 001310 | 013734 | 014002 | 014050 | | EM4,DH4,DT4,DF ;SLIDE TEST. ADDR-2 ALTERED. |
| 804 | 001316 | 015110 | | | | |
| 805 | | | | | | ;ITEM5 |
| 806 | 001320 | 014070 | 014136 | 014204 | | EM5,DH5,DT5,DF ;SLIDE TEST. ADDR+2 ALTERED. |
| 807 | 001326 | 015110 | | | | |
| 808 | | | | | | ;ITEM6 |
| 809 | 001330 | 014224 | 014265 | 014320 | | EM6,DH6,DT6,DF ;ALTERNATE BIT ERROR. |
| 810 | 001336 | 015110 | | | | |
| 811 | | | | | | ;ITEM7 |
| 812 | 001340 | 014334 | 014361 | 014420 | | EM7,DH7,DT7,DF ;RAM BYTE TEST ERROR |
| 813 | 001346 | 015110 | | | | |
| 814 | | | | | | ;ITEM10 |
| 815 | 001350 | 014436 | 014457 | 014512 | | EM10,DH10,DT10,DF ;PROM SPECIAL DATA TEST READ ERROR |
| 816 | 001356 | 015110 | | | | |
| 817 | | | | | | ;ITEM11 |
| 818 | 001360 | 014526 | 014553 | 014624 | | EM11,DH11,DT11,DF ;PROM TO RAM COPY ERROR. |
| 819 | 001366 | 015110 | | | | |
| 820 | | | | | | ;ITEM12 |
| 821 | 001370 | 014642 | 014671 | 014730 | | EM12,DH12,DT12,DF ;PROM WRITE TRAP FAILURE. |
| 822 | 001376 | 015110 | | | | |
| 823 | | | | | | ;ITEM13 |
| 824 | 001400 | 014744 | 014777 | 015024 | | EM13,DH13,DT13,DF ;BUS TIME-OUT (TRAP) ERROR. |
| 825 | 001406 | 015110 | | | | |
| 826 | | | | | | ;ITEM14 |
| 827 | 001410 | 015034 | 000000 | 000000 | | EM14,0,0,0 ;MEM MAP ERROR. |
| 828 | 001416 | 000000 | | | | |
| 829 | | | | | | ;NON-STANDARD MAP SIGNATURE. |
| 830 | | | | | | ;DH, DT, AND DF = 0 |

```

831
832 001420 000000
833 001422 000000
834 001424 000000
835 001426 000000
836 001430 000000
837 001432 000000
838 001434 000000
839
840 001436
841
842
843 001436 012706 001100
844 001442 005026
845 001444 022706 001140
846 001450 001374
847 001452 012706 001100
848
849 001456 012737 007654 000020
850 001464 012737 000340 000022
851 001472 012737 010214 000030
852 001500 012737 000340 000032
853 001506 012737 013120 000034
854 001514 012737 000340 000036
855 001522 012737 012474 000024
856 001530 012737 000340 000026
857 001536 013737 006574 006566
858 001544 005037 001160
859 001550 005037 001162
860 001554 112737 000001 001115
861 001562 012737 001562 001106
862 001570 012737 001570 001110
863
864
865 001576 013746 000004
866 001602 012737 001636 000004
867 001610 012737 177570 001140
868 001616 012737 177570 001142
869 001624 022777 177777 177306
870 001632 001012
871
872 001634 000403
873 001636 012716 001644 645:
874 001642 000002
875 001644 012737 000176 001140 655:
876 001652 012737 000174 001142
877 001660 012637 000004 665:
878
879 001664 005037 001202
880 001670 132737 000200 001215
881 001676 001403
882 001700 012737 001216 001140
883 001706

```

```

.SBTTL INITIAL START-UP.
RAM: 0 ;: RAM START LOC.
RAMEND: 0 ;: RAM LAST LOC
PRM: 0 ;: PROM START LOC
PRMEND: 0 ;: PROM LAST LOC
PRMSZ: 0 ;: PROM SIZE (IN K)
PRMX: 0 ;: 0 NORM, -1 IF SPECIAL 4K SET.
MAINT: 0 ;: 0 NORM, -1 IF ANY DUMP START.

START:
.SBTTL INITIALIZE THE COMMON TAGS
;;CLEAR THE COMMON TAGS (SCHTAG) AREA
MOV #SCHTAG,R6 ;: FIRST LOCATION TO BE CLEARED
CLR (R6)+ ;: CLEAR MEMORY LOCATION
CMP #SMR,R6 ;: DONE?
BNE -6 ;: LOOP BACK IF NO
MOV #STACK,SP ;: SETUP THE STACK POINTER
;;INITIALIZE A FEW VECTORS
MOV #SCOPE,#IOTVEC ;: IOT VECTOR FOR SCOPE ROUTINE
MOV #340,#IOTVEC+2 ;: LEVEL 7
MOV #ERROR,#EMTVEC ;: EMT VECTOR FOR ERROR ROUTINE
MOV #340,#EMTVEC+2 ;: LEVEL 7
MOV #TRAP,#TRAPVEC ;: TRAP VECTOR FOR TRAP CALLS
MOV #340,#TRAPVEC+2 ;: LEVEL 7
MOV #SPWRON,#SPWRVEC ;: POWER FAILURE VECTOR
MOV #340,#SPWRVEC+2 ;: LEVEL 7
MOV SENDCT,SEOPCT ;: SETUP END-OF-PROGRAM COUNTER
CLR STIMES ;: INITIALIZE NUMBER OF ITERATIONS
CLR ESCAPE ;: CLEAR THE ESCAPE ON ERROR ADDRESS
MOVB #1,SEMAX ;: ALLOW ONE ERROR PER TEST
MOV #.,SLPADR ;: INITIALIZE THE LOOP ADDRESS FOR SCOPE
MOV #.,SLPERR ;: SETUP THE ERROR LOOP ADDRESS
;;SIZE FOR A HARDWARE SWITCH REGISTER, IF NOT FOUND OR IT IS
;;EQUAL TO A "-1" SETUP FOR A SOFTWARE SWITCH REGISTER.
MOV #ERRVEC,-(SP) ;: SAVE ERROR VECTOR
MOV #645,#ERRVEC ;: SET UP ERROR VECTOR
MOV #DSMR,SMR ;: SETUP FOR A HARDWARE SWICH REGISTER
MOV #DISP,DISPLAY ;: AND A HARDWARE DISPLAY REGISTER
CMP #-1,SMR ;: TRY TO REFERENCE HARDWARE SMR
BNE 665 ;: BRANCH IF NO TIMEOUT TRAP OCCURRED
;: AND THE HARDWARE SMR IS NOT = -1
BR 655 ;: BRANCH IF NO TIMEOUT
;: SETUP FOR TRAP RETURN
MOV #655,(SP)
RTI
MOV #SMREG,SMR ;: POINT TO SOFTWARE SMR
MOV #DISPREG,DISPLAY
MOV (SP)+,#ERRVEC ;: RESTORE ERROR VECTOR
CLR SPASS ;: CLEAR PASS COUNT
BITB #APTSIZE,SENVN ;: TEST USER SIZE UNDER APT
BEQ 675 ;: YES, USE NON-APT SWITCH
MOV #SSWREG,SMR ;: NO, USE APT SWITCH REGISTER
675:

```

```

884 001706 005737 001434      TST      MAINT      ;MAINT FLAG SET ??
885 001712 001401              BEQ      .+4        ;NO, PROCEED WITH NORMAL START.
886 001714 000207              RTS      PC         ;YES, RETURN TO MAINT ROUTINE.
887
888
889      .SBTTL  TYPE PROGRAM NAME
      ;;TYPE THE NAME OF THE PROGRAM IF FIRST PASS
890 001716 005227 177777      INC      #-1        ;FIRST TIME?
891 001722 001054              BNE      685        ;BRANCH IF NO
892 001724 022737 006626 000042  CMP      #SENDAD,2#42 ;ACT-11?
893 001732 001450              BEQ      685        ;BRANCH IF YES
894 001734 104401 002002      TYPE     695        ;TYPE ASCIZ STRING
895      .SBTTL  GET VALUE FOR SOFTWARE SWITCH REGISTER
896 001740 005737 000042      TST      2#42      ;ARE WE RUNNING UNDER XXDP/ACT?
897 001744 001012              BNE      705        ;BRANCH IF YES
898 001746 123727 001214 000001  CMPB    #ENV,#1    ;ARE WE RUNNING UNDER APT?
899 001754 001406              BEQ      705        ;BRANCH IF YES
900 001756 023727 001140 000176  CMP      SWR,#SWREG ;SOFTWARE SWITCH REG SELECTED?
901 001764 001005              BNE      715        ;BRANCH IF NO
902 001766 104407              GTSWR                ;GET SOFT-SWR SETTINGS
903 001770 000403              BR       715
904 001772 112737 000001 001134 705:    MOVB    #1,SAUTOB  ;;SET AUTO-MODE INDICATOR
905 002000 715:
906 002000 000425              BR       685        ;GET OVER THE ASCIZ
907      ;;695: .ASCIZ <CRLF>/MD-11-DVMRA-A. LSI-11 UVPROM-RAM TEST./<CRLF>
908 002054 685:
909      .SBTTL  INITIALIZE RAM AND PROM ADDRESSES.
910      ;IF APT, ACT, OR XXDP SKIP DIALOGUE AND USE DEFAULTS.
911      ;RAM = 20000, PROM = 140000, SIZE = 4K.
912      ;
913 002054 005737 000042      TST      2#42      ;ACT OR XXDP ??
914 002060 001004              BNE      15        ;YES, USE DEFAULTS
915 002062 123727 001214 000001  CMPB    #ENV,#1    ;NO, HOW 'BOUT APT ??
916 002070 001044              BNE      INIT1     ;NO, INITIALIZE VIA OPERATOR
917
918 002072 013737 001250 001420 15:    MOV     $BASE, RAM ;ACT, XXDP, OR APT, USE DEFAULTS
919 002100 013737 001250 001422      MOV     $BASE, RAMEND ;...1ST RAM LOC = 20000
920 002106 062737 000776 001422      ADD     #776, RAMEND ;...LAST RAM LOC = 020776
921
922 002114 005037 001432              CLR     PRMX        ;CLEAR SPECIAL PROM FLAG
923 002120 013737 001254 001424      MOV     $CDW1, PRM  ;...1ST PROM LOC = 140000
924 002126 013737 001256 001430      MOV     $CDW2, PRMSZ ;...SIZE = 4K
925 002134 013701 001256              MOV     $CDW2, R1   ;...CALCULATE LAST LOC.
926 002140 012737 003776 001426      MOV     #3776, PRMEND ;...IN PRMEND
927 002146 005301 25:    DEC     R1
928 002150 001404              BEQ     35
929 002152 062737 004000 001426      ADD     #4000, PRMEND
930 002160 000772              BR      25
931 002162 063737 001424 001426 35:    ADD     PRM, PRMEND ;LAST PROM LOC = 1ST + K.-2
932 002170 012737 000377 007022      MOV     #377, MAPSW ;INH TEST 1 MAP OUTPUT.
933 002176 000137 002620              JMP     RSTR†      ;GO START 'EM UP.
    
```

```

;INITIALIZE RAM ADDRESSES. IF 0, USE DEFAULT ADDRESS.
INIT1:
        TYPE      65$          ;;TYPE ASCIZ STRING
        BR        64$          ;;GET OVER THE ASCIZ
;;65$: .ASCIZ <CRLF>/RAM ADDRESS = /
64$:
        RDOCT
        MOV      (SP)+,RAM      ;;POP STACK INTO RAM
        BNE     1$             ;;IS IT 0 ??
        MOV     $BASE,RAM      ;;YES, USE DEFAULT RAM (20000)
        BIT    RAM,#777       ;;256 WORD (1000 BYTE) BOUNDARY ??
        BNE     2$             ;;NO, COMPLAIN
        MOV     RAM,R1        ;;R1=START LOC
        MOV     #776,R2
        ADD    R1,R2          ;;R2=LAST LOC
        MOV     R2,RAMEND     ;;SAVE IT
        JSR    PC,NXM        ;;TEST BOTH FOR NXM
        BR     2$            ;;ONE OR T'OTHER TRAPPED !!!!!
        BR     INIT2         ;;OK, GO INIT FROM SIZE.

2$:     TYPE      M1          ;;SAY "INVALID ADDRESS"...
        BR      INIT1        ;;...AND TRY AGAIN.

;NOW GET THE PROM SIZE. IF 0, DEFAULT TO 4K.
;IF X, SPECIAL 4K SET INSTALLED. OTHERWISE ACCEPT 1-4 ONLY.
;ALSO ACCEPT 1024, 2048, 3072, OR 4096, ALTHO ACTUALLY USE
;JUST THE FIRST DIGIT.
INIT2:
        TYPE      65$          ;;TYPE ASCIZ STRING
        BR        64$          ;;GET OVER THE ASCIZ
;;65$: .ASCIZ <CRLF>/PROM SIZE = /
64$:
        RDLIN
        MOV     (SP)+,RO      ;;POP ADDR OF 1ST CHAR.
        MOV     (RO),PRMSZ    ;;GET THAT CHARACTER.
        MOV     PRMSZ+1,CLRB  ;;CLEAR HI BYTE.
        CLR    PRMX          ;;CLEAR SPECIAL PROM FLAG
        TST    PRMSZ         ;;SIZE = 0 (CR ONLY) ??
        BNE     1$           ;;NO IT'S NOT
        MOV     $CDW2,PRMSZ  ;;YES, DEFAULT IS 4K.
        BR     INIT3        ;;GO GET START ADDRESS.

1$:    CMP      #'X,PRMSZ    ;;SIZE = "X" ??
        BNE     2$           ;;NO.
        MOV     #-1,PRMX    ;;YES, SET SPECIAL FLAG...
        MOV     #4,PRMSZ   ;;...AND SET SIZE = 4K.
        BR     INIT3        ;;GO GET ADDRESS.

2$:    SUB      #60,PRMSZ    ;;STRIP ASCII BITS
        BMI     3$           ;;SIZE < 0, INVALID.
        SUB     #5,PRMSZ    ;;
        BPL     3$           ;;SIZE > 4, INVALID.
    
```

K02

LSI-11 UVPROM-RAM TEST. MD-11-DVMRA-A. MACY11 27(663) 24-MAR-77 13:51 PAGE 22
 DVMRAA.P11 INITIALIZE RAM AND PROM ADDRESSES.

```

988 002442 062737 000005 001430      ADD      #5,PRMSZ      ;SIZE = 1, 2, 3, OR 4.
989 002450 000403                    BR       INIT3       ;NOW GET PROM ADDRESS
990
991 002452 104401 001170      3$:     TYPE      $QUES      ;TYPE '??'
992 002456 000715                    BR       INIT2       ;...AND TRY AGAIN.
993
994                                ;NOW INITIALIZE THE PROM ADDRESSES. IF 0, USE DEFAULT.
995
996 002460                    INIT3:
997 002460 104401 002466      TYPE      #655      ;:TYPE ASCIZ STRING
998 002464 000411                    BR       #645      ;:GET OVER THE ASCIZ
999                                ;:65$: .ASCIZ <CRLF>/PROM ADDRESS = /
1000
1001 002510 104413                    64$:
1002 002512 012637 001424      RDOCT
1003 002516 001003                    MOV      (SP)+,PRM   ;:POP STACK INTO PRM
1004 002520 013737 001254 001424  BNE      #1$         ;:IS IT 0 ??
1005 002526 032737 003777 001424  1$:     MOV      $CDW1,PRM ;:YES, USE DEFAULT PROM (140000).
1006 002534 001025                    BIT      #3777,PRM  ;:ON 1K WORD (4000 BYTE) BOUNDARY ??
1007                                BNE      #4$         ;:NO, COMPLAIN.
1008
1008 002536 013701 001424      MOV      PRM,R1     ;:YES, SET UP FOR NXM CHECK.
1009 002542 012702 003776      MOV      #3776,R2   ;:DETERMINE LAST ADDRESS...
1010 002546 013703 001430      MOV      PRMSZ,R3   ;:...BASED ON PROM SIZE.
1011                                2$:     DEC      R3
1012 002554 001403                    BEQ      #3$
1013 002556 062702 004000      ADD      #4000,R2   ;:R2 = SIZE-2
1014 002562 000773                    BR       #2$
1015
1016 002564 060102 001426      3$:     ADD      R1,R2     ;:R2 = LAST PROM ADDRESS.
1017 002566 010237 001426      MOV      R2,PRMEND  ;:SAVE IT.
1018 002572 004737 006662      JSR      PC,NXM     ;:CHECK BOTH ADDRESSES.
1019 002576 000404                    BR       #4$         ;:ONE OF THEM TRAPPED !!!!!
1020 002600 005037 007022      CLR      MAPSW      ;:ENABLE TEST 1 MAP OUTPUT.
1021 002604 000137 002620      JMP      RSTRT     ;:OK, GO START TESTING.
1022
1023 002610 104401 013301 002312  4$:     TYPE      #2
1024 002614 000137 002312      JMP      INIT2     ;:SAY "INVALID PROM RANGE ETC"
                                ;:...AND TRY AGAIN.

```



```

1025 002620 005037 001202 RSTRT: CLR SPASS ;CLEAR PASS COUNTER
1026 002624 012706 001100 AGAIN: MOV #STACK,SP ;INIT THE SP. (LOOP HERE ON EOP)
1027 ;*****
1028 ;#TEST 1 BUILD AND TEST A 32K MEMORY MAP.
1029 ;*****
1030 002630 000240 TST1: NOP
1031 002632 012737 000100 001160 MOV #100,STIMES ;DO 100 ITERATIONS
1032 002640 112737 000001 001102 MOV #1,STIM ;INIT TEST NUMBER
1033 002646 012737 000001 001200 MOV #1,STESTN ;APT TOO
1034 ;ENABL LSA
1035 002654 012737 003146 001106 MOV #75,SLPADR ;SET LOOP ADDRESS
1036 002662 013737 001106 001110 MOV SLPADR,SLPERR ;AND ERROR LOOPER.
1037
1038 ;ENTER AT TST1A FROM "MDUMP" START. DUMP THE MAP AND RETURN.
1039
1040 002670 005037 007020 TST1A: CLR LOC ;START AT LOCATION 0 (0-4K).
1041 002674 012737 000020 007016 MOV #16,MAPK ;INIT SEGMENT COUNT
1042 002702 012737 007024 007012 MOV #ENMAP,RMAP ;...R MAP POINTER...
1043 002710 012737 007026 007014 MOV #ENMAP+2,WMAP ;...AND W MAP POINTER.
1044
1045 002716 105737 007022 TSTB MAPSW ;LO BYTE INHIBITS OUTPUT IF AUTO
1046 002722 001004 BNE 15 ;DOUBLE CRLF.
1047 002724 104401 001171 TYPE ,SCLF
1048 002730 104401 001171 TYPE ,SCLF
1049 002734 017700 004060 IS: MOV #LOC,RO ;READ... AND IF NO-TRAP...
1050 002740 010077 004054 RD1: MOV RO,#LOC ;...WRITE, AND IF NO-TRAP...
1051 002744 000261 WRT1: SEC ;...YOU'RE HERE. "LOC" IS R/W.
1052 002746 006177 004040 ROL #RMAP ;SET BIT IN READ MAP...
1053 002752 000261 SEC ;...AND IN WRITE MAP.
1054 002754 006177 004034 ROL #WMAP
1055 002760 105737 007022 TSTB MAPSW
1056 002764 001032 BNE 25 ;W = READ/WRITE SEGMENT.
1057 002766 104401 015120 TYPE ,W
1058 002772 000427 BR 25
1059
1060 002774 000241 RTN1: CLC ;YOU'RE HERE IF "LOC" WAS NEXM.
1061 002776 006177 004010 ROL #RMAP ;CLEAR BIT IN READ MAP...
1062 003002 000241 CLC
1063 003004 006177 004004 ROL #WMAP ;...AND WRITE MAP.
1064 003010 105737 007022 TSTB MAPSW
1065 003014 001016 BNE 25
1066 003016 104401 015114 TYPE ,DASH ;DASH = NEXM SEGMENT.
1067 003022 000413 BR 25
1068
1069 003024 000261 RTN2: SEC ;YOU'RE HERE IF "LOC" WAS READ ONLY.
1070 003026 006177 003760 ROL #RMAP ;SET BIT IN READ MAP...
1071 003032 000241 CLC
1072 003034 006177 003754 ROL #WMAP ;CLEAR IT IN WRITE MAP.
1073 003040 105737 007022 TSTB MAPSW
1074 003044 001002 BNE 25
1075 003046 104401 015116 TYPE ,R ;R = READ ONLY SEGMENT.
    
```

```

1076 003052 005337 007016      2S:  DEC  MAPK      ;BUMP SEGMENT COUNT
1077 003056 001016          BNE  3S      ;BR IF 4K (16BITS) NOT DONE YET.
1078 003060 012737 000020 007016  MOV  #16.,MAPK ;INIT COUNT FOR NEXT BANK
1079 003066 062737 000004 007012  ADD  #4,RMAP   ;BUMP MAP POINTERS
1080 003074 062737 000004 007014  ADD  #4,WMAP
1081
1082 003102 105737 007022          TSTB MAPSW
1083 003106 001002          BNE  3S
1084 003110 104401 001171          TYPE ,SCRLF      ;CRLF IF NOT INHIBITED
1085 003114 062737 001000 007020  3S:  ADD  #1000,LOC ;INIT POINTER FOR NEXT SEGMENT
1086 003122 001304          BNE  1S      ;CONTINUE...
1087                                     ;...."LOC" WILL BE 0 AT 32K.
1088 003124 105737 007022          TSTB MAPSW
1089 003130 001006          BNE  7S
1090 003132 104401 001171          TYPE ,SCRLF      ;FINAL CRLF IF NOT INHIBITED.
1091
1092                                     ;NOW, ON INITIAL PASS, COPY MEMMAP TO SAVMAP. ON SUBSEQUENT
1093                                     ;PASSES, COMPARE REFERENCE (SAVMAP) TO CURRENT (MEMMAP)
1094                                     ;AND REPORT ANY DISCREPANCIES.
1095                                     ;IF ENTRY WAS FROM "MDUMP" START, RETURN THERE WITHOUT
1096                                     ;ANY FURTHER ACTION.
1097
1098 003136 005737 001434          TST  MAINT     ;MAINT FLAG SET ??
1099 003142 001401          BEQ  .+4      ;NO
1100 003144 000207          RTS  PC       ;YES, RETURN TO "MDUMP".
1101
1102 003146 005000          7S:  CLR  R0     ;INIT MAP INDEX
1103 003150 105737 007023          TSTB MAPSW+1 ;HI BYTE = 0, ON INIT PASS.
1104 003154 001013          BNE  5S      ;NO, COMPARE THIS MAP WITH REF.
1105 003156 016060 007024 007064  4S:  MOV  MEMMAP(R0),SAVMAP(R0) ;YES, SAVE THE REF MAP.
1106 003164 005720          TST  (R0)+
1107 003166 022700 000034          CMP  #28.,R0 ;DONE ??
1108 003172 001371          BNE  4S      ;NO
1109 003174 012737 177777 007022  MOV  #-1,MAPSW ;YES, SET SWITCH TO INH OUTPUT...
1110 003202 000632          BR   TST1A   ;...AND COMPARE FROM NOW ON.
1111
1112 003204 026060 007024 007064  5S:  CMP  MEMMAP(R0),SAVMAP(R0) ;COMPARE MAPS.
1113 003212 001005          BNE  10S     ;ERROR
1114 003214 005720          6S:  TST  (R0)+ ;CONTINUE AFTER ERROR
1115 003216 022700 000034          CMP  #28.,R0 ;DONE ??
1116 003222 001370          BNE  5S      ;NOT YET
1117 003224 000456          BR   TST2   ;;DONE, GO NEXT TEST.

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1154
1155

003226 010001
003230 006201
003232 006201
003234 062701 000061
003240 110137 015064
003244 032700 000002
003250 001401
003252 005740
003254 016037 007024 001122
003262 005720
003264 016037 007024 001126
003272 005001
003274 005737 001122
003300 100007
003302 005737 001126
003306 100010
003310 112761 000127 015067
003316 000407
003320 112761 000055 015067
003326 000403
003330 112761 000122 015067
003336 006137 001122
003342 006137 001126
003346 005201
003350 022701 000020
003354 001347
003356 104014
003360 000715

```
*****  
:MAP ERROR. RE-CONSTRUCT AND OUTPUT A MAP FOR THE FAILING  
:BANK. CONTINUE AFTER OUTPUT.  
:os: MOV R0,R1 ;INDEX/2 + 1 = BANK NUMBER  
ASR R1  
ASR R1  
ADD #61,R1 ;R1 = ASCII BANK #.  
MOVB R1,EM14A ;SET # IN ERROR MSG STRING.  
BIT #BIT1,R0 ;ADJUST INDEX TO PICK 1ST WORD OF PAIR.  
BEQ .+4 ;NO ADJ REQ'D.  
TST -(R0) ;DEC INDEX  
MOV MEMMAP(R0),SBDADR ;FAILING R WORD  
TST (R0)+ ;BUMP INDEX  
MOV MEMMAP(R0),SBDAT ;FAILING W WORD  
CLR R1 ;INIT BIT COUNTER  
:11s: TST SBDADR  
BPL 12s ;IF R BIT = 0, NEXN.  
TST SBDAT ;IF R BIT = 1...  
BPL 13s ;...AND W = 0, READ ONLY.  
;IF BOTH = 1, READ/WRITE.  
;SET APPROPRIATE CHARACTER...  
MOVW #W,EM14B(R1)  
BR 14s  
:12s: MOVW #-,EM14B(R1) ;...IN ERROR MAP...  
BR 14s  
:13s: MOVW #R,EM14B(R1) ;...CHARACTER STRING.  
:14s: ROL SBDADR ;NOW ROTATE TO NEXT BIT  
ROL SBDAT  
INC R1 ;BUMP BIT COUNT (INDEX)  
CMP #16.,R1  
BNE 11s ;CONTINUE FOR 16 BITS.  
ERROR 14 ;PRINT ERROR "ITEM 14"...  
BR 6s ;...AND CONTINUE TEST.  
:;*****  
.DSABL LSB
```

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1156
1157
1158
1159 003362 000004
1160 003364 012737 000100 001160
1161 003372 005037 003416
1162 003376 004737 003420
1163
1164 003402 012737 177777 003416
1165 003410 004737 003420
1166 003414 000453
1167
1168
1169
1170 003416 000000
1171 003420 012701 000400
1172 003424 013702 001420
1173 003430 013722 003416
1174 003434 005301
1175 003436 001374
1176
1177 003440 012701 000400
1178 003444 012737 003532 001110
1179 003452 013702 001420
1180 003456 023722 003416
1181 003462 001006
1182
1183 003464 005301
1184 003466 001373
1185 003470 013737 001106 001110
1186 003476 000207
1187
1188
1189 003500 013737 003416 001124
1190 003506 016237 177776 001126
1191 003514 010237 001122
1192 003520 062737 177776 001122
1193 003526 104001
1194 003530 000755
1195
1196 003532 062702 177776
1197 003536 013712 003416
1198 003542 000745
1199
; *****
; TEST 2 RAM -- BASIC 1'S AND 0'S TEST.
; *****
TST2: SCOPE
      MOV #100,STIMES ; DO 100 ITERATIONS
      CLR PATRN ; FIRST PATTERN IS ZEROS
      JSR PC,DATTST ; RUN IT...
      MOV #-1,PATRN ; NEXT PATTERN IS ONES.
      JSR PC,DATTST ; DO IT...
      BR TST3 ; SKIP OVER SUBRTN TO NEXT TEST.
; SUBROUTINE FOR 1'S AND 0'S TEST.
PATRN: 0
DATTST: MOV #256,R1 ; WORD COUNT
        MOV RAM,R2 ; FIRST ADDRESS
1S:     MOV PATRN,(R2)+ ; FILL THE RAM.
        DEC R1
        BNE 1S ; CONTINUE TIL DONE.
        MOV #256,R1 ; OK, RESET COUNT.
        MOV #445,SLPERR ; SET FOR SWR9 OPTION IN 'ERROR'
        MOV RAM,R2 ; SET FIRST ADDRESS.
2S:     CMP PATRN,(R2)+ ; DATA OK ??
        BNE 4S ; NO, DATA ERROR.
        DEC R1 ; YES.
        BNE 2S ; CONTINUE TIL DONE
        MOV SLPADR,SLPERR ; SET FOR SWR9 OPTION IN 'SCOPE'
        RTS PC ; RETURN TO CALLER
; *****
4S:     MOV PATRN,SGOODAT ; GOOD DATA.
        MOV -2(R2),SBOODAT ; BAD DATA
        MOV R2,SBOADR
        ADD #-2,SBOADR ; BAD ADDRESS
        ERROR 1 ; PRINT ERROR "ITEM 1".
        BR 3S ; CONTINUE AFTER ERROR. (SWR9=0)
44S:    ADD #-2,R2 ; LOOP ON THIS ERROR (SWR9=1)
        MOV PATRN,(R2) ; REFRESH FAILED LOCATION...
        BR 2S ; ...AND TRY AGAIN.
; *****

```

```

1200
1201
1202
1203 003544 000004
1204 003546 012737 000100 001160
1205 003554 012701 000400
1206 003560 013702 001420
1207 003564 010212
1208 003566 005722
1209 003570 005301
1210 003572 001374
1211
1212 003574 012737 003654 001110
1213 003602 012701 000400
1214 003606 013702 001420
1215 003612 020212
1216 003614 001007
1217 003616 005722
1218 003620 005301
1219 003622 001373
1220
1221 003624 013737 001106 001110
1222 003632 000412
1223
1224
1225 003634 010237 001122
1226 003640 010237 001124
1227 003644 011237 001126
1228 003650 104002
1229 003652 000761
1230
1231 003654 010212
1232 003656 000755
1233

```

```

;*****
;#TEST 3 RAM -- BASIC ADDRESS TEST.
;*****
TST3: SCOPE
MOV 0100,STIMES ;DO 100 ITERATIONS
MOV 0256,R1 ;WORD COUNT
MOV RAM,R2 ;FIRST ADDRESS
1S: MOV R2,(R2) ;FILL WITH OWN ADDRESS.
TST (R2)+ ;BUMP ADDRESS POINTER
DEC R1 ;DONE YET ??
BNE 1S ;NO, CONTINUE FILLING.

MOV 0445,SLPERR ;SET FOR SWR9 OPTION IN 'ERROR'
MOV 0256,R1 ;RESET COUNT
MOV RAM,R2 ;AND ADDRESS.
2S: CMP R2,(R2) ;COMPARE EACH ADDRESS.
BNE 4S ;ADDRESS ERROR
3S: TST (R2)+ ;BUMP ADDRESS
DEC R1
BNE 2S ;AND CONTINUE TIL DONE

MOV SLPADR,SLPERR ;SET FOR SWR9 OPTION IN 'SCOPE'
BR TST4 ;DONE, GO TO NEXT TEST.

;*****
4S: MOV R2,SBDADR ;BAD ADDRESS
MOV R2,SGDOAT ;GOOD DATA
MOV (R2),SBDAT ;BAD DATA
ERROR 2 ;PRINT ERROR "ITEM 2".
BR 3S ;CONTINUE AFTER ERROR. (SWR9=0)
OR ;OR
44S: MOV R2,(R2) ;RETRY ON THIS ERROR (SWR9=1)
BR 2S
;*****

```

```

1234
1235
1236
1237 003660 000004
1238 003663 012737 000100 001160
1239 003670 013701 001420
1240 003674 012702 015122
1241 003700 013703 001422
1242 003704 062703 000002
1243
1244 003710 012122
1245
1246
1247
1248
1249
1250
1251
1252
1253 004710 013701 001420
1254 004714 012702 015122
1255 004720 022122
1256 004722 001003
1257
1258 004724 020103
1259 004726 001374
1260 004730 000415
1261
1262
1263 004732 016137 177776 001122
1264 004740 016237 177776 001126
1265 004746 010137 001120
1266 004752 062737 177776 001120
1267 004760 104003
1268 004762 000760
1269
1270

```

```

;*****
;TEST 4 RAM -- FAST READ TEST.
;*****
TST4: SCOPE
MOV #100,STIMES ;DO 100 ITERATIONS
MOV RAM,R1 ;RAM START LOC.
MOV #IBUF,R2 ;INTERNAL BUFFER ADDRESS
MOV RAMPEND,R3 ;END OF RAM +2.
ADD #2,R3
MOV (R1)+,(R2)+ ;FILL THE BUFFER WITH 256
;CONSECUTIVE READS.

;THE MOV INSTR ABOVE IS REPEATED 255 TIMES.
;LISTING TURNED OFF FOR ALL THAT.

;NOW THE INTERNAL BUFFER IS A RAM IMAGE.

MOV RAM,R1 ;RESET POINTERS...
MOV #IBUF,R2
15: CMP (R1)+,(R2)+ ;...AND COMPARE EACH ADDRESS
BNE #25 ;ADDRESS ERROR.

35: CMP R1,R3 ;DONE YET ??
BNE 15 ;NOPE, CONTINUE CHECKING.
BR TST5 ;YES, GO TO NEXT TEST.

;*****
25: MOV -2(R1),SBDADR ;RAM WORD
MOV -2(R2),SBDAT ;BUFFER WORD
MOV R1,SGDADR
ADD #2,SGDADR ;RAM ADDRESS (=GOOD DATA).
ERROR 3 ;PRINT ERROR "ITEM 3".
BR 35 ;CONTINUE AFTER ERROR (SMR9=0)
;...OR RESTART AT 'TST3+2' (SMR9=1)
;*****

```

E03

LSI-11 UVPR0M-RAM TEST. MD-11-DVMRA-A. MACY11 27(663) 24-MAR-77 13:51 PAGE 29
 DVMRAA.P11 TS RAM -- SLIDING 0 BIT TEST.

```

1271
1272
1273
1274 004764 000004
1275 004766 012737 000100 001160
1276 004774 012737 177777 005060
1277 005002 012737 177776 005062
1278 005010 004737 005064
1279 005014 013737 001106 001110
1280
1281
1282
1283
1284 005022 000004
1285 005024 012737 000100 001160
1286 005032 005037 005060
1287 005036 012737 000001 005062
1288 005044 004737 005064
1289 005050 013737 001106 001110
1290 005056 000544
1291
1292
1293
1294 005060 000000
1295 005062 000000
1296 005064 013702 001420
1297 005070 013703 001422
1298 005074 062703 000002
1299 005100 013722 005060
1300 005104 020203
1301 005106 001374
1302
1303 005110 012737 005360 001110
1304 005116 013702 001420
1305 005122 062703 177776
1306 005126 013701 005062
1307 005132 010112
1308 005134 020112
1309 005136 001036
1310
1311 005140 020237 001420
1312 005144 001404
1313 005146 023762 005060 177776
1314 005154 001037
1315
1316 005156 020203
1317 005160 001424
1318
1319 005162 023762 005060 000002
1320 005170 001052
1321
1322 005172 005737 005062
1323 005176 100403
1324 005200 006101

;*****
;#TEST 5 RAM -- SLIDING 0 BIT TEST.
;*****
TST5: SCOPE
MOV #100,STIMES ;DO 100 ITERATIONS
MOV @-1,FILLR ;BACKGROUND = 1'S
MOV @-2,SLIDR ;SLIDE A 0 BIT.
JSR PC,SLIDE ;GO DO IT.
MOV SLPADR,SLPERR ;SET FOR SWR9 OPTION IN 'SCOPE'

;*****
;#TEST 6 RAM -- SLIDING 1 BIT TEST.
;*****
TST6: SCOPE
MOV #100,STIMES ;DO 100 ITERATIONS
CLR FILLR ;BACKGROUND = 0'S
MOV @1,SLIDR ;SLIDE A 1 BIT.
JSR PC,SLIDE ;GO DO IT.
MOV SLPADR,SLPERR ;SET FOR SWR9 OPTION IN 'SCOPE'
BR TST7 ;DONE, SKIP OVER SUBRTNS TO NEXT TEST.

;SUBROUTINE FOR SLIDING BIT TESTS.
FILLR: 0 ;PATTERN FOR FILLER.
SLIDR: 0 ;BIT TO SLIDE.
SLIDE: MOV RAM,R2 ;FIRST LOC.
MOV RAMEND,R3
ADD #2,R3 ;LAST LOC +2.
1S: MOV FILLR,(R2)+ ;FILL WITH BACKGROUND
CMP R2,R3 ;DONE YET ??
BNE 1S ;NO.

MOV #40S,SLPERR ;SET FOR SWR9 OPTION IN 'ERROR'
MOV RAM,R2 ;RESET 1ST LOC.
ADD @-2,R3 ;R3 = LAST LOC.
MOV SLIDR,R1 ;R1 = SLIDING BIT WORD
2S: MOV R1,(R2) ;TEST 1ST (NEXT) LOC.
CMP R1,(R2) ;IS PATTERN RIGHT ??
BNE 10S ;NO, PATTERN ERROR.

11S: CMP R2,RAM ;IS THIS THE 1ST LOC ??
BEQ 21S ;YES, DONT CHECK LOC-2.
CMP FILLR,-2(R2) ;IS LOC-2 UNDISTURBED ??
BNE 20S ;NO, ERROR

21S: CMP R2,R3 ;IS THIS LAST LOC ??
BEQ 6S ;YES, YOU'RE ALL DONE.

CMP FILLR,2(R2) ;IS LOC+2 UNDISTURBED ??
BNE 30S ;NO, ERROR.

31S: TST SLIDR ;CLEAR 'C', SLIDING 0 OR 1 ??
BMI 4S ;0, SKIP NEXT 3.
ROL R1 ;SLIDE THE 1 LEFT 1.
  
```

```

1325 005202 103353          BCC 25          ; LOOP FOR 16 BITS.
1326 005204 000403          BR 55          ; OK, NOW SET FOR NEXT LOC.
1327 005206 000261          4S: SEC        ; SET 'C'.
1328 005210 006101          ROL R1        ; SLIDE THE 0 LEFT 1.
1329 005212 103747          BCS 25        ; LOOP FOR 16 BITS.
1330
1331 005214 013712 005060    5S: MOV  FILLR,(R2) ; RESET BACKGROUND PATTERN.
1332 005220 062702 000002    ADD  #2,R2      ; BUMP ADDRESS POINTER.
1333 005224 013701 005062    MOV  SLIDR,R1  ; RESET SLIDE WORD.
1334 005230 000740          BR 25         ; ...AND DO IT ALL AGAIN.
1335
1336 005232 000207          6S: RTS  PC     ; EXIT TO CALLER.
1337
1338 ;*****
1339 ;IF THE PATTERN IS STORED WRONG, OR IF AN ADJACENT LOCATION
1340 ;WAS ALTERED, REPORT THAT FACT.
1341
1342 005234 010137 001124    10S: MOV  R1,SGDAT ; GOOD DATA
1343 005240 010237 001122    MOV  R2,SBDADR  ; FAILING ADDRESS
1344 005244 011237 001126    MOV  (R2),SBDAT ; BAD DATA
1345 005250 104001          ERROR 1        ; PRINT ERROR "ITEM 1".
1346 005252 000732          BR 115        ; CONTINUE AFTER ERROR. (SMR9=0)
1347 ; ...OR RETRY AT 40S (SMR9=1)
1348
1349 005254 010237 001120    20S: MOV  R2,SGADR ; BASE LOC
1350 005260 011237 001124    MOV  (R2),SGDAT ; ...CONTENTS = SLIDE PATTERN
1351 005264 010237 001122    MOV  R2,SBDADR
1352 005270 062737 177776 001122    ADD  #2,SBDADR ; FAILING ADDR = LOC-2
1353 005276 016237 177776 001126    MOV  -2(R2),SBDAT ; ...CONTENTS = BAD DATA
1354 005304 104004          ERROR 4        ; PRINT ERROR "ITEM 4".
1355 005306 013762 005060 177776    MOV  FILLR,-2(R2) ; REFRESH FAILED LOCATION
1356 005314 000720          BR 215        ; CONTINUE AFTER ERROR. (SMR9=0)
1357 ; ...OR RETRY AT 40S (SMR9=1)
1358
1359 005316 010237 001120    30S: MOV  R2,SGADR ; SAME AS ABOVE..
1360 005322 011237 001124    MOV  (R2),SGDAT
1361 005326 010237 001122    MOV  R2,SBDADR
1362 005332 062737 000002 001122    ADD  #2,SBDADR ; FAILING ADDR = LOC+2
1363 005340 016237 000002 001126    MOV  2(R2),SBDAT
1364 005346 104005          ERROR 5        ; PRINT ERROR "ITEM 5".
1365 005350 013762 005060 000002    MOV  FILLR,2(R2) ; REFRESH FAILED LOCATION
1366 005356 000705          BR 315        ; CONTINUE AFTER ERROR. (SMR9=0)
1367 ; OR
1368 005360 013777 005060 173534 40S: MOV  FILLR,SBDADR ; REFRESH FAILED LOCATION...
1369 005366 000661          BR 25         ; ...AND RETRY (SMR9=1)
1370 ;*****

```



```

1371
1372
1373
1374 005370 000004
1375 005372 012737 000100 001160
1376 005400 013702 001420
1377 005404 013703 001422
1378 005410 062703 000002
1379 005414 012701 052525
1380
1381 005420 010122
1382 005422 005101
1383 005424 020203
1384 005426 001374
1385
1386 005430 012737 005532 001110
1387 005436 013702 001420
1388 005442 020122
1389 005444 001016
1390
1391 005446 005101
1392 005450 020203
1393 005452 001373
1394
1395
1396
1397
1398 005454 013702 001420
1399 005460 022701 052525
1400 005464 001002
1401 005466 005101
1402 005470 000753
1403
1404 005472 013737 001106 001110
1405 005500 000416
1406
1407
1408 005502 016237 177776 001126
1409 005510 010237 001120
1410 005514 062737 177776 001120
1411 005522 010137 001124
1412 005526 104006
1413 005530 000746
1414
1415 005532 010142
1416 005534 000742
1417

;*****
;TEST 7 RAM -- ALTERNATE BIT PATTERN TEST.
;*****
TST7: SCOPE
MOV #100,STIMES ;DO 100 ITERATIONS
MOV RAM,R2 ;1ST LOC
MOV RAMEND,R3
ADD #2,R3 ;LAST LOC +2
MOV #52525,R1 ;INITIAL R1 = 052525

1$: MOV R1,(R2)+ ;STORE A WORD.
COM R1 ;COMPLIMENT IT
CMP R2,R3 ;DONE ALL ??
BNE 1$ ;NO, STORE COMP IN NEXT LOC.

MOV #30$ ,SLPERR ;SET FOR SWR9 OPTION IN 'ERROR'
MOV RAM,R2 ;NOW COMPARE THEM.
2$: CMP R1,(R2)+ ;PATTERN RIGHT ??
BNE 3$ ;NO, PATTERN ERROR

4$: COM R1 ;COMP PATTERN
CMP R2,R3 ;COMPARED ALL ??
BNE 2$ ;NO, COMPARE NEXT

;AT THIS POINT 052525,125252,052525,125252, ETC HAS BEEN DONE.
;NOW USE 125252,052525,125252, ETC.

MOV RAM,R2 ;RESET START LOC.
CMP #52525,R1 ;IS THIS 1ST PASS ??
BNE 5$ ;NO, YOU'RE ALL DONE
COM R1 ;YES, SET UP FOR 2ND PASS.
BR 1$ ;...AND DO IT.

5$: MOV SLPADR,SLPERR ;SET FOR SWR9 OPTION IN 'SCOPE'
BR TST10 ;...AND GO TO NEXT TEST.

;*****
3$: MOV -2(R2),SDDAT ;BAD DATA
MOV R2,$GDADR
ADD #2,$GDADR ;FAILING ADDRESS.
MOV R1,$GDDAT ;GOOD DATA
ERROR 6 ;PRINT ERROR "ITEM 6".
BR 4$ ;CONTINUE AFTER ERROR. (SWR9=0)
OR ;OR
30$: MOV R1,-(R2) ;REFRESH FAILED LOCATION...
BR 2$ ;...AND RETRY (SWR9=1)
;*****

```

```

1418
1419
1420
1421 005536 000004
1422 005540 012737 000100 001160
1423 005546 005037 005640
1424 005552 005001
1425 005554 012737 000377 005634
1426 005562 004737 005642
1427
1428 005566 012701 000001
1429 005572 005137 005634
1430 005576 004737 005642
1431
1432 005602 012737 177777 005640
1433 005610 005001
1434 005612 004737 005642
1435
1436 005616 012701 000001
1437 005622 005137 005634
1438 005626 004737 005642
1439
1440 005632 000475
1441
1442
1443
1444 005634 000000
1445 005636 000000
1446 005640 000000
1447 005642 013702 001420
1448 005646 012703 000400
1449 005652 013722 005640
1450 005656 005303
1451 005660 001374
1452
1453 005662 012737 006016 001110
1454 005670 013737 001420 005636
1455 005676 063701 005636
1456 005702 012703 000400
1457 005706 005737 005640
1458 005712 001003
1459 005714 112711 177777
1460 005720 000401
1461 005722 105011
1462
1463 005724 000240
1464 005726 023777 005634 177702
1465 005734 001013
1466 005736 062737 000002 005636
1467 005744 062701 000002
1468 005750 005303
1469 005752 001355
1470 005754 013737 001106 001110
1471 005762 000207

```

```

*****
:TEST 10 RAM -- WRITE BYTE TEST.
*****
TST10: SCOPE
MOV #100,STIMES ;DO 100 ITERATIONS
CLR BKG ;BACKGROUND PATTERN = 0.
CLR R1 ;BYTE INDEX = 0.
MOV #377, PTRN ;TEST PATTERN = 0,-1
JSR PC, BYTTST ;FILL WITH ZEROS, WRITE LO...
;...BYTE -1, AND TEST WORD.
MOV #1, R1 ;BYTE INDEX = 1.
COM PTRN ;TEST PATTERN = -1, 0
JSR PC, BYTTST ;FILL WITH ZEROS, WRITE HI...
;...BYTE -1, AND TEST WORD.
MOV #-1, BKG ;BACKGROUND PATTERN = -1.
CLR R1 ;BYTE INDEX = 0.
JSR PC, BYTTST ;FILL WITH 1'S, WRITE LO...
;...BYTE 0, AND TEST WORD.
MOV #1, R1 ;BYTE INDEX = 1
COM PTRN ;TEST PATTERN = 0,-1
JSR PC, BYTTST ;FILL WITH 1'S, WRITE HI...
;...BYTE 0, AND TEST WORD.
BR TST11 ;DONE, GO TO NEXT TEST.

;SUBROUTINE FOR RAM BYTE TEST.
PTRN: 0 ;0 -1 OR -1, 0
WORDX: 0 ;WORD ADDRESS.
BKG: 0 ;BACKGROUND PATTERN
BYTTST: MOV RAM, R2 ;INIT RAM ADDRESS
;...AND WORD COUNT.
1$: MOV #400, R3 ;FILL RAM WITH BACKGROUND.
MOV BKG, (R2)+
DEC R3
BNE 1$

2$: MOV #11$, SLPERR ;SET FOR SWR9 OPTION IN 'ERROR'
MOV RAM, WORDX ;SET WORD ADDRESS...
;...AND BYTE ADDRESS (R1).
ADD WORDX, R1
MOV #400, R3
4$: TST BKG ;BACKGROUND = 0 ??
BNE 2$ ;NO, WRITE A 0 BYTE.
MOVB #-1, (R1) ;YES, WRITE A -1 BYTE...
;...AND SKIP NEXT INSTR.
BR 3$
2$: CLRB (R1) ;WRITE A 0 BYTE

3$: NOP
CMP PTRN, #WORDX ;PATTERN RIGHT IN RAM ??
BNE 10$ ;NO, ERROR.
5$: ADD #2, WORDX ;YES, BUMP ADDRESS POINTERS.
ADD #2, R1
DEC R3
BNE 4$
MOV SLPADR, SLPERR ;CONTINUE
PC ;SET FOR SWR9 OPTION IN 'SCOPE'
;...AND EXIT.

```

```

1472
1473
1474 005764 013737 005636 001120 10S:  MOV  WORDX,SGDADR ; FAILING WORD ADDRESS
1475 005772 017737 177640 001126      MOV  2WORDX,SBDAT ; BAD DATA WORD.
1476 006000 010137 001122      MOV  B1,SBDADR ; BYTE WRITTEN ADDRESS
1477 006004 013737 005634 001124      MOV  PTRN,SGDAT ; GOOD DATA PATTERN
1478 006012 104007      ERROR 7 ; PRINT ERROR "ITEM 7".
1479 006014 000750      BR 5S ; CONTINUE AFTER ERROR (SWR9=0)
1480                                     ; OR ...
1481 006016 013777 005640 177612 11S:  MOV  BKX,2WORDX ; REFRESH FAILED LOC...
1482 006024 000730      BR 4S ; ...AND RETRY (SWR9=1)
1483 ;*****

```

```

1484
1485
1486
1487 006026 000004
1488 006030 012737 000100 001160
1489 006036 013701 001424
1490 006042 013702 001420
1491 006046 012703 000400
1492 006052 010304
1493 006054 012737 006206 001110
1494
1495 006062 020102 15: CMP R1,R2 ;IF RAM IS WITHIN PROM SPACE...
1496 006064 001002 BNE 55
1497 006066 062701 001000 ADD #512.,R1 ;...SKIP 256 PROM LOCATIONS.
1498
1499 006072 012122 55: MOV (R1)+,(R2)+ ;COPY PROM TO RAM
1500 006074 005303 DEC R3
1501 006076 001371 BNE 15
1502
1503 006100 162701 001000 SUB #1000,R1 ;RESET PROM ADDRESS
1504 006104 013702 001420 MOV RAM,R2 ;AND RAM ADDRESS
1505 006110 021112 25: CMP (R1),(R2) ;COMPARE PROM-RAM
1506 006112 001023 BNE 105 ;ERROR !!!
1507 006114 005304 115: DEC R4 ;256 WORDS DONE ??
1508 006116 001403 BEQ 35 ;YES
1509 006120 005721 TST (R1)+ ;NO, BUMP POINTERS...
1510 006122 005722 TST (R2)+
1511 006124 000771 BR 25 ;...AND CONTINUE
1512
1513 006126 020137 001426 35: CMP R1,PROMEND ;END OF PROM ??
1514 006132 001407 BEQ 45 ;YES
1515 006134 005721 TST (R1)+ ;NO, BUMP PROM ADDRESS
1516 006136 013702 001420 MOV RAM,R2 ;INIT RAM ADDRESS...
1517 006142 012703 000400 MOV #400,R3 ;...AND COUNTERS
1518 006146 010304 MOV R3,R4
1519 006150 000744 BR 15 ;AND DO THE NEXT CHUNK.
1520
1521 006152 013737 001106 001110 45: MOV SLPADR,SLPERR ;SET FOR SWR9 OPTION IN 'SCOPE'
1522 006160 000414 BR TST12 ;...AND GO TO NEXT TEST.
1523
1524
1525 006162 010137 001120 105: MOV R1,SGDADR ;PROM ADDRESS
1526 006166 011137 001124 MOV (R1),SGDAT ;PROM DATA
1527 006172 010237 001122 MOV R2,SBADR ;RAM ADDRESS
1528 006176 011237 001126 MOV (R2),SBDAT ;RAM DATA
1529 006202 104011 ERROR 11 ;PRINT ERROR "ITEM 11".
1530 006204 000743 BR 115 ;CONTINUE AFTER ERROR (SWR9=0)
1531
1532 006206 011112 125: MOV (R1),(R2) ;... OR ...
1533 006210 000737 BR 25 ;REFRESH FAILED RAM LOC...
; ;...AND RETRY (SWR9=1)
; ;*****

```

```

1535
1536
1537
1538 006212 000004
1539 006214 012737 000100 001160
1540
1541 006222 012737 006246 001110
1542 006230 013701 001424
1543
1544 006234 020137 001420
1545 006240 001002
1546 006242 062701 001000
1547
1548 006246 011102
1549 006250 010211
1550
1551
1552
1553
1554
1555 006252 010137 001122
1556 006256 010237 001124
1557 006262 011137 001126
1558 006266 104012
1559 006270 000400
1560
1561
1562
1563
1564 006272 020137 001426
1565 006276 001402
1566 006300 005721
1567 006302 000754
1568
1569 006304 013737 001106 001110
1570 006312 000400
1571

;*****
;TEST 12 PROM -- WRITE-TRAP TEST.
;*****
TST12: SCOPE
MOV #100,STIMES ;;DO 100 ITERATIONS
ENABL LSB
MOV #15,SLPERR ;SET FOR SWR9 OPTION IN 'ERROR'
MOV PRM,R1 ;INIT PROM ADDRESS.
SS: CMP R1,PRM ;IF RAM IS WITHIN PROM SPACE...
BNE IS ;...SKIP 256 PROM LOCATIONS.
ADD #256,R1
IS: MOV (R1),R2 ;SAVE PROM WORD
MOV R2,(R1) ;AND TRY TO RE-WRITE IT.
;IT SHOULD TRAP AND RETURN TO WTRTN

;*****
;NO-TRAP IS AN ERROR CONDITION.
;*****
WRT12: MOV R1,$BADR ;ERROR, FAILING ADDRESS
MOV R2,$GDADR ;DATA WAS ...
MOV (R1),$GDADR ;DATA IS ... (SHOULD BE SAME.)
ERROR 12 ;PRINT ERROR "ITEM 12".
BR WTRTN ;CONTINUE AFTER ERROR (SWR9=0)
;...OR RETRY AT IS (SWR9=1)

;*****
;YOU'RE HERE IF THE WRITE ATTEMPT TRAPPED.
;*****
WTRTN: CMP R1,PRMEND ;END OF PROM ??
BEQ 45 ;YES
TST (R1)+ ;NO, BUMP POINTER
BR 55 ;...AND DO THE NEXT ADDRESS.
45: MOV SLPADR,SLPERR ;SET FOR SWR9 OPTION IN 'SCOPE'
BR TST13 ;DONE, NEXT TEST.
.DSABL LSB
    
```

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1572
1573
1574
1575 006314 000004
1576 006316 012737 000100 001160
1577
1578
1579
1580
1581 006324 005737 001432
1582 006330 001010
1583 006332 005037 001160
1584 006336 000137 006540
1585
1586
1587
1588
1589
1590
1591
1592
1593
1594
1595
1596
1597
1598
1599
1600
1601
1602
1603 006342 010000
1604 006344 020000
1605 006346 040000
1606 006350 100000
1607
1608 006352 000240
1609 006354 000240
1610 006356 012737 006352 001106
1611 006364 012737 006420 001110
1612 006372 013701 006342
1613 006376 013702 001424
1614
1615 006402 020237 001420
1616 006406 001004
1617 006410 062702 001000
1618 006414 062701 000400
1619
1620 006420 020112
1621 006422 001036
1622 006424 005201
1623 006426 005722
1624
1625 006430 022701 012000
    
```

```

*****
:TEST 13 PROM DATA TEST -- ENGINEERING USE ONLY.
*****
:ST13: SCOPE
      MOV #100,STIMES ;;DO 100 ITERATIONS

:IF SPECIAL TEST PROM INSTALLED, DO THIS TEST.
:SKIP TO END-PASS OTHERWISE.

:      TST PRMX ;SPECIAL TEST PROM ??
      BNE XTEST ;YES, EXECUTE THIS TEST.
      CLR STIMES ;NO, CLEAR ITER COUNT...
      JMP SEOP ;NO, END PASS.

:FOR THIS TEST, A SPECIAL SET OF PROM CHIPS (8) MUST BE
:INSTALLED IN THE PROM BOARD. THESE CHIPS (2/K) ARE BLASTED
:WITH THE FOLLOWING DATA PATTERN:

      1ST K 010000 THRU 011777
      2ND K 022000 023777
      3RD K 044000 045777
      4TH K 106000 107777

:NOTE THAT THE BIT PATTERN EMPLOYS TWO FIELDS AS FOLLOWS:
:BITS 00-11 FORM A BINARY COUNTER, RANGE 0-7777 (4095.).
:BITS 12-15 FORM A RING COUNTER, WHERE;
:      BIT 12=1 IN 1ST K.
:      BIT 13=1 IN 2ND K.
:      BIT 14=1 IN 3RD K.
:      BIT 15=1 IN 4TH K.

K1: BIT12 ;BIT12=1 IN 1ST K.
K2: BIT13 ;BIT13=1 IN 2ND K.
K3: BIT14 ;BIT14=1 IN 3RD K.
K4: BIT15 ;BIT15=1 IN 4TH K.

XTEST: NOP
      NOP
      MOV #XTEST,SLPADR ;SET ITERATION ADDRESS
      MOV #55,SLPERR ;SET FOR SMR9 OPTION IN 'ERROR'
      MOV K1,R1 ;INIT PATTERN FOR 1ST K.
      MOV PRM,R2 ;INIT ADDRESS.

6S: CMP R2,RAM ;IF RAM IS WITHIN PROM SPACE...
     BNE 55
     ADD #512.,R2 ;...SKIP 256 PROM LOCATIONS.
     ADD #256.,R1 ;...ADJUST PATTERN ACCORDINGLY.

5S: CMP R1,(R2) ;COMPARE THIS(NEXT) LOCATION.
     BNE 10S ;ERROR

11S: INC R1 ;BUMP PATTERN WORD...
     TST (R2)+ ;...AND ADDRESS.

      CMP #12000,R1 ;1ST K DONE ??
    
```

```

1626 006434 001412          BEQ      1$          ;YES
1627 006436 022701 024000    CMP      $24000,R1 ;2ND K DONE ??
1628 006442 001412          BEQ      2$          ;YES
1629 006444 022701 046000    CMP      $46000,R1 ;3RD K DONE ??
1630 006450 001412          BEQ      3$          ;YES
1631 006452 022701 110000    CMP      $110000,R1 ;4TH K DONE ??
1632 006456 001412          BEQ      4$          ;YES
1633 006460 000757          BR       5$          ;CONTINUE IN CURRENT SECTION.
1634
1635 006462 063701 006342    1$:     ADD      K1,R1 ;INIT PATTERN FOR 2ND K.
1636 006466 000745          BR       6$
1637 006470 063701 006344    2$:     ADD      K2,R1 ;INIT PATTERN FOR 3RD K.
1638 006474 000742          BR       6$
1639 006476 063701 006346    3$:     ADD      K3,R1 ;INIT PATTERN FOR 4TH K.
1640 006502 000737          BR
1641
1642 006504 000240    4$:     NOP
1643 006506 000240          NOP
1644 006510 013737 001106 001110    MOV     SLPADR,SLPERR ;SET FOR SWR9 OPTION IN 'SCOPE'
1645 006516 000410          BR       SEOP      ;;DONE, END-OF-PASS
1646
1647 ;*****
1648 006520 010237 001122    10$:   MOV     R2,$BADADR ;FAILING ADDRESS
1649 006524 010137 001124    MOV     R1,$GDDAT  ;GOOD DATA
1650 006530 011237 001126    MOV     (R2),$BDDAT ;BAD DATA
1651 006534 104010          ERROR   10          ;PRINT ERROR "ITEM 10".
1652 006536 000732          BR      11$        ;...AND GO ON (SWR9=0).
1653 ;...OR RETRY AT 5$ (SWR9=1)
1654 ;*****

```

.SBTTL END OF PASS ROUTINE

```

1655
1656
1657
1658
1659
1660
1661
1662
1663 005540
1664 006540 000004
1665 006542 005037 001102
1666 006546 005037 001160
1667 006552 005237 001202
1668 006556 042737 100000 001202
1669 006564 005327
1670 006566 000001
1671 006570 003022
1672 006572 012737
1673 006574 000001
1674 006576 006566
1675 006600 104401 006645
1676 006604 013746 001202
1677 006610 104405
1678 006612 104401 006642
1679 006616 013700 000042
1680 006622 001405
1681 006624 000005
1682 006626 004710
1683 006630 000240
1684 006632 000240
1685 006634 000240
1686 006636
1687 006636 000137
1688 006640 002624
1689 006642 377 377 000
1690 006645 015 042412 042116
1691 006652 050040 051501 020123
1692 006660 000043

```

```

*****
; INCREMENT THE PASS NUMBER (SPASS)
; TYPE "END PASS #XXXXX" (WHERE XXXXX IS A DECIMAL NUMBER)
; IF THERES A MONITOR GO TO IT
; IF THERE ISN'T JUMP TO AGAIN

SEOP:
SCOPE
CLR STSTN ; ZERO THE TEST NUMBER
CLR STIMES ; ZERO THE NUMBER OF ITERATIONS
INC SPASS ; INCREMENT THE PASS NUMBER
BIC #100000, SPASS ; DON'T ALLOW A NEG. NUMBER
DEC (PC)+ ; LOOP?

SEOPCT: .WORD 1
BGT SDOAGN ; YES
MOV (PC)+, 2(PC)+ ; RESTORE COUNTER

SENDCT: .WORD 1
SEOPCT
TYPE SENDMG ; TYPE "END PASS #"
MOV SPASS, -(SP) ; SAVE SPASS FOR TYPEOUT
TYPDS ; GO TYPE--DECIMAL ASCII WITH SIGN
TYPE SENULL ; TYPE A NULL CHARACTER
SGET42: MOV #42, R0 ; GET MONITOR ADDRESS
BEQ SDOAGN ; BRANCH IF NO MONITOR
RESET ; CLEAR THE WORLD
SENDAD: JSR PC, (R0) ; GO TO MONITOR
NOP ; SAVE ROOM
NOP ; FOR
NOP ; ACT11

SDOAGN:
JMP 2(PC)+ ; RETURN
SRTNAD: .WORD AGAIN
SENULL: .BYTE -1, -1, 0 ; NULL CHARACTER STRING
SENDMG: .ASCIZ <15><12>/END PASS #/

```



```

1693
1694
1695
1696
1697
1698
1699
1700
1701
1702
1703 006662 005711
1704 006664 005712
1705 006666 062716 000002
1706 006672 000207
1707 006674 000207
1708
1709
1710
1711
1712
1713
1714
1715 006676 022716 002740
1716 006702 001003
1717 006704 012716 002774
1718 006710 000002
1719
1720 006712 022716 002744
1721 006716 001003
1722 006720 012716 003024
1723 006724 000002
1724
1725 006726 022716 006252
1726 006732 001003
1727 006734 012716 006272
1728 006740 000002
1729
1730 006742 022716 006664
1731 006746 001403
1732 006750 022716 006666
1733 006754 001003
1734 006756 012716 006674
1735 006762 000002
1736
1737 006764
1738 006764 012637 001122
1739 006770 012637 001126
1740 006774 012737 007004 001110
1741 007002 104013
1742
1743
1744 007004 000000
1745 007006 000137 001436
    
```

```

.SBTTL COMMON SUBROUTINES AND TABLES
*****
THIS ROUTINE WILL TEST FOR A NEXM IN THE RANGE (R1) THRU (R2).

CALL:  MOV ADR1,R1
      MOV ADR2,R2
      JSR PC,NXM
      RETURN IF BUSERR TRAP
      RETURN IF NO-TRAP

NXM:   TST    (R1)           ;TEST THE ADDRESSES.
      TST    (R2)           ;...IF EITHER TRAPS, RETURN TO NXM1.
      ADD    #2,(SP)        ;IF NOT, BUMP RETURN PC...
      RTS   PC              ;...AND RETURN TO CALL+6.
NXM1:  RTS    PC            ;...ONE OR THE OTHER TRAPPED...
      ;...RETURN TO CALL+4.

*****
BUS TIME-OUT PROCESSOR.  BUS ERRORS ARE EXPECTED DURING
INITIALIZATION, AND IN TESTS 1 AND 12.
IF ANY OTHER, REPORT BUS-ERROR, AND HALT.

TIMEOUT: CMP    #ADR1,(SP)   ;TST1 READ TRAP ??
        BNE    15           ;NO
        MOV    #RTN1,(SP)   ;YES, ADJUST RETURN PC
        RTI                    ;AND RETURN THERE.

15:     CMP    #WRT1,(SP)   ;TST1 WRITE TRAP ??
        BNE    25           ;NO
        MOV    #RTN2,(SP)   ;YES...
        RTI                    ;...RETURN THERE

25:     CMP    #WRT12,(SP)  ;TST12 WRITE TRAP ??
        BNE    35           ;NO
        MOV    #WTRTN,(SP)  ;YES...
        RTI                    ;...RETURN THERE.

35:     CMP    #NXM+2,(SP)  ;NEXM ROUTINE TRAP 1 ??
        BEQ    45           ;YES
        CMP    #NXM+4,(SP)  ;NO, NEXM TRAP 2 ??
        BNE    55           ;NO
45:     MOV    #NXM1,(SP)   ;RETURN TO NEXM ROUTINE.
        RTI

55:     MOV    (SP)+,SBODR   ;POP STACK INTO SBODR
        MOV    (SP)+,SBODAT ;POP STACK INTO SBODAT
        MOV    #STOP,SLPERR ;OVERRIDE SWR9 OPTION.
        ERROR 13           ;REPORT BUS ERROR...
                        ;...SBODR = TRAP PC
                        ;...SBODAT = TRAP PS
STOP:   HALT
      JMP    START        ;...RESTART ON "CONTINUE".
    
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007012 007024
007014 007026
007016 000020
007020 000000
007022 000 000
007024 000020
007064 000020

001456
012737 177777 001434
012706 001100
004737 001456
005037 007022
004737 002670
005037 001434
000000
000137 007124

MEMORY MAP FOR TEST 1.
THE MAP CONSISTS OF 2 WORDS FOR EACH 4K BANK THRU 32K.
WORD 1 IS THE READ MAP WHERE EACH BIT REPRESENTS 1 256 WORD
SEGMENT OF THE BANK. EACH BIT IS SET 0 IF THAT SEGMENT IS
"NEXT", SET 1 OTHERWISE.
WORD 2 IS THE WRITE MAP FOR THE SAME BANK. EACH BIT IS
SET 0 IF THAT SEGMENT IS "READ-ONLY" OR "NEXT", SET 1 OTHERWISE.
BITS TO ADDRESS CORRESPONDENCE IS AS FOLLOWS:

| BIT # | RANGE |
|-------|---------------|
| 15 | 0 - 776 |
| 14 | 1000 - 1776 |
| 13 | 2000 - 2776 |
| 12 | 3000 - 3776 |
| 11 | 4000 - 4776 |
| 10 | 5000 - 5776 |
| 09 | 6000 - 6776 |
| 08 | 7000 - 7776 |
| 07 | 10000 - 10776 |
| 06 | 11000 - 11776 |
| 05 | 12000 - 12776 |
| 04 | 13000 - 13776 |
| 03 | 14000 - 14776 |
| 02 | 15000 - 15776 |
| 01 | 16000 - 16776 |
| 00 | 17000 - 17776 |

RMAP: .WORD MEMMAP ;POINTS TO 1ST WORD IN PAIR.
WMAP: .WORD MEMMAP+2 ;POINTS TO 2ND WORD IN PAIR.
MAPK: .WORD 16. ;BIT (SEGMENT) COUNTER.
LOC: .WORD 0 ;POINTS TO LOC TO TEST.
MAPSW: .BYTE 0,0 ;LO BYTE IS TYPE/INHIB SWITCH
;HI BYTE IS COPY/COMPARE SWITCH
MEMMAP: .BLKW 16. ;THIS IS THE WORKING (SCRATCH) MAP.
;2 WORDS/4K BANK AS DESCRIBED ABOVE.
SAVMAP: .BLKW 16. ;INITIAL COPY OF "MEMMAP" IS SAVED
;HERE AS THE REFERENCE MAP.

SUBROUTINE TO DUMP A 32K MAP AND HALT. START AT LOC 220.

STARTM=START+20
MDUMP: MOV 8-1 MAINT ;SET MAINT FLAG.
MOV 8STACK SP ;INIT THE STACK
JSR PC STARTM ;INIT VECTORS, AND RETURN.
CLR MAPSW ;ENABLE MAP OUTPUT.
JSR PC TST1A ;...DO IT...
CLR MAINT ;...RETURN...
HALT ;...AND HALT.
JMP MDUMP

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1800 .....
1801 : THIS ROUTINE WILL OUTPUT ANY GIVEN MEM RANGE TO THE
1802 : CONSOLE TTY.
1803
1804 : START AT LOC 210 TO DUMP OCTAL,
1805 : AND LOC 214 TO DUMP BINARY.
1806
1807 : OPERATOR IS REQUIRED TO INPUT FIRST ADDRESS,
1808 : AND NUMBER OF WORDS TO DUMP.
1809
1810 : AN ODD ADDRESS INPUT WILL BE DECREMENTED BY 1, BEFORE USE.
1811 : WORD COUNT INPUT MUST BE NON-ZERO.
1812 : RESTART DUMPER AT END.
1813
1814 : MODE: 0 ; 0 = OCTAL, -1 = BINARY
1815 : ADDRS: 0 ; ADDRESS OF DUMP RANGE
1816 : WORDS: 0 ; WORD COUNT
1817 : LINE: 0 ; NUMBER OF WORDS/LINE
1818
1819 007164 000000 177777 001434 00DUMP: MOV 8-1,MAINT ; SET MAINT FLAG
1820 007202 012706 001100 MOV 8STACK,SP ; INIT THE STACK
1821 007206 004737 001456 JSR PC,STARTM ; INIT VECTORS, AND RETURN
1822 007212 005037 001434 CLR MAINT
1823 007216 104401 007224 TYPE 65S ; TYPE ASCIZ STRING
1824 007222 000417 BR 64S ; GET OVER THE ASCIZ
1825 ; 65S: .ASCIZ <CRLF><CRLF>/OCTAL MEMORY DUMP ROUTINE./<CRLF>
1826 64S:
1827 007262 005037 007164 CLR MODE ; 0 = OCTAL DUMP
1828 007266 012737 000004 007172 MOV 84,LINE ; WITH 4 WORDS PER LINE.
1829 007274 000442 BR DSTRT
1830
1831 007276 012737 177777 001434 00DUMP: MOV 8-1,MAINT ; SET MAINT FLAG
1832 007304 012706 001100 MOV 8STACK,SP ; INIT THE STACK
1833 007310 004737 001456 JSR PC,STARTM ; INIT VECTORS, AND RETURN
1834 007314 005037 001434 CLR MAINT
1835 007320 104401 007326 TYPE 65S ; TYPE ASCIZ STRING
1836 007324 000420 BR 64S ; GET OVER THE ASCIZ
1837 ; 65S: .ASCIZ <CRLF><CRLF>/BINARY MEMORY DUMP ROUTINE./<CRLF>
1838 64S:
1839 007366 012737 177777 007164 MOV 8-1,MODE ; -1 = BINARY DUMP
1840 007374 012737 000002 007172 MOV 82,LINE ; WITH 2 WORDS PER LINE.
1841
1842 007402 DSTRT:
1843 007402 104401 007410 TYPE 65S ; TYPE ASCIZ STRING
1844 007406 000411 BR 64S ; GET OVER THE ASCIZ
1845 ; 65S: .ASCIZ <CRLF>/FIRST ADDRESS = /
1846 64S:
1847 007432 RDOCT ; RECEIVE HIS RESPONSE.
1848 007434 104413 MOV (SP)+,ADDRS ; POP STACK INTO ADDRS
1849 007440 012637 007166 BIC 8BIT0,ADDRS ; INSURE ITS AN EVEN ADDRESS.
1850
1851 007446 2S:
1852 007446 104401 007454 TYPE 67S ; TYPE ASCIZ STRING
1853 007452 000414 BR 66S ; GET OVER THE ASCIZ

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1854      ::67S: .ASCIZ <CRLF>/WORD COUNT (OCTAL) = /
1855      66S:
1856      007504 104413      RDOCT      ;GET THE NUMBER
1857      007506 012637 007170  MOV      (SP)+,WORDS ;POP STACK INTO WORDS
1858      007512 001755      BEQ      2S      ;CANT USE 0, ASK FOR ANOTHER.
1859
1860      007514 013701 007166  MOV      ADDR,R1    ;FIRST ADDRESS IN R1.
1861      007520 013702 007170  MOV      WORDS,R2   ;WORD COUNT...
1862      007524 006302      ASL      R2        ;... TIMES 2 = BYTE COUNT.
1863      007526 062702 177776  ADD      #2,R2     ;ADJUST
1864      007532 060102      ADD      R1,R2     ;PLUS (R1) = LAST ADDR IN R2.
1865      007534 004737 006662  JSR      PC,NXM    ;SEE IF THE RANGE IS VALID.
1866      007540 000401      BR      10S      ;ITS NOT, RANGE IS NEXM.
1867      007542 000403      BR      DMPGO    ;IT IS, START DUMPING.
1868
1869      007544 104401 013412  10S:    TYPE      M3      ;INVALID DUMP RANGE.
1870      007550 000714      BR      DSTRT    ;TRY AGAIN.
1871
1872      007552 104401 001171  DMPGO:  TYPE      ,SCLF
1873      007556 013746 007166  MOV      ADDR,-(SP) ;PUSH ADDR ON STACK
1874      007562 104403      TYP0S     ;TYPE THE ADDRESS.
1875      007564      006      .BYTE      6
1876      007565      000      .BYTE      0
1877      007566 013701 007172  MOV      LINE,R1   ;SUPPRESS LEAD ZEROS.
1878                                     ;SET WORDS/LINE COUNT
1879
1879      007572 104401 007650  3S:    TYPE      ,SPACE3 ;3 SPACES BETWEEN WORDS
1880      007576 017746 177364  MOV      ADDR,-(SP) ;PUSH ADDR ON STACK
1881      007602 005737 007164  TST      MODE
1882      007606 001402      BEQ      1S
1883      007610 104406      TYPBN    ;TYPE BINARY DATA.
1884      007612 000401      BR      2S
1885
1886      007614 104402      1S:    TYPE      ,TYOC   ;TYPE OCTAL DATA.
1887      007616 062737 000002 007166  2S:    ADD      #2,ADDRS ;BUMP ADDRESS POINTER.
1888      007624 005337 007170  DEC      WORDS    ;AND WORD COUNT
1889      007630 001403      BEQ      4S      ;QUIT WHEN WC = 0.
1890
1891      007632 005301      DEC      R1      ;BUMP WORDS/LINE COUNT
1892      007634 001746      BEQ      DMPGO   ;START A NEW LINE
1893      007636 000755      BR      3S      ;NEXT WORD, THIS LINE
1894
1895      007640 104401 001171  4S:    TYPE      ,SCLF
1896      007644 000240      NOP
1897      007646 000655      BR      DSTRT   ;RESTART ON 'CONT'
1898
1899      007650      040      040      040  SPACE3: .BYTE 40,40,40,0 ;3 SPACES
1900      007653      000
    
```

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1901          .SBTTL SYSMAC UTILITIES FOLLOW:
1902          .SBTTL SCOPE HANDLER ROUTINE
1903
1904          ;*****
1905          ;THIS ROUTINE CONTROLS THE LOOPING OF SUBTESTS. IT WILL INCREMENT
1906          ;AND LOAD THE TEST NUMBER(STSTNM) INTO THE DISPLAY REG.(DISPLAY<7:0>)
1907          ;AND LOAD THE ERROR FLAG (SERFLG) INTO DISPLAY<15:08>
1908          ;THE SWITCH OPTIONS PROVIDED BY THIS ROUTINE ARE:
1909          ;SM14=1      LOOP ON TEST
1910          ;SM11=1      INHIBIT ITERATIONS
1911          ;SM09=1      LOOP ON ERROR
1912          ;SM08=1      LOOP ON TEST IN SWR<7:0>
1913          ;CALL
1914          ;*          SCOPE          ;;SCOPE=IOT
1915
1916          $SCOPE:
1917          007654      104410      CКСWR          ;;TEST FOR CHANGE IN SOFT-SWR
1918          007654      032777      040000      171254      1S:      BIT          #BIT14,2SWR      ;;LOOP ON PRESENT TEST?
1919          007664      001131      BNE          $OVER          ;;YES IF SM14=1
1920          ;####START OF CODE FOR THE XOR TESTER####
1921          007666      000416      $XTSTR: BR          6S          ;;IF RUNNING ON THE "XOR" TESTER CHANGE
1922          ;THIS INSTRUCTION TO A "NOP" (NOP=240)
1923          007670      013746      000004      MOV          2$ERRVEC, -(SP)      ;;SAVE THE CONTENTS OF THE ERROR VECTOR
1924          007674      012737      007714      000004      MOV          #5S, 2$ERRVEC      ;;SET FOR TIMEOUT
1925          007702      005737      177060      TST         2$177060      ;;TIME OUT ON XOR?
1926          007706      012637      000004      MOV          (SP)+, 2$ERRVEC      ;;RESTORE THE ERROR VECTOR
1927          007712      000500      BR          $$VLAD          ;;GO TO THE NEXT TEST
1928          007714      022626      5S:      CMP          (SP)+, (SP)+      ;;CLEAR THE STACK AFTER A TIME OUT
1929          007716      012637      000004      MOV          (SP)+, 2$ERRVEC      ;;RESTORE THE ERROR VECTOR
1930          007722      000440      BR          7S          ;;LOOP ON THE PRESENT TEST
1931          007724      6S: ;####END OF CODE FOR THE XOR TESTER####
1932          007724      032777      000400      171206      BIT          #BIT08, 2SWR      ;;LOOP ON SPEC. TEST?
1933          007732      001421      BEQ         2S          ;;BR IF NO
1934          007734      005046      CLR         -(SP)      ;;CLEAR A TEMP. LOCATION
1935          007736      117716      171176      MOVB        2$SWR, (SP)      ;;PICKUP THE DESIRED TEST NUMBER
1936          007742      001414      BEQ         8S          ;;BRANCH IF BAD TEST NUMBER IN SWR
1937          007744      022716      000013      CMP         #13, (SP)      ;;CHECK THE NUMBER IN THE SWR
1938          007750      002411      BLT         8S          ;;BRANCH IF TEST NUMBER IS OUT OF RANGE
1939          007752      011637      001102      MOV         (SP), STSTNM      ;;UPDATE THE TEST NUMBER
1940          007756      005316      DEC         (SP)      ;;BACKUP BY ONE
1941          007760      006316      ASL        (SP)      ;;SCALE THE TEST NUMBER AS AN INDEX
1942          007762      062716      010166      ADD         $$SWDBTL, (SP)      ;;FORM THE ADDRESS OF TEST POINTER
1943          007766      013637      001106      MOV         2(SP)+, $LPADR      ;;SET LOOP ADDRESS TO DESIRED TEST
1944          007772      000466      BR          $OVER          ;;GO LOOP ON THE TEST
1945          007774      005726      8S:      TST         (SP)+      ;;CLEAN THE BAD TEST NUMBER OFF OF THE STACK
1946          007776      105737      001103      2S:      TSTB        SERFLG      ;;HAS AN ERROR OCCURRED?
1947          010002      001421      BEQ         3S          ;;BR IF NO
1948          010004      123737      001115      001103      CMPB       $ERMAX, SERFLG      ;;MAX. ERRORS FOR THIS TEST OCCURRED?
1949          010012      101015      BHI         3S          ;;BR IF NO
1950          010014      032777      001000      171116      BIT          #BIT09, 2SWR      ;;LOOP ON ERROR?
1951          010022      001404      BEQ         4S          ;;BR IF NO
1952          010024      013737      001110      001106      7S:      MOV         $LPERR, $LPADR      ;;SET LOOP ADDRESS TO LAST SCOPE
1953          010032      000446      BR          $OVER          ;;
1954          010034      105037      001103      4S:      CLRB        SERFLG          ;;ZERO THE ERROR FLAG

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|------|--------|--------|--------|--------|---------------|----------------|--|
| 1955 | 010040 | 005037 | 001160 | | CLR | STIMES | ::: CLEAR THE NUMBER OF ITERATIONS TO MAKE |
| 1956 | 010044 | 000415 | | | BR | IS | ::: ESCAPE TO THE NEXT TEST |
| 1957 | 010046 | 032777 | 004000 | 171064 | 3S: BIT | #BIT11,2SWR | ::: INHIBIT ITERATIONS? |
| 1958 | 010054 | 001011 | | | BNE | IS | ::: BR IF YES |
| 1959 | 010056 | 005737 | 001202 | | TST | SPASS | ::: IF FIRST PASS OF PROGRAM |
| 1960 | 010062 | 001406 | | | BEQ | IS | ::: INHIBIT ITERATIONS |
| 1961 | 010064 | 005237 | 001104 | | INC | SICNT | ::: INCREMENT ITERATION COUNT |
| 1962 | 010070 | 023737 | 001160 | 001104 | CMF | STIMES,SICNT | ::: CHECK THE NUMBER OF ITERATIONS MADE |
| 1963 | 010076 | 002024 | | | BGE | SOVER | ::: BR IF MORE ITERATION REQUIRED |
| 1964 | 010100 | 012737 | 000001 | 001104 | 1S: MOV | #1,SICNT | ::: REINITIALIZE THE ITERATION COUNTER |
| 1965 | 010106 | 013737 | 010164 | 001160 | MOV | SMXCNT,STIMES | ::: SET NUMBER OF ITERATIONS TO DO |
| 1966 | 010114 | 105237 | 001102 | | SSVLAD: INCB | STSTN | ::: COUNT TEST NUMBERS |
| 1967 | 010120 | 113737 | 001102 | 001200 | MOVB | STSTN,STESTN | ::: SET TEST NUMBER IN APT MAILBOX |
| 1968 | 010126 | 011637 | 001106 | | MOV | (SP),SLPADR | ::: SAVE SCOPE LOOP ADDRESS |
| 1969 | 010132 | 011637 | 001110 | | MOV | (SP),SLPERR | ::: SAVE ERROR LOOP ADDRESS |
| 1970 | 010136 | 005037 | 001162 | | CLR | SESCAPE | ::: CLEAR THE ESCAPE FROM ERROR ADDRESS |
| 1971 | 010142 | 112737 | 000001 | 001115 | MOVB | #1,SERMAX | ::: ONLY ALLOW ONE(1) ERROR ON NEXT TEST |
| 1972 | 010150 | 013777 | 001102 | 170764 | SOVER: MOV | STSTN,2DISPLAY | ::: DISPLAY TEST NUMBER |
| 1973 | 010156 | 013716 | 001106 | | MOV | SLPADR,(SP) | ::: FUDGE RETURN ADDRESS |
| 1974 | 010162 | 000002 | | | RTI | | ::: FIXES PS |
| 1975 | 010164 | 003720 | | | SMXCNT: 2000. | | ::: MAX. NUMBER OF ITERATIONS |
| 1976 | 010166 | | | | SSWOBTBL: | | |
| 1977 | 010166 | 002632 | | | .WORD | TST1+2 | ::: STARTING ADDRESS OF TEST 1 |
| 1978 | 010170 | 003364 | | | .WORD | TST2+2 | ::: STARTING ADDRESS OF TEST 2 |
| 1979 | 010172 | 003546 | | | .WORD | TST3+2 | ::: STARTING ADDRESS OF TEST 3 |
| 1980 | 010174 | 003662 | | | .WORD | TST4+2 | ::: STARTING ADDRESS OF TEST 4 |
| 1981 | 010176 | 004766 | | | .WORD | TST5+2 | ::: STARTING ADDRESS OF TEST 5 |
| 1982 | 010200 | 005024 | | | .WORD | TST6+2 | ::: STARTING ADDRESS OF TEST 6 |
| 1983 | 010202 | 005372 | | | .WORD | TST7+2 | ::: STARTING ADDRESS OF TEST 7 |
| 1984 | 010204 | 005540 | | | .WORD | TST10+2 | ::: STARTING ADDRESS OF TEST 10 |
| 1985 | 010206 | 006030 | | | .WORD | TST11+2 | ::: STARTING ADDRESS OF TEST 11 |
| 1986 | 010210 | 006214 | | | .WORD | TST12+2 | ::: STARTING ADDRESS OF TEST 12 |
| 1987 | 010212 | 006316 | | | .WORD | TST13+2 | ::: STARTING ADDRESS OF TEST 13 |

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010214 104410
010216 105237 001103
010222 001775
010224 013777 001102 170710
010232 032777 002000 170700
010240 001402
010242 104401 001164
010246 005237 001112
010252 011637 001116
010256 162737 000002 001116
010264 117737 170626 001114
010272 032777 020000 170640
010300 001004
010302 004737 010414
010306 104401 001171
010312
010312 122737 000001 001214
010320 001007
010322 113737 001114 010334
010330 004737 012670
010334 000
010335 000
010336 000777
010340 005777 170574
010344 100002
010346 000000
010350 104410
010352 032777 001000 170560
010360 001402
010362 013716 001110
010366 005737 001162
010372 001402
010374 013716 001162
010400
010400 022737 006626 000042
010406 001001
010410 000000
010412
010412 000002

.SBTTL ERROR HANDLER ROUTINE

: THIS ROUTINE WILL INCREMENT THE ERROR FLAG AND THE ERROR COUNT,
: SAVE THE ERROR ITEM NUMBER AND THE ADDRESS OF THE ERROR CALL
: AND GO TO SERRTYP ON ERROR
: THE SWITCH OPTIONS PROVIDED BY THIS ROUTINE ARE:
: #SW15=1 HALT ON ERROR
: #SW13=1 INHIBIT ERROR TYPEOUTS
: #SW10=1 BELL ON ERROR
: #SW09=1 LOOP ON ERROR
: #CALL
: * ERROR N ;;ERROR=EMT AND N=ERROR ITEM NUMBER

SERROR:

7S: CKSMR ;; TEST FOR CHANGE IN SOFT-SWR
INCB SERFLG ;; SET THE ERROR FLAG
BEQ 7S ;; DON'T LET THE FLAG GO TO ZERO
MOV STSTNM,DISP ;; DISPLAY TEST NUMBER AND ERROR FLAG
BIT #BIT10,SWR ;; BELL ON ERROR?
BEQ 1S ;; NO - SKIP
TYPE SBELL ;; RING BELL
1S: INC SERTTL ;; COUNT THE NUMBER OF ERRORS
MOV (SP),SERAPC ;; GET ADDRESS OF ERROR INSTRUCTION
SUB #2,SERAPC
MOVB #SERAPC,SITEMB ;; STRIP AND SAVE THE ERROR ITEM CODE
BIT #BIT13,SWR ;; SKIP TYPEOUT IF SET
BNE 20S ;; SKIP TYPEOUTS
JSR PC,SERRTYP ;; GO TO USER ERROR ROUTINE
TYPE ,SCLF
20S: CMPB #APTENV,SENV ;; RUNNING IN APT MODE
BNE 2S ;; NO SKIP APT ERROR REPORT
MOVB SITEMB,21S ;; SET ITEM NUMBER AS ERROR NUMBER
JSR PC,SATY4 ;; REPORT FATAL ERROR TO APT
21S: .BYTE 0
.BYTE 0
22S: BR 22S ;; APT ERROR LOOP
2S: TST SWR ;; HALT ON ERROR
BPL 3S ;; SKIP IF CONTINUE
HALT ;; HALT ON ERROR!
3S: BIT #BIT09,SWR ;; TEST FOR CHANGE IN SOFT-SWR
BEQ 4S ;; LOOP ON ERROR SWITCH SET?
MOV SLPERR,(SP) ;; BR IF NO
TST SESCAPE ;; FUDGE RETURN FOR LOOPING
BEQ 5S ;; CHECK FOR AN ESCAPE ADDRESS
MOV SESCAPE,(SP) ;; BR IF NONE
5S: CMP #SENDAD,#42 ;; FUDGE RETURN ADDRESS FOR ESCAPE
BNE 6S ;; ACT-11 AUTO-ACCEPT?
HALT ;; BRANCH IF NO
6S: RTI ;; YES
;; RETURN

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2049 010414
2050 010414 104401 001171
2051 010420 010046
2052 010422 005000
2053 010424 153700 001114
2054 010430 001004
2055
2056 010432 013746 001116
2057
2058 010436 104402
2059 010440 000426
2060 010442 005300
2061 010444 006300
2062 010446 006300
2063 010450 006300
2064 010452 062700 001260
2065 010456 012037 010466
2066 010462 001404
2067 010464 104401
2068 010466 000000
2069 010470 104401 001171
2070 010474 012037 010504
2071 010500 001404
2072 010502 104401
2073 010504 000000
2074 010506 104401 001171
2075 010512 011000
2076 010514 001004
2077 010516 012600
2078 010520 104401 001171
2079 010524 000207
2080 010526
2081 010526 013046
2082 010530 104402
2083 010532 005710
2084 010534 001770
2085 010536 104401 010544
2086 010542 000771
2087 010544 020040 000
2088 010550
    
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.SBTTL ERROR MESSAGE TYPEOUT ROUTINE

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*****
: THIS ROUTINE USES THE "ITEM CONTROL BYTE" (SITEMB) TO DETERMINE WHICH
: ERROR IS TO BE REPORTED. IT THEN OBTAINS, FROM THE "ERROR TABLE" (SERRTB),
: AND REPORTS THE APPROPRIATE INFORMATION CONCERNING THE ERROR.
    
```

SERRTYP:

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TYPE SCRLF : "CARRIAGE RETURN" & "LINE FEED"
MOV RO,-(SP) : SAVE RO
CLR RO : PICKUP THE ITEM INDEX
BISB @SITEMB,RO
BNE IS : IF ITEM NUMBER IS ZERO, JUST
: TYPE THE PC OF THE ERROR
MOV SERRPC,-(SP) : SAVE SERRPC FOR TYPEOUT
: ERROR ADDRESS
TYPOC : GO TYPE--OCTAL ASCII(ALL DIGITS)
BR 6S : GET OUT
IS: DEC RO : ADJUST THE INDEX SO THAT IT WILL
: WORK FOR THE ERROR TABLE
ASL RO
ASL RO
ASL RO
ADD @SERRTB,RO : FORM TABLE POINTER
MOV (RO)+,2S : PICKUP "ERROR MESSAGE" POINTER
BEQ 3S : SKIP TYPEOUT IF NO POINTER
TYPE : TYPE THE "ERROR MESSAGE"
: "ERROR MESSAGE" POINTER GOES HERE
WORD 0 : "CARRIAGE RETURN" & "LINE FEED"
TYPE SCRLF : PICKUP "DATA HEADER" POINTER
MOV (RO)+,4S : SKIP TYPEOUT IF 0
BEQ 5S : TYPE THE "DATA HEADER"
: "DATA HEADER" POINTER GOES HERE
WORD 0 : "CARRIAGE RETURN" & "LINE FEED"
TYPE SCRLF : PICKUP "DATA TABLE" POINTER
MOV (RO),RO : GO TYPE THE DATA
BNE 7S : RESTORE RO
MOV (SP)+,RO : "CARRIAGE RETURN" & "LINE FEED"
TYPE SCRLF : RETURN
RTS PC
7S: MOV @ (RO)+,-(SP) : SAVE @ (RO)+ FOR TYPEOUT
TYPOC : GO TYPE--OCTAL ASCII(ALL DIGITS)
TST (RO) : IS THERE ANOTHER NUMBER?
BEQ 6S : BR IF NO
TYPE 8S : TYPE TWO(2) SPACES
BR 7S : LOOP
8S: .ASCIZ / / : TWO(2) SPACES
.EVEN
    
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2142

.SBTTL TYPE ROUTINE

#ROUTINE TO TYPE ASCIZ MESSAGE. MESSAGE MUST TERMINATE WITH A 0 BYTE.
#THE ROUTINE WILL INSERT A NUMBER OF NULL CHARACTERS AFTER A LINE FEED.
#NOTE1: #NULL CONTAINS THE CHARACTER TO BE USED AS THE FILLER CHARACTER.
#NOTE2: #SFILLS CONTAINS THE NUMBER OF FILLER CHARACTERS REQUIRED.
#NOTE3: #SFILLC CONTAINS THE CHARACTER TO FILL AFTER.

#CALL:
#1) USING A TRAP INSTRUCTION
TYPE ,MESADR ;;MESADR IS FIRST ADDRESS OF AN ASCIZ STRING
#OR
TYPE
MESADR

010550 105737 001157
010554 100002
010556 000000
010560 000430
010562 010046
010564 017600 000002
010570 122737 000001 001214
010576 001011
010600 132737 000100 001215
010606 001405
010610 010037 010620
010614 004737 012660
010620 000000
010622 132737 000040 001215
010630 001003
010632 112046
010634 001005
010636 005726
010640 012600
010642 062716 000002
010646 000002
010650 122716 000011
010654 001430
010656 122716 000200
010662 001006
010664 005726
010666 104401
010670 001171
010672 105037 011026
010676 000755
010700 004737 010762
010704 123726 001156
010710 001350
010712 013746 001154
010716 105366 000001
010722 002770

STYPE: TSTB \$TPFLG
BPL 1\$
HALT
BR 3\$
1\$: MOV R0,-(SP)
MOV #2(SP),R0
CMPB #APTENV,SENV
BNE 62\$
BITB #APTSP00L,SENVH
BEQ 62\$
MOV R0,61\$
JSR PC,SATY3
61\$: .WORD 0
62\$: BITB #APTCSUP,SENVH
BNE 60\$
2\$: MOVB (R0)+,-(SP)
BNE 4\$
TST (SP)+
60\$: MOV (SP)+,R0
3\$: ADD #2,(SP)
RTI
4\$: CMPB #HT,(SP)
BEQ 8\$
CMPB #CRLF,(SP)
BNE 5\$
TST (SP)+
TYPE SCRLF
CLRB \$CHARCNT
BR 2\$
5\$: JSR PC,STYPEC
6\$: CMPB #FILLC,(SP)+
BNE 2\$
MOV #NULL,-(SP)
7\$: DECB 1(SP)
BLT 6\$

;; IS THERE A TERMINAL?
;; BR IF YES
;; HALT HERE IF NO TERMINAL
;; LEAVE
;; SAVE R0
;; GET ADDRESS OF ASCIZ STRING
;; RUNNING IN APT MODE
;; NO, GO CHECK FOR APT CONSOLE
;; SPOOL MESSAGE TO APT
;; NO, GO CHECK FOR CONSOLE
;; SETUP MESSAGE ADDRESS FOR APT
;; SPOOL MESSAGE TO APT
;; MESSAGE ADDRESS
;; APT CONSOLE SUPPRESSED
;; YES, SKIP TYPE OUT
;; PUSH CHARACTER TO BE TYPED ONTO STACK
;; BR IF IT ISN'T THE TERMINATOR
;; IF TERMINATOR POP IT OFF THE STACK
;; RESTORE R0
;; ADJUST RETURN PC
;; RETURN
;; BRANCH IF <HT>
;; BRANCH IF NOT <CRLF>
;; POP <CR><LF> EQUIV
;; TYPE A CR AND LF
;; CLEAR CHARACTER COUNT
;; GET NEXT CHARACTER
;; GO TYPE THIS CHARACTER
;; IS IT TIME FOR FILLER CHARS.?
;; IF NO GO GET NEXT CHAR.
;; GET # OF FILLER CHARS. NEEDED
;; AND THE NULL CHAR.
;; DOES A NULL NEED TO BE TYPED?
;; BR IF NO--GO POP THE NULL OFF OF STACK

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2143 010724 004737 010762      JSR    PC,STYPEC      ;; GO TYPE A NULL
2144 010730 105337 011026      DECB   SCHARCNT      ;; DO NOT COUNT AS A COUNT
2145 010734 000770              BR     75             ;; LOOP
2146
2147      ;HORIZONTAL TAB PROCESSOR
2148
2149 010736 112716 000040      BS:    MOVB   8' (SP)      ;; REPLACE TAB WITH SPACE
2150 010742 004737 010762      9S:    JSR    PC,STYPEC      ;; TYPE A SPACE
2151 010746 132737 000007 011026      BITB   87,SCHARCNT      ;; BRANCH IF NOT AT
2152 010754 001372              BNE    9S             ;; TAB STOP
2153 010756 005726              TST    (SP)+          ;; POP SPACE OFF STACK
2154 010760 000724              BR     25             ;; GET NEXT CHARACTER
2155 010762 105777 170162      STYPEC: TSTB   25TPS      ;; WAIT UNTIL PRINTER IS READY
2156 010766 100375              BPL    STYPEC
2157 010770 116677 000002 170154      MOVB   2(SP),2STPB      ;; LOAD CHAR TO BE TYPED INTO DATA REG.
2158 010776 122766 000015 000002      CMPB   8CR,2(SP)        ;; IS CHARACTER A CARRIAGE RETURN?
2159 011004 001003              BNE    1S             ;; BRANCH IF NO
2160 011006 105037 011026      CLAB   SCHARCNT        ;; YES--CLEAR CHARACTER COUNT
2161 011012 000406              BR     STYPEX          ;; EXIT
2162 011014 122766 000012 000002      1S:    CMPB   8LF,2(SP)   ;; IS CHARACTER A LINE FEED?
2163 011022 001402              BEQ    STYPEX          ;; BRANCH IF YES
2164 011024 105227              INCB   (PC)+          ;; COUNT THE CHARACTER
2165 011026 000000      SCHARCNT: WORD 0      ;; CHARACTER COUNT STORAGE
2166 011030 000207      STYPEX: RTS    PC
2167
    
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011032 010046
011034 010146
011036 010246
011040 010346
011042 010546
011044 012746 020200
011050 016605 000020
011054 100004
011056 005405
011060 112766 000055 000001
011066 005000
011070 012703 011246
011074 112723 000040
011100 005002
011102 016001 011236
011106 160105
011110 002402
011112 005202
011114 000774
011116 060105
011120 005702
011122 001002
011124 105716
011126 100407
011130 106316
011132 103003
011134 116663 000001 177777
011142 052702 000060
011146 052702 000040
011152 110223
011154 005720
011156 020027 000010
011162 002746
011164 003002
011166 010502
011170 000764
011172 105726
011174 100003
011176 116663 177777 177776
011204 105013
011206 012605

.SBTTL CONVERT BINARY TO DECIMAL AND TYPE ROUTINE

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*****
: THIS ROUTINE IS USED TO CHANGE A 16-BIT BINARY NUMBER TO A 5-DIGIT
: SIGNED DECIMAL (ASCII) NUMBER AND TYPE IT. DEPENDING ON WHETHER THE
: NUMBER IS POSITIVE OR NEGATIVE A SPACE OR A MINUS SIGN WILL BE TYPED
: BEFORE THE FIRST DIGIT OF THE NUMBER. LEADING ZEROS WILL ALWAYS BE
: REPLACED WITH SPACES.
: CALL:
: *   MOV     NUM,-(SP)      ;; PUT THE BINARY NUMBER ON THE STACK
: *   TYPDS   ;; GO TO THE ROUTINE

STYPDS:
MOV     R0,-(SP)      ;; PUSH R0 ON STACK
MOV     R1,-(SP)      ;; PUSH R1 ON STACK
MOV     R2,-(SP)      ;; PUSH R2 ON STACK
MOV     R3,-(SP)      ;; PUSH R3 ON STACK
MOV     R5,-(SP)      ;; PUSH R5 ON STACK
MOV     #20200,-(SP)   ;; SET BLANK SWITCH AND SIGN
MOV     20(SP),R5     ;; GET THE INPUT NUMBER
BPL     R5             ;; BR IF INPUT IS POS.
NEG     R5             ;; MAKE THE BINARY NUMBER POS.
MOVB   #'-,1(SP)     ;; MAKE THE ASCII NUMBER NEG.
1$:    CLR     R0      ;; ZERO THE CONSTANTS INDEX
MOV     #SDBLK,R3    ;; SETUP THE OUTPUT POINTER
MOVB   #' ,(R3)+    ;; SET THE FIRST CHARACTER TO A BLANK
2$:    CLR     R2      ;; CLEAR THE BCD NUMBER
MOV     $OTBL(R0),R1 ;; GET THE CONSTANT
3$:    SUB     R1,R5   ;; FORM THIS BCD DIGIT
BLT    R5           ;; BR IF DONE
INC    R2          ;; INCREASE THE BCD DIGIT BY 1
4$:    ADD     R1,R5   ;; ADD BACK THE CONSTANT
TST    R2          ;; CHECK IF BCD DIGIT=0
BNE    R5          ;; FALL THROUGH IF 0
TSTB  (SP)        ;; STILL DOING LEADING 0'S?
BMI    R5          ;; BR IF YES
5$:    ASLB   (SP)   ;; MSD?
BCC    R5          ;; BR IF NO
MOVB  1(SP),-1(R3) ;; YES--SET THE SIGN
BIS   #'0,R2      ;; MAKE THE BCD DIGIT ASCII
7$:   BIS   #' ,R2 ;; MAKE IT A SPACE IF NOT ALREADY A DIGIT
MOVB  R2,(R3)+    ;; PUT THIS CHARACTER IN THE OUTPUT BUFFER
TST   (R0)+       ;; JUST INCREMENTING
CMP   R0,#10     ;; CHECK THE TABLE INDEX
BLT   R5         ;; GO DO THE NEXT DIGIT
BGT   R5         ;; GO TO EXIT
MOV   R5,R2      ;; GET THE LSD
BR    R5         ;; GO CHANGE TO ASCII
8$:   TSTB  (SP)+  ;; WAS THE LSD THE FIRST NON-ZERO?
BPL   R5         ;; BR IF NO
MOVB  -1(SP),-2(R3) ;; YES--SET THE SIGN FOR TYPING
9$:   CLRB  (R3)   ;; SET THE TERMINATOR
MOV   (SP)+,R5   ;; POP STACK INTO R5
    
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| | | | | | | |
|------|--------|--------|---------------|--------|-------------|------------------------|
| 2222 | 011210 | 012603 | | MOV | (SP)+,R3 | :: POP STACK INTO R3 |
| 2223 | 011212 | 012602 | | MOV | (SP)+,R2 | :: POP STACK INTO R2 |
| 2224 | 011214 | 012601 | | MOV | (SP)+,R1 | :: POP STACK INTO R1 |
| 2225 | 011216 | 012600 | | MOV | (SP)+,R0 | :: POP STACK INTO R0 |
| 2226 | 011220 | 104401 | 011246 | TYPE | \$DBLK | :: NOW TYPE THE NUMBER |
| 2227 | 011224 | 016666 | 000002 000004 | MOV | 2(SP),4(SP) | :: ADJUST THE STACK |
| 2228 | 011232 | 012616 | | MOV | (SP)+,(SP) | |
| 2229 | 011234 | 000002 | | RTI | | :: RETURN TO USER |
| 2230 | 011236 | 023420 | | SOTBL: | 10000. | |
| 2231 | 011240 | 001750 | | | 1000. | |
| 2232 | 011242 | 000144 | | | 100. | |
| 2233 | 011244 | 000012 | | | 10. | |
| 2234 | 011246 | 000004 | | SDBLK: | .BLKW 4 | |

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011256 017646 000000
 011262 116637 000001 011501
 011270 112637 011503
 011274 062716 000002
 011300 000406
 011302 112737 000001 011501
 011310 112737 000006 011503
 011316 112737 000005 011500
 011324 010346
 011326 010446
 011330 010546
 011332 113704 011503
 011336 005404
 011340 062704 000006
 011344 110437 011502
 011350 113704 011501
 011354 016605 000012
 011360 005003
 011362 006105
 011364 000404
 011366 006105
 011370 006105
 011372 006105
 011374 010503
 011376 006103
 011400 105337 011502
 011404 100016
 011406 042703 177770
 011412 001002

```
.SBTTL BINARY TO OCTAL (ASCII) AND TYPE

*****
*THIS ROUTINE IS USED TO CHANGE A 16-BIT BINARY NUMBER TO A 6-DIGIT
*OCTAL (ASCII) NUMBER AND TYPE IT.
*STYPOS---ENTER HERE TO SETUP SUPPRESS ZEROS AND NUMBER OF DIGITS TO TYPE
*CALL:
*   MOV     NUM,-(SP)      ;; NUMBER TO BE TYPED
*   TYPOS   N              ;; CALL FOR TYPEOUT
*   .BYTE  N              ;; N=1 TO 6 FOR NUMBER OF DIGITS TO TYPE
*   .BYTE  M              ;; M=1 OR 0
*                               ;; 1=TYPE LEADING ZEROS
*                               ;; 0=SUPPRESS LEADING ZEROS
*
*STYON---ENTER HERE TO TYPE OUT WITH THE SAME PARAMETERS AS THE LAST
*STYPOS OR STYPOC
*CALL:
*   MOV     NUM,-(SP)      ;; NUMBER TO BE TYPED
*   TYPON   N              ;; CALL FOR TYPEOUT
*
*STYPOC---ENTER HERE FOR TYPEOUT OF A 16 BIT NUMBER
*CALL:
*   MOV     NUM,-(SP)      ;; NUMBER TO BE TYPED
*   TYPOC   N              ;; CALL FOR TYPEOUT
*
*STYPOS: MOV     2(SP),-(SP)  ;; PICKUP THE MODE
*        MOVB   1(SP),SOFILL ;; LOAD ZERO FILL SWITCH
*        MOVB   (SP)+,SOMODE+1 ;; NUMBER OF DIGITS TO TYPE
*        ADD    2,(SP)      ;; ADJUST RETURN ADDRESS
*        BR     STYON
*STYPOC: MOVB   81,SOFILL   ;; SET THE ZERO FILL SWITCH
*        MOVB   86,SOMODE+1 ;; SET FOR SIX(6) DIGITS
*STYPON: MOVB   85,SOCNT    ;; SET THE ITERATION COUNT
*        MOV    R3,-(SP)    ;; SAVE R3
*        MOV    R4,-(SP)    ;; SAVE R4
*        MOV    R5,-(SP)    ;; SAVE R5
*        MOVB   SOMODE+1,R4 ;; GET THE NUMBER OF DIGITS TO TYPE
*        NEG    R4
*        ADD    86,R4      ;; SUBTRACT IT FOR MAX. ALLOWED
*        MOVB   R4,SOMODE  ;; SAVE IT FOR USE
*        MOVB   SOFILL,R4  ;; GET THE ZERO FILL SWITCH
*        MOV    12(SP),R5  ;; PICKUP THE INPUT NUMBER
*        CLR    R3        ;; CLEAR THE OUTPUT WORD
*        ROL   R5        ;; ROTATE MSB INTO "C"
*        BR    3$        ;; GO DO MSB
*        ROL   R5        ;; FORM THIS DIGIT
*        ROL   R5
*        ROL   R5
*        MOV    R5,R3
*        ROL   R3
*        DECB  SOMODE     ;; GET LSB OF THIS DIGIT
*        BPL  7$        ;; TYPE THIS DIGIT?
*        BIC  #177770,R3 ;; BR IF NO
*        BNE  4$        ;; GET RID OF JUNK
*        BNE  4$        ;; TEST FOR 0
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|------|--------|--------|---------------|-------|-------------|-----|---------------------------------|
| 2289 | 011414 | 005704 | | TST | R4 | ... | SUPPRESS THIS 0? |
| 2290 | 011416 | 001403 | | BEQ | 5S | ... | BR IF YES |
| 2291 | 011420 | 005204 | | INC | R4 | ... | DON'T SUPPRESS ANYMORE 0'S |
| 2292 | 011424 | 052703 | 000060 | BIS | 8'0,R3 | ... | MAKE THIS DIGIT ASCII |
| 2293 | 011428 | 052703 | 000040 | BIS | 8'R3 | ... | MAKE ASCII IF NOT ALREADY |
| 2294 | 011432 | 110337 | 011476 | MOV8 | R3,8S | ... | SAVE FOR TYPING |
| 2295 | 011436 | 104401 | 011476 | TYPE | 8S | ... | GO TYPE THIS DIGIT |
| 2296 | 011440 | 105337 | 011500 | DECB | SOCNT | ... | COUNT BY 1 |
| 2297 | 011444 | 003347 | | BGT | 2S | ... | BR IF MORE TO DO |
| 2298 | 011450 | 002402 | | BLT | 6S | ... | BR IF DONE |
| 2299 | 011452 | 005204 | | INC | R4 | ... | INSURE LAST DIGIT ISN'T A BLANK |
| 2300 | 011454 | 000744 | | BR | 2S | ... | GO DO THE LAST DIGIT |
| 2301 | 011456 | 012605 | | MOV | (SP)+,R5 | ... | RESTORE R5 |
| 2302 | 011460 | 012604 | | MOV | (SP)+,R4 | ... | RESTORE R4 |
| 2303 | 011462 | 012603 | | MOV | (SP)+,R3 | ... | RESTORE R3 |
| 2304 | 011464 | 016666 | 000002 000004 | MOV | 2(SP),4(SP) | ... | SET THE STACK FOR RETURNING |
| 2305 | 011472 | 012616 | | MOV | (SP)+,(SP) | ... | |
| 2306 | 011474 | 000002 | | RTI | | ... | RETURN |
| 2307 | 011476 | 000 | | .BYTE | 0 | ... | STORAGE FOR ASCII DIGIT |
| 2308 | 011477 | 000 | | .BYTE | 0 | ... | TERMINATOR FOR TYPE ROUTINE |
| 2309 | 011500 | 000 | | .BYTE | 0 | ... | OCTAL DIGIT COUNTER |
| 2310 | 011501 | 000 | | .BYTE | 0 | ... | ZERO FILL SWITCH |
| 2311 | 011502 | 000000 | | .WORD | 0 | ... | NUMBER OF DIGITS TO TYPE |

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2312          .SBTTL  BINARY TO ASCII AND TYPE ROUTINE
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2314          :: *****
2315          :: #THIS ROUTINE IS USED TO CHANGE A 16-BIT BINARY NUMBER TO A 16-BIT
2316          :: #BINARY-ASCII NUMBER AND TYPE IT.
2317          :: #CALL:
2318          :: #      MOV      NUMBER,-(SP)      :: NUMBER TO BE TYPED
2319          :: #      TYPBN
2320          :: #
2321          STYPBN: MOV      R1,-(SP)          :: SAVE R1 ON THE STACK
2322          MOV      6(SP),R1                :: GET THE INPUT NUMBER
2323          SEC
2324          MOV      000060 011556 1$:      MOV      0'0,SBIN          :: SET "C" SO CAN KEEP TRACK OF THE NUMBER OF BITS
2325          ROL      R1                      :: SET CHARACTER TO AN ASCII "0".
2326          BEQ      2$                      :: GET THIS BIT
2327          ADCB     SBIN                    :: DONE?
2328          TYPE     ,SBIN                   :: NO--SET THE CHARACTER EQUAL TO THIS BIT
2329          CLC
2330          BR       1$                      :: GO TYPE THIS BIT
2331          MOV      (SP)+,R1                :: CLEAR "C" SO CAN KEEP TRACK OF BITS
2332          MOV      000002 000004 2$:      MOV      2(SP),4(SP)      :: GO DO THE NEXT BIT
2333          MOV      (SP)+,(SP)              :: POP THE STACK INTO R1
2334          RTI
2335          SBIN:  .BYTE  0,0                :: ADJUST THE STACK
                :: RETURN TO USER
                :: STORAGE FOR ASCII CHAR. AND TERMINATOR
    
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.SBTTL TTY INPUT ROUTINE

:::*****

.ENABL LSB

:::*****

*SOFTWARE SWITCH REGISTER CHANGE ROUTINE.
*ROUTINE IS ENTERED FROM THE TRAP HANDLER, AND WILL
*SERVICE THE TEST FOR CHANGE IN SOFTWARE SWITCH REGISTER TRAP CALL
*WHEN OPERATING IN TTY FLAG MODE.

011560 022737 000176 001140
011566 001074
011570 105777 167350
011574 100071
011576 117746 167344
011602 042716 177600
011606 022726 000007
011612 001062
011614 123727 001134 000001
011622 001456

011624 104401 012305
011630 104401 012312
011634 013746 000176
011640 104402
011642 104401 012323
011646 005046
011650 005046
011652 105777 167266
011656 100375

011660 117746 167262
011664 042716 177600

011670 021627 000025
011674 001005
011676 104401 012300
011702 062706 000006
011706 000757

011710 021627 000015
011714 001022
011716 005766 000004
011722 001403
011724 016677 000002 167206
011732 062706 000006
011736 104401 001171
011742 123727 001135 000001
011750 001003
011752 012777 000100 167164
011760 000002

\$CKSMR: CMP #SMREG,SMR
BNE 15\$
TSTB @STKB
BPL 15\$
MOVB @STKB,-(SP)
BIC @C177,(SP)
CMP #7,(SP)+
BNE 15\$
CMPB \$AUTOB,#1
BEQ 15\$

SGTSMR: TYPE ,SCNTLG
TYPE ,SMSMR
MOV SMREG,-(SP)
TYPOC
TYPE ,SMNEW
19\$: CLR -(SP)
CLR -(SP)
7\$: TSTB @STKB
BPL 7\$

MOVB @STKB,-(SP)
BIC @C177,(SP)

9\$: CMP (SP),#25
BNE 10\$
TYPE ,SCNTLU
20\$: ADD #6,SP
BR 19\$

10\$: CMP (SP),#15
BNE 16\$
TST 4(SP)
BEQ 11\$
MOV 2(SP),@SMR
11\$: ADD #6,SP
14\$: TYPE ,SCALF
CMPB \$INTAG,#1
BNE 15\$
MOV #100,@STKB
15\$: RTI

::: IS THE SOFT-SMR SELECTED?
::: BRANCH IF NO
::: CHAR THERE?
::: IF NO, DON'T WAIT AROUND
::: SAVE THE CHAR
::: STRIP-OFF THE ASCII
::: IS IT A CONTROL G?
::: NO, RETURN TO USER
::: ARE WE RUNNING IN AUTO-MODE?
::: BRANCH IF YES

::: ECHO THE CONTROL-G (#G)
::: TYPE CURRENT CONTENTS
::: SAVE SMREG FOR TYPEOUT
::: GO TYPE--OCTAL ASCII(ALL DIGITS)
::: PROMPT FOR NEW SMR
::: CLEAR COUNTER
::: THE NEW SMR
::: CHAR THERE?
::: IF NOT TRY AGAIN

::: PICK UP CHAR
::: MAKE IT 7-BIT ASCII

::: IS IT A CONTROL-U?
::: BRANCH IF NOT
::: YES, ECHO CONTROL-U (#U)
::: IGNORE PREVIOUS INPUT
::: LET'S TRY IT AGAIN

::: IS IT A <CR>?
::: BRANCH IF NO
::: YES, IS IT THE FIRST CHAR?
::: BRANCH IF YES
::: SAVE NEW SMR
::: CLEAR UP STACK
::: ECHO <CR> AND <LF>
::: RE-ENABLE TTY KBD INTERRUPTS?
::: BRANCH IF NOT
::: RE-ENABLE TTY KBD INTERRUPTS
::: RETURN

2390 011762 004737 010762
2391 011766 021627 000060
2392 011772 012420
2393 011774 021627 000067
2394 012000 003015
2395 012002 042726 000060
2396 012006 005766 000002
2397 012012 001403
2398 012014 006316
2399 012016 006316
2400 012020 006316
2401 012022 005266 000002
2402 012026 056616 177776
2403 012032 000707
2404 012034 104401 001170
2405 012040 000720

16S: JSR PC, STYPEC
CMP (SP), #60
BLT 18S
CMP (SP), #67
BGT 18S
BIC #60 (SP)+
TST 2(SP)
BEQ 17S
ASL (SP)
ASL (SP)
ASL (SP)
17S: INC 2(SP)
BIS -2(SP), (SP)
BR 7S
18S: TYPE \$QUES
BR 20S
.DSABL LSB

:: ECHO CHAR
:: CHAR < 0?
:: BRANCH IF YES
:: CHAR > 7?
:: BRANCH IF YES
:: STRIP-OFF ASCII
:: IS THIS THE FIRST CHAR
:: BRANCH IF YES
:: NO, SHIFT PRESENT
:: CHAR OVER TO MAKE
:: ROOM FOR NEW ONE.
:: KEEP COUNT OF CHAR
:: SET IN NEW CHAR
:: GET THE NEXT ONE
:: TYPE ?(CR)<LF>
:: SIMULATE CONTROL-U

: THIS ROUTINE WILL INPUT A SINGLE CHARACTER FROM THE TTY
: *CALL:
: * RDCHR
: * RETURN HERE
: *
: * INPUT A SINGLE CHARACTER FROM THE TTY
: * CHARACTER IS ON THE STACK
: * WITH PARITY BIT STRIPPED OFF

012042 011646
012044 016666 000004 000002
012052 105777 167066
012056 100375
012060 117766 167062 000004
012066 042766 177600 000004
012074 026627 000004 000023
012102 001013
012104 105777 167034
012110 100375
012112 117746 167030
012116 042716 177600
012122 022627 000021
012126 001366
012130 000750
012132 026627 000004 000140
012140 002407
012142 026627 000004 000175
012150 003003
012152 042766 000040 000004
012160 000002

SROCHR: MOV (SP), -(SP)
MOV 4(SP), 2(SP)
1S: TSTB 2STKS
BPL 1S
MOVB 2STKB, 4(SP)
BIC #C(177), 4(SP)
CMP 4(SP), #23
BNE 3S
2S: TSTB 2STKS
BPL 2S
MOVB 2STKB, -(SP)
BIC #C(177), (SP)
CMP (SP)+, #21
BNE 2S
BR 1S
3S: CMP 4(SP), #140
BLT 4S
CMP 4(SP), #175
BGT 4S
BIC #40, 4(SP)
4S: RTI

:: PUSH DOWN THE PC
:: SAVE THE PS
:: WAIT FOR
:: A CHARACTER
:: READ THE TTY
:: GET RID OF JUNK IF ANY
:: IS IT A CONTROL-5?
:: BRANCH IF NO
:: WAIT FOR A CHARACTER
:: LOOP UNTIL ITS THERE
:: GET CHARACTER
:: MAKE IT 7-BIT ASCII
:: IS IT A CONTROL-0?
:: IF NOT DISCARD IT
:: YES, RESUME
:: IS IT UPPER CASE?
:: BRANCH IF YES
:: IS IT A SPECIAL CHAR?
:: BRANCH IF YES
:: MAKE IT UPPER CASE
:: GO BACK TO USER

: THIS ROUTINE WILL INPUT A STRING FROM THE TTY
: *CALL:
: * ROLIN
: * RETURN HERE
: *
: * INPUT A STRING FROM THE TTY
: * ADDRESS OF FIRST CHARACTER WILL BE ON THE STACK
: * TERMINATOR WILL BE A BYTE OF ALL 0'S

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|--------|--------|--------|--------|----------------|------------------|---|
| 012162 | 010346 | | | SRDLIN: MOV | R3, -(SP) | ::: SAVE R3 |
| 012164 | 012703 | 012270 | | 15: MOV | #STTYIN, R3 | ::: GET ADDRESS |
| 012170 | 022703 | 012300 | | 25: CMP | #STTYIN+8., R3 | ::: BUFFER FULL? |
| 012174 | 101405 | | | | 45 | ::: BR IF YES |
| 012176 | 104411 | | | | | ::: GO READ ONE CHARACTER FROM THE TTY |
| 012200 | 112613 | | | | | ::: GET CHARACTER |
| 012202 | 122713 | 000177 | | 105: CMPB | #177, (R3) | ::: IS IT A RUBOUT |
| 012206 | 001003 | | | | 35 | ::: SKIP IF NOT |
| 012210 | 104401 | 001170 | | 45: TYPE | 90UES | ::: TYPE A '?' |
| 012214 | 000763 | | | | 15 | ::: CLEAR THE BUFFER AND LOOP |
| 012216 | 111337 | 012266 | | 35: MOVB | (R3), 95 | ::: ECHO THE CHARACTER |
| 012222 | 104401 | 012266 | | | 95 | |
| 012226 | 122723 | 000015 | | | | ::: CHECK FOR RETURN |
| 012230 | 001356 | | | | 25 | ::: LOOP IF NOT RETURN |
| 012234 | 105063 | 177777 | | | | ::: CLEAR RETURN (THE 15) |
| 012240 | 104401 | 001172 | | | | ::: TYPE A LINE FEED |
| 012244 | 012603 | | | | | ::: RESTORE R3 |
| 012246 | 011646 | | | | | ::: ADJUST THE STACK AND PUT ADDRESS OF THE |
| 012250 | 016666 | 000004 | 000002 | | | ::: FIRST ASCII CHARACTER ON IT |
| 012254 | 012767 | 012270 | 000004 | | | |
| 012254 | 000002 | | | | | ::: RETURN |
| 012256 | 000 | | | 95: .BYTE | 0 | ::: STORAGE FOR ASCII CHAR. TO TYPE |
| 012257 | 000 | | | .BYTE | 0 | ::: TERMINATOR |
| 012270 | 000010 | | | STTYIN: .BLKB | 8. | ::: RESERVE 8 BYTES FOR TTY INPUT |
| 012300 | 052536 | 005015 | 000 | SCNTLU: .ASCIZ | /↑U/<15><12> | ::: CONTROL "U" |
| 012305 | 136 | 006507 | 000012 | SCNTLG: .ASCIZ | /↑G/<15><12> | ::: CONTROL "G" |
| 012312 | 005015 | 053523 | 020122 | SMSR: .ASCIZ | <15><12>/SMR = / | |
| 012320 | 020075 | 000 | | | | |
| 012323 | 040 | 047040 | 053505 | SMNEW: .ASCIZ | / NEW = / | |
| 012330 | 036440 | 000040 | | | | |

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.SBTTL READ AN OCTAL NUMBER FROM THE TTY

```

: *****
: #THIS ROUTINE WILL READ AN OCTAL (ASCII) NUMBER FROM THE TTY AND
: #CHANGE IT TO BINARY.
: #THE INPUT CHARACTERS WILL BE CHECKED TO INSURED THEY ARE LEGAL
: #OCTAL DIGITS. IF AN ILLEGAL CHARACTER IS READ A "?" WILL BE TYPED
: #FOLLOWED BY A CARRIAGE RETURN-LINE FEED. THE COMPLETE NUMBER MUST
: #THEN BE RETYPED. THE INPUT IS TERMINATED BY TYPING A CARRIAGE RETURN.
: #CALL:
: #   RDOCT          :: READ AN OCTAL NUMBER
: #   RETURN HERE    :: LOW ORDER BITS ARE ON TOP OF THE STACK
: #                 :: HIGH ORDER BITS ARE IN SHIOCT

```

```

012339 011646
012336 016666 000004 000002
012344 010046
012346 010146
012350 010246
012354 104412
012357 012600
012358 010037 012462
012363 005001
012364 005002
012366 112046
012370 001420
012372 122716 000060
012376 003026
012400 122716 000067
012410 002423
012412 006301
012414 006102
012418 006301
012419 006102
012421 006301
012422 006102
012423 042716 177770
012424 006301
012425 000736
012426 005726 35:
012427 010166 000012
012428 010237 012472
012429 012602
012430 012601
012431 012600
012432 000002
012433 005726 45:
012434 105010
012435 104401
012436 000000
012437 104401 001170
012470 000730
012472 000000

```

```

SRDOCT: MOV (SP), -(SP)
MOV 4(SP), 2(SP)
MOV R0, -(SP)
MOV R1, -(SP)
MOV R2, -(SP)
15: RDLIN
MOV (SP)+, R0
MOV R0, 55
CLR R1
CLR R2
25: MOVB (R0)+, -(SP)
BEQ 35
CHPB #'0, (SP)
BGT 45
CHPB #'7, (SP)
BLT 45
ASL R1 ;;#2
ROL R2
ASL R1 ;;#4
ROL R2
ASL R1 ;;#8
ROL R2
BIC #'C7, (SP)
ADD (SP)+, R1
BR 25
35: TST (SP)+
MOV R1, 12(SP)
MOV R2, SHIOCT
MOV (SP)+, R2
MOV (SP)+, R1
MOV (SP)+, R0
RTI
45: TST (SP)+
CLRB (R0)
TYPE
55: .WORD 0
TYPE 15
BR 15
SHIOCT: .WORD 0

```

```

: PROVIDE SPACE FOR THE
: INPUT NUMBER
: PUSH R0 ON STACK
: PUSH R1 ON STACK
: PUSH R2 ON STACK
: READ AN ASCII LINE
: GET ADDRESS OF 1ST CHARACTER
: AND SAVE IT
: CLEAR DATA WORD
: PICKUP THIS CHARACTER
: IF ZERO GET OUT
: MAKE SURE THIS CHARACTER
: IS AN OCTAL DIGIT
:
: #2
: #4
: #8
: STRIP THE ASCII JUNK
: ADD IN THIS DIGIT
: LOOP
: CLEAN TERMINATOR FROM STACK
: SAVE THE RESULT
: POP STACK INTO R2
: POP STACK INTO R1
: POP STACK INTO R0
: RETURN
: CLEAN PARTIAL FROM STACK
: SET A TERMINATOR
: TYPE UP THRU THE BAD CHAR.
: "?" "CR" & "LF"
: TRY AGAIN
: HIGH ORDER BITS GO HERE

```

.SBTTL POWER DOWN AND UP ROUTINES

:POWER DOWN ROUTINE

| | | | | | | |
|--------|--------|--------|--------|-------------|-------------------|-----------------------|
| 012774 | 012737 | 012634 | 000024 | SPWRDN: NOV | 8SILLUP, 28PWRVEC | :: SET FOR FAST UP |
| 012780 | 012737 | 000340 | 000026 | NOV | 8340, 28PWRVEC+2 | :: PRIO:7 |
| 012786 | 010046 | | | NOV | RD, -(SP) | :: PUSH RD ON STACK |
| 012792 | 010146 | | | NOV | R1, -(SP) | :: PUSH R1 ON STACK |
| 012798 | 010246 | | | NOV | R2, -(SP) | :: PUSH R2 ON STACK |
| 012804 | 010346 | | | NOV | R3, -(SP) | :: PUSH R3 ON STACK |
| 012810 | 010446 | | | NOV | R4, -(SP) | :: PUSH R4 ON STACK |
| 012816 | 010546 | | | NOV | R5, -(SP) | :: PUSH R5 ON STACK |
| 012822 | 017746 | 166410 | | NOV | 2SMR, -(SP) | :: PUSH 2SMR ON STACK |
| 012828 | 010637 | 012640 | | NOV | SP, 2SAVR6 | :: SAVE SP |
| 012834 | 012737 | 012546 | 000024 | NOV | 8SPWRUP, 28PWRVEC | :: SET UP VECTOR |
| 012840 | 000000 | | | HALT | | |
| 012846 | 000776 | | | BR | .-2 | :: HANG UP |

:POWER UP ROUTINE

| | | | | | | |
|--------|--------|--------|--------|----------------|-------------------|---------------------------------------|
| 012852 | 012737 | 012634 | 000024 | SPWRUP: NOV | 8SILLUP, 28PWRVEC | :: SET FOR FAST DOWN |
| 012858 | 013706 | 012640 | | NOV | 2SAVR6, SP | :: GET SP |
| 012864 | 005037 | 012640 | | CLR | 2SAVR6 | :: WAIT LOOP FOR THE TTY |
| 012870 | 005237 | 012640 | | IS: INC | 2SAVR6 | :: WAIT FOR THE INC |
| 012876 | 001375 | | | BNE | IS | :: OF WORD |
| 012882 | 012677 | 166342 | | NOV | (SP)+, 2SMR | :: POP STACK INTO 2SMR |
| 012888 | 012605 | | | NOV | (SP)+, R5 | :: POP STACK INTO R5 |
| 012894 | 012604 | | | NOV | (SP)+, R4 | :: POP STACK INTO R4 |
| 012900 | 012603 | | | NOV | (SP)+, R3 | :: POP STACK INTO R3 |
| 012906 | 012602 | | | NOV | (SP)+, R2 | :: POP STACK INTO R2 |
| 012912 | 012601 | | | NOV | (SP)+, R1 | :: POP STACK INTO R1 |
| 012918 | 012600 | | | NOV | (SP)+, RD | :: POP STACK INTO RD |
| 012924 | 012737 | 012474 | 000024 | NOV | 8SPWRDN, 28PWRVEC | :: SET UP THE POWER DOWN VECTOR |
| 012930 | 012737 | 000340 | 000026 | NOV | 8340, 28PWRVEC+2 | :: PRIO:7 |
| 012936 | 104401 | | | TYPE | | :: REPORT THE POWER FAILURE |
| 012942 | 012642 | | | SPWRNG: .WORD | SPOWER | :: POWER FAIL MESSAGE POINTER |
| 012948 | 000002 | | | RTI | | |
| 012954 | 000000 | | | SILLUP: HALT | | :: THE POWER UP SEQUENCE WAS STARTED |
| 012960 | 000776 | | | BR | .-2 | :: BEFORE THE POWER DOWN WAS COMPLETE |
| 012966 | 000000 | | | 2SAVR6: 0 | | :: PUT THE SP HERE |
| 012972 | 005015 | 047520 | 042527 | SPOWER: .ASCIZ | <15><12>"POWER" | |
| 012978 | 000122 | | | .EVEN | | |

012774
012780
012786
012792
012798
012804
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012816
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012870
012876
012882
012888
012894
012900
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012942
012948
012954
012960
012966
012972
012978

.SBTTL APT COMMUNICATIONS ROUTINE

```

012652 112737 000001 013116 SATY1: MOVB #1,SFFLG ;;TO REPORT FATAL ERROR
012660 112737 000001 013114 SATY3: MOVB #1,SMFLG ;;TO TYPE A MESSAGE
012666 000403 BR SATYC
012670 112737 000001 013116 SATY4: MOVB #1,SFFLG ;;TO ONLY REPORT FATAL ERROR
012676 SATYC:
012676 010046 MOV RO,-(SP) ;;PUSH RO ON STACK
012700 010146 MOV RI,-(SP) ;;PUSH RI ON STACK
012702 105737 013114 TSTB SMFLG ;;SHOULD TYPE A MESSAGE?
012706 001450 BEQ 55 ;;IF NOT: BR
012710 122737 000001 001214 CMPB #APTENV,SENV ;;OPERATING UNDER APT?
012716 001031 BNE 35 ;;IF NOT: BR
012720 132737 000100 001215 BITB #APTPOOL,SENVH ;;SHOULD SPOOL MESSAGES?
012726 001425 BEQ 35 ;;IF NOT: BR
012730 017600 000004 MOV #4(SP),RO ;;GET MESSAGE ADDR.
012734 062766 000002 000004 ADD #2,4(SP) ;;BUMP RETURN ADDR.
012742 005737 001174 1S: TST SMSGTYPE ;;SEE IF DONE W/ LAST XMISSION?
012746 001375 BNE 15 ;;IF NOT: WAIT
012750 010037 001210 MOV RO,SMSGAD ;;PUT ADDR IN MAILBOX
012754 105720 2S: TSTB (RO)+ ;;FIND END OF MESSAGE
012756 001376 BNE 25
012760 163700 001210 SUB SMSGAD,RO ;;SUB START OF MESSAGE
012764 006200 ASR RO ;;GET MESSAGE LNTH IN WORDS
012766 010037 001212 MOV RO,SMSG LGT ;;PUT LENGTH IN MAILBOX
012772 012737 000004 001174 MOV #4,SMSGTYPE ;;TELL APT TO TAKE MSG.
013000 000413 BR 55
013002 017637 000004 013026 3S: MOV #4(SP),4S ;;PUT MSG ADDR IN JSR LINKAGE
013010 062766 000002 000004 ADD #2,4(SP) ;;BUMP RETURN ADDRESS
013016 013746 177776 MOV 177776,-(SP) ;;PUSH 177776 ON STACK
013022 004737 010550 JSR PC,STYPE ;;CALL TYPE MACRO
013026 000000 4S: .WORD 0
013030 5S:
013030 105737 013116 10S: TSTB SFFLG ;;SHOULD REPORT FATAL ERROR?
013034 001416 BEQ 12S ;;IF NOT: BR
013036 005737 001214 TST SENV ;;RUNNING UNDER APT?
013042 001413 BEQ 12S ;;IF NOT: BR
013044 005737 001174 11S: TST SMSGTYPE ;;FINISHED LAST MESSAGE?
013050 001375 BNE 11S ;;IF NOT: WAIT
013052 017637 000004 001176 MOV #4(SP),SFATAL ;;GET ERROR #
013060 062766 000002 000004 ADD #2,4(SP) ;;BUMP RETURN ADDR.
013066 005237 001174 INC SMSGTYPE ;;TELL APT TO TAKE ERROR
013072 105037 013116 12S: CLRB SFFLG ;;CLEAR FATAL FLAG
013076 105037 013115 CLRB SLFLG ;;CLEAR LOG FLAG
013102 105037 013114 CLRB SMFLG ;;CLEAR MESSAGE FLAG
013106 012601 MOV (SP)+,R1 ;;POP STACK INTO R1
013110 012600 MOV (SP)+,RO ;;POP STACK INTO RO
013112 000207 RTS PC ;;RETURN
013114 000 SMFLG: .BYTE 0 ;;MESSG. FLAG
013115 000 SLFLG: .BYTE 0 ;;LOG FLAG
013116 000 SFFLG: .BYTE 0 ;;FATAL FLAG
013120 .EVEN
000200 APTSIZE=200

```

J05

LSI-11 UVPRAM-RAM TEST. MD-11-DVMRA-A. MACY11 27(663) 24-MAR-77 13:51 PAGE 60
DVMRAA.P11 APT COMMUNICATIONS ROUTINE

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2626
2627

000001
000100
000040

APTENV=001
APTSPool=100
APTCSUP=040

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013121
013122
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013173
013174
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013177
013178
013179
013200
013201
013202

.SBTTL TRAP DECODER

```

;*****
;THIS ROUTINE WILL PICKUP THE LOWER BYTE OF THE "TRAP" INSTRUCTION
;AND USE IT TO INDEX THROUGH THE TRAP TABLE FOR THE STARTING ADDRESS
;OF THE DESIRED ROUTINE. THEN USING THE ADDRESS OBTAINED IT WILL
;GO TO THAT ROUTINE.
    
```

```

013120 010046
013122 016600 000002
013126 005740
013130 111000
013132 006300
013134 016000 013154
013140 000200
    
```

```

STRAP:  MOV    RD, -(SP)           ;; SAVE RD
        MOV    2(SP), RD         ;; GET TRAP ADDRESS
        TST   -(RD)             ;; BACKUP BY 2
        MOVB  (RD), RD          ;; GET RIGHT BYTE OF TRAP
        ASL   RD                ;; POSITION FOR INDEXING
        MOV   STRPAD(RD), RD     ;; INDEX TO TABLE
        RTS   RD                ;; GO TO ROUTINE
    
```

;; THIS IS USE TO HANDLE THE "GETPRI" MACRO

```

013142 011646
013144 016666 000004 000002
013152 000002
    
```

```

STRAP2: MOV    (SP), -(SP)        ;; MOVE THE PC DOWN
        MOV    4(SP), 2(SP)      ;; MOVE THE PSW DOWN
        RTI
        ;; RESTORE THE PSW
    
```

.SBTTL TRAP TABLE

```

;THIS TABLE CONTAINS THE STARTING ADDRESSES OF THE ROUTINES CALLED
;BY THE "TRAP" INSTRUCTION.
    
```

```

013154 013142
013156 010550
013160 011302
013162 011256
013164 011316
013166 011032
013170 011504
013172 011630
013174 011560
013176 012042
013200 012162
013202 012334
    
```

| WORD | ROUTINE |
|---------|--|
| STRPAD | ROUTINE |
| \$TYPE | CALL=TYPE TRAP+1(104401) TTY TYPEOUT ROUTINE |
| \$TYPOC | CALL=TYPOC TRAP+2(104402) TYPE OCTAL NUMBER (WITH LEADING ZEROS) |
| \$TYPOS | CALL=TYPOS TRAP+3(104403) TYPE OCTAL NUMBER (NO LEADING ZEROS) |
| \$TYPON | CALL=TYPON TRAP+4(104404) TYPE OCTAL NUMBER (AS PER LAST CALL) |
| \$TYPOS | CALL=TYPOS TRAP+5(104405) TYPE DECIMAL NUMBER (WITH SIGN) |
| \$TYPBN | CALL=TYPBN TRAP+6(104406) TYPE BINARY (ASCII) NUMBER |
| SGTSMR | CALL=GTSMR TRAP+7(104407) GET SOFT-SMR SETTING |
| SCKSMR | CALL=CKSMR TRAP+10(104410) TEST FOR CHANGE IN SOFT-SMR |
| SRDCHR | CALL=RDCHR TRAP+11(104411) TTY TYPEIN CHARACTER ROUTINE |
| SRDLIN | CALL=RDLIN TRAP+12(104412) TTY TYPEIN STRING ROUTINE |
| SRDOCT | CALL=RDOCT TRAP+13(104413) READ AN OCTAL NUMBER FROM TTY |

```

2672
2673
2674 013204 047111 040526 044514
2675 013212 020104 040522 020115
2676 013220 042101 051104 051505
2677 013228 020123 040522 043516
2678 013234 027105 200
2679 013237 116 054105 020115
2680 013244 051117 047040 052117
2681 013252 047440 020116 032462
2682 013260 020066 047527 042122
2683 013268 041040 052517 042116
2684 013274 051101 027131 000
2685 013301 111 053116 046101
2686 013306 042111 050040 047522
2687 013314 020115 042101 051104
2688 013322 051505 020123 040522
2689 013330 043516 027105 200
2690 013336 116 054105 026115
2691 013342 044440 041516 051117
2692 013350 042522 052103 051440
2693 013356 055111 025105 047440
2694 013364 020122 047516 020124
2695 013372 047117 030440 020113
2696 013400 047502 047123 040504
2697 013406 054522 000056
2698 013412 047111 040526 044514
2699 013420 020104 052504 050115
2700 013426 051040 047101 042507
2701 013434 000056
2702 013436 040522 020115 042522
2703 013444 042101 053455 044522
2704 013452 042524 042440 051122
2705 013460 051117 000056
2706 013464 042524 022123 004443
2707 013472 051105 050122 004503
2708 013500 042101 051104 043411
2709 013506 047517 004504 040502
2710 013514 000104
2711
2712 013516 001200 001116 001122
2713 013524 001124 001126 000000
2714 013532 040522 020115 042101
2715 013540 051104 051505 020123
2716 013546 051105 047522 027122
2717 013554 000
2718 013555 124 051505 021524
2719 013562 042411 051122 041520
2720 013570 040411 042104 004522
2721 013576 047507 042117 041011
2722 013604 042101 000
2723
2724 013610 001200 001116 001122
2725 013616 001124 001126 000000

```

```

.SBTTL ERROR MESSAGES AND MISCELLANEOUS.
*****
M1: .ASCII /INVALID RAM ADDRESS RANGE./<CRLF>

.ASCIZ /NEXM OR NOT ON 256 WORD BOUNDARY./

M2: .ASCII /INVALID PROM ADDRESS RANGE./<CRLF>

.ASCIZ /NEXM, INCORRECT SIZE, OR NOT ON 1K BOUNDARY./

M3: .ASCIZ /INVALID DUMP RANGE./

EM1: .ASCIZ /RAM READ-WRITE ERROR./

DH1: .ASCIZ /TEST# ERRPC ADDR GOOD BAD/

DH2: .ASCIZ /TEST# ERRPC ADDR GOOD BAD/

DT1: .EVEN
.WORD STESTN,SERRPC,SBDADR,SGDDAT,SBDDAT,0

EM2: .ASCIZ /RAM ADDRESS ERROR./

DT2: .EVEN
.WORD STESTN,SERRPC,SBDADR,SGDDAT,SBDDAT,0

```


| | | | | | | | |
|------|--------|--------|--------|--------|------|--------|---|
| 2726 | 013624 | 040522 | 020115 | 040506 | EM3: | .ASCIZ | /RAM FAST READ ERROR./ |
| 2727 | 013632 | 052123 | 051040 | 040505 | | | |
| 2728 | 013640 | 020104 | 051105 | 047522 | | | |
| 2729 | 013646 | 027122 | 000 | | | | |
| 2730 | 013651 | 124 | 051505 | 021524 | DH3: | .ASCIZ | /TEST# ERRPC ADDR GOOD RAMDAT BUFDAT/ |
| 2731 | 013656 | 042411 | 051122 | 041520 | | | |
| 2732 | 013664 | 040411 | 042104 | 004522 | | | |
| 2733 | 013672 | 047507 | 042117 | 051011 | | | |
| 2734 | 013700 | 046501 | 040504 | 004524 | | | |
| 2735 | 013706 | 052502 | 042106 | 052101 | | | |
| 2736 | 013714 | 000 | | | | | |
| 2737 | | 013716 | | | | | |
| 2738 | 013716 | 001200 | 001116 | 001120 | DT3: | .EVEN | STESTN,SERRPC,SGDADR,SGDADR,SBDADR,SBDDAT,0 |
| 2739 | 013724 | 001120 | 001122 | 001126 | | .WORD | |
| 2740 | 013732 | 000000 | | | | | |
| 2741 | 013734 | 040522 | 020115 | 046123 | EM4: | .ASCIZ | /RAM SLIDING BIT. ADDR-2 WAS ALTERED./ |
| 2742 | 013742 | 042111 | 047111 | 020107 | | | |
| 2743 | 013750 | 044502 | 027124 | 020040 | | | |
| 2744 | 013756 | 042101 | 051104 | 031055 | | | |
| 2745 | 013764 | 053440 | 051501 | 040440 | | | |
| 2746 | 013772 | 052114 | 051105 | 042105 | | | |
| 2747 | 014000 | 000056 | | | | | |
| 2748 | 014002 | 042524 | 052123 | 004443 | DH4: | .ASCIZ | /TEST# ERRPC ADDR PATRN ADR-2 GOOD BAD/ |
| 2749 | 014010 | 051105 | 050122 | 004503 | | | |
| 2750 | 014016 | 042101 | 051104 | 050011 | | | |
| 2751 | 014024 | 052101 | 047122 | 040411 | | | |
| 2752 | 014032 | 051104 | 031055 | 043411 | | | |
| 2753 | 014040 | 047517 | 004504 | 040502 | | | |
| 2754 | 014046 | 000104 | | | | | |
| 2755 | | | | | | | |
| 2756 | 014050 | 001200 | 001116 | 001120 | DT4: | .EVEN | STESTN,SERRPC,SGDADR,SGDDAT,SBDADR,FILLR,SBDDAT,0 |
| 2757 | 014056 | 001124 | 001122 | 005060 | | .WORD | |
| 2758 | 014064 | 001126 | 000000 | | | | |
| 2759 | 014070 | 040522 | 020115 | 046123 | EM5: | .ASCIZ | /RAM SLIDING BIT. ADDR+2 WAS ALTERED./ |
| 2760 | 014076 | 042111 | 047111 | 020107 | | | |
| 2761 | 014104 | 044502 | 027124 | 020040 | | | |
| 2762 | 014112 | 042101 | 051104 | 031053 | | | |
| 2763 | 014120 | 053440 | 051501 | 040440 | | | |
| 2764 | 014126 | 052114 | 051105 | 042105 | | | |
| 2765 | 014134 | 000056 | | | | | |
| 2766 | 014136 | 042524 | 052123 | 004443 | DH5: | .ASCIZ | /TEST# ERRPC ADDR PATRN ADR+2 GOOD BAD/ |
| 2767 | 014144 | 051105 | 050122 | 004503 | | | |
| 2768 | 014152 | 042101 | 051104 | 050011 | | | |
| 2769 | 014160 | 052101 | 047122 | 040411 | | | |
| 2770 | 014166 | 051104 | 031053 | 043411 | | | |
| 2771 | 014174 | 047517 | 004504 | 040502 | | | |
| 2772 | 014202 | 000104 | | | | | |
| 2773 | | | | | | | |
| 2774 | 014204 | 001200 | 001116 | 001120 | DT5: | .EVEN | STESTN,SERRPC,SGDADR,SGDDAT,SBDADR,FILLR,SBDDAT,0 |
| 2775 | 014212 | 001124 | 001122 | 005060 | | .WORD | |
| 2776 | 014220 | 001126 | 000000 | | | | |
| 2777 | 014224 | 040522 | 020115 | 046101 | EM6: | .ASCIZ | /RAM ALTERNATE BIT PATTERN ERROR./ |
| 2778 | 014232 | 042524 | 047122 | 052101 | | | |
| 2779 | 014240 | 020105 | 044502 | 020124 | | | |

| | | | | | | | | | | | | |
|------|--------|--------|--------|--------|-------|--------|-----------|------------|---------|---------|---------|----------|
| 2780 | 014246 | 040520 | 052124 | 051105 | | | | | | | | |
| 2781 | 014254 | 020116 | 051105 | 047522 | | | | | | | | |
| 2782 | 014263 | 027122 | 000 | | | | | | | | | |
| 2783 | 014265 | 124 | 051505 | 021524 | DH6: | .ASCIZ | /TEST# | ERRPC | ADDR | GOOD | BAD/ | |
| 2784 | 014272 | 042411 | 051122 | 041520 | | | | | | | | |
| 2785 | 014300 | 040411 | 042104 | 004522 | | | | | | | | |
| 2786 | 014306 | 047507 | 042117 | 041011 | | | | | | | | |
| 2787 | 014314 | 042101 | 000 | | | | | | | | | |
| 2788 | | 014320 | | | | | | | | | | |
| 2789 | 014320 | 001200 | 001116 | 001120 | DT6: | .EVEN | | | | | | |
| 2790 | 014326 | 001124 | 001126 | 000000 | | .WORD | STESTN, | SERRPC, | SGDADR, | SGDDAT, | SBDAT,0 | |
| 2791 | 014334 | 040522 | 020115 | 054502 | EM7: | .ASCIZ | /RAM | BYTE | TEST | ERROR./ | | |
| 2792 | 014342 | 040524 | 052040 | 051505 | | | | | | | | |
| 2793 | 014350 | 020124 | 051105 | 047522 | | | | | | | | |
| 2794 | 014356 | 027122 | 000 | | | | | | | | | |
| 2795 | 014361 | 124 | 051505 | 021524 | DH7: | .ASCIZ | /TEST# | ERRPC | ADDR | GOOD | BAD | BYTE/ |
| 2796 | 014366 | 042411 | 051122 | 041520 | | | | | | | | |
| 2797 | 014374 | 040411 | 042104 | 004522 | | | | | | | | |
| 2798 | 014402 | 047507 | 042117 | 041011 | | | | | | | | |
| 2799 | 014410 | 042101 | 041011 | 052131 | | | | | | | | |
| 2800 | 014416 | 000105 | | | | | | | | | | |
| 2801 | | | | | | | | | | | | |
| 2802 | 014420 | 001200 | 001116 | 001120 | DT7: | .EVEN | | | | | | |
| 2803 | 014426 | 001124 | 001126 | 001122 | | .WORD | STESTN, | SERRPC, | SGDADR, | SGDDAT, | SBDAT, | SBDADR,0 |
| 2804 | 014434 | 000000 | | | | | | | | | | |
| 2805 | 014436 | 051120 | 046517 | 051040 | EM10: | .ASCIZ | /PROM | READ | ERROR./ | | | |
| 2806 | 014444 | 040505 | 020104 | 051105 | | | | | | | | |
| 2807 | 014452 | 047522 | 027122 | 000 | | | | | | | | |
| 2808 | 014457 | 124 | 051505 | 021524 | DH10: | .ASCIZ | /TEST# | ERRPC | ADDR | GOOD | BAD/ | |
| 2809 | 014464 | 042411 | 051122 | 041520 | | | | | | | | |
| 2810 | 014472 | 040411 | 042104 | 004522 | | | | | | | | |
| 2811 | 014500 | 047507 | 042117 | 041011 | | | | | | | | |
| 2812 | 014506 | 042101 | 000 | | | | | | | | | |
| 2813 | | 014512 | | | | | | | | | | |
| 2814 | 014512 | 001200 | 001116 | 001122 | DT10: | .EVEN | | | | | | |
| 2815 | 014520 | 001124 | 001126 | 000000 | | .WORD | STESTN, | SERRPC, | SBDADR, | SGDDAT, | SBDAT,0 | |
| 2816 | 014526 | 051120 | 046517 | 051055 | EM11: | .ASCIZ | /PROM-RAM | COPY | ERROR./ | | | |
| 2817 | 014534 | 046501 | 041440 | 050117 | | | | | | | | |
| 2818 | 014542 | 020131 | 051105 | 047522 | | | | | | | | |
| 2819 | 014550 | 027122 | 000 | | | | | | | | | |
| 2820 | 014553 | 124 | 051505 | 021524 | DH11: | .ASCIZ | /TEST# | ERRPC | PRMADD | PRMDAT | RAMADD | RAMDAT/ |
| 2821 | 014560 | 042411 | 051122 | 041520 | | | | | | | | |
| 2822 | 014566 | 050011 | 046522 | 042101 | | | | | | | | |
| 2823 | 014574 | 004504 | 051120 | 042115 | | | | | | | | |
| 2824 | 014602 | 052101 | 051011 | 046501 | | | | | | | | |
| 2825 | 014610 | 042101 | 004504 | 040522 | | | | | | | | |
| 2826 | 014616 | 042115 | 052101 | 000 | | | | | | | | |
| 2827 | | 014624 | | | | | | | | | | |
| 2828 | 014624 | 001200 | 001116 | 001120 | DT11: | .EVEN | | | | | | |
| 2829 | 014632 | 001124 | 001122 | 001126 | | .WORD | STESTN, | SERRPC, | SGDADR, | SGDDAT, | SBDADR, | SBDAT,0 |
| 2830 | 014640 | 000000 | | | | | | | | | | |
| 2831 | 014642 | 051120 | 046517 | 053440 | EM12: | .ASCIZ | /PROM | WRITE-TRAP | ERROR./ | | | |
| 2832 | 014650 | 044522 | 042524 | 052055 | | | | | | | | |
| 2833 | 014656 | 040522 | 020120 | 051105 | | | | | | | | |

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2834 014664 047522 027122 000
2835 014671 124 051505 021524 DH12: .ASCIZ /TEST# ERRPC ADDR DATWAS DATIS/
2836 014676 042411 051122 041520
2837 014704 040411 042104 004522
2838 014712 040504 053524 051501
2839 014720 042011 052101 051511
2840 014726 000
2841 014730 014730
2842 014730 001200 001116 001122 DT12: .EVEN
2843 014736 001124 001126 000000 .WORD $TESTN,$ERRPC,$BADDR,$GDDAT,$BDDAT,0
2844 014744 052502 020123 044524 EM13: .ASCIZ /BUS TIME-OUT (TRAP) ERROR./
2845 014752 042515 047455 052125
2846 014760 024040 051124 050101
2847 014766 020051 051105 047522
2848 014774 027122 000
2849 014777 124 051505 021524 DH13: .ASCIZ /TEST# TRAPPC TRAPPS/
2850 015004 052011 040522 050120
2851 015012 004503 051124 050101
2852 015020 051520 000
2853 015024 015024
2854 015024 001200 001122 001126 DT13: .EVEN
2855 015032 000000 .WORD $TESTN,$BADDR,$BDDAT,0
2856 015034 042515 047515 054522 EM14: .ASCII /MEMORY MAP ERROR./<15><12>/BANK /
2857 015042 046440 050101 042440
2858 015050 051122 051117 006456
2859 015056 041012 047101 020113
2860 015064 020060 040
2861 015067 055 026455 026455 EM14A: .ASCII /0 /
2862 015074 026455 026455 026455 EM14B: .ASCIZ /-----/
2863 015102 026455 026455 000055
2864
2865
2866 015110 000000 000000 DF: .EVEN
2867 015114 000055 DASH: .WORD 0,0 ;OCTAL FORMAT FOR ALL.
2868 015116 000122 R: .ASCIZ /-/
2869 015120 000127 W: .ASCIZ /W/
2870
2871 ;DATA BUFFER FOR RAM FAST ADDRESS TEST.
2872
2873 015122 000400 IBUF: .BLKW 256.
2874
2875
2876 016122 000240 ;;*****
2877 000001 NOP ;END. HERE THRU 17776 IS FREE MEMORY.
.END

```

| | | | | | | | | |
|-----------------|-------|-------|-------|-------|------|------|-------|--|
| ABASE = 020000 | 502# | 731 | 772 | | | | | |
| ACOM1 = 140000 | 503# | 731 | 774 | | | | | |
| ACOM2 = 000004 | 504# | 731 | 775 | | | | | |
| ACPUOP = 000000 | 731 | 746 | | | | | | |
| ADDRS = 007166 | 1815# | 1848# | 1849# | 1860 | 1873 | 1880 | 1887# | |
| ADDND = 000000 | 731 | | | | | | | |
| ADDN1 = 000000 | 731 | | | | | | | |
| ADDN10 = 000000 | 731 | | | | | | | |
| ADDN11 = 000000 | 731 | | | | | | | |
| ADDN12 = 000000 | 731 | | | | | | | |
| ADDN13 = 000000 | 731 | | | | | | | |
| ADDN14 = 000000 | 731 | | | | | | | |
| ADDN15 = 000000 | 731 | | | | | | | |
| ADDN2 = 000000 | 731 | | | | | | | |
| ADDN3 = 000000 | 731 | | | | | | | |
| ADDN4 = 000000 | 731 | | | | | | | |
| ADDN5 = 000000 | 731 | | | | | | | |
| ADDN6 = 000000 | 731 | | | | | | | |
| ADDN7 = 000000 | 731 | | | | | | | |
| ADDN8 = 000000 | 731 | | | | | | | |
| ADDN9 = 000000 | 731 | | | | | | | |
| ADEVCT = 000000 | 731 | 737 | | | | | | |
| ADEVN = 000000 | 731 | 773 | | | | | | |
| ARENV = 000000 | 731 | 742 | | | | | | |
| RENVM = 000000 | 731 | 743 | | | | | | |
| AFATAL = 000000 | 731 | 734 | | | | | | |
| AGAIN = 002624 | 1026# | 1688 | | | | | | |
| AMADR1 = 000000 | 731 | 759 | | | | | | |
| AMADR2 = 000000 | 731 | 763 | | | | | | |
| AMADR3 = 000000 | 731 | 766 | | | | | | |
| AMADR4 = 000000 | 731 | 769 | | | | | | |
| AMARS1 = 000000 | 731 | 753 | | | | | | |
| AMARS2 = 000000 | 731 | 761 | | | | | | |
| AMARS3 = 000000 | 731 | 764 | | | | | | |
| AMARS4 = 000000 | 731 | 767 | | | | | | |
| AMSGAD = 000000 | 731 | 739 | | | | | | |
| AMSGLC = 000000 | 731 | 740 | | | | | | |
| AMSGTY = 000000 | 731 | 733 | | | | | | |
| AMTYP1 = 000000 | 731 | 754 | | | | | | |
| AMTYP2 = 000000 | 731 | 762 | | | | | | |
| AMTYP3 = 000000 | 731 | 765 | | | | | | |
| AMTYP4 = 000000 | 731 | 768 | | | | | | |
| APASS = 000000 | 731 | 736 | | | | | | |
| APRIOR = 000000 | 731 | | | | | | | |
| APTCSU = 000740 | 2119 | 2627# | | | | | | |
| APTEW = 000001 | 2019 | 2112 | 2583 | 2625# | | | | |
| APTSIZ = 000200 | 880 | 2624# | | | | | | |
| APTSPO = 000100 | 2114 | 2585 | 2626# | | | | | |
| ASIREG = 000000 | 731 | 744 | | | | | | |
| ATESTN = 000000 | 731 | 735 | | | | | | |
| AUNIT = 000000 | 731 | 738 | | | | | | |
| AUSMR = 000000 | 731 | 745 | | | | | | |
| AVECT1 = 000000 | 731 | 770 | | | | | | |
| AVECT2 = 000000 | 731 | 771 | | | | | | |

F06

LSI-11 UVPROM-RAM TEST. MD-11-DVMRA-A. MACY11 27(663) 24-MAR-77 13:51 PAGE 69
DVMRAA.P11 CROSS REFERENCE TABLE

| | | | | | | | | | | | | | | |
|--------|----------|-------|-------|-------|-------|-------|-------|-------|-------|-------|-------|-------|-------|-------|
| DDUMP | 007174 | 536 | 1819# | | | | | | | | | | | |
| PATRN | 003416 | 1161# | 1164# | 1170# | 1173 | 1180 | 1189 | 1197 | | | | | | |
| PC | =x000007 | 569# | 886# | 951# | 1018# | 1100# | 1162# | 1165# | 1186# | 1278# | 1288# | 1336# | 1426# | 1430# |
| | | 1434# | 1438# | 1471# | 1669# | 1672# | 1682# | 1687 | 1706# | 1707# | 1794# | 1796# | 1821# | 1833# |
| | | 1865# | 2016# | 2022# | 2079# | 2117# | 2136# | 2143# | 2150# | 2164# | 2166# | 2390# | 2602# | 2619# |
| | | 555# | | | | | | | | | | | | |
| PIRQ | = 177772 | 649# | | | | | | | | | | | | |
| PIRQVE | = 000240 | 834# | 923# | 931 | 1002# | 1004# | 1005 | 1008 | 1489 | 1542 | 1613 | | | |
| PRM | 001424 | 835# | 926# | 929# | 931# | 1017# | 1513 | 1564 | | | | | | |
| PRMEND | 001426 | 836# | 924# | 970# | 971# | 973 | 975# | 978 | | | | | | |
| PRMSZ | 001430 | 837# | 922# | 972# | 980# | 1581 | | | | | | | | |
| PRMCK | 001432 | | | | | | | | | | | | | |
| PR0 | = 000000 | 572# | | | | | | | | | | | | |
| PR1 | = 000040 | 573# | | | | | | | | | | | | |
| PR2 | = 000100 | 574# | | | | | | | | | | | | |
| PR3 | = 000140 | 575# | | | | | | | | | | | | |
| PR4 | = 000200 | 576# | | | | | | | | | | | | |
| PR5 | = 000240 | 577# | | | | | | | | | | | | |
| PR6 | = 000300 | 578# | | | | | | | | | | | | |
| PR7 | = 000340 | 579# | | | | | | | | | | | | |
| PS | = 177776 | 552# | 553 | | | | | | | | | | | |
| PSM | = 177776 | 553# | | | | | | | | | | | | |
| PTRN | 005634 | 1425# | 1429# | 1437# | 1444# | 1464 | 1477 | | | | | | | |
| PVRVEC | = 000024 | 644# | 855# | 856# | 2532# | 2533# | 2542# | 2548# | 2560# | 2561# | | | | |
| R | 015116 | 1075 | 2868# | | | | | | | | | | | |
| RAM | 001420 | 832# | 918# | 942# | 944# | 945 | 947 | 1172 | 1179 | 1206 | 1214 | 1239 | 1253 | 1296 |
| | | 1304 | 1311 | 1376 | 1387 | 1398 | 1447 | 1454 | 1490 | 1504 | 1516 | 1544 | 1615 | |
| | | 833# | 919# | 920# | 950# | 1241 | 1297 | 1377 | | | | | | |
| | | 2449 | 2669# | | | | | | | | | | | |
| | | 968 | 2494 | 2670# | | | | | | | | | | |
| | | 941 | 1001 | 1847 | 1856 | 2671# | | | | | | | | |
| | | 1050# | 1715 | | | | | | | | | | | |
| | | 639# | | | | | | | | | | | | |
| | | 1042# | 1052# | 1061# | 1070# | 1079# | 1777# | | | | | | | |
| | | 534 | 933 | 1021 | 1025# | | | | | | | | | |
| | | 1060# | 1717 | | | | | | | | | | | |
| | | 1069# | 1722 | | | | | | | | | | | |
| | | 560# | 969# | 970 | 1049# | 1050 | 1102# | 1105# | 1106 | 1107 | 1112 | 1114 | 1115 | 1122 |
| | | 1128 | 1130 | 1131 | 1132 | 1133 | 1679# | 1682 | 2051 | 2052# | 2053# | 2060# | 2061# | 2062# |
| | | 2063# | 2064# | 2065 | 2070 | 2075# | 2077# | 2081 | 2083 | 2110 | 2111# | 2116 | 2121 | 2124# |
| | | 2181 | 2191# | 2195 | 2211 | 2212 | 2225# | 2491 | 2495# | 2496 | 2499 | 2519# | 2522# | 2534 |
| | | 2559# | 2579 | 2587# | 2591 | 2592 | 2594# | 2595# | 2596 | 2618# | 2636 | 2637# | 2638 | 2639# |
| | | 2640# | 2641# | 2642# | | | | | | | | | | |
| | | 561# | 925# | 927# | 947# | 949 | 1008# | 1016 | 1122# | 1123# | 1124# | 1125# | 1126 | 1135# |
| | | 1141# | 1143# | 1145# | 1149# | 1150 | 1171# | 1174# | 1177# | 1183# | 1205# | 1209# | 1213# | 1218# |
| | | 1239# | 1244 | 1250 | 1253# | 1255 | 1258 | 1263 | 1265 | 1306# | 1307 | 1308 | 1324# | 1328# |
| | | 1333# | 1342 | 1379# | 1381 | 1382# | 1388 | 1391# | 1399 | 1401# | 1411 | 1415 | 1424# | 1428# |
| | | 1433# | 1436# | 1455# | 1459# | 1461# | 1467# | 1476 | 1489# | 1495 | 1497# | 1499 | 1503# | 1505 |
| | | 1509 | 1513 | 1515 | 1525 | 1526 | 1532 | 1542# | 1544 | 1546# | 1548 | 1549# | 1555 | 1557 |
| | | 1564 | 1566 | 1612# | 1618# | 1620 | 1622# | 1625 | 1627 | 1629 | 1631 | 1635# | 1637# | 1639# |
| | | 1649 | 1703 | 1860# | 1864 | 1877# | 1891# | 2182 | 2195# | 2196 | 2200 | 2224# | 2321 | 2322# |
| | | 2325# | 2331# | 2492 | 2497# | 2505# | 2507# | 2509# | 2512# | 2515 | 2518# | 2535 | 2558# | 2580 |
| | | 2617# | | | | | | | | | | | | |
| | | 562# | 948# | 949# | 950 | 1009# | 1013# | 1016# | 1017 | 1172# | 1173# | 1179# | 1180 | 1190 |
| R2 | =x000002 | 1191 | 1196# | 1197# | 1206# | 1207# | 1208 | 1214# | 1215 | 1217 | 1225 | 1226 | 1227 | 1231# |

| | | |
|--------|------|------|
| .SERRT | 4878 | 2042 |
| .SPOWE | 4878 | 2528 |
| .SRDOC | 4878 | 2475 |
| .SREAO | 4878 | 2336 |
| .SSCOP | 4878 | 1902 |
| .STRAP | 4878 | 2628 |
| .STYPB | 4878 | 2312 |
| .STYPD | 4878 | 2168 |
| .STYPE | 4878 | 2089 |
| .STYPO | 4878 | 2235 |

| | | | | | | | | | | | | | | | | | |
|--------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|--|--|
| .BLKB | 2869 | | | | | | | | | | | | | | | | |
| .BLKH | 2468 | | | | | | | | | | | | | | | | |
| .BYTE | 1783 | 1785 | 2234 | 2873 | | | | | | | | | | | | | |
| | 693 | 694 | 699 | 700 | 708 | 709 | 717 | 718 | 719 | 720 | 742 | 743 | 753 | 754 | 761 | | |
| | 762 | 764 | 765 | 767 | 768 | 1689 | 1781 | 1875 | 1876 | 1899 | 2023 | 2024 | 2307 | 2308 | 2309 | | |
| | 2310 | 2335 | 2466 | 2467 | 2620 | 2621 | 2622 | | | | | | | | | | |
| .DSABL | 1155 | 1571 | 2406 | | | | | | | | | | | | | | |
| .ENABL | 487 | 1034 | 1540 | 2339 | | | | | | | | | | | | | |
| .END | 2877 | | | | | | | | | | | | | | | | |
| .ENDC | 493 | 512 | 514 | 515 | 516 | 525 | 544 | 636 | 650 | 654 | 658 | 660 | 665 | 667 | 674 | | |
| | 687 | 691 | 693 | 721 | 722 | 723 | 724 | 728 | 731 | 753 | 761 | 764 | 767 | 770 | 771 | | |
| | 772 | 773 | 774 | 775 | 778 | 840 | 847 | 848 | 851 | 853 | 855 | 857 | 858 | 859 | 861 | | |
| | 863 | 884 | 892 | 894 | 900 | 906 | 908 | 940 | 967 | 1000 | 1028 | 1029 | 1030 | 1031 | 1032 | | |
| | 1118 | 1119 | 1155 | 1157 | 1158 | 1159 | 1160 | 1161 | 1167 | 1189 | 1200 | 1201 | 1202 | 1203 | 1204 | | |
| | 1205 | 1223 | 1225 | 1234 | 1235 | 1236 | 1237 | 1238 | 1239 | 1261 | 1263 | 1271 | 1272 | 1273 | 1274 | | |
| | 1275 | 1276 | 1282 | 1283 | 1284 | 1285 | 1286 | 1291 | 1339 | 1371 | 1372 | 1373 | 1374 | 1375 | 1376 | | |
| | 1406 | 1408 | 1418 | 1419 | 1420 | 1421 | 1422 | 1423 | 1441 | 1474 | 1484 | 1485 | 1486 | 1487 | 1488 | | |
| | 1489 | 1523 | 1525 | 1535 | 1536 | 1537 | 1538 | 1539 | 1540 | 1553 | 1562 | 1571 | 1573 | 1574 | 1575 | | |
| | 1576 | 1577 | 1646 | 1648 | 1655 | 1658 | 1659 | 1660 | 1662 | 1665 | 1671 | 1674 | 1675 | 1679 | 1681 | | |
| | 1687 | 1689 | 1690 | 1693 | 1695 | 1711 | 1747 | 1789 | 1801 | 1826 | 1838 | 1846 | 1855 | 1905 | 1908 | | |
| | 1913 | 1918 | 1920 | 1931 | 1934 | 1946 | 1948 | 1950 | 1957 | 1961 | 1966 | 1968 | 1972 | 1975 | 1976 | | |
| | 1988 | 1991 | 1994 | 2004 | 2011 | 2016 | 2017 | 2018 | 2026 | 2037 | 2041 | 2042 | 2045 | 2060 | 2089 | | |
| | 2092 | 2121 | 2171 | 2238 | 2315 | 2339 | 2340 | 2342 | 2370 | 2406 | 2410 | 2438 | 2439 | 2446 | 2448 | | |
| | 2451 | 2453 | 2469 | 2475 | 2478 | 2484 | 2528 | 2531 | 2540 | 2541 | 2547 | 2553 | 2554 | 2564 | 2571 | | |
| | 2574 | 2575 | 2578 | 2605 | 2620 | 2631 | 2637 | 2640 | 2659 | 2660 | 2661 | 2662 | 2663 | 2664 | 2665 | | |
| | 2666 | 2667 | 2668 | 2669 | 2670 | 2671 | 2672 | 2674 | 2876 | | | | | | | | |
| .EQUIV | 544 | 545 | 553 | 598 | 599 | 600 | 601 | 602 | 603 | 604 | 605 | 606 | 607 | 626 | 627 | | |
| | 628 | 629 | 630 | 631 | 632 | 633 | 634 | 635 | | | | | | | | | |
| .EVEN | 731 | 908 | 940 | 967 | 1000 | 1826 | 1838 | 1846 | 1855 | 2088 | 2570 | 2623 | 2711 | 2723 | 2737 | | |
| | 2755 | 2773 | 2788 | 2801 | 2813 | 2827 | 2841 | 2853 | 2865 | | | | | | | | |
| .IF | 489 | 512 | 513 | 514 | 515 | 516 | 525 | 542 | 608 | 636 | 653 | 656 | 658 | 664 | 666 | | |
| | 673 | 686 | 690 | 692 | 721 | 722 | 723 | 727 | 728 | 730 | 753 | 761 | 764 | 767 | 770 | | |
| | 771 | 772 | 773 | 774 | 775 | 776 | 778 | 840 | 842 | 847 | 849 | 851 | 853 | 855 | 857 | | |
| | 858 | 859 | 861 | 879 | 891 | 892 | 893 | 895 | 898 | 907 | 939 | 966 | 999 | 1027 | 1029 | | |
| | 1031 | 1032 | 1117 | 1118 | 1154 | 1156 | 1158 | 1160 | 1161 | 1166 | 1188 | 1199 | 1200 | 1202 | 1204 | | |
| | 1205 | 1222 | 1224 | 1233 | 1234 | 1236 | 1238 | 1239 | 1260 | 1262 | 1270 | 1271 | 1273 | 1275 | 1276 | | |
| | 1281 | 1283 | 1285 | 1286 | 1290 | 1338 | 1370 | 1371 | 1373 | 1375 | 1376 | 1405 | 1407 | 1417 | 1418 | | |
| | 1420 | 1422 | 1423 | 1440 | 1473 | 1483 | 1484 | 1486 | 1488 | 1489 | 1522 | 1524 | 1534 | 1535 | 1537 | | |
| | 1539 | 1540 | 1552 | 1561 | 1570 | 1572 | 1574 | 1576 | 1577 | 1645 | 1647 | 1654 | 1657 | 1658 | 1659 | | |
| | 1660 | 1661 | 1662 | 1664 | 1670 | 1673 | 1675 | 1679 | 1681 | 1687 | 1689 | 1690 | 1694 | 1710 | 1746 | | |
| | 1788 | 1800 | 1825 | 1837 | 1845 | 1854 | 1904 | 1907 | 1912 | 1918 | 1930 | 1932 | 1933 | 1934 | 1946 | | |
| | 1947 | 1948 | 1957 | 1959 | 1967 | 1969 | 1974 | 1975 | 1976 | 1990 | 1993 | 2004 | 2007 | 2014 | 2016 | | |
| | 2017 | 2019 | 2026 | 2030 | 2037 | 2041 | 2042 | 2044 | 2059 | 2075 | 2091 | 2112 | 2170 | 2237 | 2314 | | |
| | 2338 | 2340 | 2341 | 2342 | 2370 | 2409 | 2410 | 2438 | 2446 | 2447 | 2451 | 2452 | 2468 | 2469 | 2475 | | |
| | 2477 | 2480 | 2496 | 2530 | 2540 | 2541 | 2546 | 2553 | 2554 | 2562 | 2564 | 2568 | 2573 | 2575 | 2578 | | |
| | 2605 | 2620 | 2630 | 2636 | 2640 | 2651 | 2660 | 2661 | 2662 | 2663 | 2664 | 2666 | 2668 | 2669 | 2670 | | |
| | 2671 | 2672 | 2673 | 2875 | | | | | | | | | | | | | |
| .IFF | 512 | 514 | 515 | 516 | 542 | 654 | 658 | 660 | 665 | 667 | 674 | 687 | 690 | 693 | 721 | | |
| | 728 | 731 | 847 | 891 | 893 | 1027 | 1028 | 1029 | 1030 | 1031 | 1118 | 1119 | 1155 | 1157 | 1158 | | |
| | 1159 | 1160 | 1167 | 1189 | 1200 | 1201 | 1202 | 1203 | 1204 | 1223 | 1225 | 1234 | 1235 | 1236 | 1237 | | |
| | 1238 | 1261 | 1263 | 1271 | 1272 | 1273 | 1274 | 1275 | 1282 | 1283 | 1284 | 1285 | 1291 | 1339 | 1371 | | |
| | 1372 | 1373 | 1374 | 1375 | 1406 | 1408 | 1418 | 1419 | 1420 | 1421 | 1422 | 1441 | 1474 | 1484 | 1485 | | |
| | 1486 | 1487 | 1488 | 1523 | 1525 | 1535 | 1536 | 1537 | 1538 | 1539 | 1553 | 1562 | 1571 | 1573 | 1574 | | |
| | 1575 | 1576 | 1645 | 1648 | 1655 | 1658 | 1661 | 1665 | 1671 | 1674 | 1689 | 1695 | 1711 | 1747 | 1789 | | |

| | | | | | | | | | | | | | | | |
|--------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|
| | 1801 | 1905 | 1931 | 1934 | 1946 | 1948 | 1975 | 1976 | 1991 | 1993 | 2007 | 2037 | 2042 | 2045 | 2060 |
| | 2089 | 2092 | 2171 | 2238 | 2315 | 2339 | 2342 | 2410 | 2412 | 2417 | 2438 | 2439 | 2448 | 2452 | 2469 |
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| .IIF | 488 | 493 | 498 | 499 | 509 | 510 | 511 | 512 | 515 | 516 | 522 | 727 | 731 | 848 | 851 |
| | 857 | 858 | 859 | 861 | 862 | 892 | 1659 | 1665 | 1666 | 1677 | 1689 | 1693 | 1908 | 1909 | 1910 |
| | 1911 | 1912 | 1913 | 1917 | 1936 | 1955 | 1956 | 1972 | 1975 | 1976 | 1994 | 1995 | 1996 | 1997 | 1998 |
| | 2003 | 2029 | 2037 | 2042 | 2057 | 2082 | 2168 | 2339 | 2360 | 2461 | 2469 | 2475 | 2528 | 2659 | 2660 |
| | 2661 | 2662 | 2663 | 2664 | 2666 | 2668 | 2669 | 2670 | 2671 | | | | | | |
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| | 1739 | 1848 | 1857 | 1873 | 1880 | 1937 | 1977 | 1978 | 1979 | 1980 | 1981 | 1982 | 1983 | 1984 | 1985 |
| | 1986 | 1987 | 2181 | 2221 | 2491 | 2517 | 2534 | 2540 | 2553 | 2554 | 2579 | 2580 | 2601 | 2617 | 2618 |
| .LIST | 1 | 487 | 515 | 522 | 650 | 721 | 728 | 731 | 840 | 863 | 892 | 895 | 908 | 940 | 967 |
| | 1000 | 1027 | 1031 | 1156 | 1160 | 1200 | 1204 | 1234 | 1238 | 1250 | 1271 | 1275 | 1281 | 1285 | 1371 |
| | 1375 | 1418 | 1422 | 1484 | 1488 | 1535 | 1539 | 1572 | 1576 | 1665 | 1681 | 1826 | 1838 | 1846 | 1855 |
| | 1912 | 1976 | 1978 | 1979 | 1980 | 1981 | 1982 | 1983 | 1984 | 1985 | 1986 | 1987 | 1988 | 2037 | 2438 |
| | 2651 | 2659 | 2660 | 2661 | 2662 | 2663 | 2664 | 2665 | 2666 | 2667 | 2668 | 2669 | 2670 | 2671 | 2672 |
| .MACRO | 516 | 684 | 879 | 2651 | | | | | | | | | | | |
| .MCALL | 487 | 650 | 728 | 863 | 895 | | | | | | | | | | |
| .MEXIT | 777 | | | | | | | | | | | | | | |
| .MLIST | 1 | 487 | 515 | 522 | 650 | 721 | 728 | 731 | 840 | 863 | 892 | 895 | 908 | 940 | 967 |
| | 1000 | 1027 | 1031 | 1156 | 1160 | 1200 | 1204 | 1234 | 1238 | 1250 | 1271 | 1275 | 1281 | 1285 | 1371 |
| | 1375 | 1418 | 1422 | 1484 | 1488 | 1535 | 1539 | 1572 | 1576 | 1665 | 1681 | 1826 | 1838 | 1846 | 1855 |
| | 1912 | 1976 | 1978 | 1979 | 1980 | 1981 | 1982 | 1983 | 1984 | 1985 | 1986 | 1987 | 1988 | 2037 | 2438 |
| | 2651 | 2659 | 2660 | 2661 | 2662 | 2663 | 2664 | 2665 | 2666 | 2667 | 2668 | 2669 | 2670 | 2671 | 2672 |
| .PAGE | 516 | 684 | 778 | 831 | 884 | 934 | 1025 | 1076 | 1118 | 1156 | 1200 | 1234 | 1271 | 1371 | 1418 |
| | 1484 | 1535 | 1572 | 1655 | 1693 | 1746 | 1800 | 1901 | 1988 | 2042 | 2089 | 2168 | 2235 | 2312 | 2336 |
| | 2475 | 2528 | 2571 | 2628 | 2672 | | | | | | | | | | |
| .REM | 1 | | | | | | | | | | | | | | |
| .REPT | 522 | 1250 | 1977 | | | | | | | | | | | | |
| .SBTTL | 505 | 516 | 540 | 651 | 662 | 684 | 728 | 778 | 831 | 841 | 888 | 895 | 909 | 1027 | 1156 |
| | 1200 | 1234 | 1271 | 1281 | 1371 | 1418 | 1484 | 1535 | 1572 | 1655 | 1693 | 1901 | 1902 | 1988 | 2042 |
| | 2089 | 2168 | 2235 | 2312 | 2336 | 2475 | 2528 | 2571 | 2628 | 2651 | 2672 | | | | |
| .TITLE | 488 | | | | | | | | | | | | | | |
| .WORD | 522 | 523 | 524 | 527 | 530 | 659 | 678 | 679 | 680 | 681 | 682 | 683 | 692 | 695 | 696 |
| | 697 | 698 | 701 | 702 | 703 | 704 | 705 | 706 | 707 | 710 | 711 | 712 | 733 | 734 | 735 |
| | 736 | 737 | 738 | 739 | 740 | 744 | 745 | 746 | 759 | 763 | 766 | 769 | 770 | 771 | 772 |
| | 773 | 774 | 775 | 1670 | 1673 | 1688 | 1777 | 1778 | 1779 | 1780 | 1977 | 1978 | 1979 | 1980 | 1981 |
| | 1982 | 1983 | 1984 | 1985 | 1986 | 1987 | 2068 | 2073 | 2118 | 2165 | 2311 | 2524 | 2527 | 2563 | 2603 |
| | 2658 | 2712 | 2724 | 2738 | 2756 | 2774 | 2789 | 2802 | 2814 | 2828 | 2842 | 2854 | 2866 | | |

ERRORS DETECTED: 0

#DVMRAA.BIC,DVMRAA.SEG/SOL/NL:TOC=DVMRAA.P11
RUN-TIME: 26 10 1 SECONDS
CORE USED: 26K