

RH70

CONTROLLER DIAGNOSTIC
MD-11-DERHA-B

EP-DERHA-B-DL-A

NOV 1976

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FICHE 1 OF 2

MADE IN USA

The main body of the document is a large grid of 20 columns and 20 rows of small, rectangular data blocks. Each block contains technical information, likely diagnostic test results or component specifications, arranged in a structured, tabular format. The text within these blocks is too small to be legible but appears to follow a consistent layout across the grid.

RH70

CONTROLLER DIAGNOSTIC
MD-11-DERHA-B

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FICHE 2 OF 2

MADE IN USA

IDENTIFICATION

PRODUCT CODE: MAINDEC-11-DERHA-B-D
PRODUCT NAME: RH70 FUNCTIONAL CONTROLLER TEST
DATE RELEASED: AUGUST, 1976
MAINTAINER: DIAGNOSTIC GROUP

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THIS PROGRAM WAS ASSEMBLED USING THE PDP-11 MAINDEC SYSMAC
PACKAGE (MAINDEC-11-DZQAC-A4).

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OPERATIONAL SWITCH SETTINGS

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SWITCH	USE
15	HALT ON ERROR
14	LOOP ON TEST
13	INHIBIT ERROR TYPEOUTS
11	INHIBIT ITERATIONS
10	BELL ON ERROR
9	LOOP ON ERROR
8	LOOP ON TEST IN SWR(7:0)
7	STOP FURTHER COMPARES IF SW08 IS LOW
6	TYPE ALL REG. WITH ERROR IF SW8 LOW

27

BASIC DEFINITIONS

- 29 INITIAL ADDRESS OF THE STACK POINTER *** 1000 ***
- 40 MISCELLANEOUS DEFINITIONS
- 46 GENERAL PURPOSE REGISTER DEFINITIONS
- 58 PRIORITY LEVEL DEFINITIONS
- 68 "SWITCH REGISTER" SWITCH DEFINITIONS
- 96 DATA BIT DEFINITIONS (BIT00 TO BIT15)
- 124 BASIC "CPU" TRAP VECTOR ADDRESSES

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139 *****
TRAP CATCHER

142 ALL UNUSED LOCATIONS FROM 4 - 776 CONTAIN A ".+2,HALT"
SEQUENCE TO CATCH ILLEGAL TRAPS AND INTERRUPTS
LOCATION 0 CONTAINS 0 TO CATCH IMPROPERLY LOADED VECTORS

146 *****
STARTING ADDRESS(ES)

152 STARTING ADDRESS 200 FOR NORMAL STARTS
THIS WILL TEST ALL RPO4'S ON THE SYSTEM A SINGLE DRIVE AT A TIME
STARTING ADDRESS 210 WILL TEST ONLY ONE SPECIFIED DRIVE
STARTING ADDRESS 220 WILL JUMP OVER THE TESTS REQUIRING AN OPERA
AT THE DRIVE

160 *****
MEMORY MANAGEMENT DEFINITIONS

- 162 KT11 VECTOR ADDRESS
- 166 KT11 STATUS REGISTER ADDRESSES
- 173 KERNEL "I" PAGE DESCRIPTOR REGISTERS
- 184 KERNEL "I" PAGE ADDRESS REGISTERS

199 *****
COMMON TAGS

201 THIS TABLE CONTAINS VARIOUS COMMON STORAGE LOCATIONS
USED IN THE PROGRAM.

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ERROR POINTER TABLE

254 THIS TABLE CONTAINS THE INFORMATION FOR EACH ERROR THAT CAN OCCU
THE INFORMATION IS OBTAINED BY USING THE INDEX NUMBER FOUND IN
LOCATION \$ITEMB. THIS NUMBER INDICATES WHICH ITEM IN THE TABLE I
NOTE1: IF \$ITEMB IS 0 THE ONLY PERTINENT DATA IS (\$ERRPC).
NOTE2: EACH ITEM IN THE TABLE CONTAINS 4 POINTERS EXPLAINED AS

260 EM ;:POINTS TO THE ERROR MESSAGE
DH ;:POINTS TO THE DATA HEADER
DT ;:POINTS TO THE DATA
DF ;:POINTS TO THE DATA FORMAT

269 *****

1587

REGISTER ADDRESSES

1834

REGISTER TEST

1899 TEST 1 SIZE FOR RH DEVICES
THIS TEST DETERMINS WHICH RH DEVICE IS ON THE SYST
IF THE SYSTEM HAS MORE THAN ONE RH DEVICE THEN ONLY ONE
THE DEVICES WILL BE USED IN THE FOLLOWING TESTS.
THE ONE THAT WILL BE USED IS THE FIRST ONE THAT IS PRESE
IN THE FOLLOWING LIST
RS
RP
TM

THE WAY THE TEST WORKS IS AS FOLLOWS:-
A REFERENCE IS MADE TO THE CONTROL AND STATUS REGISTER 1
FOR THE RS BY A TST INSTRUCTION. IF IT RESPONDS THEN IT
ASSUMED PRESENT AND THE LOCATIONS FOR THE I/O REGISTERS
FILLED WITH THE APPROPRIATE ADDRESSES.
THEN THE DRIVE TYPE REGISTER IS CHECKED TO HAVE GOOD
VALUES.
IF THE TST INSTRUCTION TRAPS TO A NON EXISTANT VECTOR
THEN A SIMILAR ATTEMPT IS MADE TO A RP CONTROL AND STATU
REGISTER 1.
THEN A TM IS TRYED.
THEN A MIXED SYSTEM WITH MORE THAN ONE RH DEVICES IS TRYE

2195

TEST 2 UNIT UNDER TEST
THIS TYPES THE UNIT TO BE TESTED
AND IS THE FIRST TEST AFTER THE END OF THE PROGRAM

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2302

TEST 3 BIT BANG RHCS1

2304

TEST LOADING AND READING OF ALL POSSIBLE BITS
IN RHCS1 REGISTER.
USE A PATTERN OF WALKING 1'S (1,2,4,10 ETC)
AND WALKING 0'S (-2,-3,-5 ETC)
IN THIS TEST SC:TRF:MCPE:DVA:BIT12:BIT10:RDY:GO BITS WIL
AND RDY:DVA BITS WILL ALWAYS BE SET
AND BIT10 BITS WILL ALWAYS BE CLEARED
IF SWITCH 14 OR 9 OR 8 ARE SET FOR DEBUGGING
THEN A RH CLEAR (RHCS2 BIT #5) WILL BE
GIVEN TO AID SCOPE SYNCs ON THE CLEAR
SIGNAL.

2383

TEST 4 BIT BANG RHCS2

2385

TEST LOADING AND READING OF ALL POSSIBLE BITS
IN RHCS2 REGISTER.
USE A PATTERN OF WALKING 1'S (1,2,4,10 ETC)
AND WALKING 0'S (-2,-3,-5 ETC)
IN THIS TEST 177740 BITS WILL NOT BE WRITTEN INTO
AND IR BITS WILL ALWAYS BE SET
IF SWITCH 14 OR 9 OR 8 ARE SET FOR DEBUGGING
THEN A RH CLEAR (RHCS2 BIT #5) WILL BE
GIVEN TO AID SCOPE SYNCs ON THE CLEAR
SIGNAL.

2460

TEST 5 BIT BANG RHCS3

2462

TEST LOADING AND READING OF ALL POSSIBLE BITS
IN RHCS3 REGISTER.
USE A PATTERN OF WALKING 1'S (1,2,4,10 ETC)
AND WALKING 0'S (-2,-3,-5 ETC)
IN THIS TEST 177660 BITS WILL NOT BE WRITTEN INTO
AND 0 BITS WILL ALWAYS BE SET
AND BIT9:BIT8:BIT7:BITS:BIT4 BITS WILL ALWAYS BE CLEARED
IF SWITCH 14 OR 9 OR 8 ARE SET FOR DEBUGGING
THEN A RH CLEAR (RHCS2 BIT #5) WILL BE
GIVEN TO AID SCOPE SYNCs ON THE CLEAR
SIGNAL.

2541

TEST 6 BIT BANG RHCW

2543

TEST LOADING AND READING OF ALL POSSIBLE BITS
IN RHCW REGISTER.
USE A PATTERN OF WALKING 1'S (1,2,4,10 ETC)
AND WALKING 0'S (-2,-3,-5 ETC)
IN THIS TEST 0 BITS WILL NOT BE WRITTEN INTO
AND 0 BITS WILL ALWAYS BE SET
IF SWITCH 14 OR 9 OR 8 ARE SET FOR DEBUGGING
THEN A RH CLEAR (RHCS2 BIT #5) WILL BE
GIVEN TO AID SCOPE SYNCs ON THE CLEAR
SIGNAL.

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2618

TEST 7 BIT BANG RHBA

2620

TEST LOADING AND READING OF ALL POSSIBLE BITS
IN RHBA REGISTER.
USE A PATTERN OF WALKING 1'S (1,2,4,10 ETC)
AND WALKING 0'S (-2,-3,-5 ETC)
IN THIS TEST 0 BITS WILL NOT BE WRITTEN INTO
AND 0 BITS WILL ALWAYS BE SET
AND BIT00 BITS WILL ALWAYS BE CLEARED
IF SWITCH 14 OR 9 OR 8 ARE SET FOR DEBUGGING
THEN A RH CLEAR (RHCS2 BIT #5) WILL BE
GIVEN TO AID SCOPE SYNC'S ON THE CLEAR
SIGNAL.

2699

TEST 10 BIT BANG RHBAE

2701

TEST LOADING AND READING OF ALL POSSIBLE BITS
IN RHBAE REGISTER.
USE A PATTERN OF WALKING 1'S (1,2,4,10 ETC)
AND WALKING 0'S (-2,-3,-5 ETC)
IN THIS TEST 177700 BITS WILL NOT BE WRITTEN INTO
AND 0 BITS WILL ALWAYS BE SET
AND 177700 BITS WILL ALWAYS BE CLEARED
IF SWITCH 14 OR 9 OR 8 ARE SET FOR DEBUGGING
THEN A RH CLEAR (RHCS2 BIT #5) WILL BE
GIVEN TO AID SCOPE SYNC'S ON THE CLEAR
SIGNAL.

2784

TEST 11 SILO TEST 1 (ONE WORD WRITE)
AFTER A RH CLEAR IS GIVEN (SET BIT #5 IN RHCS2)
RHCS2 IS CHECKED TO HAVE "IR" (BIT #6) HIGH
TOGETHER WITH UNIT NUMBER
ONE WORD OF ALL ZEROS IS WRITTEN INTO RHDB
BY "CLR"
"OR" (BIT #7) IS WAITED FOR BY A TRAP INSTRUCTION
CALLED "WAT" (NO TIMING IS DONE)
HOWEVER IF "OR" DOES NOT SET WITHIN "WAT" COUNT
DOWN AN ERROR IS REPORTED
RHDB IS READ AND CHECKED TO CONTAIN ZEROS
RHCS2 WILL BE CHECKED TO HAVE "IR" AND UNIT NUMBER
RHCS1, RHCS3, RHBA, RHBAE, RHWC, WILL BE CHECKED TO HAVE
APPROPRIATE VALUES

2973

TEST 12 SILO TEST 2 (ONE WORD WRITE)
AFTER A RH CLEAR IS GIVEN (SET BIT #5 IN RHCS2)
RHCS2 IS CHECKED TO HAVE "IR" (BIT #6) HIGH
TOGETHER WITH UNIT NUMBER
ONE WORD OF ALL ONES IS WRITTEN INTO RHDB
BY "CLR"
"OR" (BIT #7) IS WAITED FOR BY A TRAP INSTRUCTION
CALLED "WAT" (NO TIMING IS DONE)
HOWEVER IF "OR" DOES NOT SET WITHIN "WAT" COUNT
DOWN AN ERROR IS REPORTED
RHDB IS READ AND CHECKED TO CONTAIN ALL ONES

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RHCS2 WILL BE CHECKED TO HAVE "IR" AND UNIT NUMBER
RHCS1, RHCS3, RHBA, RHBAE, RHWC, WILL BE CHECKED TO HAVE
APPROPRIATE VALUES

3144 TEST 13 SILO TEST 3 (TWO WORD WRITE)

3146 AFTER A RH CLEAR IS GIVEN (SET BIT #5 IN RHCS2)
RHCS2 IS CHECKED TO HAVE "IR" (BIT #6) HIGH,
TOGETHER WITH UNIT NUMBER
ONE WORD = 52525 IS WRITTEN INTO RHDB
RHCS2 IS CHECKED TO HAVE "IR" AND UNIT NUMBER
A SECOND WORD = 12525 IS WRITTEN INTO RHDB
"OR" (BIT #7 IN RHCS2) IS WAITED FOR BY A TRAP
INSTRUCTION CALLED "WAT"
RHDB IS READ AND CHECKED TO CONTAIN 52525
RHCS2 IS CHECKED TO HAVE "IR", "OR" AND UNIT NUMBER
RHDB IS READ A SECOND TIME AND CHECKED TO
CONTAIN 12525
RHCS2 IS CHECKED TO HAVE "IR" AND UNIT NUMBER
THEN ALL REGISTERS RHCS1, RHCS3, RHBA, RHBAE, RHWC
ARE CHECKED TO HAVE APPROPRIATE VALUE

3416 TEST 14 SILO TEST 4 (COUNT PATTERN)

3418 AFTER A RH CLEAR IS GIVEN (SET BIT #5 IN RHCS2)
RHCS2 IS CHECKED TO HAVE "IR" (BIT #6) HIGH, TOGETHER
WITH UNIT NUMBER
EIGHT WORDS, A COUNT PATTERN 0 THRU 7 IS WRITTEN
INTO RHDB
"OR" (BIT #7 IN RHCS2) IS WAITED FOR BY A TRAP
INSTRUCTION CALLED "WAT"
THEN RHCS2 IS CHECKED TO HAVE "IR" LOW AND
"OR" HIGH TOGETHER WITH THE UNIT NUMBER
THEN RHBS READ AND COMPARED TO HAVE THE RIGHT VALUE
EIGHT TIES
THEN RHCS2, RHCS1, RHCS3, RHBA, RHBAE, RHWC ARE
CHECKED TO HAVE THE APPROPRIATE VALUE

3808 TEST 15 SILO TEST 5 (FLOATING ONES)

3810 AFTER ARH CLEAR IS GIVEN (SET BIT #5 IN RHCS2)
RHCS2 IS CHECKED TO HAVE "IR" (BIT #6) HIGH,
TOGETHER WITH UNIT NUMBER
EIGHT WORDS, A PATTERN OF FLOATING ONES (1,2,4,10,20,40,
IS WRITTEN INTO RHDB
"OR" (BIT #7 IN RHCS2) IS WAITED FOR BY A TRAP
INSTRUCTION CALLED "WAT"
THEN RHCS2 IS CHECKED TO HAVE "IR" LOW AND
"OR" HIGH TOGETHER WITH THE UNIT NUMBER
THEN RHDB IS READ AND COMPARED TO HAVE THE
RIGHT VALUE AFTER EACH OF THE EIGHT READS.
THEN RHCS2 IS CHECKED TO HAVE "IR"

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AND UNIT NUMBER
THEN RHCS1, RHCS3, RHBA, RHBAE, RHWC ARE
CHECKED TO HAVE THE APPROPRIATE VALUE

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4227 TEST 16 SILO TEST 6 (FLOATING ONES IN UPPER BYTE)

4229 AFTER A RH CLEAR IS GIVEN (SET BIT #5 IN RHCS2)
RHCS2 IS CHECKED TO HAVE "IR" (BIT #6) HIGH,
TOGETHER WITH UNIT NUMBER
EIGHT WORDS, A PATTERN OF FLOATING ONES IN UPPER BYTE
400,1000,2000,4000,10000,20000,40000,100000
IS WRITTEN INTO RHDB
"OR" (BIT #7 IN RHCS2) IS WAITED FOR BY A TRAP
INSTRUCTION CALLED "WAT"
THEN RHCS2 IS CHECKED TO HAVE "IR" LOW AND
"OR" HIGH TOGETHER WITH THE UNIT NUMBER
THEN RHDB IS READ AND COMPARED TO HAVE THE
RIGHT VALUE AFTER EACH OF THE EIGHT READS.
THEN RHCS2 IS CHECKED TO HAVE "IR"
AND UNIT NUMBER
THEN RHCS1, RHCS3, RHBA, RHBAE, RHWC ARE
CHECKED TO HAVE THE APPROPRIATE VALUE

4649 TEST 17 SILO TEST 7 (FLOATING 0 IN LOWER BYTE)

4651 AFTER A RH CLEAR IS GIVEN (SET BIT #5 IN RHCS2)
RHCS2 IS CHECKED TO HAVE "IR" (BIT #6) HIGH,
TOGETHER WITH UNIT NUMBER
EIGHT WORDS OF FLOATING ZEROS 177776, 177775, 177773,
177767, 177757, 177737, 177677, 177577
IS WRITTEN INTO RHDB
"OR" (BIT #7 IN RHCS2) IS WAITED FOR BY A TRAP
INSTRUCTION CALLED "WAT"
THEN RHCS2 IS CHECKED TO HAVE "IR" LOW AND
"OR" HIGH TOGETHER WITH THE UNIT NUMBER
THEN RHDB IS READ AND COMPARED TO HAVE THE
RIGHT VALUE AFTER EACH OF THE EIGHT READS.
THEN RHCS2 IS CHECKED TO HAVE "IR"
AND UNIT NUMBER
THEN RHCS1, RHCS3, RHBA, RHBAE, RHWC ARE
CHECKED TO HAVE THE APPROPRIATE VALUE

5070 TEST 20 SILO TEST 8 (FLOATING ZEROS IN UPPER BYTE)

5072 AFTER A RH CLEAR IS GIVEN (SET BIT #5 IN RHCS2)
RHCS2 IS CHECKED TO HAVE "IR" (BIT #6) HIGH,

5074 TOGETHER WITH UNIT NUMBER
EIGHT WORDS, A PATTERN OF FLOATING ZEROS IN UPPER BYTE
177377, 177677, 175777, 173777, 167777, 157777, 137777,
IS WRITTEN INTO RHDB
"OR" (BIT #7 IN RHCS2) IS WAITED FOR BY A TRAP
INSTRUCTION CALLED "WAT"
THEN RHCS2 IS CHECKED TO HAVE "IR" LOW AND

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"OR" HIGH TOGETHER WITH THE UNIT NUMBER
THEN RHDB IS READ AND COMPARED TO HAVE THE
RIGHT VALUE AFTER EACH OF THE EIGHT READS.
THEN RHCS2 IS CHECKED TO HAVE "IR"
AND UNIT NUMBER
THEN RHCS1, RHCS3, RHBA, RHBAE, RHWC ARE
CHECKED TO HAVE THE APPROPRIATE VALUE

5508 TEST 21 RHCS1 - MCPE BIT #13 (PARITY LINE = 0)

5510 AN RH CLEAR (SET BIT #5 IN RHCS2) IS GIVEN TO
CLEAR ALL DEVICE REGISTERS
THEN RHER1 (ERROR REGISTER 1) IS CHECKED TO HAVE
ZEROS
SET "PAT" (BIT #4 IN RHCS2) TO INVERT PARITY CHECKING
READ ANY DEVICE REGISTER - HERE RHER1
READ AND CHECK RHCS1 TO CONTAIN SC (BIT #15)
AND MCPE (BIT #13)
WRITE "1" INTO TRE (BIT #14 IN RHCS1)
READ RHCS1 SC, MCPE, AND RDY SHOULD BE SET
GIVE AN RH CLEAR (CLR - BIT #5 IN RHCS2)
CHECK RHCS1 TO HAVE RDY
CHECK RHCS2 TO HAVE ONLY IR AND UNIT NUMBER
CHECK RHCS3, RHBA, RHBAE, RHWC TO HAVE APPROPRIATE
VALUES.

5785 TEST 22 RHCS1 - MCPE BIT #13 (PARITY LINE = 1)

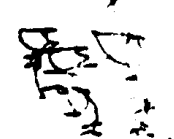
5787 AN RH CLEAR (SET BIT #5 IN RHCS2) IS GIVEN TO CLEAR
ALL DEVICE REGISTERS
WRITE A ONE INTO DISK ADDRESS REGISTER (RHDA)
(IN TAPE DRIVES CALLED REGISTER)
SET "PAT" (RHCS2 BIT #4) THIS WILL INVERT THE PARITY CHE
READ RHDA AND COMPARE IT HAS ONE IN IT
THIS READING SHOULD SET SC, MCPE IN RHCS1
CHECK RHCS1 TO HAVE ONLY RDY SET
CHECK RHCS2, RHCS3, RHBA, RHBAE, RHWC TO HAVE APPROPRIATE
VALUES

5959 TEST 23 TEST DUPLICATED A16 (RHCS1 BIT #8)

5961 CLEAR THE SUBSYSTEM BY A CONTROLLER CLEAR (RHCS2 BIT #5)
MOVE 400 (ONE INTO A16) IN RHCS1
READ RHCS1 TO CONTAIN 600 (BIT #8 AND RDY)
READ RHBAE TO CONTAIN "1" (BIT #0 HIGH)

MOVE 0 INTO RHBAE (WRITING 0 INTO RHBAE BIT #0)
READ RHBAE TO CONTAIN 0
READ RHCS1 TO CONTAIN ONLY RDY (BIT #8 IN RHCS1 IS ZERO)

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5424

RHBA TO HAVE ADDRESS OF "WAFROM"
RHBAE AND RHWC TO HAVE ZEROS

NOW SET UP READ FOR THE SAME DATA WITH
BAI SET TO READ INTO A BUFFER TAGGED "REINTC"
AFTER READ CHECK
RHCS1 TO HAVE ONLY RDY AND COMMAND
RHCS2 TO HAVE BAI
RHCS3 TO HAVE 0
RHBA TO HAVE ADDRESS OF "REINTC"
RHBAE AND RHWC TO HAVE ZEROS
DATA IN REINTO BUFFER IS CHECKED WITH DATA IN
WAFROM BUFFER

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- 6571 TEST 30 RHCS2 MDPE BIT #8 AND RHCS3 IPCK0 BIT #0
CLEAR THE SUBSYSTEM BY A CONTROLLER CLEAR (RHCS2 BIT #5)
SET IPCK0 (RHCS3 BIT #0)
MOVE ALL ZEROS INTO RHDB ONCE
THIS SHOULD SET TRE SC IN RHCS1
READ RHDB ONCE THIS SHOULD SET MDPE (RHCS2 BIT #8)
CHECK RHCS1 FOR RDY (BIT #7), TRE (BIT #14), SC (BIT #15)
"CLR" (RHCS2 BIT #5) IS GIVEN
ALL ERRORS ARE CLEARED
CHECK RHCS1, RHCS2, RHCS3, RHBA, RHBAE, RHWC
- 6307 TEST 31 RHCS2 MDPE BIT #8 AND RHCS3 IPCK1 BIT #1
CLEAR THE SUBSYSTEM BY A CONTROLLER CLEAR (RHCS2 BIT #5)
SET IPCK1 (RHCS3 BIT #1)
MOVE ALL ZEROS INTO RHDB ONCE
THIS SHOULD SET TRE SC IN RHCS1
READ RHDB ONCE THIS SHOULD SET MDPE (RHCS2 BIT #8)
CHECK RHCS1 FOR RDY (BIT #7), TRE (BIT #14), SC (BIT #15)
"CLR" (RHCS2 BIT #5) IS GIVEN
ALL ERRORS ARE CLEARED
CHECK RHCS1, RHCS2, RHCS3, RHBA, RHBAE, RHWC
- 7043 TEST 32 RHCS2 MDPE BIT #8 AND RHCS3 IPCK2 BIT #2
CLEAR THE SUBSYSTEM BY A CONTROLLER CLEAR (RHCS2 BIT #5)
SET IPCK2 (RHCS3 BIT #2)
MOVE ALL ZEROS INTO RHDB TWICE
THIS SHOULD NOT SET TRE SC IN RHCS1
READ RHDB ONCE THIS SHOULD SET MDPE (RHCS2 BIT #8)
CHECK RHCS1 FOR RDY (BIT #7), TRE (BIT #14), SC (BIT #15)
READ RHDB A SECOND TIME. CHECK RHCS1, RHCS2, RHCS3
"CLR" (RHCS2 BIT #5) IS GIVEN
ALL ERRORS ARE CLEARED
CHECK RHCS1, RHCS2, RHCS3, RHBA, RHBAE, RHWC
- 7345 TEST 33 RHCS2 MDPE BIT #8 AND RHCS3 IPCK3 BIT #3
CLEAR THE SUBSYSTEM BY A CONTROLLER CLEAR (RHCS2 BIT #5)
SET IPCK3 (RHCS3 BIT #3)
MOVE ALL ZEROS INTO RHDB TWICE
THIS SHOULD NOT SET TRE SC IN RHCS1
READ RHDB ONCE THIS SHOULD SET MDPE (RHCS2 BIT #8)

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CHECK RHCS1 FOR RDY (BIT #7), TRE (BIT #14), SC (BIT #15)
READ RHDB A SECOND TIME. CHECK RHCS1, RHCS2, RHCS3
"CLR" (RHCS2 BIT #5) IS GIVEN
ALL ERRORS ARE CLEARED
CHECK RHCS1, RHCS2, RHCS3, RHBA, RHBAE, RHWC

7648 TEST 34 RHCS2-WCE BIT #14 AND RHCS3-WCE OW BIT #12

7650 CLEAR THE SUBSYSTEM BY A CONTROLLER CLEAR (RHCS2 BIT #5)
WRITE NINE WORD OF ALL NINES ON TO THE DEVICE
SET UP TO DO WRITE CHECK ON THAT WORD FROM AN
ODD WORD BOUNDARY
DO A ONE WORD WRITE CHECK WITH RHBA FROM AN
ODD WORD BOUNDARY
THIS SHOULD SET WCE OW (RHCS3 BIT #12)
AND WCE (RHCS2 BIT #14)
"CLR" (RHCS2 BIT #5) IS GIVEN
ALL ERRORS ARE CLEARED
CHECK RHCS1, RHCS2, RHCS3, RHBA, RHBAE, RHWC

7891 TEST 35 RHCS2-WCE BIT #14 AND RHCS3-WCE OW BIT #12

7393 CLEAR THE SUBSYSTEM BY A CONTROLLER CLEAR (RHCS2 BIT #5)
WRITE NINE WORD OF ALL NINES ON TO THE DEVICE
SET UP TO DO WRITE CHECK ON THAT WORD FROM AN
ODD WORD BOUNDARY
DO A ONE WORD WRITE CHECK WITH RHBA FROM AN
ODD WORD BOUNDARY
THIS SHOULD SET WCE OW (RHCS3 BIT #12)
AND WCE (RHCS2 BIT #14)
"CLR" (RHCS2 BIT #5) IS GIVEN
ALL ERRORS ARE CLEARED
CHECK RHCS1, RHCS2, RHCS3, RHBA, RHBAE, RHWC

8114 TEST 36 RHCS2-WCE BIT #14 AND RHCS3-WCE EW BIT #11

8116 CLEAR THE SUBSYSTEM BY A CONTROLLER CLEAR (RHCS2 BIT #5)
WRITE NINE WORD OF ALL NINES ON TO THE DEVICE
SET UP TO DO WRITE CHECK ON THAT WORD FROM AN
EVEN WORD BOUNDARY
DO A ONE WORD WRITE CHECK WITH RHBA FROM AN
EVEN WORD BOUNDARY
THIS SHOULD SET WCE EW (RHCS3 BIT #11)
AND WCE (RHCS2 BIT #14)
"CLR" (RHCS2 BIT #5) IS GIVEN
ALL ERRORS ARE CLEARED
CHECK RHCS1, RHCS2, RHCS3, RHBA, RHBAE, RHWC

8347 TEST 37 RHCS2-WCE BIT #14 AND RHCS3-WCE EW BIT #11

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8349

CLEAR THE SUBSYSTEM BY A CONTROLLER CLEAR (RHCS2 BIT #5)
WRITE NINE WORD OF ALL NINES ON TO THE DEVICE
SET UP TO DO WRITE CHECK ON THAT WORD FROM AN
EVEN WORD BOUNDARY
DO A ONE WORD WRITE CHECK WITH RHBA FROM AN
EVEN WORD BOUNDARY
THIS SHOULD SET WCE EW (RHCS3 BIT #11)
AND WCE (RHCS2 BIT #14)
"CLR" (RHCS2 BIT #5) IS GIVEN
ALL ERRORS ARE CLEARED
CHECK RHCS1, RHCS2, RHCS3, RHBA, RHBAE, RHWC

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- 8581 TEST 40 TEST DBL (RHCS3 BIT #10) TEST A
- 9583 CLEAR THE SUBSYSTEM BY A CONTROLLER CLEAR (RHCS2 BIT #5)
- 9594 SET UP FOR A NINE WORD WRITE FROM AN EVEN WORD BOUNDARY
DO A NINE WORD WRITE FROM AN EVEN WORD BOUNDARY
THIS SHOULD NOT SET RHCS3 BIT #10 DBL
CHECK RHCS3
CHECK RHCS1, RHCS2, RHBA, RHBAE, RHWC
- 8694 TEST 41 TEST DBL (RHCS3 BIT #10) TEST B
- 8696 CLEAR THE SUBSYSTEM BY A CONTROLLER CLEAR (RHCS2 BIT #5)
SET UP FOR A TEN WORD WRITE FROM AN EVEN WORD BOUNDARY
DO A TEN WORD WRITE FROM AN EVEN WORD BOUNDARY
THIS SHOULD SET RHCS3 BIT #10 DBL
CHECK RHCS3
CHECK RHCS1, RHCS2, RHBA, RHBAE, RHWC
- 8908 TEST 42 TEST DBL (RHCS3 BIT #10) TEST C
- 8910 CLEAR THE SUBSYSTEM BY A CONTROLLER CLEAR (RHCS2 BIT #5)
SET UP FOR A TEN WORD WRITE FROM AN ODD WORD BOUNDARY
DO A TEN WORD WRITE FROM AN ODD WORD BOUNDARY
THIS SHOULD NOT SET RHCS3 BIT #10 DBL
CHECK RHCS3
CHECK RHCS1, RHCS2, RHBA, RHBAE, RHWC
- 8921 TEST 43 TEST DBL (RHCS3 BIT #10) TEST D
- 8923 CLEAR THE SUBSYSTEM BY A CONTROLLER CLEAR (RHCS2 BIT #5)
SET UP FOR A ELEVEN WORD WRITE FROM AN EVEN WORD BOUNDARY
DO A ELEVEN WORD WRITE FROM AN EVEN WORD BOUNDARY
THIS SHOULD NOT SET RHCS3 BIT #10 DBL
CHECK RHCS3
CHECK RHCS1, RHCS2, RHBA, RHBAE, RHWC

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9034

TEST 44 TEST DBL (RHCS3 BIT #10) TEST E

9036

CLEAR THE SUBSYSTEM BY A CONTROLLER CLEAR (RHCS2 BIT #5)
SET UP FOR A TEN WORD WRITE FROM AN EVEN WORD BOUNDARY
WITH BAI IN RHCS2 BIT #3 SET
DO A TEN WORD WRITE FROM AN EVEN WORD BOUNDARY
THIS SHOULD NOT SET RHCS3 BIT #10 DBL
CHECK RHCS3
CHECK RHCS1, RHCS2, RHBA, RHBAE, RHWC

9153

TEST 45 TEST DBL (RHCS3 BIT #10) TEST F

9155

CLEAR THE SUBSYSTEM BY A CONTROLLER CLEAR (RHCS2 BIT #5)
SET UP FOR A ELEVEN WORD WRITE FROM AN ODD WORD BOUNDARY
DO A ELEVEN WORD WRITE FROM AN ODD WORD BOUNDARY
THIS SHOULD SET RHCS3 BIT #10 DBL
CHECK RHCS3
CHECK RHCS1, RHCS2, RHBA, RHBAE, RHWC

9268

TEST 46 TEST DBL (RHCS3 BIT #10) TEST G

9270

CLEAR THE SUBSYSTEM BY A CONTROLLER CLEAR (RHCS2 BIT #5)
SET UP FOR A NINE WORD READ FROM AN EVEN WORD BOUNDARY
DO A NINE WORD READ FROM AN EVEN WORD BOUNDARY
THIS SHOULD NOT SET RHCS3 BIT #10 DBL
CHECK RHCS3
CHECK RHCS1, RHCS2, RHBA, RHBAE, RHWC

9381

TEST 47 TEST DBL (RHCS3 BIT #10) TEST H

9383

CLEAR THE SUBSYSTEM BY A CONTROLLER CLEAR (RHCS2 BIT #5)
SET UP FOR A TEN WORD READ FROM AN EVEN WORD BOUNDARY
DO A TEN WORD READ FROM AN EVEN WORD BOUNDARY
THIS SHOULD SET RHCS3 BIT #10 DBL
CHECK RHCS3
CHECK RHCS1, RHCS2, RHBA, RHBAE, RHWC

9495

TEST 50 TEST DBL (RHCS3 BIT #10) TEST I

9497

CLEAR THE SUBSYSTEM BY A CONTROLLER CLEAR (RHCS2 BIT #5)
SET UP FOR A TEN WORD READ FROM AN ODD WORD BOUNDARY
DO A TEN WORD READ FROM AN ODD WORD BOUNDARY
THIS SHOULD NOT SET RHCS3 BIT #10 DBL
CHECK RHCS3

9502

CHECK RHCS1, RHCS2, RHBA, RHBAE, RHWC

9608

TEST 51 TEST DBL (RHCS3 BIT #10) TEST J

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9610

CLEAR THE SUBSYSTEM BY A CONTROLLER CLEAR (RHCS2 BIT #5)
SET UP FOR A ELEVEN WORD READ FROM AN EVEN WORD BOUNDARY
DO A ELEVEN WORD READ FROM AN EVEN WORD BOUNDARY
THIS SHOULD NOT SET RHCS3 BIT #10 DBL
CHECK RHCS3
CHECK RHCS1, RHCS2, RHBA, RHBAE, RHWC

9721 TEST 52 TEST DBL (RHCS3 BIT #10) TEST K

9723 CLEAR THE SUBSYSTEM BY A CONTROLLER CLEAR (RHCS2 BIT #5)
SET UP FOR A ELEVEN WORD READ FROM AN ODD WORD BOUNDARY
DO A ELEVEN WORD READ FROM AN ODD WORD BOUNDARY
THIS SHOULD SET RHCS3 BIT #10 DBL
CHECK RHCS3
CHECK RHCS1, RHCS2, RHBA, RHBAE, RHWC

9835 TEST 53 TEST DBL (RHCS3 BIT #10) TEST L

9837 CLEAR THE SUBSYSTEM BY A CONTROLLER CLEAR (RHCS2 BIT #5)
SET UP FOR A TEN WORD READ FROM AN EVEN WORD BOUNDARY
WITH BAI IN RHCS2 BIT #3 SET
DO A TEN WORD READ FROM AN EVEN WORD BOUNDARY
THIS SHOULD NOT SET RHCS3 BIT #10 DBL
CHECK RHCS3
CHECK RHCS1, RHCS2, RHBA, RHBAE, RHWC

9955 TEST 54 TEST DBL (RHCS3 BIT #10) TEST M

9957 CLEAR THE SUBSYSTEM BY A CONTROLLER CLEAR (RHCS2 BIT #5)
SET UP FOR A ONE WORD WRITE REVERSE FROM AN EVEN WORD BO
DO A ONE WORD WRITE REVERSE FROM AN EVEN WORD BOUNDARY
THIS SHOULD NOT SET RHCS3 BIT #10 DBL
CHECK RHCS3
CHECK RHCS1, RHCS2, RHBA, RHBAE, RHWC

10068 TEST 55 TEST DBL (RHCS3 BIT #10) TEST N

10070 CLEAR THE SUBSYSTEM BY A CONTROLLER CLEAR (RHCS2 BIT #5)
SET UP FOR A TWO WORD WRITE REVERSE FROM AN EVEN WORD BO
DO A TWO WORD WRITE REVERSE FROM AN EVEN WORD BOUNDARY
THIS SHOULD NOT SET RHCS3 BIT #10 DBL
CHECK RHCS3
CHECK RHCS1, RHCS2, RHBA, RHBAE, RHWC

10181 TEST 56 TEST DBL (RHCS3 BIT #10) TEST O

10193 CLEAR THE SUBSYSTEM BY A CONTROLLER CLEAR (RHCS2 BIT #5)
SET UP FOR A TWO WORD WRITE REVERSE FROM AN ODD WORD BCU
DO A TWO WORD WRITE REVERSE FROM AN ODD WORD BOUNDARY
THIS SHOULD SET RHCS3 BIT #10 DBL
CHECK RHCS3
CHECK RHCS1, RHCS2, RHBA, RHBAE, RHWC

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10295 TEST 57 TEST DBL (RHCS3 BIT #10) TEST P

10297 . CLEAR THE SUBSYSTEM BY A CONTROLLER CLEAR (RHCS2 BIT #5)
SET UP FOR A THREE WORD WRITE REVERSE FROM AN EVEN WCRC
DO A THREE WORD WRITE REVERSE FROM AN EVEN WORD BOUNDARY
THIS SHOULD SET RHCS3 BIT #10 DBL
CHECK RHCS3
CHECK RHCS1, RHCS2, RHBA, RHBAE, RHWC

10409 TEST 60 TEST DBL (RHCS3 BIT #10) TEST Q

10411 CLEAR THE SUBSYSTEM BY A CONTROLLER CLEAR (RHCS2 BIT #5)
SET UP FOR A THREE WORD WRITE REVERSE FROM AN ODD WORD B
DO A THREE WORD WRITE REVERSE FROM AN ODD WORD BOUNDARY
THIS SHOULD NOT SET RHCS3 BIT #10 DBL
CHECK RHCS3
CHECK RHCS1, RHCS2, RHBA, RHBAE, RHWC

10522 TEST 61 TEST DBL (RHCS3 BIT #10) TEST R

10524 CLEAR THE SUBSYSTEM BY A CONTROLLER CLEAR (RHCS2 BIT #5)
SET UP FOR A TWO WORD WRITE REVERSE FROM AN EVEN WORD BO
WITH BAI IN RHCS2 BIT #3 SET
DO A TWO WORD WRITE REVERSE FROM AN EVEN WORD BOUNDARY

10528 THIS SHOULD NOT SET RHCS3 BIT #10 DBL
CHECK RHCS3
CHECK RHCS1, RHCS2, RHBA, RHBAE, RHWC

10642 TEST 62 TEST DBL (RHCS3 BIT #10) TEST S

10644 CLEAR THE SUBSYSTEM BY A CONTROLLER CLEAR (RHCS2 BIT #5)
SET UP FOR A NINE WORD READ REVERSE FROM AN EVEN WORD BO
DO A NINE WORD READ REVERSE FROM AN EVEN WORD BOUNDARY
THIS SHOULD SET RHCS3 BIT #10 DBL
CHECK RHCS3
CHECK RHCS1, RHCS2, RHBA, RHBAE, RHWC
IF MAG. TAPE NOT USED. THIS TEST IS NOT DONE

10761 TEST 63 TEST DBL (RHCS3 BIT #10) TEST T

10763 CLEAR THE SUBSYSTEM BY A CONTROLLER CLEAR (RHCS2 BIT #5)
SET UP FOR A TEN WORD READ REVERSE FROM AN EVEN WORD BOU
DO A TEN WORD READ REVERSE FROM AN EVEN WORD BOUNDARY
THIS SHOULD NOT SET RHCS3 BIT #10 DBL
CHECK RHCS3
CHECK RHCS1, RHCS2, RHBA, RHBAE, RHWC
IF MAG. TAPE NOT USED. THIS TEST IS NOT DONE

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958          MAINDEC-11-DERHAB-A
959          10979  TEST 64 TEST DBL (RHCS3 BIT #10) TEST U
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961          10881  CLEAR THE SUBSYSTEM BY A CONTROLLER CLEAR (RHCS2 BIT #5)
962          SET UP FOR A TEN WORD READ REVERSE FROM AN ODD WORD BOUN
963          DO A TEN WORD READ REVERSE FROM AN ODD WORD BOUNDARY
964          THIS SHOULD SET RHCS3 BIT #10 DBL
965          CHECK RHCS3
966          CHECK RHCS1, RHCS2, RHBA, RHBAE, RHWC
967          IF MAG. TAPE NOT USED. THIS TEST IS NOT DONE
968
969          10998  TEST 55 TEST DBL (RHCS3 BIT #10) TEST V
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971          11000  CLEAR THE SUBSYSTEM BY A CONTROLLER CLEAR (RHCS2 BIT #5)
972          SET UP FOR A ELEVEN WORD READ REVERSE FROM AN EVEN WORD
973          DO A ELEVEN WORD READ REVERSE FROM AN EVEN WORD BOUNDARY
974          THIS SHOULD SET RHCS3 BIT #10 DBL
975          CHECK RHCS3
976          CHECK RHCS1, RHCS2, RHBA, RHBAE, RHWC
977          IF MAG. TAPE NOT USED. THIS TEST IS NOT DONE
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979          11117  TEST 66 TEST DBL (RHCS3 BIT #10) TEST W
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981          11119  CLEAR THE SUBSYSTEM BY A CONTROLLER CLEAR (RHCS2 BIT #5)
982          SET UP FOR A ELEVEN WORD READ REVERSE FROM AN ODD WORD B
983          DO A ELEVEN WORD READ REVERSE FROM AN ODD WORD BOUNDARY
984
985          11122  THIS SHOULD NOT SET RHCS3 BIT #10 DBL
986          CHECK RHCS3
987          CHECK RHCS1, RHCS2, RHBA, RHBAE, RHWC
988          IF MAG. TAPE NOT USED. THIS TEST IS NOT DONE
989
990          11235  TEST 67 TEST DBL (RHCS3 BIT #10) TEST X
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992          11237  CLEAR THE SUBSYSTEM BY A CONTROLLER CLEAR (RHCS2 BIT #5)
993          SET UP FOR A TEN WORD READ REVERSE FROM AN ODD WORD BOUN
994          WITH BAI IN RHCS2 BIT #3 SET
995          DO A TEN WORD READ REVERSE FROM AN ODD WORD BOUNDARY
996          THIS SHOULD NOT SET RHCS3 BIT #10 DBL
997          CHECK RHCS3
998          CHECK RHCS1, RHCS2, RHBA, RHBAE, RHWC
999          IF MAG. TAPE NOT USED. THIS TEST IS NOT DONE
1000
1001          11362  TEST 70 END OF ONE RH
1002          THIS IS THE END OF TEST FOR ONE RH
1003          IF THERE ARE MORE RH THEN THE PROGRAM
1004          JUMPS TO TEST 2 FOR NEXT RH TEST
1005          END PASS IS REACHED ONLY AFTER ALL RH ARE COMPLETE
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11400 *****
END OF PASS ROUTINE

11402 INCREMENT THE PASS NUMBER (\$PASS)
TYPE "END PASS #XXXXX" (WHERE XXXXX IS A DECIMAL NUMBER)
IF THERES A MONITOR GO TO IT
IF THERE ISN'T JUMP TO TST2

11439 *****
SUBROUTINES

11655 *****
RS DATA TRANSFER SUBROUTINE

11657 THIS SUBROUTINE WRITES OR READS OR DOES A
WRITE CHECK ON CYLINDER 0 TRACK 0 SECTOR 0
IT ASSUMES THE RH REGISTERS ARE APPROPRIATELY
FILLED
WHEN IT RETURNS FROM THIS SUBROUTINE TO THE
MAIN PROGRAM IT MEANS THE TRANSFER IS COMPLETE

THE CALL IS BY A JSR R0, @COMND COMAND

11704 *****
RPO4 DATA TRANSFER SUBROUTINE

11706 THIS SUBROUTINE WRITE/READ/WRITE CHECK ON THE RPO4 CYLIN
TRACK 0, SECTOR 0.

11708 IT ASSUMES THE RH REGISTERS ARE APPROPRIATELY FILLED.
WHEN IT RETURNS FROM THIS SUBROUTINE TO THE MAIN
PROGRAM IT MEANS THE COMMAND IS COMPLETE

THE CALL IS BY A JSR R0, @COMND COMMAND.

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11758 *****
TMO2 DATA TRANSFER SUBROUTINE

11760 THIS SUBROUTINE WRITES/READS/WRITE CHECKS ON THE TMO2-TU
ON THE FIRST BLOCK FROM BEGINNING OF TAPE (BOT)

11762 IT ASSUMES THE RH REGISTERS ARE APPROPRIATELY FILLED.
WHEN IT RETURNS FROM THIS SUBROUTINE TO THE
MAIN PROGRAM IT MEANS THE COMMAND IS COMPLETE.

THE CALL IS
JSR RO, @COMND

11915 THIS ROUTINE WILL ALLOW THE CHANGE OF THE BASE
ADDRESS FROM 176700 TO ANY TYPED VALUE

11982 *****

11985 *****
SCOPE HANDLER ROUTINE

11987 THIS ROUTINE CONTROLS THE LOOPING OF SUBTESTS. IT WILL INCREMENT
AND LOAD THE TEST NUMBER(\$TSTNM) INTO THE DISPLAY REG.(DISPLAY<7
AND LOAD THE ERROR FLAG (\$ERFLG) INTO DISPLAY<15:08>
THE SWITCH OPTIONS PROVIDED BY THIS ROUTINE ARE:

SW14=1 LOOP ON TEST
SW11=1 INHIBIT ITERATIONS
SW09=1 LOOP ON ERROR
SW08=1 LOOP ON TEST IN SWR<7:0>
CALL

SCOPE ; ;SCOPE=IOT

12051 *****
CONVERT BINARY TO DECIMAL AND TYPE ROUTINE

12053 THIS ROUTINE IS USED TO CHANGE A 16-BIT BINARY NUMBER TO A 5-DIG
SIGNED DECIMAL (ASCII) NUMBER AND TYPE IT. DEPENDING ON WHETHER
NUMBER IS POSITIVE OR NEGATIVE A SPACE OR A MINUS SIGN WILL BE T
BEFORE THE FIRST DIGIT OF THE NUMBER. LEADING ZEROS WILL ALWAYS
REPLACED WITH SPACES.

CALL:
MOV NUM, -(SP) ;:PUT THE BINARY NUMBER ON THE S
TYPDS ;:GO TO THE ROUTINE

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*****
12119 TYPE ROUTINE
*****

12121 ROUTINE TO TYPE ASCIZ MESSAGE. MESSAGE MUST TERMINATE WITH A 0 B
THE ROUTINE WILL INSERT A NUMBER OF NULL CHARACTERS AFTER A LINE
NOTE1: $NULL CONTAINS THE CHARACTER TO BE USED AS THE FILLER CH
NOTE2: $FILLS CONTAINS THE NUMBER OF FILLER CHARACTERS REQUIRED
NOTE3: $FILLC CONTAINS THE CHARACTER TO FILL AFTER.

CALL:
1) USING A TRAP INSTRUCTION
   TYPE      ,MESADR      ;;MESADR IS FIRST ADDRESS OF AN
OR
   TYPE
   MESADR

2) USING A JSR INSTRUCTION
   MOV      PS,-(SP)      ;;PUSH PROCESSOR STATUS WORD ON
   JSR      PC,$TYPE      ;;CALL TYPE ROUTINE
   MESADDR      ;;FIRST ADDRESS OF MESSAGE

12192 *****
TTY INPUT ROUTINE
*****

12201 TK INITIALIZE ROUTINE
THIS ROUTINE WILL INITIALIZE THE TTY KEYBOARD INPUT QUEUE
SETUP THE INTERRUPT VECTOR AND TURN ON THE KEYBOARD INTERRUPT
CALL:
   JSR      PC,$TKINT
   RETURN

12218 TK SERVICE ROUTINE
THIS ROUTINE WILL SERVICE THE TTY KEYBOARD INTERRUPT
BY READING THE CHARACTER FROM THE INPUT BUFFER AND PUTTING
IT IN THE QUEUE.
IF THE CHARACTER IS A "CONTROL-C" (↑C) $TKINT IS CALLED AND
UPON RETURN EXIT IS MADE TO THE "CONTROL-C" RESTART ADDRESS (OPE

12245 THIS ROUTINE WILL INPUT A SINGLE CHARACTER FROM THE TTY
CALL:
   RDCHR      ;;GET A CHARACTER FROM THE QUEUE
   RETURN HERE ;;CHARACTER IS ON THE STACK
               ;;WITH PARITY BIT STRIPPED OFF

12266 THIS ROUTINE WILL INPUT A STRING FROM THE TTY
CALL:
   RDLIN      ;;INPUT A STRING FROM THE TTY
   RETURN HERE ;;ADDRESS OF FIRST CHARACTER WIL
               ;;TERMINATOR WILL BE A BYTE OF A

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12303 *****
READ AN OCTAL NUMBER FROM THE TTY

12305 THIS ROUTINE WILL READ AN OCTAL (ASCII) NUMBER FROM THE TTY AND
CHANGE IT TO BINARY.
THE INPUT CHARACTERS WILL BE CHECKED TO INSURED THEY ARE LEGAL
OCTAL DIGITS. IF AN ILLEGAL CHARACTER IS READ A "?" WILL BE TYPE
FOLLOWED BY A CARRIAGE RETURN-LINE FEED. THE COMPLETE NUMBER MUS
THEN BE RETYPED. THE INPUT IS TERMINATED BY TYPING A CARRIAGE RE
CALL:
RDOCT ;:READ AN OCTAL NUMBER
RETURN HERE ;:LOW ORDER BITS ARE ON TOP OF T
;:HIGH ORDER BITS ARE IN \$HIOCT

12357 *****
ERROR HANDLER ROUTINE

12359 THIS ROUTINE WILL INCREMENT THE ERROR FLAG AND THE ERROR COUNT,
SAVE THE ERROR ITEM NUMBER AND THE ADDRESS OF THE ERROR CALL
AND GO TO \$ERRTYP ON ERROR
THE SWITCH OPTIONS PROVIDED BY THIS ROUTINE ARE:
SW15=1 HALT ON ERROR
HALT CAN OCCUR BEFORE AND AFTER THE ERROR TYPEOU
SW13=1 INHIBIT ERROR TYPEOUTS
SW10=1 BELL ON ERROR
SW09=1 LOOP ON ERROR
CALL ERROR N ;:ERROR=EMT AND N=ERROR ITEM NUMBER

12401 *****
ERROR MESSAGE TIMEOUT ROUTINE

12402 THIS ROUTINE USES THE "ITEM CONTROL BYTE" (\$ITEMB) TO DETERMINE
ERROR IS TO BE REPORTED. IT THEN OBTAINS, FROM THE "ERROR TABLE
AND REPORTS THE APPROPRIATE INFORMATION CONCERNING THE ERROR.
IT IS A COPY OF THE \$ERRTYP SUBROUTINE FROM SYSMAC.
WITH ONLY MINOR CHANGES
FIRST IF SWITCH 6 IS SET AND SWITCH 8 RESET THEN
ALL REGISTER CONTENTS WILL BE TYPED BEFOR REPORTING THE ERROR

12409 SECOND IF THE CURRENT ERROR HAS THE SAME ITEM NUMBER
AS THE PREVIOUS ERROR THEN ONLY THE DATA WILL BE TYPED
AND NOT THE ERROR MESSAGE AND HEADER.

12627 *****

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1221
1222
1223
1224
1225
1226
1227
1228
1229
1230
1231
1232
1233
1234
1235
1236
1237
1238
1239
1240
1241

MAINDEC-11-DERHAB-A

12631

BINARY TO OCTAL (ASCII) AND TYPE

12633

THIS ROUTINE IS USED TO CHANGE A 16-BIT BINARY NUMBER TO A 5-DIG
OCTAL (ASCII) NUMBER AND TYPE IT.
\$TYPOS---ENTER HERE TO SETUP SUPPRESS ZEROS AND NUMBER OF DIGITS
CALL:

MOV NUM, -(SP) ;:NUMBER TO BE TYPED
TYPOS ;:CALL FOR TYPEOUT
.BYTE N ;:N=1 TO 6 FOR NUMBER OF DIGITS
.BYTE M ;:M=1 OR 0
;:1=TYPE LEADING ZEROS
;:0=SUPPRESS LEADING ZER

\$TYPON----ENTER HERE TO TYPE OUT WITH THE SAME PARAMETERS AS THE
\$TYPOS OR \$TYPOC

CALL:

MOV NUM, -(SP) ;:NUMBER TO BE TYPED
TYPON ;:CALL FOR TYPEOUT

\$TYPOC---ENTER HERE FOR TYPEOUT OF A 16 BIT NUMBER

CALL:

MOV NUM, -(SP) ;:NUMBER TO BE TYPED
TYPOC ;:CALL FOR TYPEOUT

12709

TRAP DECODER

12711

THIS ROUTINE WILL PICKUP THE LOWER BYTE OF THE "TRAP" INSTRUCTIO
AND USE IT TO INDEX THROUGH THE TRAP TABLE FOR THE STARTING ADDR
OF THE DESIRED ROUTINE. THEN USING THE ADDRESS OBTAINED IT WILL
GO TO THAT ROUTINE.

12724

TRAP TABLE

12726

THIS TABLE CONTAINS THE STARTING ADDRESSES OF THE ROUTINES CALLE
BY THE "TRAP" INSTRUCTION.


```

.*TITLE MAINDEC-11-DETHAB-A
.*COPYRIGHT (C) 1975-1976
.*DIGITAL EQUIPMENT CORP.
.*MAYNARD, MASS. 01754
.*
.*THIS PROGRAM WAS ASSEMBLED USING THE PDP-11 MAINDEC SYSMAC
.*PACKAGE (MAINDEC-11-DZQAC-C1),MAR 24, 1976.
.*

```

.SBTTL OPERATIONAL SWITCH SETTINGS

```

.*
.*      SWITCH          USE
.*      -----
.*      15             HALT ON ERROR
.*      14             LOOP ON TEST
.*      13             INHIBIT ERROR TYPEOUTS
.*      11             INHIBIT ITERATIONS
.*      10             BELL ON ERROR
.*      9              LOOP ON ERROR
.*      8              LOOP ON TEST IN SWR<7:0>
.*      7              STOP FURTHER COMPARES IF SW08 IS LOW
.*      6              TYPE ALL REG. WITH ERROR IF SW8 LOW

```

.SBTTL BASIC DEFINITIONS

.*INITIAL ADDRESS OF THE STACK POINTER *** 1000 ***

001000

```

STACK= 1000
.EQUIV EMT.ERROR      ::BASIC DEFINITION OF ERROR CALL
.EQUIV IOT.SCOPE     ::BASIC DEFINITION OF SCOPE CALL

```

.*MISCELLANEOUS DEFINITIONS

000011
000012
000015
000200
177776

177774
177772
177570
177570

```

HT= 11             ::CODE FOR HORIZONTAL TAB
LF= 12             ::CODE FOR LINE FEED
CR= 15             ::CODE FOR CARRIAGE RETURN
CRLF= 200         ::CODE FOR CARRIAGE RETURN-LINE FEED
PS= 177776        ::PROCESSOR STATUS WORD
.EQUIV PS,PSW
STKLMT= 177774    ::STACK LIMIT REGISTER
PIRQ= 177772     ::PROGRAM INTERRUPT REQUEST REGISTER
DSWR= 177570     ::HARDWARE SWITCH REGISTER
DDISP= 177570    ::HARDWARE DISPLAY REGISTER

```

.*GENERAL PURPOSE REGISTER DEFINITIONS

000000
000001
000002
000003
000004
000005
000006
000007

```

R0= %0            ::GENERAL REGISTER
R1= %1            ::GENERAL REGISTER
R2= %2            ::GENERAL REGISTER
R3= %3            ::GENERAL REGISTER
R4= %4            ::GENERAL REGISTER
R5= %5            ::GENERAL REGISTER
R6= %6            ::GENERAL REGISTER
R7= %7            ::GENERAL REGISTER
.EQUIV R6,SP      ::STACK POINTER
.EQUIV R7,PC      ::PROGRAM COUNTER

```

.*PRIORITY LEVEL DEFINITIONS

000000

PRO= 0 ::PRIORITY LEVEL 0

1207 000040
1208 000100
1209 000140
1210 000200
1211 000240
1212 000300
1213 000340

PR1 = 40
PR2 = 100
PR3 = 140
PR4 = 200
PR5 = 240
PR6 = 300
PR7 = 340

:: PRIORITY LEVEL 1
:: PRIORITY LEVEL 2
:: PRIORITY LEVEL 3
:: PRIORITY LEVEL 4
:: PRIORITY LEVEL 5
:: PRIORITY LEVEL 6
:: PRIORITY LEVEL 7

1214 100000
1215 040000
1216 020000
1217 010000
1218 004000
1219 002000
1220 001000
1221 000400
1222 000200
1223 000100
1224 000040
1225 000020
1226 000010
1227 000004
1228 000002
1229 000001

:: *"SWITCH REGISTER" SWITCH DEFINITIONS

SW15 = 100000
SW14 = 40000
SW13 = 20000
SW12 = 10000
SW11 = 4000
SW10 = 2000
SW09 = 1000
SW08 = 400
SW07 = 200
SW06 = 100
SW05 = 40
SW04 = 20
SW03 = 10
SW02 = 4
SW01 = 2
SW00 = 1

.EQUIV SW09, SW9
.EQUIV SW08, SW8
.EQUIV SW07, SW7
.EQUIV SW06, SW6
.EQUIV SW05, SW5
.EQUIV SW04, SW4
.EQUIV SW03, SW3
.EQUIV SW02, SW2
.EQUIV SW01, SW1
.EQUIV SW00, SW0

1230 100000
1231 040000
1232 020000
1233 010000
1234 004000
1235 002000
1236 001000
1237 000400
1238 000200
1239 000100
1240 000040
1241 000020
1242 000010
1243 000004
1244 000002
1245 000001

:: *DATA BIT DEFINITIONS (BIT00 TO BIT15)

BIT15 = 100000
BIT14 = 40000
BIT13 = 20000
BIT12 = 10000
BIT11 = 4000
BIT10 = 2000
BIT09 = 1000
BIT08 = 400
BIT07 = 200
BIT06 = 100
BIT05 = 40
BIT04 = 20
BIT03 = 10
BIT02 = 4
BIT01 = 2
BIT00 = 1

.EQUIV BIT09, BIT9
.EQUIV BIT08, BIT8
.EQUIV BIT07, BIT7

1246 100000
1247 040000
1248 020000
1249 010000
1250 004000
1251 002000
1252 001000
1253 000400
1254 000200
1255 000100
1256 000040
1257 000020
1258 000010
1259 000004
1260 000002
1261 000001

1262

```

1363 .EQUIV BIT06,BIT6
1364 .EQUIV BIT05,BIT5
1365 .EQUIV BIT04,BIT4
1366 .EQUIV BIT03,BIT3
1367 .EQUIV BIT02,BIT2
1368 .EQUIV BIT01,BIT1
1369 .EQUIV BIT00,BIT0
1370
1371
1372
1373 000004
1374 000010
1375 000014
1376 000014
1377 000014
1378 000020
1379 000024
1380 000030
1381 000034
1382 000060
1383 000064
1384 000240
1385
1386
1387 000000
1388
1389
1390
1391
1392 000174 000000
1393 000176 000000
1394
1395 000200 000137 005522
1396
1397 000046 037544
1398
1399 000052 060000
1400
1401 000210 000210 005532
1402
1403
1404
1405
1406
1407
1408
1409
1410
1411
1412
1413 000250
1414
1415
1416
1417 177572
1418 177574

```

```

; *BASIC "CPU" TRAP VECTOR ADDRESSES
ERRVEC= 4 ;: TIME OUT AND OTHER ERRORS
RESVEC= 10 ;: RESERVED AND ILLEGAL INSTRUCTIONS
TBITVEC=14 ;: "T" BIT
TRTVEC= 14 ;: TRACE TRAP
BPTVEC= 14 ;: BREAKPOINT TRAP (BPT)
IOTVEC= 20 ;: INPUT/OUTPUT TRAP (IOT) **SCOPE**
PWRVEC= 24 ;: POWER FAIL
EMTVEC= 30 ;: EMULATOR TRAP (EMT) **ERROR**
TRAPVEC=34 ;: "TRAP" TRAP
TKVEC= 60 ;: TTY KEYBOARD VECTOR
TPVEC= 64 ;: TTY PRINTER VECTOR
PIRQVEC=240 ;: PROGRAM INTERRUPT REQUEST VECTOR

.SBTTL TRAP CATCHER

.=0
; *ALL UNUSED LOCATIONS FROM 4 - 776 CONTAIN A ".+2,HALT"
; *SEQUENCE TO CATCH ILLEGAL TRAPS AND INTERRUPTS
; *LOCATION 0 CONTAINS 0 TO CATCH IMPROPERLY LOADED VECTORS
.=174
DISPREG: .WORD 0 ;: SOFTWARE DISPLAY REGISTER
SWREG: .WORD 0 ;: SOFTWARE SWITCH REGISTER
.SBTTL STARTING ADDRESS(ES)
JMP @#BEGIN ;: JUMP TO STARTING ADDRESS OF PROGRAM
.=46
$ENDAD
.=52
60000
.=210
JMP @#BEGIN2 ;: JUMP SELECT TEST
; *STARTING ADDRESS 200 FOR NORMAL STARTS
; *THIS WILL TEST ALL RPO4'S ON THE SYSTEM A SINGLE DRIVE AT A TIME
; *
; *STARTING ADDRESS 210 WILL TEST ONLY ONE SPECIFIED DRIVE
; *
; *STARTING ADDRESS 220 WILL JUMP OVER THE TESTS REQUIRING AN OPERATOR
; *AT THE DRIVE
.SBTTL MEMORY MANAGEMENT DEFINITIONS

; *KT11 VECTOR ADDRESS
MMVEC= 250

; *KT11 STATUS REGISTER ADDRESSES
SRO= 177572
SRI= 177574

```

1419 177576 SR2= 177576
1420 172516 SR3= 172516

;*KERNEL "I" PAGE DESCRIPTOR REGISTERS

1421
1422
1423
1424 172300 KIPDR0= 172300
1425 172302 KIPDR1= 172302
1426 172304 KIPDR2= 172304
1427 172306 KIPDR3= 172306
1428 172310 KIPDR4= 172310
1429 172312 KIPDR5= 172312
1430 172314 KIPDR6= 172314
1431 172316 KIPDR7= 172316

;*KERNEL "I" PAGE ADDRESS REGISTERS

1432
1433
1434
1435 172340 KIPAR0= 172340
1436 172342 KIPAR1= 172342
1437 172344 KIPAR2= 172344
1438 172346 KIPAR3= 172346
1439 172350 KIPAR4= 172350
1440 172352 KIPAR5= 172352
1441 172354 KIPAR6= 172354
1442 172356 KIPAR7= 172356

;*****
.=1110

1443
1444
1445 CO1110

.SBTTL COMMON TAGS

*THIS TABLE CONTAINS VARIOUS COMMON STORAGE LOCATIONS
*USED IN THE PROGRAM.

Address	Tag Name	Value	Description
1446			
1447			
1448			
1449			
1450			
1451			
1452		001100	
1453	001100		\$CMTAG: .WORD 0
1454	001100	000000	\$PASS: .WORD 0
1455	001102	000	\$STNM: .BYTE 0
1456	001103	000	\$ERFLG: .BYTE 0
1457	001104	000000	\$ICNT: .WORD 0
1458	001106	000000	\$LPADR: .WORD 0
1459	001110	000000	\$LPERR: .WORD 0
1460	001112	000000	\$ERTTL: .WORD 0
1461	001114	000	\$ITEMB: .BYTE 0
1462	001115	001	\$ERMAX: .BYTE 1
1463	001116	000000	\$FRRPC: .WORD 0
1464	001120	000000	\$GDADR: .WORD 0
1465	001122	000000	\$BDADR: .WORD 0
1466	001124	000000	\$GDDAT: .WORD 0
1467	001126	000000	\$BDDAT: .WORD 0
1468	001130	000000	
1469	001132	000000	
1470	001134	000	\$AUTOB: .BYTE 0
1471	001135	000	\$INTAG: .BYTE 0
1472	001136	000000	
1473	001140	177570	\$SWR: .WORD DSWR
1474	001142	177570	\$DISPLAY: .WORD DDISP
1475	001144	177560	\$TKS: 177560
1476	001146	177562	\$TKB: 177562
1477	001150	177564	\$TPS: 177564
1478	001152	177566	\$TPB: 177566
1479	001154	000	\$NULL: .BYTE 0
1480	001155	002	\$FILLS: .BYTE 2
1481	001156	012	\$FILLC: .BYTE 12
1482	001157	000	\$TPFLG: .BYTE 0
1483	001160	000000	\$REGAD: .WORD 0
1484			
1485	001162	000000	\$REG0: .WORD 0
1486	001164	000000	\$REG1: .WORD 0
1487	001166	000000	\$REG2: .WORD 0
1488	001170	000000	\$REG3: .WORD 0
1489	001172	000000	\$REG4: .WORD 0
1490	001174	000000	\$REG5: .WORD 0
1491	001176	000000	\$TMP0: .WORD 0
1492	001200	000000	\$TMP1: .WORD 0
1493	001202	000000	\$TMP2: .WORD 0
1494	001204	000000	\$TMP3: .WORD 0
1495	001206	000000	\$TMP4: .WORD 0
1496	001210	000000	\$TMP5: .WORD 0
1497	001212	000000	\$TIMES: 0
1498	001214	000000	\$ESCAPE: 0
1499	001216	177607	\$BELL: .ASCIZ <207><377><377>
1500	001222	077	\$QUES: .ASCII /?/
1501	001223	015	\$CRLF: .ASCII <15>

000377

01

H03

MAINDEC-11-DERHAB-A MACY11 27(732) 22-SEP-76 15:11 PAGE 35
DERHAB.SRC COMMON TAGS

1502 001224 000012
1503

\$LF: .ASCIZ <12> ;;LINE FEED
;*****

00
00

.SBTTL ERROR POINTER TABLE

::*THIS TABLE CONTAINS THE INFORMATION FOR EACH ERROR THAT CAN OCCUR.
::*THE INFORMATION IS OBTAINED BY USING THE INDEX NUMBER FOUND IN
::*LOCATION \$ITEMB. THIS NUMBER INDICATES WHICH ITEM IN THE TABLE IS PERTINENT.
::*NOTE1: IF \$ITEMB IS 0 THE ONLY PERTINENT DATA IS (\$ERRPC).
::*NOTE2: EACH ITEM IN THE TABLE CONTAINS 4 POINTERS EXPLAINED AS FOLLOWS:

::* EM ::POINTS TO THE ERROR MESSAGE
::* DH ::POINTS TO THE DATA HEADER
::* DT ::POINTS TO THE DATA
::* DF ::POINTS TO THE DATA FORMAT

1504
1505
1506
1507
1508
1509
1510
1511
1512
1513
1514
1515
1516
1517
1518 001226
1519
1520
1521
1522
1523
1524
1525
1526
1527 001226 046700
1528
1529
1530
1531 001230 065121
1532
1533 001232 066554
1534 001234 066736
1535
1536
1537 001236 047052
1538
1539
1540
1541
1542
1543 001240 065121
1544
1545 001242 066554
1546 001244 066736
1547
1548
1549 001246 047254
1550
1551
1552
1553
1554 001250 065121
1555
1556 001252 066554
1557 001254 066736
1558
1559

\$ERRTB:

::*****
;

:ITEM 1

EM1 :THE RH BASE ADDRESS WAS 772040
DH1 :INDICATING A RS04 OR RS03
DT1 :BUT THE DRIVE TYPE DID NOT
DF1 :CONTAIN 0,1,2,3 OR 4
:PC
:RHDT
:\$ERRPC,\$BDDAT,0
:0

:ITEM 2

EM2 :THE RH BASE ADDRESS WAS 776700
:INDICATING AN RP04, RP05, OR RP06
:BUT THE DRIVE TYPE DID NOT
:CONTAIN 20020 24020, 20021, 24021,
:20022, OR 24022

:ITEM 3

EM3 :THE RH BASE ADDRESS WAS 772440
:INDICATING A TM02. BUT THE
:DRIVE TYPE DID NOT CONTAIN
:142010 INDICATING NO
:TM02
:PC
:RHDT
:\$ERRPC,\$BDDAT,0
:0,0

:ITEM 4

1560	001256	047410	EM4		: THE RH BASE ADDRESS WAS 776300
1561					: INDICATING MORE THAN ONE
1562					: RH DEVICE. BUT THE DRIVE
1563					: TYPE DID NOT CONTAIN
1564					: 0,1,2,3,4,20020,24020,20021,24021,
1565					: 20022,24022, OR 142010 INDICATING NO
1566					: RH DEVICE IS PRESENT
1567	001260	065121	DH1		: PC
1568					: RHDT
1569	001262	066554	DF1		: \$ERRPC, \$BDDAT, 0
1570	001264	066736	DF1		: 0, 0
1571					
1572				: ITEM 5	
1573	001266	047707	EM5		: THE UNIBUS TIMED OUT
1574					: FOR EACH OF THE FOLLOWING
1575					: ADDRESSES, INDICATING
1576					: NO RH ON THE SYSTEM
1577					:
1578					: SO ABORT PROGRAM
1579	001270	065130	DH5		: PC
1580					: UNIBUS ADDRESSES ON WHICH
1581					: TIME OUT OCCURRED
1582	001272	066562	DT5		: \$ERRPC, RSCS1, RPCS1, TMCS1, MIXCS1
1583	001274	066740	DF5		: 0, 0, 0, 0, 0
1584					
1585				: ITEM 6	
1586	001276	050062	EM6		: AFTER AN RH CLEAR (BIT #5
1587					: IN RHCS2) RHCS2 DOES NOT
1588					: HAVE ONLY IR AND UNIT
1589					: NUMBER
1590	001300	065204	DH6		: PC
1591					: TEST NO
1592					: RHCS2 GOOD
1593					: RHCS2 BAD
1594	001302	066576	DT6		: \$ERRPC, TSTNM, \$GDDAT, \$BDDAT
1595	001304	066745	DF6		: 0, 0, 0, 0
1596					
1597				: ITEM 7	
1598	001306	050200	EM7		: AFTER CLEARING THE RH AND
1599					: WRITING ONE WORD INTO
1600					: RHDB AND READING IT
1601					: BACK CAUSED RHDB TO
1602					: HAVE WRONG VALUE GIVEN IN BAD RHDB
1603	001310	065247	DH7		: PC
1604					: TEST NO
1605					: RHDB GOOD
1606					: RHDB BAD
1607	001312	066576	DT6		: \$ERRPC, TSTNM, \$GDDAT, \$BDDAT
1608	001314	066745	DF6		: 0, 0, 0, 0
1609					
1610				: ITEM 10	
1611	001316	050372	EM10		: AFTER CLEARING THE RH AND
1612					: WRITING ONE WORD INTO
1613					: RHDB AND READING IT
1614					: BACK, CAUSED RHCS2 TO
1615					: HAVE WRONG DATA

1616				:GIVEN IN BAD RHCS2
1617	001320	065204	DH6	:PC
1618				:TEST NUMBER
1619				:RHCS2 GOOD
1620				:RHCS2 BAD
1621	001322	066576	DT6	:SERRPC, TSTNM, \$GDDAT, \$BDDAT
1622	001324	066745	DF6	:0,0,0,0
1623				
1624				
1625	001326	050565	;ITEM 11 EM11	:AFTER CLEARING THE RH AND
1626				:WRITING ONE WORD INTO
1627				:RHDB AND READING IT
1628				:BACK, CAUSED RHCS1 TO
1629				:HAVE WRONG DATA
1630				:GIVEN IN BAD RHCS1
1631	001330	065307	DH11	:PC
1632				:TEST NUMBER
1633				:RHCS1 GOOD
1634				:RHCS1 BAD
1635	001332	066576	DT6	:SERRPC, TSTNM, \$GDDAT, \$BDDAT
1636	001334	066745	DF6	:0,0,0,0
1637				
1638				
1639	001336	050760	;ITEM 12 EM12	:AFTER CLEARING THE RH AND
1640				:WRITING ONE WORD INTO
1641				:RHDB AND READING IT
1642				:BACK, CAUSED RHCS3 TO
1643				:HAVE WRONG DATA
1644				:GIVEN IN BAD RHCS3
1645	001340	065351	DH12	:PC
1646				:TEST NUMBER
1647				:RHCS3 GOOD
1648				:RHCS3 BAD
1649	001342	066576	DT6	:SERRPC, TSTNM, \$GDDAT, \$BDDAT
1650	001344	066745	DF6	:0,0,0,0
1651				
1652				
1653	001346	051153	;ITEM 13 EM13	:AFTER CLEARING THE RH AND
1654				:WRITING ONE WORD INTO
1655				:RHDB AND READING IT
1656				:BACK, CAUSED RHBA TO
1657				:HAVE WRONG DATA
1658				:GIVEN IN BAD RHBA
1659	001350	065413	DH13	:PC
1660				:TEST NUMBER
1661				:RHBA GOOD
1662				:RHBA BAD
1663	001352	066576	DT6	:SERRPC, TSTNM, \$GDDAT, \$BDDAT
1664	001354	066745	DF6	:0,0,0,0
1665				
1666				
1667	001356	051345	;ITEM 14 EM14	:AFTER CLEARING THE RH AND
1668				:WRITING ONE WORD INTO
1669				:RHDB AND READING IT
1670				:BACK, CAUSED RHBAE TO
1671				:HAVE WRONG DATA

1672					: GIVEN IN BAD RHBAE
1673	001360	065453		DH14	: PC
1674					: TEST NUMBER
1675					: RHBAE GOOD
1676					: RHBAE BAD
1677	001362	066576		DT6	: \$ERRPC, TSTNM, \$GDDAT, \$BDDAT
1678	001364	066745		DF6	: 0,0,0,0
1679					
1680					: ; ITEM 15
1681	001366	051540		EM15	: AFTER CLEARING THE RH AND
1682					: WRITING ONE WORD INTO
1683					: RHDB AND READING IT
1684					: BACK, CAUSED RHWC TO
1685					: HAVE WRONG DATA
1686					: GIVEN IN BAD RHWC
1687	001370	065515		DH15	: PC
1688					: TEST NUMBER
1689					: RHWC GOOD
1690					: RHWC BAD
1691	001372	066576		DT6	: \$ERRPC, TSTNM, \$GDDAT, \$BDDAT
1692	001374	066745		DF6	: 0,0,0,0
1693					
1694					: ; ITEM 16
1695	001376	051732		EM16	: AFTER CLEARING THE RH AND
1696					: WRITING ONE WORD INTO
1697					: RHDB
1698					: CAUSED RHCS2 TO
1699					: HAVE WRONG DATA
1700					: GIVEN IN BAD RHCS2
1701	001400	065204		DH6	: PC
1702					: TEST NUMBER
1703					: RHCS2 GOOD
1704					: RHCS2 BAD
1705	001402	066576		DT6	: \$ERRPC, TSTNM, \$GDDAT, \$BDDAT
1706	001404	066745		DF6	: 0,0,0,0
1707					
1708					: ; ITEM 17
1709	001406	052125		EM17	: AFTER CLEARING THE RH AND
1710					: WRITING TWO WORD INTO
1711					: RHDB AND READING IT
1712					: BACK, CAUSED RHDB TO
1713					: HAVE WRONG DATA
1714					: GIVEN IN BAD RHDB
1715	001410	065247		DH7	: PC
1716					: TEST NUMBER
1717					: RHDB GOOD
1718					: RHDB BAD
1719	001412	066576		DT6	: \$ERRPC, TSTNM, \$GDDAT, \$BDDAT
1720	001414	066745		DF6	: 0,0,0,0
1721					
1722					: ; ITEM 20
1723	001416	052317		EM20	: AFTER CLEARING THE RH AND
1724					: WRITING TWO WORD INTO
1725					: RHDB AND READING ONE
1726					: BACK, CAUSED RHCS2 TO
1727					: HAVE WRONG DATA

1728					; GIVEN IN BAD RHCS2
1729	001420	065204	DH6		; PC
1730					; TEST NUMBER
1731					; RHCS2 GOOD
1732					; RHCS2 BAD
1733	001422	066576	DT6		; \$ERRPC, TSTNM, \$GDDAT, \$BDDAT
1734	001424	066745	DF6		; 0,0,0,0
1735					
1736				; ITEM 21	
1737	001426	052513	EM21		; AFTER CLEARING THE RH AND
1738					; WRITING TWO WORD INTO
1739					; RHDB AND READING IT
1740					; BACK TWICE, CAUSED RHDB TO
1741					; HAVE WRONG DATA
1742					; GIVEN IN BAD RHDB
1743	001430	065247	DH7		; PC
1744					; TEST NUMBER
1745					; RHDB GOOD
1746					; RHDB BAD
1747	001432	066576	DT6		; \$ERRPC, TSTNM, \$GDDAT, \$BDDAT
1748	001434	066745	DF6		; 0,0,0,0
1749					
1750				; ITEM 22	
1751	001436	052713	EM22		; AFTER CLEARING THE RH AND
1752					; WRITING TWO WORD INTO
1753					; RHDB AND READING IT
1754					; BACK TWICE, CAUSED RHCS2 TO
1755					; HAVE WRONG DATA
1756					; GIVEN IN BAD RHCS2
1757	001440	065204	DH6		; PC
1758					; TEST NUMBER
1759					; RHCS2 GOOD
1760					; RHCS2 BAD
1761	001442	066576	DT6		; \$ERRPC, TSTNM, \$GDDAT, \$BDDAT
1762	001444	066745	DF6		; 0,0,0,0
1763					
1764				; ITEM 23	
1765	001446	053114	EM23		; AFTER CLEARING THE RH AND
1766					; WRITING EIGHT WORD INTO
1767					; RHDB
1768					; CAUSED RHCS2 TO
1769					; HAVE WRONG DATA
1770					; GIVEN IN BAD RHCS2
1771	001450	065204	DH6		; PC
1772					; TEST NUMBER
1773					; RHCS2 GOOD
1774					; RHCS2 BAD
1775	001452	066576	DT6		; \$ERRPC, TSTNM, \$GDDAT, \$BDDAT
1776	001454	066745	DF6		; 0,0,0,0
1777					
1778				; ITEM 24	
1779	001456	053311	EM24		; THE RH WAS CLEARED
1780					; AND A PATTERN OF 8 WORDS
1781					; WERE WRITTEN INTO RHDB
1782					; READING RHDB FOR THE
1783					; "N" TH. TIME GAVE WRONG

1784				: VALUE IN RHDB
1785				: N IS GIVEN IN "WORD NO"
1786	001460	065555	DH24	: PC
1787				: TEST NO
1788				: WORD NO
1789				: RHDB GOOD
1790				: RHDB BAD
1791	001462	066610	DT24	: \$ERRPC, TSTNM, \$GDDAT, \$BDDAT
1792	001464	066751	DF24	: 0,0,0,0,0
1793				
1794			: ITEM 25	
1795	001466	053541	EM25	: THE RH WAS CLEARED AND
1796				: A PATTERN OF 8 WORDS WERE
1797				: WRITTEN INTO RHDB
1798				: AFTER READING ALL 8 WORDS
1799				: FOLLOWING REGISTER CONTAINED WRONG
1800				: VALUE
1801	001470	065204	DH6	: PC
1802				: TEST NO
1803				: RHCS2 GOOD
1804				: RHCS2 BAD
1805	001472	066576	DT6	: \$ERRPC, TSTNM, \$GDDAT, \$BDDAT
1806	001474	066745	DF6	: 0,0,0,0
1807				
1808			: ITEM 26	
1809	001476	053541	EM25	
1810	001500	065307	DH11	
1811	001502	066576	DT6	
1812	001504	066745	DF6	: 0,0
1813				
1814			: ITEM 27	
1815	001506	053541	EM25	
1816	001510	065351	DH12	: PC
1817				: TEST NO
1818				: RHCS3 GOOD
1819				: RHCS3 BAD
1820	001512	066576	DT6	: \$ERRPC, TSTNM, \$GDDAT, \$BDDAT
1821	001514	066745	DF6	: 0,0,0,0
1822				
1823			: ITEM 30	
1824	001516	053541	EM25	
1825	001520	065413	DH13	: PC
1826				: TEST NO
1827				: RHBA GOOD
1828				: RHBA BAD
1829	001522	066576	DT6	: \$ERRPC, TSTNM, \$GDDAT, \$BDDAT
1830	001524	066745	DF6	: 0,0,0,0
1831				
1832			: ITEM 31	
1833	001526	053541	EM25	
1834	001530	065453	DH14	: PC
1835				: TEST NO
1836				: RHBAE GOOD
1837				: RHBAE BAD
1838	001532	066576	DT6	: \$ERRPC, TSTNM, \$GDDAT, \$BDDAT
1839	001534	066745	DF6	: 0,0,0,0

1996				: ON CHECKING RHCSI
1997				: IT DID NOT CONTAIN SC,MCPE
1998				: AND RDY
1999	001600	065307	DH11	: PC
1900				: TEST NUMBER
1901				: RHCSI GOOD
1902				: RHCSI BAD
1903	001602	066576	DT6	: SERRPC,TSTNM,\$GDDAT,\$BDDAT
1904	001604	066745	DF6	: C,O,O,O
1905				
1906				: ITEM 37
1907	001606	055021	EM37	: RH WAS CLEARED
1908				: RHERI WAS CHECKED
1909				: PAT (RHCS2-BIT #4) WAS SET
1910				: RHERI WAS READ
1911				: "1" WAS WRITTEN IN "TRE" RHCSI
1912				: AN RH CLEAR WAS TO
1913				: GIVE WHAT IS IN "GOOD"
1914				: BUT GAVE WHAT IS IN "BAD"
1915	001610	065307	DH11	: PC
1916				: TEST NO
1917				: RHCSI GOOD
1918				: RHCSI BAD
1919	001612	066576	DT6	: SERRPC,TSTNM,\$GDDAT,\$BDDAT
1920	001614	066745	DF6	: C,O,O,O
1921				
1922				: ITEM 40
1923	001616	055021	EM37	
1924	001620	065204	DH6	
1925	001622	066576	DT6	
1926	001624	066745	DF6	
1927				
1928				: ITEM 41
1929	001626	055021	EM37	
1930	001630	065351	DH12	
1931	001632	066576	DT6	
1932	001634	066745	DF6	
1933				
1934				: ITEM 42
1935	001636	055021	EM37	
1936	001640	065413	DH13	
1937	001642	066576	DT6	
1938	001644	066745	DF6	
1939				
1940				: ITEM 43
1941	001646	055021	EM37	
1942	001650	065453	DH14	
1943	001652	066576	DT6	
1944	001654	066745	DF6	
1945				
1946				: ITEM 44
1947	001656	055021	EM37	
1948	001660	065515	DH15	
1949	001662	066576	DT6	
1950	001664	066745	DF6	
1951				

1952			:ITEM 45		
1953	001566	055323		EM45	: AFTER AN RH CLEAR
1954					: "1" WAS WRITTEN IN THE DISK
1955					: ADDRESS REGISTER (IN TAPE
1956					: DRIVES CALLED FRAME COUNT)
1957					: PAT IN RHCSI WAS SET
1958					: ON READING RHDA (IN TAPE
1959					: DRIVES RHFC) IT DID NOT
1960					: CONTAIN "1"
1961					: RHDA=RHFC
1962	001670	065667		DH45	: PC
1963					: TEST NO
1964					: RHDA GOOD
1965					: RHDA BAD
1966	001672	066576		DT6	: SERRPC, TSTNM, SGDDAT, SBDDAT
1967	001674	066745		DF6	: 0,0,0,0
1968					
1969			:ITEM 46		
1970	001676	055561		EM46	: AFTER AN RH CLEAR
1971					: "1" WAS WRITTEN IN THE
1972					: DISK ADDRESS REGISTER (IN
1973					: TAPE
1974					: PAT IN RHCSI WAS SET
1975					: ON READING RHDA (IN TAPE
1976					:) FOLLOWING
1977					: REGISTER DID NOT CONTAIN
1978					: WHAT IS IN "GOOD"
1979	001700	065307		DH11	: PC
1980					: TEST NO
1981					: RHCSI GOOD
1982					: RHCSI BAD
1983	001702	066576		DT6	: SERRPC, TSTNM, SGDDAT, SBDDAT
1984	001704	066745		DF6	
1985					
1986			:ITEM 47		
1987	001706	055561		EM46	
1988	001710	065204		DH6	
1989	001712	066576		DT6	
1990	001714	066745		DF6	
1991					
1992			:ITEM 50		
1993	001716	055561		EM46	
1994	001720	065351		DH12	
1995	001722	066576		DT6	
1996	001724	066745		DF6	
1997					
1998			:ITEM 51		
1999	001726	055561		EM46	
2000	001730	065413		DH13	
2001	001732	066576		DT6	
2002	001734	066745		DF6	
2003					
2004			:ITEM 52		
2005	001736	055561		EM46	
2006	001740	065453		DH14	
2007	001742	066576		DT6	

2008	001744	066745	DF6	
2009				
2010				: ITEM 53
2011	001746	055561	EM46	
2012	001750	065515	DH15	
2013	001752	066576	DT6	
2014	001754	066745	DF6	
2015				: ITEM 54
2016				
2017	001756	056062	EM54	: AN RH CLEAR (RHCS2 BIT #5)
2018				: WAS GIVEN
2019				: A16 (RHCS1 BIT #8) WAS SET
2020				: ON READING THE FOLLOWING
2021				: REGISTER IT DID NOT
2022				: CONTAIN WHAT IS IN "GOOD"
2023	001760	065307	DH11	
2024	001762	066576	DT6	
2025	001764	066745	DF6	
2026				: ITEM 55
2027				
2028	001766	056062	EM54	
2029	001770	065453	DH14	
2030	001772	066576	DT6	
2031	001774	066745	DF6	
2032				: ITEM 56
2033				
2034	001776	056273	EM56	: AN RH CLEAR (RHCS2 BIT #5)
2035				: WAS GIVEN
2036				: A16 WAS WRITTEN IN RHCS1
2037				: ALL ZEROS WERE WRITTEN IN RHBAE
2038				: THE FOLLOWING REGISTER DID
2039				: NOT CONTAIN WHAT IS IN "GOOD"
2040	002000	065453	DH14	
2041	002002	066576	DT6	
2042	002004	066745	DF6	
2043				: ITEM 57
2044				
2045	002006	056273	EM56	
2046	002010	065307	DH11	
2047	002012	066576	DT6	
2048	002014	066745	DF6	
2049				: ITEM 60
2050				
2051	002016	056531	EM60	: AN RH CLEAR (RHCS2 BIT #5)
2052				: WAS GIVEN
2053				: A17 (RHCS1 BIT #9) WAS SET
2054				: ON READING THE FOLLOWING
2055				: REGISTER IT DID NOT
2056				: CONTAIN WHAT IS IN "GOOD"
2057	002020	065307	DH11	
2058	002022	066576	DT6	
2059	002024	066745	DF6	
2060				: ITEM 61
2061				
2062	002026	056531	EM60	
2063	002030	065453	DH14	

2120			
2121			
2122			
2123			
2124			
2125			
2126			
2127	002120	065307	DH11
2128	002122	066576	DT6
2129	002124	066745	DF6
2130			
2131			: ITEM 71
2132	002126	057646	EM70
2133	002130	065204	DH6
2134	002132	066576	DT6
2135	002134	066745	DF6
2136			
2137			: ITEM 72
2138	002136	057646	EM70
2139	002140	065351	DH12
2140	002142	066576	DT6
2141	002144	066745	DF6
2142			
2143			: ITEM 73
2144	002146	057646	EM70
2145	002150	065413	DH13
2146	002152	066576	DT6
2147	002154	066745	DF6
2148			
2149			: ITEM 74
2150	002156	057646	EM70
2151	002160	065453	DH14
2152	002162	066576	DT6
2153	002164	066745	DF6
2154			
2155			: ITEM 75
2156	002166	057646	EM70
2157	002170	065515	DH15
2158	002172	066576	DT6
2159	002174	066745	DF6
2160			
2161			: ITEM 76
2162	002176	060122	EM76
2163			
2164			
2165			
2166			
2167			
2168			
2169			
2170	002200	065307	DH11
2171	002202	066576	DT6
2172	002204	066745	DF6
2173			
2174			: ITEM 77
2175	002206	060122	EM76

: TWO SUCCESSIVE "GO" (RHCSI BIT #0)
 : WAS GIVEN WITHOUT GIVING
 : TIME FOR FIRST "GO" TO
 : COMPLETE
 : THE FOLLOWING REGISTER
 : DID NOT CONTAIN WHAT IS
 : IN "GOOD"

: RH CLEAR WAS GIVEN A 2 WORD
 : WRITE WAS DONE FROM A
 : LOCATION TAGED WRFROM
 : AND WITH BAI BIT SET
 : AT THE END OF THE WRITE
 : THE FOLLOWING REGISTER DID
 : NOT CONTAIN WHAT IS
 : IN GOOD

2176	002210	065204	DH6
2177	002212	066576	DT6
2178	002214	066745	DF6
2179			
2180			: ITEM 100
2181	002216	060122	EM76
2182	002220	065351	DH12
2183	002222	066576	DT6
2184	002224	066745	DF6
2185			
2186			: ITEM 101
2187	002226	060122	EM76
2188	002230	065413	DH13
2189	002232	066576	DT6
2190	002234	066745	DF6
2191			
2192			: ITE 102
2193	002236	060122	EM76
2194	002240	065453	DH14
2195	002242	066576	DT6
2196	002244	066745	DF6
2197			
2198			: ITEM 103
2199	002246	060122	EM76
2200	002250	065515	DH15
2201	002252	066576	DT6
2202	002254	066745	DF6
2203			
2204			: ITEM 104
2205	002256	060415	EM104
2206			
2207			
2208			
2209			
2210			
2211			
2212	002260	065727	DH104
2213	002262	066624	DT104
2214	002264	066756	DF104
2215			
2216			: ITEM 105
2217	002266	060415	EM104
2218	002270	065777	DH105
2219	002272	066640	DT105
2220	002274	066763	DF105
2221			
2222			: ITEM 106
2223	002276	060653	EM106
2224			
2225			
2226			
2227			
2228			
2229			
2230			
2231			

```

:RH CLEAR WAS GIVEN AN
:IPCK BIT SHOWN IN "IPCK" WAS SET
:ZEROS WERE MOVED INTO RHDB
:ON READING RHDB
:THE FOLLOWING REGISTER
:DID NOT CONTAIN WHAT
:IS IN GOOD
:PC TEST NO, IPCK, RHCS3 GOOD, RHCS3 BAD
:$ERRPC, TSTNM, IP, $GDDAT, $BDDAT
:0,0,0,0,0

```

```

:PC TST NO, IPCK, RHCS2 GOOD, RHCS2 BAD
:$ERRPC, TSTNM, IP, $GDDAT, $BDDAT
:0,0,0,0,0

```

```

:RH CLEAR WAS GIVEN AN
:IPCK BIT SHOWN IN "IPCK" WAS SET
:ZEROS WERE MOVED INTO
:RHDB FROM AN ODD WORD
:RHDB WAS READ
:AN RH CLEAR WAS GIVEN
:TO CLEAR ALL ERRORS
:THE FOLLOWING REGISTER
:DID NOT CONTAIN WHAT

```

2232					: IS IN GOOD
2233	002300	066047	DH106		: PC, TSTNM, IPCK, RHCS1 GOOD, RHCS1 BAD
2234	002302	066654	DT106		: \$ERRPC, TSTNM, IP, \$GDDAT, \$BDDAT
2235	002304	066770	DF106		: 0,0,0,0,0
2236					
2237				: ITEM 107	
2238	002306	060653	EM106		
2239	002310	065777	DH105		: PC, TSTNM, IPCK, RHCS2 GOOD, RHCS2 BAD
2240	002312	066640	DT105		
2241	002314	066763	DF105		
2242					
2243				: ITEM 110	
2244	002316	060653	EM106		
2245	002320	065727	DH104		
2246	002322	066624	DT104		
2247	002324	066756	DF104		
2248					
2249				: ITEM 111	
2250	002326	060653	EM106		
2251	002330	066117	DH111		: PC, TSTNM, IPCK, RHBA GOOD, RHBA BAD
2252	002332	066624	DT104		: \$ERRPC, TSTNM, IP, \$GDDAT, \$BDDAT
2253	002334	066756	DF104		: 0,0,0,0,C
2254					
2255				: ITEM 112	
2256	002336	060653	EM106		
2257	002340	066165	DH112		: PC, TSTNM, IPCK, RHBAE GOOD, RHBAE BAD
2258	002342	066624	DT104		: \$ERRPC, TSTNM, IP, \$GDDAT, \$BDDAT
2259	002344	066756	DF104		: 0,0,0,0,0
2260					
2261				: ITEM 113	
2262	002346	060653	EM106		
2263	002350	065235	DH113		: PC, TSTNM, IPCK, RHWC GOOD, RHWC BAD
2264	002352	066624	DT104		: \$ERRPC, TSTNM, IP, \$GDDAT, \$BDDAT
2265	002354	066756	DF104		: 0,0,0,0,0
2266					
2267				: ITEM 114	
2268	002356	061144	EM114		: RH CLEAR WAS GIVEN AN
2269					: A WRITE CHECK WAS DONE
2270					: THE FOLLOWING REGISTER
2271					: DID NOT CONTAIN WHAT IS
2272					: IN "GOOD"
2273	002360	065204	DH6		
2274	002362	066576	DT6		
2275	002364	066745	DF6		
2276					
2277				: ITEM 115	
2278	002366	061144	EM114		: RH CLEAR WAS GIVEN AN
2279					: A WRITE CHECK WAS DONE
2280					: THE FOLLOWING REGISTER
2281					: DID NOT CONTAIN WHAT IS
2282					: IN "GOOD"
2283	002370	065351	DH12		
2284	002372	066576	DT6		
2285	002374	066745	DF6		
2286					
2287				: ITEM 116	

2298	002376	061313	EM116
2299			
2290			
2291			
2292			
2293			
2294	002400	065307	DH11
2295	002402	066576	DT6
2296	002404	066745	DF6
2297			
2298			; ITEM 117
2299	002406	061313	EM116
2300	002410	065204	DH6
2301	002412	066576	DT6
2302	002414	066745	DF6
2303			
2304			; ITEM 120
2305	002416	061313	EM116
2306	002420	065351	DH12
2307	002422	066576	DT6
2308	002424	066745	DF6
2309			
2310			; ITEM 121
2311	002426	061313	EM116
2312	002430	065413	DH13
2313	002432	066576	DT6
2314	002434	066745	DF6
2315			
2316			; ITEM 122
2317	002436	061313	EM116
2318	002440	065453	DH14
2319	002442	066576	DT6
2320	002444	066745	DF6
2321			
2322			; ITEM 123
2323	002446	061313	EM116
2324	002450	065515	DH15
2325	002452	066576	DT6
2326	002454	066745	DF6
2327			
2328			; ITEM 124
2329	002456	061523	EM124
2330			
2331			
2332			
2333			
2334	002460	065351	DH12
2335	002462	066576	DT6
2336	002464	066745	DF6
2337			
2338			; ITEM 125
2339	002466	061523	EM124
2340	002470	065307	DH11
2341	002472	066576	DT6
2342	002474	066745	DF6
2343			

```

:RH CLEAR WAS GIVEN AN
:A WRITE CHECK WAS DONE
:THEN ANOTHER RH CLEAR WAS
:GIVEN TO CLEAR ALL ERRORS
:THE FOLLOWING REGISTER DID
:NOT CONTAIN WHAT IS IN "GOOD"

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:ON A SILO TEST TO
:TEST DBL RHCS3-BIT #10
:THE FOLLOWING REGISTER
:DID NOT CONTAIN WHAT
:IS IN "GOOD"

```

2344			: ITEM 126	
2345	002476	061523	EM124	
2346	002500	065204	DH6	
2347	002502	066576	DT6	
2348	002504	066745	DF6	
2349				
2350			: ITEM 127	
2351	002506	061523	EM124	
2352	002510	065413	DH13	
2353	002512	066576	DT6	
2354	002514	066745	DF6	
2355				
2356			: ITEM 130	
2357	002516	061523	EM124	
2358	002520	065473	DH14	
2359	002522	066576	DT6	
2360	002524	066745	DF6	
2361				
2362			: ITEM 131	
2363	002525	061523	EM124	
2364	002530	065515	DH15	
2365	002532	066576	DT6	
2366	002534	066745	DF6	
2367			: ITEM 132	
2368	002536	061666	EM132	: BEFORE DATA TRANSFER
2369				: COMMAND WAS TO BE GIVEN
2370				: THE RP DRIVE
2371				: STATUS REGISTER DID NOT
2372				: CONTAIN WHAT IS IN GOOD
2373				
2374	002540	066303	DH132	: PC
2375				: TEST NO.
2376				: RHDS1 GOOD
2377				: RHDS1 BAD
2378	002542	066576	DT6	
2379	002544	066745	DF6	
2380			: ITEM 133	
2381	002546	062044	EM133	: BEFORE DATA TRANSFER
2382				: COMMAND WAS TO BE GIVEN
2383				: THE RS DRIVE
2384				: STATUS REGISTER DID NOT
2385				: CONTAIN WHAT IS IN GOOD
2386				
2387	002550	066303	DH132	: PC
2388				: TEST NO.
2389				: RHDS1 GOOD
2390				: RHDS1 BAD
2391	002552	066576	DT6	
2392	002554	066745	DF6	
2393			: ITEM 134	
2394	002556	062222	EM134	: BEFORE DATA TRANSFER COMMAND
2395				: WAS TO BE GIVEN THE
2396				: MAG TAPE DRIVE STATUS
2397				: REGISTER DID NOT CONTAIN WHAT
2398				: IS IN GOOD
2399				

2400	002560	066303		DH132	
2401	002562	066576		DT6	
2402	002564	066745		DF6	
2403			; ITEM135		
2404	002566	062412		EM135	: WAS WAITING FOR A BIT TO SET
2405					: BIT IN QUESTION IS IN "BIT WAITED FOR"
2406					: REGISTER IN QUESTION IN "REG ADDR"
2407	002570	066345		DH135	: PC
2408					: TEST NO
2409					: PC OF WAT
2410					: BIT
2411					: REG ADDR
2412	002572	066670		DT135	: \$ERRPC, TSTNM, WATIPC, WAITBT, WAITRE
2413	002574	066775		DF135	: 0,0,0,0,0
2414			; ITEM136		
2415	002576	062612		EM136	: WAS WAITING FOR A BIT TO RESET
2416					: BIT IN QUESTION IS IN "BIT WAITED FOR"
2417					: REGISTER IN QUESTION IN "REG ADDR"
2418	002600	066345		DH135	: PC
2419					: TEST NO
2420					: PC OF WAT
2421					: BIT
2422					: REG ADDR
2423	002602	066670		DT135	: \$ERRPC, TSTNM, WATIPC, WAITBT, WAITRE
2424	002604	066775		DF135	: 0,0,0,0,0
2425			; ITEM 137		
2426	002606	063014		EM137	: ON WRITING AND READING
2427					: THE REGISTER # IN "REG. ADDR"
2428					: IT DID NOT CONTAIN
2429					: EXPECTED VALUE.
2430	002610	066425		DH137	: PC
2431					: TEST NO
2432					: REG ADDR
2433					: GOOD DATA
2434					: BAD DATA
2435	002612	066704		DT137	: \$ERRPC, TSTNM, \$BDADR, \$GDDAT, \$BDDAT
2436	002614	067002		DF137	: 0,0,0,0,0
2437					
2438					
2439			; ITEM 140		
2440	002616	063142		EM140	
2441	002620	066047		DH106	
2442	002622	066654		DT106	
2443	002624	066770		DF106	
2444			; ITEM 141		
2445	002626	063374		EM141	: AFTER CLEARING THE RH AND
2446					: WRITING TWO WORD INTO
2447					: RHDB AND READING IT
2448					: BACK TWICE, CAUSED RHCSI TO
2449					: HAVE WRONG DATA,
2450					: GIVEN IN BAD RHCSI
2451	002630	065307		DH11	: PC
2452					: TEST NUMBER
2453					: RHCSI GOOD
2454					: RHCSI BAD
2455	002632	066576		DT6	: \$ERRPC, TSTNM, \$GDDAT, \$BDDAT

2456	002634	066745	DF6	;0,0,0,0
2457				
2458				
2459				
2460	002636	063575	;ITEM 142 EM142	; AFTER CLEARING THE RH AND ; WRITING TWO WORD INTO ; RHDB AND READING IT ; BACK TWICE, CAUSED RHCS3 TO ; HAVE WRCNG DATA, ; GIVEN IN BAD RHCS3
2461				
2462				
2463				
2464				
2465				
2466	002640	065351	DH12	; PC ; TEST NUMBER ; RHCS3 GOOD ; RHCS3 BAD
2467				
2468				
2469				
2470	002642	066576	DT6	; \$ERRPC, TSTNM, \$GDDAT, \$BDDAT
2471	002644	066745	DF6	;0,0,0,0
2472				
2473				
2474				
2475	002646	063776	;ITEM 143 EM143	; AFTER CLEARING THE RH AND ; WRITING TWO WORD INTO ; RHDB AND READING IT ; BACK TWICE, CAUSED RHBA TO ; HAVE WRONG DATA, ; GIVEN IN BAD RHBA
2476				
2477				
2478				
2479				
2480				
2481	002650	065413	DH13	; PC ; TEST NUMBER ; RHBA GOOD ; RHBA BAD
2482				
2483				
2484				
2485	002652	066576	DT6	; \$ERRPC, TSTNM, \$GDDAT, \$BDDAT
2486	002654	066745	DF6	;0,0,0,0
2487				
2488				
2489				
2490	002656	064176	;ITEM 144 EM144	; AFTER CLEARING THE RH AND ; WRITING TWO WORD INTO ; RHDB AND READING IT ; BACK TWICE, CAUSED RHBAE TO ; HAVE WRONG DATA, ; GIVEN IN BAD RHBAE
2491				
2492				
2493				
2494				
2495				
2496	002660	065453	DH14	; PC ; TEST NUMBER ; RHBAE GOOD ; RHBAE BAD
2497				
2498				
2499				
2500	002662	066576	DT6	; \$ERRPC, TSTNM, \$GDDAT, \$BDDAT
2501	002664	066745	DF6	;0,0,0,0
2502				
2503				
2504				
2505	002666	064377	;ITEM 145 EM145	; AFTER CLEARING THE RH AND ; WRITING TWO WORD INTO ; RHDB AND READING IT ; BACK TWICE, CAUSED RHWC TO ; HAVE WRONG DATA, ; GIVEN IN BAD RHWC
2506				
2507				
2508				
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2510				
2511	002670	065515	DH15	; PC

2512				: TEST NUMBER
2513				: RHC GOOD
2514				: RHC BAD
2515	002672	066576	DT6	: \$ERRPC, TSTNM, \$GDDAT, \$BDDAT
2516	002674	066745	DF6	: 0, 0, 0, 0
2517			; ITEM146	
2518	002676	064577	EM146	: A DEVICE BASE ADDRESS DID NOT
2519				: TIME OUT BUT CORRESPONDING
2520				: VECTOR ADDRESS DID TIME OUT
2521	002700	066477	DH146	: PC
2522				: BASE
2523				: VECTOR
2524	002702	066720	DT146	: \$ERRPC, TESTDV, TESTVC, 0
2525	002704	067007	DF146	: 0, 0, 0
2526				
2527			; ITEM147	
2528	002706	064771	EM147	: A DEVICE ADDRESS DID NOT
2529				: TIME OUT BUT NO UNITS HAD
2530				: APPROPRIATE DRIVE TYPE
2531	002710	066526	DH147	: PC
2532				: BASE ADDRESS
2533	002712	066730	DT147	: \$ERRPC, TESTDV
2534	002714	067012	DF147	
2535				

002716 000254
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 :RH70 REGISTERS

002716 000254

RPVEC: 254 ; RP VECTOR ADDRESS

 :WORD COUNT REGISTER (RHWC)
 :EACH BIT IS CALLED BY BIT NUMBER

:BUS ADDRESS REGISTER (RHBA)
 :EACH BIT IS CALLED BY BIT NUMBER

:CONTROL AND STATUS REGISTER 2 (RHCS2)

US1 = 1	:UNIT SELECT (BIT #0)
US2 = 2	:UNIT SELECT (BIT #1)
US4 = 4	:UNIT SELECT (BIT #2)
SAI = 10	:BUS ADDRESS INCREMENT INHIBIT (BIT #3)
PAT = 20	:INVERT PARITY
CLR = 40	:CLEAR (BIT #5)
IR = 100	:INPUT READY (BIT #6)
OR = 200	:OUTPUT READY (BIT #7)
MP = 400	:MASS BUS PARITY ERROR (BIT #8)
MP = 400	:MASS BUS PARITY ERROR (BIT #9)
MT = 1000	:MISSED TRANSFER ERROR (BIT #9)
PE = 2000	:PROGRAM ERROR (BIT #10)
NEM = 4000	:NON EXISTANT MEMORY (BIT #11)
NED = 10000	:NON EXISTANT DRIVE (BIT #12)
PE = 20000	:UNIBUS PARITY ERROR (BIT #13)
WCE = 40000	:WRITE CHECK ERROR (BIT #14)
DLT = 100000	:DATA LATE (BIT #15)

:CONTROL AND STATUS REGISTER 3 (RHCS3)

IPCK0 = 1	:INVERT PARITY CHECK BIT 0
IPCK1 = 2	:INVERT PARITY CHECK BIT 1
IPCK2 = 4	:INVERT PARITY CHECK BIT 2
IPCK3 = 10	:INVERT PARITY CHECK BIT 3
IE = 100	:INTERRUPT ENABLE (BIT #6)
OBL = 2000	:DOUBLE WORD BOUNDARY (BIT #10)
WCEEW = 4000	:WRITE CHECK EVEN WORD (BIT #11)
WCEOW = 10000	:WRITE CHECK ODD WORD (BIT #12)
OPEEW = 20000	:DATA PARITY ERROR EVEN WORD (BIT #13)
OPEOW = 40000	:DATA PARITY ERROR ODD WORD (BIT #14)
APE = 100000	:ADDRESS PARITY ERROR (BIT #15)

:DATA BUFFER REGISTER (RHDB)

:EACH BIT IS CALLED BY BIT NUMBER

:AP04 REGISTERS

:CONTROL AND STATUS 1 REGISTER. (#00)

000001	GO=	1	:GO (BIT #0)
000100	IE=	100	:INTERRUPT ENABLE (BIT #5)
000200	RDY=	200	:READY (BIT #7)
000400	A16=	400	:HIGH ORDER UNIBUS BITS (BIT #8)
001000	A17=	1000	:HIGH ORDER UNIBUS BITS (BIT #9)
002000	PSEL=	2000	:PORT SELECT (BIT #10)
004000	DVA=	4000	:DEVICE AVAILABLE (BIT #11)
008000	MCPE=	20000	:MASSBUS PARITY ERROR (BIT #13)
010000	TRE=	40000	:TRANSFER ERROR (BIT #14)
100000	SC=	100000	:SPECIAL CONDITION (BIT #15)

:STATUS REGISTER (RHDS1) (#01)

000001	DFFS=	1	:DRIVE FORWARD 5"/SEC. (BIT #0)
000002	BOT=	2	:BEGINING OF TAPE (BIT #1)
000004	OFF20=	2	:DRIVE FORWARD 20"/SEC. (BIT #1)
000008	DIG8=	4	:DRIVE TO INNER GAVRD BAND (BIT #2)
000010	GRV=	10	:GO REVERSE (BIT #3)
000020	DL64=	20	:DIFFERENCE LESS THAN 64 (BIT #4)
000040	DE1=	40	:DIFFERENCE EQUALS 1 (BIT #5)
000100	VV=	100	:VOLUME VALID (BIT #6)
000200	DRY=	200	:DRIVE READY (BIT #7)
000400	DPR=	400	:DRIVE PRESENT (BIT #8)
001000	PROG=	1000	:PROGRAMABLE (BIT #9)
002000	LBT=	2000	:LAST SECTOR TRANSFERRED (BIT #10)
004000	WAL=	4000	:WRITE LOCK (BIT #11)
010000	MOL=	10000	:MEDIUM ON-LINE (BIT #12)
020000	PIP=	20000	:POSITIONING OPERATION IN PROGRESS (BIT #13)
040000	ERR=	40000	:COMPOSIT ERROR (BIT #14)
100000	ATA=	100000	:ATTENTION ACTIVE (BIT #15)

:ERROR REGISTER #01 (RHER1) (#02)

000001	ILF=	1	:ILLEGAL FUNCTION (BIT #0)
000002	ILR=	2	:ILLEGAL REGISTER (BIT #1)
000004	RMR=	4	:REGISTER MODIFICATION REFUSED (BIT #2)
000010	PAR=	10	:PARITY ERROR (BIT #3)
000020	FER=	20	:FORMAT ERROR (BIT #4)
000040	WCF=	40	:WRITE CLOCK FAIL (BIT #5)
000100	ECH=	100	:ECC HARD ERROR (BIT #6)
000200	HCE=	200	:HEADER COMPARE ERROR (BIT #7)
000400	HCRC=	400	:HEADER CRC ERROR (BIT #8)
001000	ROE=	1000	:ADDRESS OVERFLOW ERROR (BIT #9)
002000	YAE=	2000	:INVALID ADDRESS ERROR (BIT #10)
004000	WLE=	4000	:WRITE LOCK ERROR (BIT #11)
010000	DTE=	10000	:DRIVE TIMING ERROR (BIT #12)

000001
000100
000200
000400
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002000
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020000
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040000
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000200

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000200
000400
001000
002000
004000
010000
020000
040000
100000

000001
000002
000004
000010
000020
000040

JPI= 20000
UNS= 40000
DCK= 100000

;MAINTAINABILITY REGISTER (RHMR) (#03)
DMD= 1
MCLK= 2
MINX= 4
MSTCK= 10
MRD= 20
MWR= 40
DTSY= 1000

;ATTENTION SUMMARY PSEUDO-REGISTER (RHAS) (#04)
AT0= 1
AT1= 2
AT2= 4
AT3= 10
AT4= 20
AT5= 40
AT6= 100
AT7= 200

;DESIRED SECTOR/TRACK ADDRESS REGISTER (RHDST) (#1)
;EACH BIT IS CALLED BY BIT NUMBER
;DRIVE TYPE REGISTER (RHDT) (#06)
;EACH BIT IS CALLED BY BIT NUMBER
;LOOK-AHEAD REGISTER (RHLA) (#07)
EXT1= 1
EXT2= 2
EXT4= 4
EXT10= 10
EXT20= 20
EXT40= 40
SC1= 100
SC2= 200
SC4= 400
SC10= 1000
SC20= 2000
TRK1= 4000
TRK2= 10000
TRK4= 20000
TRK10= 40000
TRK20= 100000

;ERROR REGISTER #2 (RHER2) (#10)
WCU= 1
CSF= 2
WSU= 4
CSU= 10
MSE= 20
TDF= 40

;OPERATION INCOMPLETE (BIT #13)
;DRIVE UNSAFE (BIT #14)
;DATA CHECK ERROR (BIT 15)

;DIAGINOSTIC MODE (BIT #0)
;MAINTAINABILITY CLOCK (BIT #1)
;MAINTAINABILITY INDEX (BIT #2)
;MAINTAINABILITY SECTOR CLOCK (BIT #3)
;MAINTAINABILITY READ (BIT #4)
;MAINTAINABILITY WRITE (BIT #5)
;MAINTAINABILITY SYNC DETECTED (BIT #9)

;DEVICE 0 (BIT #0)
;DEVICE 1 (BIT #1)
;DEVICE 2 (BIT #2)
;DEVICE 3 (BIT #3)
;DEVICE 4 (BIT #4)
;DEVICE 5 (BIT #5)
;DEVICE 6 (BIT #6)
;DEVICE 7 (BIT #7)

;EXTENSION 1 (BIT #0)
;EXTENSION 2 (BIT #1)
;EXTENSION 3 (BIT #2)
;EXTENSION 4 (BIT #3)
;EXTENSION 5 (BIT #4)
;EXTENSION 6 (BIT #5)
;SECTOR COUNT FIELD 0 (BIT #6)
;SECTOR COUNT FIELD 1 (BIT #7)
;SECTOR COUNT FIELD 2 (BIT #8)
;SECTOR COUNT FIELD 3 (BIT #9)
;SECTOR COUNT FIELD 4 (BIT #10)
;TRACK FIELD 1 (BIT #11)
;TRACK FIELD 2 (BIT #12)
;TRACK FIELD 3 (BIT #13)
;TRACK FIELD 4 (BIT #14)
;TRACK FIELD 5 (BIT #15)

;WRITE CURRENT UNSAFE (BIT #0)
;CURRENT SINK FAILURE (BIT #1)
;WRITE SELECT UNSAFE (BIT #2)
;CURRENT SWITCH UNSAFE (BIT #3)
;MOTJR SEQUENCE ERROR (BIT #4)
;TRANSITIONS DETECTOR FAILURE (BIT #5)

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002000
004000
010000
020000
100000

TUF= 100
FEN= 200
WRU= 400
MHS= 1000
NHS= 2000
IXE= 4000
VJ30= 10000
PLU= 20000
ACU= 100000

: TRANSITIONS UNSAFE (BIT #6)
: FAILSAFE ENABLED (BIT #7)
: WRITE READY UNSAFE (BIT #8)
: MULTIPLE HEAD SELECT (BIT #9)
: NO HEAD SELECTION (BIT #10)
: INDEX ERROR (BIT #11)
: 30VOLT UNSAFE (BIT #12)
: PLO UNSAFE (BIT #13)
: ACUNSAFE (BIT #15)

; OFFSET REGISTER (RHOF) (#11)

000001
000002
000004
000010
000020
000040

OF25= 1
OF50= 2
OF100= 4
OF200= 10
OF400= 20
OF800= 40

: OFFSET 25 MICRO INCHES (BIT #0)
: OFFSET 50 MICRO INCHES (BIT #1)
: OFFSET 100 MICRO INCHES (BIT #2)
: OFFSET 200 MICRO INCHES (BIT #3)
: OFFSET 400 MICRO INCHES (BIT #4)
: OFFSET 800 MICRO INCHES (BIT #5)

000200
002000
004000
010000

OFREV= 200
HCI= 2000
ECI= 4000
FMT22= 10000

: OFFSET NEGATIVE (REVERSE) (BIT #5)
: HEADER COMPARE INHIBIT (BIT #10)
: ERROR CORRECTION CODE INHIBIT (BIT #11)
: FORMAT BIT (BIT #12)

; TAPE CONTROL REGISTER (RHTC)

000001
000002
000004
000010
000020
000040
000100
000200
000400
001000
002000
001400
000300

S1= 1
S2= 2
S4= 4
EPAR= 10
FMT1= 20
FMT2= 40
FMT4= 100
FMT8= 200
DEN1= 400
DEN2= 1000
DEN4= 2000
BPI8= 1400
NML= 300

: SLAVE NUMBER
: SLAVE NUMBER
: SLAVE NUMBER
: EVEN PARITY
: FORMAT
: FORMAT
: FORMAT
: FORMAT
: DENSITY
: DENSITY
: DENSITY
: 800 BPI
: NORMAL - 2 FRAMES PER WORD

; DRIVE TYPE REGISTER

002000
010000

SLVPR= 2000
CH7= 10000

: SLAVE PRESENT (BIT #10)
: CHANNEL 7 (BIT #12)

: DESIRED CYLINDER ADDRESS (RHCA) (#12)
: EACH BIT IS CALLED BY BIT NUMBER.

; CURRENT CYLINDER ADDRESS (RHCC) (#13)



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:EACH BIT IS CALLED BY BIT NUMBER

:SERIAL NUMBER REGISTER (RHSN) (#14)
:EACH IS CALLED BY BIT NUMBER

;ERROR REGISTER #03 (RHER3) (#15)

000001
000002
000010
000020
000040
000100
040000
100000

PSU= 1 :PACK SPEED UNSAFE (BIT #0)
VUF= 2 :VELOCITY UNSAFE (BIT #1)
UWR= 10 :ANY UNSAFE EXCEPT READ/WRITE (BIT #3)
PRE= 20 :DISK PACK ROTATION ERROR (BIT #4)
ACL= 40 :AC LOW (BIT #5)
DCL= 100 :DC LOW (BIT #6)
SKI= 40000 :SEEK INCOMPLETE (BIT #14)
OCYL= 100000 :OFF CYLINDER (BIT #15)

:ECC POSITION REGISTER (RHEC1) (#16)
:EACH BIT IS CALLED BY BIT NUMBER

:ECC PATTERN REGISTER (RHEC2) (#17)
:EACH BIT IS CALLED BY BIT NUMBER

;;*****

2800			CS1=	0		: CONTROL STATUS 1
2801	000000		WC=	2		: WORD COUNT
2802	000002		BA=	4		: BUS ADDRESS
2803	000004		FC=	6		: FRAME COUNT
2804	000006		DA=	6		: DESIRED SECTOR/TRACK
2805	000006		CS2=	10		: CONTROL STATUS 2
2806	000010		DS=	12		: DRIVE STATUS
2807	000012		ER1=	14		: ERROR 1
2808	000014		AS=	16		: ATTENTION SUMMERY
2809	000016		LA=	20		: LOOK AHEAD
2810	000020		DB=	22		: DATA BUFFER
2811	000022		MR=	24		: MAINTENANCE REGISTER
2812	000024		DT=	26		: DRIVE TYPE
2813	000026		SN=	30		: SERIAL NUMBER
2814	000030		OF=	32		: OFFSET
2815	000032		TC=	32		: TAPE CONTROL
2816	000032		DC=	34		: DESIRED CYLINDER
2817	000034		CC=	36		: CURRENT CYLINDER
2818	000036		ER2=	40		: ERROR 2
2819	000040		ER3=	42		: ERROR 3
2820	000042		EC1=	44		: ECC 1
2821	000044		EC2=	46		: ECC 2
2822	000046					
2823						
2824						
2825			: BUFFERS			
2826		003000	. =3000			
2827	003000		BUFEW:			
2828	003000	000000	BFEVEN: 0			
2829	003002		BUFOW:			
2830	003002	000422	BFODD: .BLKW 274.			: BUFFER
2831			: STARTING ADDRESS OF REGISTERS			
2832						
2833	004046	172040	RSCS1: 172040			: RS ALONG
2834	004050	176700	RPCS1: 176700			: RP ALONE
2835	004052	172440	TMCS1: 172440			: TM ALONE
2836	004054	176300	MIXCS1: 176300			: MIXED SYSTEM
2837	004056	000000	PRESENT: 0			
2838						
2839						
2840						
2841			.SBTTL REGISTER ADDRESSES			

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2842      ; THE FOLLOWING ARE THE I/O REGISTERS FOR THE DEVICE UNDER TEST
2843      ; THEY WILL BE FILLED WITH ADDRESSES DEPENDING ON WHAT DEVICE IS ON
2844      ; THE SYSTEM.
2845      ; IN THE CASE OF A MIXED SYSTEM THAT IS WITH MORE THAN ONE RH DEVICE
2846      ; THE FIRST ONE THAT EXISTS IN THE FOLLOWING LIST WILL BE PICKED
2847      ; AND THE OTHERS WILL NOT BE USED.
2848      ; LIST:-
2849      ; 1)   RS03
2850      ; 2)   RS04
2851      ; 3)   RPO4,5,6
2852      ; 4)   TMO2
2853      ; THE CONTENTS OF RHCS1 WHICH IS THE BASE ADDRESS IS
2854      ; 172040 FOR RS03/04
2855      ; 176700 FOR RPO4,5,6
2856      ; 172440 FOR TMO2
2857      ; THE REST OF THE LOCATIONS WILL BE FILLED BY THE PROGRAM
2858 004060 000000 RHCS1: 0 ; CONTROL AND STATUS 1
2859 004062 000000 RHWC: 0 ; WORD COUNT
2860 004064 000000 RHBA: 0 ; BUS ADDRESS
2861 004066 000000 RHFC: 0 ; FRAME COUNT
2862 004066 000000 RHDA: 0 ; DISK ADDRESS
2863 004066 000000 RHDST: 0 ; DESIRED SECTOR/TRACK ADDRESS
2864 004070 000000 RHCS2: 0 ; CONTROL AND STATUS 2
2865 004072 000000 RHDS1: 0 ; DRIVE STATUS
2866 004074 000000 RHER1: 0 ; ERROR #1
2867 004076 000000 RHAS: 0 ; ATTENTION SUMMARY
2868 004100 000000 RHCC: 0 ; CHECK CHARACTER
2869 004100 000000 RHLA: 0 ; LOOK-AHEAD
2870 004102 000000 RHOB: 0 ; DATA BUFFER
2871 004104 000000 RHMR: 0 ; MAINTAINABILITY
2872 004106 000000 RHDT: 0 ; DRIVE TYPE
2873 004110 000000 RHSN: 0 ; SERIAL NUMBER
2874 004112 000000 RHTC: 0 ; TAPE CONTROL
2875 004112 000000 RHOF: 0 ; OFFSET
2876 004114 000000 RHCA: 0 ; DESIRED CYLINDER ADDRESS
2877 004116 000000 RHCCA: 0 ; CURRENT CYLINDER ADDRESS
2878 004120 000000 RHER2: 0 ; ERROR #2
2879 004122 000000 RHER3: 0 ; ERROR #3
2880 004124 000000 RHEC1: 0 ; ECC POSITION
2881 004126 000000 RHEC2: 0 ; ECC PATTERN
2882 004130 000000 RHBAE: 0 ; BUS ADDRESS EXTENSION
2883 004132 000000 RHCS3: 0 ; CONTROL AND STATUS 3
2884
2885
2886
2887
2888
2889      ; FUNCTION EQUATES
2890
2891      ; TABLE OF FUNCTIONS FOR RHCS1 THEN "GO" BIT HAS TO BE SET
2892 004134 FUTABL:
2893 004134 000000 NOPERA: 0 ; NO OPERATION
2894 004136 000002 UNLOAD: 2 ; UNLOAD (STAND BY)
2895 004140 000006 RECALI: 6 ; RECALIBRATE
2896 004142 000010 DCLEAR: 10 ; DRIVE CLEAR
2897 004144 000012 RELEAS: 12 ; RELEASE (DUAL-FORT OPERATION)
    
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2998 004146 000030 SERCH: 30 ;SEARCH COMMAND
2999 004150 000050 WRCHK: 50 ;WRITE CHECK DATA
2900 004152 000052 WRCHDT: 52 ;WRITE CHECK HEADER AND DATA
2901 004154 000060 WRIDAT: 60 ;WRITE DATA
2902 004156 000062 WRIFOR: 62 ;WRITE HEADER AND DATA (FORMAT)
2903 004160 000070 READAT: 70 ;READ DATA
2904 004162 000072 REFOR: 72 ;READ HEADER AND DATA
2905 004164 000004 SEECOM: 4 ;SEEK COMMAND
2906 004166 000014 OFSETC: 14 ;OFFSET COMMAND
2907 004170 000016 RETCL: 16 ;RETURN TO CENTERLINE
2908 004172 000022 PKACK: 22 ;PACK ACKNOWLEDGE
2909 004174 000020 READIN: 20 ;READ IN
2910 004176 000006 REWIND: 6 ;REWIND
2911 004200 000076 REVRED: 76 ;REVERSE READ
2912 004202 000030 SPACFD: 30 ;SPACE FORWARD
2913 004204 000066 REVWRT: 66 ;REVERSE WRITE
2914 004206 000000 ILLEGL: .WORD 0 ;COMPUTED ILLEGAL FUNCTION
2915
2916 ;DATA BUFFER FOR READ WRITE
2917 004210 000106 WRFROM: .BLKW 70. ;WRITE FROM THIS BUFFER
2918 004424 000106 REINTO: .BLKW 70. ;READ INTO THIS BUFFER
2919 004640 000000 COMND: 0 ;STORE COMMAND FOR COMMAND ROUTINE
2920 004642 000000 COMAND: 0 ;STORE COMMACD
2921 004644 000000 NOGO: 0 ;IF ZERO GO IS TO BE GIVEN IN COMMAND ROUTINE
2922 ;IF ONES GO IS NOT TO BE GIVEN IN COMMAND ROUTINE
2923 ;USED IN PGE TEST
2924 004646 000000 WATO: 0 ;IF ZERO WAIT FOR BIT TO SET
2925 ;IF ONES WAIT FOR BIT TO RESET
2926 ;USED IN WAIT TRAP
2927 004650 000000 WRTBIT: 0 ;BITS NOT WRITTEN INTO
2928 004652 000000 SETBIT: 0 ;BITS ALWAYS SET
2929 004654 000000 CLRBIT: 0 ;BITS ALWAYS CLEARED
2930
2931
2932
2933 ;RESERVED LOCATIONS
2934 004656 000000 TSTNM: 0 ;TEST NUMBER
2935 004660 000000 SLAVE: 0 ;KEEP SLAVE NO.
2936 004662 000000 IP: 0 ;IPCK NUMBER FOR ERROR PRINTOUT
2937 004664 000000 SILONM: 0 ;SILO WORD NUMBER FOR ERROR PRINT OUT
2938 004666 000000 WAITPC: 0 ;WAIT TRAP PC
2939 004670 000000 WAITRE: 0 ;WAITING FOR REGISTER ADDRESS IN WAIT TRAP
2940 004672 000000 WAITBT: 0 ;WAITING FOR BIT IN WAIT TRAP
2941
2942 ;TABLE FOR ATTENTION BITS
2943 ;ATTENTION TABLE
2944 004674 001 002 004 ATABLE: .BYTE 1,2,4,10,20,40,100,200
2945 004677 010 020 040
2946 004702 100 200
2947
2948
2949
2950 ;RESERVED LOCATIONS FOR UNIT SELECT
2951 004704 172040 BASEAD: 172040 ;RS BASE ADDRESS
2952 004706 176700 BASPAD: 176700 ;RP BASE ADDRESS
2953 004710 172440 BASTAD: 172440 ;TU BASE ADDRESS

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2954	004712	176300	BASEMAD:	176300	; MIXED SYSTEM BASE ADDRESS
2955					
2956					
2957	004714	000000	BASEVC:	0	; RS VECTOR
2958	004716	000254	BASP:	254	; RP VECTOR
2959	004720	000001	BAST:	1	; TU VECTOR
2960	004722	000002	BASM:	2	; MIXED VECTOR
2961					
2962					
2963	004724	000010	; TABLE FOR GIVEN BASE ADDRESSES		
2964			BSGIVA:	.BLKW 8.	
2965					
2966			; TABLE FOR GIVEN VECTOR		
2967	004744	000010	BSGIVV:	.BLKW 8.	
2968					
2969					
2970	004764	000004	NMRHS:	4	; NUMBER OF RH
2971	004766	000000	BASINX:	0	; INDEX FOR BASE
2972	004770	000000	WORKBS:	0	; WORKING BASE
2973	004772	000000	WORKNM:	0	; WORKING NUMBER FOR RH
2974	004774	000000	WORKVC:	0	; WORKING VECTOR FOR RH
2975	004776	000000	FORBAE:	0	; TOTAL NO. OF REG. FOR BAE CALCULATION
2976	005000	000000	LOPCT:	0	; LOOP COUNT FOR 200 START
2977	005002	000000	LOPCT1:	0	; LOOP COUNT FOR 210 START
2978	005004	000000	USEVEC:	0	; USE VECTOR
2979					
2980					
2981					
2982	005006	000000	GIVE:	0	; IF ONES OPERATOR WILL GIVE ADDRESS
2983	005010	000000	UNIT:	0	; UNIT UNDER TEST
2984	005012	000000	ST200:	0	; ALL ONES INDICATE STARTING FROM 200
2985			; TESTING TABLE		
2986	005014	000010	TSRHNO:	.BLKW 8.	; RH NO TO BE TESTED IN ORDER OF TEST
2987					
2988	005034	000010	TSDEVICE:	.BLKW 8.	; DEVICE TO BE TESTED IN ORDER OF TEST
2989					; 1=RS03,3/L,3/LA 2=RS04,4/L 4=RP04,5,6 SINGLE PORT
2990					; 10=RP04,5,6 DUAL PORT 20=TM02
2991					
2992					
2993	005054	000010	TSUNIT:	.BLKW 8.	; UNIT NO. TO BE TESTED IN ORDER OF TEST
2994					
2995					
2996	005074	000010	TSSLAV:	.BLKW 8.	; SLAVE NO TO BE TESTED IN ORDER OF TEST
2997					
2998					
2999	005114	000000	TSTOTL:	0	; TOTAL NO. OF UNITS TO BE TESTED
3000	005116	000000	TSINDX:	0	; INDEX TO ONE UNDER TEST
3001					
3002					
3003	005120	000010	TSVEC:	.BLKW 8.	; VECTOR ADDRESS IN ORDER OF TEST
3004					
3005					
3006	005140	000010	TSBAE:	.BLKW 8.	; RHBAE ADDRESS IN ORDER OF TEST
3007					
3008					
3009	005160	000010	TSCS3:	.BLKW 8.	; RHCS3 ADDRESS IN ORDER OF TEST

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3010
3011
3012 005200 000010 TSCOMD: .BLKW 9. ;COMMAND ADDRESS IN ORDER OF TEST
3013
3014
3015 005220 000010 TSBASE: .BLKW 8. ;BASE ADDRESS IN ORDER OF TEST
3016
3017
3018
3019
3020
3021
3022 005240 000000 ERFLG$: 0 ;ERROR FLAG
3023 005242 000000 FIRST: 0 ;IF ZERO WILL TYPE HEADER
3024 ;IF ONES WILL NOT TYPE HEADER
3025
3026
3027
3028 005244 000000 ATTENT: 0 ;ATTENTION BIT FOR PRESENT UNIT
3029 005246 000000 TOTALAT: 0 ;TATAL ATTENTION BITS
3030
3031 005250 000000 TMPO: .WORD 0 ;TEMP STORAGE
3032 005252 000000 TMP1: .WORD 0
3033 005254 000000 TMP4: .WORD 0 ;TEMP STORAGE
3034 ;PERMANENT TABLE
3035 005256 172040 PERRS: 172040 ;RS BASE ADDRESS
3036 005260 176700 PERRP: 176700 ;RP BASE ADDRESS
3037 005262 172440 PERTU: 172440 ;TU BASE ADDRESS
3038 005264 176300 PERMX: 176300 ;MIXED BASE ADDRESS
3039
3040 005266 000204 PERRSV: 204 ;RS VECTOR ADDRESS
3041 005270 000254 PERRPV: 254 ;RP VECTOR ADDRESS
3042 005272 000224 PERTUV: 224 ;TU VECTOR ADDRESS
3043 005274 000000 PERMXV: 0 ;MIXED VECTOR ADDRESS
3044
3045 005276 000004 PERNUM: 4 ;NUMBER OF RH
3046
3047 ;WORKING TABLE
3048
3049 005300 000004 DEVPNT: .BLKW 4 ;BASE ADDRESS TO BE TESTED
3050 005310 000004 VECPNT: .BLKW 4 ;VECTOR ADDRESS TO BE TESTED
3051 005320 000000 LTRY: 0 ;NO. OF UNITS TO BE TESTED
3052 005322 000000 TFOUND: 0 ;TWICE NUMBER OF RH FOJND
3053 005324 000000 TESTDV: 0 ;STORES BASE ADDRESS OF TEST DEVICE
3054 005326 000000 TESTVC: 0 ;STORES VECTOR OF TEST DEVICE
3055 ;THE ABOVE TWO IS BEFORE ANY
3056 ;DEVICE IS FOUND.
3057 ;TABLE FOR DRIVE TYPES
3058 ;0=RS03, 1=RS03/L, 2=RS04, 3=RS04/L, 4=RS03/LA
3059 ;20020=SINGLE PORT RPO4, 24020=DUAL PORT RPO4
3060 ;20021=SINGLE PORT RPO5, 24021=DUAL PORT RPO5
3061 ;20022=SINGLE PORT RPO6, 24022=DUAL PORT RPO6
3062 ;142010=TU16
3063
3064 005330 000000 000001 000002 TYPNT: .WORD 0,1,2,3,4,20020,24020,20021,24021,20022,24022,142010
3065 005336 000003 000004 020020
  
```

3066	005344	024020	020021	024021		
3067	005352	020022	024022	142010		
3068	005360	000000			POINTT: 0	;POITER TO ABOVE TABLE
3069	005362	000014			NTYPNT: 14	;NUMBER OF ABOVE TYPES
3070						
3071	005364	000000			TMPSLV: 0	;TEMPORARY SLAVE COUNTER
3072						
3073						;TABLE OF DATA FOR DEVICES FOUND
3074	005366	000004			FDEVIC: .BLKW 4	;DEVICE FOUND, 1=TU, 2=RP DUAL
3075						;3=RP SINGLE, 4=RS04, 5=RS03
3076	005376	000004			FUNITN: .BLKW 4	;UNIT NUMBER FOUND
3077	005406	000004			FTYPE: .BLKW 4	;DRIVE TYPE FOUND
3078	005416	000004			FVECTR: .BLKW 4	;VECTOR FOUND
3079	005426	000004			FBASEA: .BLKW 4	;BASE ADDRESS FOUND
3080	005436	000004			FRHNM: .BLKW 4	;RH NUMBER FOUND
3081	005446	000004			FSLAVE: .BLKW 4	;TU16 SLAVE NUMBER FOUND
3082	005456	000004			FDRIVR: .BLKW 4	;DRIVER ADDRESS FOUND
3083	005466	000004			FBAE: .BLKW 4	;BAE ADDRESS FOUND
3084	005476	000004			FCS3: .BLKW 4	;CS3 ADDRESS FOUND
3085						
3086	005506	000000			TSTUNT: 0	;TOTAL NUMBER OF RH FOUND AT
3087						;END OF SIZE
3088	005510	000000			WORUNT: 0	;SAME AS ABOVE BUT TO BE
3089						;DECREASED AT END PROGRAM
3090						
3091	005512	000000			VECTOR: 0	;VECTOR UNDER TEST
3092	005514	177740			LERADD: 177740	;LOW ERROR ADDRESS REG.
3093	005516	177742			HERADD: 177742	;HIGH ERROR ADDRESS REG.
3094	005520	177744			MEMERR: 177744	;MEMORY SYSTEM ADDRESS REG.
3095		000114			PARE70=114	;PARITY ERROR VECTOR FOR 70

M05

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 DERHAB.SRC REGISTER TEST

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3096 .SBTTL REGISTER TEST
3097 005522 012737 177777 005012 BEGIN: MOV #-1,ST200
3098 005530 000402 BR START
3099 005532 005037 005012 BEGIN2: CLR ST200
3100
3101 005536 START:
3102 .SBTTL INITIALIZE THE COMMON TAGS
3103 ;;CLEAR THE COMMON TAGS ($CMTAG) AREA
3104 005536 012706 001100 MOV $CMTAG,R6 ;;FIRST LOCATION TO BE CLEARED
3105 005542 005026 CLR (R6)+ ;;CLEAR MEMORY LOCATION
3106 005544 022706 001140 CMP #SWR,R6 ;;DONE?
3107 005550 001374 BNE -6 ;;LOOP BACK IF NO
3108 005552 012706 001000 MOV #STACK,SP ;;SETUP THE STACK POINTER
3109 ;;INITIALIZE A FEW VECTORS
3110 005556 012737 043120 000020 MOV $$SCOPE,@#IOTVEC ;;IOT VECTOR FOR SCOPE ROUTINE
3111 005564 012737 000340 000022 MOV #340,@#IOTVEC+2 ;;LEVEL 7
3112 005572 012737 044744 000030 MOV $ERROR,@#EMTVEC ;;EMT VECTOR FOR ERROR ROUTINE
3113 005600 012737 000340 000032 MOV #340,@#EMTVEC+2 ;;LEVEL 7
3114 005606 012737 046436 000034 MOV $TRAP,@#TRAPVEC ;;TRAP VECTOR FOR TRAP CALLS
3115 005614 012737 000340 000036 MOV #340,@#TRAPVEC+2 ;;LEVEL 7
3116 005622 012737 046516 000024 MOV $PWRDN,@#PWRVEC ;;POWER FAILURE VECTOR
3117 005630 012737 000340 000026 MOV #340,@#PWRVEC+2 ;;LEVEL 7
3118 005636 005037 001212 CLR $TIMES ;;INITIALIZE NUMBER OF ITERATIONS
3119 005642 005037 001214 CLR $ESCAPE ;;CLEAR THE ESCAPE ON ERROR ADDRESS
3120 005646 112737 000001 001115 MOVB #1,$ERMAX ;;ALLOW ONE ERROR PER TEST
3121 005654 012737 005654 001106 MOV #,$LPADR ;;INITIALIZE THE LOOP ADDRESS FOR SCOPE
3122 005662 012737 005662 001110 MOV #,$LPERR ;;SETUP THE ERROR LOOP ADDRESS
3123 ;;SIZE FOR A HARDWARE SWITCH REGISTER. IF NOT FOUND OR IT IS
3124 ;;EQUAL TO A "-1" SETUP FOR A SOFTWARE SWITCH REGISTER.
3125 005670 013746 000004 MOV @#ERRVEC,-(SP) ;;SAVE ERROR VECTOR
3126 005674 012737 005730 000004 MOV #64,$@#ERRVEC ;;SET UP ERROR VECTOR
3127 005702 012737 177570 001140 MOV #DSWR,SWR ;;SETUP FOR A HARDWARE SWICH REGISTER
3128 005710 012737 177570 001142 MOV #DDISP,DISPLAY ;;AND A HARDWARE DISPLAY REGISTER
3129 005716 022777 177777 173214 CMP #-1,@SWR ;;TRY TO REFERENCE HARDWARE SWR
3130 005724 001012 BNE 66$ ;;BRANCH IF NO TIMEOUT TRAP OCCURRED
3131 ;;AND THE HARDWARE SWR IS NOT = -1
3132 005726 000403 BR 65$ ;;BRANCH IF NO TIMEOUT
3133 005730 012716 005736 64$: MOV #65$,(SP) ;;SET UP FOR TRAP RETURN
3134 005734 000002 RTI
3135 005736 012737 000176 001140 65$: MOV #SWREG,SWR ;;POINT TO SOFTWARE SWR
3136 005744 012737 000174 001142 MOV #DISPREG,DISPLAY
3137 005752 012637 000004 66$: MOV (SP)+,@#ERRVEC ;;RESTORE ERROR VECTOR
3138
3139
3140
3141 005756 012737 000000 177776 MOV #0,PS ;SET PROCESSOR STATUS TO 0
3142 005764 012737 000200 000036 MOV #200,@#TRAPVEC+2 ;TRAP PRIORITY = 4
3143 005772 013700 002716 MOV @#RPVEC,RO ;GET RP VECTOR ADDRESS
3144 005776 012720 043056 MOV #RPVECT,(RO)+ ;THIS IS FOR UNTIMELY INTERRUPTS
3145 006002 012710 000340 MOV #340,(RO) ;RPO4 INTERRUPT SERVICE ROUTINE
3146 ;PRIORITY = 7
3147 006006 004737 044064 JSR PC,@#STKINT ;INITILIZE THE TK
3148 006012 005037 045100 CLR PRITEM ;CLEAR FOR ERROR MESSAGE PRINTOUT
3149 006016 005737 005242 TST @#FIRST ;IS THIS FIRST TIME ROUND
3150 006022 0010C1 BNE 1$ ;BRANCH IF NOT
3151 006024 000402 BR 2$
  
```



```

3152 006026 000137 006114      1$:  JMP      Q#SND1
3153 006032
3154 006032 104401 006040      2$:  TYPE      ,69$          ;;TYPE ASCIZ STRING
3155 006036 000426          B?     67$          ;;GET OVER THE ASCIZ
3156          ;;68$: .ASCIZ <15><12>/RH70 FUNCTIONAL CONTROLLER TEST DERHA-B/
3157 006114          67$:
3158 006114 012737 177777 005242  SND1:  MOV      #-1,Q#FIRST      ;NEXT TIME DO NOT GIVE HEADER
3159 006122 012737 037736 000114      MOV      #PARITY,Q#PARE70    ;PARITY VECTOR
3160 006130 012737 000340 000116      MOV      #340,Q#PARE70+2    ;PRIORITY 7
3161 006136 012737 037600 000004      MOV      #TIEOUT,Q#ERRVEC   ;TIMEOUT VECTOR
3162 006144 012737 000340 000006      MOV      #340,Q#ERRVEC+2    ;PRIORITY 7
3163
3164
3165
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*****
*TEST 1      SIZE FOR RH DEVICES
*      THIS TEST DETERMINS WHICH RH DEVICE IS ON THE SYSTEM.
*      IF THE SYSTEM HAS MORE THAN ONE RH DEVICE THEN ONLY ONE OF
*      THE DEVICES WILL BE USED IN THE FOLLOWING TESTS.
*      THE ONE THAT WILL BE USED IS THE FIRST ONE THAT IS PRESENT
*      IN THE FOLLOWING LIST
*      RS
*      RP
*      TM
*
*      THE WAY THE TEST WORKS IS AS FOLLOWS:-
*      A REFERENCE IS MADE TO THE CONTROL AND STATUS REGISTER 1
*      FOR THE RS BY A TST INSTRUCTION. IF IT RESPONDS THEN IT IS
*      ASSUMED PRESENT AND THE LOCATIONS FOR THE I/O REGISTERS ARE
*      FILLED WITH THE APPROPRIATE ADDRESSES.
*      THEN THE DRIVE TYPE REGISTER IS CHECKED TO HAVE GOOD
*      VALUES.
*      IF THE TST INSTRUCTION TRAPS TO A NON EXISTANT VECTOR
*      THEN A SIMILAR ATTEMPT IS MADE TO A RP CONTROL AND STATUS
*      REGISTER 1.
*      THEN A TM IS TRYED.
*      THEN A MIXED SYSTEM WITH MORE THAN ONE RH DEVICES IS TRYED.
*****
TST1:  SCOPE

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006152 000004

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006154 012703 000001 001212 MOV #1,STIMES ::DO 1 ITERATION
006156 005012 TST ST200 ::IS IT A 200 START
006158 001412 BEQ 25 ::BRANCH IF NOT 200 START
006160 001412 MOV #PERRS,RO ::POINTER TO MOVE 9 WMRCS FROM
006162 001412 MOV #9,R1 ::NUMBER TO MOVE
006164 001412 MOV #DEVPNT,R2 ::POINTER TO MOVE 9 TORDS TC
006166 001412 MOV (R0)+,(R2)+ ::FILL WORK TABLE
006168 001412 DEC R1
006170 001412 BNE 15 ::BRANCH IF 9 NOT DONE
006172 001312 JMP SETSIZ
006174 104401 006224 25: TYPE ,655 ::TYPE ASCIZ STRING
006176 000417 BR 645 ::GET OVER THE ASCIZ
006178 645: .ASCIZ <15><12>/HOW MANY RH TO BE TESTED /
006180 104401 006270 645: TYPE ,675 ::TYPE ASCIZ STRING
006182 000402 BR 665 ::GET OVER THE ASCIZ
006184 665: .ASCIZ <15><12>/ /
006186 104410 006274 RDOCT
006188 011637 005320 MOV (SP),LTRY ::GET NUMBER OF RH TO BE TESTED
006190 012637 005250 MOV (SP)+,TMP0 ::WORKING NUMBER OF RH
006192 012737 000001 005252 MOV #1,TMP1 ::RH NUMBER TO BE TYPED
006194 005000 CLR RO ::INDEX TO WORKING TABLE
006196 104401 006324 35: TYPE ,695 ::TYPE ASCIZ STRING
006198 000417 BR 685 ::GET OVER THE ASCIZ
006200 685: .ASCIZ <15><12>.TYPE BASE ADDRESS OF RH NO.
006202 013746 005752 685: MOV TMP1,-(SP)
006204 104405 TYPDS
006206 104401 006376 705: TYPE ,715 ::TYPE ASCIZ STRING
006208 000402 BR 705 ::GET OVER THE ASCIZ
006210 705: .ASCIZ <15><12>/ /
006212 104410 006402 RDOCT
006214 012660 005300 MOV (SP)+,DEVPNT(RO) ::FILL WORKING TABLE WITH BASE ADDRESS
006216 104401 006416 735: TYPE ,735 ::TYPE ASCIZ STRING
006218 000420 BR 725 ::GET OVER THE ASCIZ
006220 725: .ASCIZ <15><12>/TYPE VECTOR ADDRESS OF RH NO/
006222 013746 005252 725: MOV TMP1,-(SP)
006224 104405 TYPDS
006226 104401 000200 TYPE ,CRLF
006228 104410 RDOCT
006230 012660 005310 MOV (SP)+,VECPNT(RO) ::FILL WORKING TABLE WITH VECTOR ADDRESS
006232 005720 TST (RO)+ ::ADD 2 TO RO
006234 005237 005252 INC TMP1 ::GET NEXT RH NUMBER
006236 005337 005250 DEC TMP0 ::COUNT DOWN RH TO BE TESTED
006238 001302 BNE 35 ::BRANCH IF ALL NOT DONE
006240 012700 005300 SETSIZ: ;RO WILL HAVE DEVICE POINTER, R1 WILL HAVE VECTOR POINTER
006242 012701 005310 MOV #DEVPNT,RO ;DEVICE POINTER
006244 005037 005322 MOV #VECPNT,R1 ;VECTOR PMINTER
006246 CLR TFOUND ;WILL BE USED AS POINTER
    
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3264 006526          552:
3265 006526 012737 010520 000004 19:  MOV      #554,ERRVEC      ;TIME OUT VECTOR TO REPORT CE TRY
3266 006534 012037 005324          MOV      (R0)+,TESTDV    ;ADDRESS OF DEVICE TO BE TESTED
3267 006540 012137 005326          MOV      (R1)+,TESTVC    ;VECTOR TO BE TESTED
3268 006544 005777 176554          TST      @TESTDV         ;TRY TIME OUT ON DEVICE BASE
3269          ;IF NO TIME OUT OCCURS THEN DEVICE IN TESTDV IS PRESENT
3270          ;NOW FILL ALL REGISTER ADDRESS TABLE
3271 006550 012702 000026          MOV      #22,R2         ;COUNT 22
3272 006554 012703 004060          MOV      @RHCS1,R3      ;GET BASE LOCATION
3273 006560 0137C  005324          MOV      TESTDV,R4      ;GET BASE ADDRESS
3274 006564 01042          25:  MOV      R4,(R3)+       ;FILL PROPER ADDRESS
3275 006566 062704 000002          ADD      #2,R4          ;INCREMENT ADDRESS BY 2
3276 006572 005302          DEC      R2             ;COUNT DOWN
3277 006574 001373          BNE      25             ;BRANCH IF 22 NOT DONE.
3278 006576 005077 175266          CLR      @RHCS2        ;SET UNIT NUMBER
3279 006602          556:
3280 006602 012737 005330 005360 35:  MOV      #TYPNT,POINTT  ;POINTER TO DRIVE TYPE
3281 006610 012737 000014 005362          MOV      #14,NTYPNT    ;NUMBER OF DRIVE TYPES
3282
3283 006616          557:
3284 006616 022737 000001 005362 45:  CMP      #1,NTYPNT     ;IS IT FOR TU
3285 006624 001424          BEQ      55             ;IF FOR TU BRANCH
3286 006626 017737 175254 001126          MOV      @RHDT,$BDDAT  ;READ DRIVE TYPE
3287 006634 012702 000014          MOV      #14,R2        ;GET NUMBER FOR DT NOT DONE
3288 006640 163702 005362          SUB      NTYPNT,R2     ;GET DT TO DO
3289 006644 006302          ASL      R2            ;MULTIPLY R2 BY 2
3290 006646 026237 005330 001126          CMP      TYPNT(R2),$BDDAT ;IS DRIVE TYPE FOUND
3291 006654 001402          BEQ      16$           ;DRIVE TYPE FOUND SO BRANCH
3292 006656 000137 010544          JMP      558
3293 006662 032777 010000 175200 16$:  BIT      #NED,@RHCS2   ;IS IT NON EXISTANT DRIVE
3294 006670 001437          BEQ      9$            ;NED NOT SET, DRIVE TYPE FOUND SO BRANCH
3295          ;A DEVICE HAS NOT BEEN FOUND
3296 006672 000137 010536          JMP      555           ;A DEVICE HAS NOT BEEN FOUND SO BRANCH
3297
3298          ;TRYING THE TU16 DRIVE TYPE
3299 006676 005037 005364          CLR      TMSPLV        ;SLAVE 0
3300 006702 013777 005364 175202 6$:  MOV      TMSPLV,@RHCT  ;MOVE SLAVE NUMBER IN TAPE CONTROL
3301 006710 017737 175172 001126          MOV      @RHDT,$BDDAT  ;READ DRIVE TYPE
3302 006716 032737 002000 001126          BIT      #SLVPR,$BDDAT ;IS SLAVE PRESENT
3303 006724 001014          BNE      7$            ;
3304 006726 022737 000007 005364          CMP      #7,TMSPLV     ;IS 7 DONE
3305 006734 001002          BNE      17$          ;BRANCH IF 7 DONE AND NONE FOUND
3306 006736 000137 010544          JMP      558
3307 006742 005237 005364          INC      TMSPLV        ;GET NEXT SLAVE
3308 006746 012777 040000 175104 17$:  MOV      @TRE,@RHCS1   ;CLEAR CONTROLLER ERRORS
3309 006754 000752          BR       6$            ;TRY NEXT SLAVE
3310          ;A DEVICE HAS BEEN FOUND SAVE SLAVE
3311 006756 013702 005322          MOV      TFOUND,R2     ;GET NO OF RH FOUND X2 FOR INDEX
3312 006762 013762 005364 005446          MOV      TMSPLV,FSLAVE(R2)
3313          ;A DEVICE HAS BEEN FOUND TEST VECTOR
3314 006770 012737 007004 000004 8$:  MOV      #95,ERRVEC    ;TIME OUT VECTOR TO REPORT ERROR
3315 006776 005777 176324          TST      @TESTVC       ;TRY TIME OUT ON VECTOR
3316 007002 000403          BR       10$          ; VECTOR EXISTS SO CONTINUE
3317 007004 104146          9$:  ERROR   146           ;A DEVICE BASE ADDRESS DID NOT
3318          ;TIME OUT BUT ITS CORRESPONDING
3319          ;VECTOR ADDRESS TIMED OUT

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3320                                     ;HI* CONTINUE TO REPEAT THIS
3321                                     ;TEST
3322 007006 000000 HALT
3323 007010 000767 BR 8$ ;REPEAT THIS TEST
3324                                     ;A DEVICE HAS BEEN FOUND AND VECTOR RESPONDS SO STORE RESULTS
3325 007012 013702 005322 10$: MOV TFOUND,R2 ;GET NO OF RH FOUND X2 FOR INDEX
3326 007016 013762 005362 005366 MOV NTYPNT,FDEVIC(R2) ;DEVICE 1=TU; 2,3,4,5,6,7=RP;
                                     ;10,11,12,13,14=RS
3327
3328 007024 017762 175040 005376 MOV JRHCS2,FUNITN(R2) ;GET RHCS2
3329 007032 042762 177770 005376 BIC #177770,FUNITN(R2) ;KEEP UNIT NUMBER
3330 007040 017762 175042 005406 MOV JRHDT,FTYPE(R2) ;DRIVE BYTE
3331 007046 013762 005326 005416 MOV TESTVC,FVECTR(R2) ;VECTOR
3332 007054 013762 005324 005426 MOV TESTDV,FBASEA(R2) ;BASE ADDRESS
3333 007062 013762 005322 005436 MOV TFOUND,FRHNM(R2) ;RH NUMBER X2 MINUS ONE
3334 007070 006262 005436 ASR FRHNM(R2) ;RH NUMBER MINUS ONE
3335 007074 005262 005436 INC FRHNM(R2) ;RH NUMBER
3336 007100 022762 000001 005366 CMP #1,FDEVIC(R2) ;IS IT TU
3337 007106 001016 BNE 11$ ;BRANCH IF NOT TU
3338 007110 012762 041710 005456 MOV #CMNDTM,FDRIVR(R2) ;DRIVER ADDRESS FOR TU
3339 007116 013746 005324 MOV TESTDV,-(SP) ;BASE ADDRESS
3340 007122 062716 000034 ADD #(2*14),(SP) ;15TH ADDRESS IS BAE FOR TU
3341 007126 011662 005466 MOV (SP),FBAE(R2) ;SAVE BAE FOR TU
3342 007132 062716 000002 ADD #2,(SP) ;16TH ADDRESS IS CS3 FOR TU
3343 007136 012662 005476 MOV (SP)+,FCS3(R2) ;SAVE CS3 FOR TU
3344
3345 007142 000511 BR SS1
3346 007144 022762 000002 005366 11$: CMP #2,FDEVIC(R2) ;IS IT RP DUAL PORT
3347 007152 001424 BEQ 12$ ;BRANCH IF RP FOUND
3348 007154 022762 000003 005366 CMP #3,FDEVIC(R2) ;IS IT RP
3349 007162 001420 BEQ 12$ ;BRANCH IF RP FOUND
3350 007164 022762 000004 005366 CMP #4,FDEVIC(R2) ;IS IT RP DUAL PORT
3351 007172 001414 BEQ 12$ ;BRANCH IF RP FOUND
3352 007174 022762 000005 005366 CMP #5,FDEVIC(R2) ;IS IT RP
3353 007202 001410 BEQ 12$ ;BRANCH IF RP FOUND
3354 007204 022762 000006 005366 CMP #6,FDEVIC(R2) ;IS IT RP DUAL PORT
3355 007212 001404 BEQ 12$ ;BRANCH IF RP FOUND
3356 007214 022762 000007 005366 CMP #7,FDEVIC(R2) ;IS IT RP
3357 007222 001016 BNE 13$ ;BRANCH IF NO RP
3358 007224 012762 041572 005456 12$: MOV #CMNDRP,FDRIVR(R2) ;DRIVER ADDRESS FOR RP
3359 007232 013746 005324 MOV TESTDV,-(SP) ;BASE ADDRESS
3360 007236 062716 000050 ADD #(2*20),(SP) ;21ST ADDRESS IS BAE FOR RP
3361 007242 011662 005466 MOV (SP),FBAE(R2) ;SAVE BAE FOR RP
3362 007246 062716 000002 ADD #2,(SP) ;22ND ADDRESS IS CS3 FOR RP
3363 007252 012662 005476 MOV (SP)+,FCS3(R2) ;SAVE CS3 FOR RP
3364
3365 007256 000443 BR SS1
3366 007260 022762 000010 005366 13$: CMP #10,FDEVIC(R2) ;IS IT RS03/LA
3367 007266 001420 BEQ 14$ ;BRANCH IF RS03/LA
3368 007270 022762 000011 005366 CMP #11,FDEVIC(R2) ;IS IT RS04/L
3369 007276 001414 BEQ 14$ ;BRANCH IF RS04/L
3370 007300 022762 000012 005366 CMP #12,FDEVIC(R2) ;IS IT RS04
3371 007306 001410 BEQ 14$ ;BRANCH IF RS04
3372 007310 022762 000013 005366 CMP #13,FDEVIC(R2) ;IS IT RS03/L
3373 007316 001404 BEQ 14$ ;BRANCH IF RS03/L
3374 007320 022762 000014 005366 CMP #14,FDEVIC(R2) ;IS IT RS03
3375 007326 001016 BNE 15$ ;BRANCH IF NOT RS03

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3376 007330 012762 041502 005456 14$: MOV #CMNDRS,FDRIVR(R2) ;DRIVER ADDRESS FOR RS
3377 007336 013746 005324 MOV TESTDV,-(SP) ;BASE ADDRESS
3378 007342 062716 000030 ADD #<2*12>,(SP) ;13TH ADDRESS IS BAE FOR RS
3379 007346 011662 005466 MOV (SP),FBAE(R2) ;SAVE BAE FOR RS
3380 007352 062716 000002 ADD #2,(SP) ;14TH ADDRESS IS CS3 FOR RS
3381 007356 012662 005476 MOV (SP)+,FCS3(R2) ;SAVE CS3 FOR RS
3382 007352 000401 BR SSI ;CONTINUE
3383 007364 000000 15$: HALT ;PROGRAM ERROR
3384
3385 ;NOW TYPE RESULT
3386
3387 007366 SSI:
3388 007356 104401 007374 TYPE 65$ ;:TYPE ASCIZ STRING
3389 007372 000406 BR 64$ ;:GET OVER THE ASCIZ
3390 ;:65$: .ASCIZ <15><12>/ON RH NO/
3391 64$:
3392 007410 MOV FRHNM(R2),-(SP) ;:TYPE RH NUMBER
3393 007410 016246 005436 TYPDS
3394 007414 104405 TYPE 67$ ;:TYPE ASCIZ STRING
3395 007416 104401 007424 BR 66$ ;:GET OVER THE ASCIZ
3396 ;:67$: .ASCIZ / BASE /
3397 66$:
3398 007446 MOV FBASEA(R2),-(SP) ;:TYPE BASE ADDRESS
3399 007446 016246 005426 TYPOC
3400 007452 104402 TYPE 69$ ;:TYPE ASCIZ STRING
3401 007454 104401 007452 BR 68$ ;:GET OVER THE ASCIZ
3402 ;:69$: .ASCIZ / FOUND/
3403 68$:
3404 007472 CMP #1,FDEVIC(R2) ;:IS IT TU
3405 007472 022762 000001 005366 BNE 1$ ;:BRANCH IF NOT TU
3406 007500 001020 TYPE 71$ ;:TYPE ASCIZ STRING
3407 007502 104401 007510 BR 70$ ;:GET OVER THE ASCIZ
3408 ;:71$: .ASCIZ / TU16 AT SLAVE/
3409 70$:
3410 007530 MOV FSLAVE(R2),-(SP) ;:TYPE SLAVE NUMBER
3411 007530 016246 005446 TYPDS
3412 007534 104405 JMP 6$
3413 007536 000137 010336 6$
3414 ;:THE FOLLOWING TYPES OUT
3415 ;:THE DEVICE AND REDEFINES
3416 ;:FDEVIC: 1=TU, 2=RP DUAL PORT
3417 ;:3=RP SINGLE PORT
3418 ;:4=RS04/L RS04
3419 ;:5=RS03/LA RS03/L RS03
3420 007542 022762 000002 005366 1$: CMP #2,FDEVIC(R2) ;:IS IT RPO6 DUAL PORT
3421 007550 001016 BNE 2$ ;:BRANCH IF NOT RPO6 DUAL PORT
3422 007552 104401 007560 TYPE 73$ ;:TYPE ASCIZ STRING
3423 007556 000411 BR 72$ ;:GET OVER THE ASCIZ
3424 ;:73$: .ASCIZ / RPO6 DUAL PORT /
3425 72$:
3426 007602 JMP 6$
3427 007606 022762 000003 005366 2$: CMP #3,FDEVIC(R2) ;:IS IT RPO6 SINGLE PORT
3428 007614 001016 BNE 3$ ;:BRANCH IF NOT RPO6 SINGLE PORT
3429 007616 104401 007624 TYPE 75$ ;:TYPE ASCIZ STRING
3430 007622 000411 BR 74$ ;:GET OVER THE ASCIZ
3431 ;:75$: .ASCIZ / RPO6 SINGLE PORT /
74$:

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SIZE FOR RH DEVICES

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3432 007646 000137 010336          JMP      6$
3433 007652 022762 000004 005366 3$:  CMP      #4,FDEVIC(R2) ;IS IT RPO5 DUAL PORT
3434 007660 001020          BNE      4$ ;BRANCH IF NOT RPO5 DUAL PORT
3435 007662 012762 000002 005366  MOV      #2,FDEVIC(R2)
3436 007670 104401 007676          TYPE     77$ ;:TYPE ASCIZ STRING
3437 007674 000410          BR       76$ ;:GET OVER THE ASCIZ
3438          ;:77$: .ASCIZ / RPO5 DUAL PORT/
3439          76$:
3440 007716 000137 010336          JMP      6$
3441 007722 022762 000005 005366 4$:  CMP      #5,FDEVIC(R2) ;IS IT RPO5 SINGLE PORT
3442 007730 001020          BNE      8$ ;BRANCH IF NOT RPO5 SINGLE PORT
3443 007732 012762 000003 005366  MOV      #3,FDEVIC(R2)
3444 007740 104401 007746          TYPE     79$ ;:TYPE ASCIZ STRING
3445 007744 000411          BR       78$ ;:GET OVER THE ASCIZ
3446          ;:79$: .ASCIZ / RPO5 SINGLE PORT/
3447          78$:
3448 007770 000562          BR       6$
3449 007772 022762 000006 005366 8$:  CMP      #6,FDEVIC(R2) ;IS IT RPO4 DUAL PORT
3450 010000 001017          BNE      9$ ;BRANCH IF NOT RPO4 DUAL PORT
3451 010002 012762 000002 005366  MOV      #2,FDEVIC(R2)
3452 010010 104401 010016          TYPE     81$ ;:TYPE ASCIZ STRING
3453 010014 000410          BR       80$ ;:GET OVER THE ASCIZ
3454          ;:81$: .ASCIZ / RPO4 DUAL PORT/
3455          80$:
3456 010036 000537          BR       6$
3457 010040 022762 000007 005366 9$:  CMP      #7,FDEVIC(R2) ;IS IT RPO4 SINGLE PORT
3458 010046 001020          BNE     10$ ;BRANCH IF NOT RPO4 SINGLE PORT
3459 010050 012762 000003 005366  MOV      #3,FDEVIC(R2)
3460 010056 104401 010064          TYPE     83$ ;:TYPE ASCIZ STRING
3461 010062 000411          BR       82$ ;:GET OVER THE ASCIZ
3462          ;:83$: .ASCIZ / RPO4 SINGLE PORT/
3463          82$:
3464 010106 000513          BR       6$
3465 010110 022762 000010 005366 10$: CMP      #10,FDEVIC(R2) ;IS IT RS03-LA
3466 010116 001014          BNE     11$ ;BRANCH IF NOT RS03-LA
3467 010120 012762 000005 005366  MOV      #5,FDEVIC(R2)
3468 010126 104401 010134          TYPE     85$ ;:TYPE ASCIZ STRING
3469 010132 000405          BR       84$ ;:GET OVER THE ASCIZ
3470          ;:85$: .ASCIZ / RS03-LA/
3471          84$:
3472 010146 000473          BR       6$
3473 010150 022762 000011 005366 11$: CMP      #11,FDEVIC(R2) ;IS IT RS04-L
3474 010156 001013          BNE     12$ ;BRANCH IF NOT RS04-L
3475 010160 012762 000004 005366  MOV      #4,FDEVIC(R2)
3476 010166 104401 010174          TYPE     87$ ;:TYPE ASCIZ STRING
3477 010172 000404          BR       86$ ;:GET OVER THE ASCIZ
3478          ;:87$: .ASCIZ / RS04-L/
3479          86$:
3480 010204 000454          BR       6$
3481 010206 022762 000012 005366 12$: CMP      #12,FDEVIC(R2) ;IS IT RS04
3482 010214 001012          BNE     13$ ;BRANCH IF NOT RS04
3483 010216 012762 000004 005366  MOV      #4,FDEVIC(R2)
3484 010224 104401 010232          TYPE     89$ ;:TYPE ASCIZ STRING
3485 010230 000403          BR       88$ ;:GET OVER THE ASCIZ
3486          ;:89$: .ASCIZ / RS04/
3487          88$:

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3498 010240 000436 BR 6$
3499 010242 022762 000013 005366 13$: CMP #13,FDEVIC(R2) ;IS IT RS03-L
3490 010250 001013 BNE 14$ ;BRANCH IF NOT RS03-L
3491 010252 012762 000005 005366 MOV #5,FDEVIC(R2)
3492 010260 104401 010256 TYPE 91$ ;;TYPE ASCIZ STRING
3493 010264 000404 BR 90$ ;;GET OVER THE ASCIZ
3494 ;;91$: .ASCIZ / RS03-L/
3495 90$:
3496 010276 000417 BR 6$
3497 010300 022762 000014 005366 14$: CMP #14,FDEVIC(R2) ;IS IT RS03
3498 010306 001012 BNE 5$ ;BRANCH IF NOT RS03
3499 010310 012762 000005 005366 MOV #5,FDEVIC(R2)
3500 010316 104401 010324 TYPE 93$ ;;TYPE ASCIZ STRING
3501 010322 000403 BR 92$ ;;GET OVER THE ASCIZ
3502 ;;93$: .ASCIZ / RS03/
3503 92$:
3504 010332 000401 BR 6$
3505 010334 000000 5$: HALT ;PROGRAM ERROR
3506 010336 010334 6$:
3507 010336 104401 010344 TYPE 95$ ;;TYPE ASCIZ STRING
3508 010342 000411 BR 94$ ;;GET OVER THE ASCIZ
3509 ;;95$: .ASCIZ <15><12>/AT UNIT NUMBER/
3510 94$:
3511 010366 016246 005376 MOV FUNITN(R2),-(SP) ;TYPE UNIT NUMBER
3512 010372 104405 TYPDS
3513 010374 104401 010402 TYPE 97$ ;;TYPE ASCIZ STRING
3514 010400 000411 BR 96$ ;;GET OVER THE ASCIZ
3515 ;;97$: .ASCIZ / VECTOR /
3516 96$:
3517 010424 016246 005416 MOV FVECTR(R2),-(SP) ;TYPE VECTOR
3518 010430 104402 TYPDC
3519 010432 104401 010440 TYPE 99$ ;;TYPE ASCIZ STRING
3520 010436 000402 BR 98$ ;;GET OVER THE ASCIZ
3521 ;;99$: .ASCIZ <15><12>/ /
3522 98$:
3523 010444 104401 010452 TYPE 101$ ;;TYPE ASCIZ STRING
3524 010450 000402 BR 100$ ;;GET OVER THE ASCIZ
3525 ;;101$: .ASCIZ <15><12>/ /
3526 100$:
3527 010456
3528 :UPDATE TFOUND
3529 010456 062737 000002 005322 ADD #2,TFOUND ;GET READY FOR NEXT DEVICE
3530
3531 :ALL UNITS DONE? IF LTRY BECOMES ZERO YES ALL DONE
3532 010464 005337 005320 DEC LTRY ;REDUCE DEVICES LEFT TO TRY
3533 010470 001402 BEQ 7$ ;BRANCH IF ALL COMPLETE
3534 010472 000137 006526 JMP 52$ ;ALL NOT DONE
3535 010476 7$:
3536 010476 013746 005322 53$: MOV TFOUND,-(SP) ;GET TWICE NUMBER OF RH FOUND
3537 010502 006216 ASR (SP) ;DIVIDE BY 2
3538 010504 011637 005506 MOV (SP),TSTUNT
3539 010510 012637 005510 MOV (SP),WORUNT
3540 010514 000137 010622 JMP TST2 ;JUMP TO NEXT TEST
3541
3542 :A RH TIMED OUT SO TRY NEXT UNIT OR END TRYING
3543 010520 005337 005320 54$: DEC LTRY ;RH TIMES OUT SO DECREASE RH TYPED

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3544 010524 001402      BEQ      1$          ;BRANCH IF ALL DONE
3545 010526 000137 006525      JMP      SS2        ;ALL NOT DONE SO JMP.
3546 010532 000137 010476      1$:     JMP      SS3        ;ALL DONE SO STORE NUMBER OF RH FOUND
3547
3548 ;A DRIVE TYPE DID NOT MATCH SO TRY NEXT
3549 010536 012777 040000 173314  SS5:    MOV      #TRE, @RHCS1 ;CLEAR NED ERROR
3550 010544 005337 005362  SS8:    DEC      NTYPNT      ;DECREASE NUMBER OF DRIVE TYPES TRIED
3551 010550 001402      BEQ      1$          ;BRANCH IF ALL DONE
3552 010552 000137 006616      JMP      SS7        ;ALL NOT DONE
3553 010556 005277 173306      1$:     INC      @RHCS2    ;GET NEXT UNIT NO.
3554 010562 012777 040000 173270      MOV      #TRE, @RHCS1 ;CLEAR NED ERROR
3555 010570 017746 173274      MOV      @RHCS2, -(SP) ;GET RHCS2
3556 010574 042716 177770      BIC      #177770, (SP) ;GET UNIT NO
3557 010600 022726 000010      CMP      @8., (SP)+   ;ARE 7 DONE
3558 010604 001402      BEQ      2$          ;BRANCH IF 7 DONE
3559 010606 000137 006602      JMP      SS6        ;7 NOT DONE SO TRY NEXT UNIT NO
3560
3561 010612 104147      2$:     ERROR    147      ;DEVICE ADDRESS DID NOT TIME
3562 010614 000000      HALT
3563
3564 010616 000137 010520      JMP      SS4
3565
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;*****
;TEST 2 UNIT UNDER TEST
; THIS TYPES THE UNIT TO BE TESTED
; AND IS THE FIRST TEST AFTER THE END OF THE PROGRAM
;*****
TST2: SCOPE
      MOV      #1, $TIMES ;;DO 1 ITERATION
      MOV      #TIMEOUT, @#ERRVEC ;;TIMEOUT VECTOR
      MOV      #340, @#ERRVEC+2 ;;PRIORITY 7
      MOV      WORUNT, -(SP) ;;GET WORKING RH NUMBER LEFT
      MOV      TSTUNT, R2 ;;GET TOTAL NO OF RH FOUND
      SUB      (SP)+, R2 ;;NO OF RH TO BE TESTED
      ASL      R2 ;;INDEX FOR RH TO BE TESTED.
      TYPE      , 65$ ;;TYPE ASCIZ STRING
      BR      64$ ;;GET OVER THE ASCIZ
;;65$: .ASCIZ <15><12> // /
64$:
      TYPE      , 67$ ;;TYPE ASCIZ STRING
      BR      66$ ;;GET OVER THE ASCIZ
;;67$: .ASCIZ <15><12>/TESTING RH NO/
66$:
      MOV      FRHNM(R2), -(SP) ;;TYPE RH NO.
      TYPDS
      TYPE      , 69$ ;;TYPE ASCIZ STRING
      BR      68$ ;;GET OVER THE ASCIZ
;;69$: .ASCIZ / USING /

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3600 010756          68$:
3601 010756 104401 010764      TYPE      71$          ;;TYPE ASCIZ STRING
3602 010762 000404          BR      70$          ;;GET OVER THE ASCIZ
3603          ;;71$: .ASCIZ / BASE /
3604 010774          70$:
3605 010774 016246 005426      MOV      FBASEA(R2),-(SP) ;TYPE BASE ADDRESS
3606 011000 104402          TYPDS
3607 011002 022762 000001 005366  CMP      #1,FDEVIC(R2)    ;IS IT TU
3608 011010 001017          BNE      1$          ;BRANCH IF NOT TU
3609 011012 104401 011020      TYPE      73$          ;;TYPE ASCIZ STRING
3610 011016 000410          BR      72$          ;;GET OVER THE ASCIZ
3611          ;;73$: .ASCIZ / TU16 AT SLAVE/
3612          72$:
3613 011040          MOV      FSLAVE(R2),-(SP) ;TYPE SLAVE NUMBER
3614 011044 104405          TYPDS
3615 011046 000467          BR
3616 011050 022762 000002 005366 1$:  CMP      #2,FDEVIC(R2)    ;IS IT RP DUAL PORT
3617 011056 001014          BNE      2$          ;BRANCH IF NOT RP DUAL PORT
3618 011060 104401 011066      TYPE      75$          ;;TYPE ASCIZ STRING
3619 011064 000410          BR      74$          ;;GET OVER THE ASCIZ
3620          ;;75$: .ASCIZ / RP DUAL PORT /
3621          74$:
3622 011106          BR      6$
3623 011110 000447 000003 005366 2$:  CMP      #3,FDEVIC(R2)    ;IS IT RO SINGLE PORT
3624 011116 001014          BNE      3$          ;BRANCH IF NOT RP
3625 011120 104401 011126      TYPE      77$          ;;TYPE ASCIZ STRING
3626 011124 000410          BR      76$          ;;GET OVER THE ASCIZ
3627          ;;77$: .ASCIZ / RP SINGLE PORT/
3628          76$:
3629 011146          BR      6$
3630 011150 000427 000004 005366 3$:  CMP      #4,FDEVIC(R2)    ;IS IT RS04
3631 011156 001007          BNE      4$          ;BRANCH IF NOT RS04
3632 011160 104401 011166      TYPE      79$          ;;TYPE ASCIZ STRING
3633 011164 000403          BR      78$          ;;GET OVER THE ASCIZ
3634          ;;79$: .ASCIZ / RS04/
3635          78$:
3636 011174          BR      6$
3637 011174 000414 000005 005366 4$:  CMP      #5,FDEVIC(R2)    ;IS IT RS03
3638 011204 001007          BNE      5$          ;BRANCH IF NOT RS03
3639 011206 104401 011214      TYPE      81$          ;;TYPE ASCIZ STRING
3640 011212 000403          BR      80$          ;;GET OVER THE ASCIZ
3641          ;;81$: .ASCIZ / RS03/
3642          80$:
3643 011222          BR      6$
3644 011224 000401          5$:  HALT
3645          ;PROGRAM ERROR
3646          6$:
3647 011226 104401 011234      TYPE      83$          ;;TYPE ASCIZ STRING
3648 011232 000411          BR      82$          ;;GET OVER THE ASCIZ
3649          ;;83$: .ASCIZ <15><12>/AT UNIT NUMBER/
3650          82$:
3651 011256          MOV      FUNITN(R2),-(SP) ;TYPE UNIT NUMBER
3652 011262 104405          TYPDS
3653 011264 104401 011272      TYPE      85$          ;;TYPE ASCIZ STRING
3654 011270 000411          BR      84$          ;;GET OVER THE ASCIZ
3655          ;;85$: .ASCIZ / VECTOR /

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3656 011314
3657 011314 016246 005416
3658 011320 104402
3659 011322 012703 000024
3660 011326 012704 024050
3661 011332 016205 005426
3662 011336 010524
3663 011340 062705 000002
3664 011344 005303
3665 011346 001373
3666
3667 011350 016237 005376 005010
3668 011356 016237 005416 005512
3669 011364 016237 005446 004660
3670 011372 016237 005456 004640
3671 011400 016237 005466 004130
3672 011406 016237 005476 004132
3673
3674 011414 005037 045100
3675
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3700 011420 000004
3701 011422 012706 001000
3702 011426 012737 000003 004656
3703
3704 011434 004737 040312
3705
3706
3707
3708 011440 012737 176201 004650
3709 011446 012737 004200 004652
3710 011454 012737 002000 004654
3711

```

```

84$: MOV FVECTR(R2),-(SP) ;TYPE VECTOR
      TYP0C
      MOV #20,R3 ;COUNT 20
      MOV #RHCS1,R4 ;GET BASE LOCATION
      MOV FBASEA(R2),R5 ;GET BASE ADDRESS
7$: MOV R5,(R4)+ ;FILL PROPER ADDRESS
      ADD #2,R5 ;INCREMENT BY 2
      DEC R3 ;COUNT DOWN
      BNE 7$ ;BRANCH IF 20 NOT DONE

      MOV FUNITN(R2).UNIT ;UNIT NUMBER
      MOV FVECTR(R2).VECTOR ;VECTOR
      MOV FSLAVE(R2).SLAVE ;SLAVE NO
      MOV FDRIVR(R2).COMND ;DRIVER ADDRESS
      MOV FBAE(R2).RHBAE ;BAE ADDRESS
      MOV FCS3(R2).RHCS3 ;CS3 ADDRESS

      CLR PRITEM

```

```

*****
*TEST 3 BIT BANG RHCS1

```

```

* TEST LOADING AND READING OF ALL POSSIBLE BITS
* IN RHCS1 REGISTER.
* USE A PATTERN OF WALKING 1'S (1,2,4,10 ETC)
* AND WALKING 0'S (-2,-3,-5 ETC)
* IN THIS TEST SC!TRE!MCPE!DVA!BIT12!BIT10!RDY!GO BITS WILL NOT BE WRITTEN INTO
* AND RDY!DVA BITS WILL ALWAYS BE SET
* AND BIT10 BITS WILL ALWAYS BE CLEARED
* IF SWITCH 14 OR 9 OR 8 ARE SET FOR DEBUGGING
* THEN A RH CLEAR (RHCS2 BIT #5) WILL BE
* GIVEN TO AID SCOPE SYNC'S ON THE CLEAR
* SIGNAL.

```

```

*****
†ST3: SCOPE

```

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      MOV #STACK,SP ;RESET STACK
      MOV #3,2#TSTNM ;SAVE TEST NUMBER

      JSR PC,2#CLDISK ;GIVE RH INITIALIZE
      ;SETUP UNIT NUBER
      ;CLEAR RHWC AND FUNCTION BITS IN RHCS1

      MOV #SC!TRE!MCPE!DVA!BIT12!BIT10!RDY!GO,WRTBIT ;SC!TRE!MCPE!DVA!BIT12!B
      MOV #RDY!DVA,SETBIT ;RDY!DVA ARE BITS ALWAYS SET
      MOV #BIT10,CLRBIT ;BIT10 ARE BITS ALWAYS CLEARED

```

K06

MAINDEC-11-DERHAB-A
DERHAB.SRC T3

MACY11 27(732)
BIT BANG RHCS1

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```

3712                                     ;FLOAT 1'S THRU THE RHCS1 REGISTER
3713 011462 012737 011506 001110      MOV    #2$, $LPERR      ;SET LOOP ON ERROR ADDRESS
3714 011470 012705 000001              MOV    #1, R5          ;GETTING READY TO FLOAT A ONE
3715 011474 010537 001124              1$:   MOV    R5, $GDDAT     ;START WITH DATA
3716 011500 042737 176201 001124      BIC    #SC!TRE!MCPE!DVA!BIT12!BIT10!RDY!GO, $GDDAT ;CLEAR BITS NOT WRITTEN
3717 011506 032737 041400 001140      2$:   BIT    #BIT14!BIT9!BIT8, SWR ;ARE SWITCHES 14 OR 9 OR 8 SET
3718 011514 001403                      BEQ    3$              ;BRANCH IF ANY ONE SET
3719 011516 052777 000040 172344      BIS    #CLR, $RHCS2    ;GIVE CLEAR FOR SCOPE
3720                                     ;SYNC IF IN DEBUG:
3721 011524 013777 001124 172326      3$:   MOV    $GDDAT, $RHCS1 ;WRITE RHCS1 REGISTER
3722 011532 017737 172322 001126      MOV    $RHCS1, $BDDAT  ;READ RHCS1 REGISTER
3723 011540 043737 004654 001124      BIC    CLRBIT, $GDDAT  ;CLEAR ALWAYS CLEARED BITS
3724 011546 053737 004652 001124      BIS    SETBIT, $GDDAT  ;SET ALWAYS SET BITS
3725 011554 023737 001124 001126      CMP    $GDDAT, $BDDAT ;TEST
3726 011562 001404                      BEQ    4$              ;BRANCH IF GOOD
3727 011564 013737 004060 001122      MOV    RHCS1, $SDADR   ;GET REGISTER ADDRESS
3728 011572 104137                      ERROR  137            ;ONE WAS BEING FLOATED
3729                                     ;THRU RHCS1 REGISTER
3730                                     ;ON READING RHCS1 BACK
3731                                     ;IT DID NOT CONTAIN WHAT
3732                                     ;WAS EXPECTED.
3733 011574 000241              4$:   CLC                    ;CLEAR CARRY
3734 011576 006305              ASL    R5              ;GET 1 ONE LEFT
3735 011600 103335              BCC    1$              ;BRANCH IF 16 NOT DONE
3736
3737                                     ;FLOAT A ZERO THRU RHCS1 REGISTER.
3738
3739 011602 012737 011626 001110      MOV    #6$, $LPERR     ;SET LOOP BACK POINT.
3740 011610 012705 177775              MOV    #177776, R5     ;GET READY TO FLOAT A ZERO
3741 011614 010537 001124              5$:   MOV    R5, $GDDAT     ;GET READY TO WRITE DATA
3742 011620 042737 176201 001124      BIC    #SC!TRE!MCPE!DVA!BIT12!BIT10!RDY!GO, $GDDAT ;CLEAR BITS NOT WRITTEN
3743 011626 032737 041400 001140      6$:   BIT    #BIT14!BIT9!BIT8, SWR ;ARE SWITCHES 14 OR 9 OR 8 SET
3744 011634 001403                      BEQ    7$              ;BRANCH IF ANY OF THE ABOVE SET
3745 011636 012777 000040 172224      MOV    #CLR, $RHCS2    ;CLEAR FOR SCOPE AID TO
3746                                     ;SYNC IF IN DEBUG
3747 011644 013777 001124 172206      7$:   MOV    $GDDAT, $RHCS1 ;WRITE INTO RH'XA.
3748 011652 017737 172202 001126      MOV    $RHCS1, $BDDAT  ;READ RHCS1
3749 011660 053737 004652 001124      BIS    SETBIT, $GDDAT  ;SET BITS ALWAYS SET
3750 011666 043737 004654 001124      BIC    CLRBIT, $GDDAT  ;CLEAR BITS ALWAYS 0
3751 011674 023737 001124 001126      CMP    $GDDAT, $BDDAT ;TEST
3752 011702 001404                      BEQ    8$              ;BRANCH IF GOOD
3753 011704 013737 004060 001122      MOV    RHCS1, $SDADR   ;GET REGISTER ADDRESS
3754 011712 104137                      ERROR  137            ;ZERO WAS BEING FLOATED
3755                                     ;THRU RHCS1 REGISTER
3756                                     ;ON READING IT BACK IT
3757                                     ;DID NOT CONTAIN WHAT
3758                                     ;WAS EXPECTED
3759
3760 011714 000261              8$:   SEC                    ;
3761 011716 006105              ROL    R5              ;
3762 011720 103735              BCS    5$              ;
3763
3764
3765                                     ;*****
3766                                     ;*TEST 4          BIT BANG RHCS2
3767

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3768      ;*      TEST LOADING AND READING OF ALL POSSIBLE BITS
3769      ;*      IN RHCS2 REGISTER.
3770      ;*      USE A PATTERN OF WALKING 1'S (1,2,4,10 ETC)
3771      ;*      AND WALKING 0'S (-2,-3,-5 ETC)
3772      ;*      IN THIS TEST 177740 BITS WILL NOT BE WRITTEN INTO
3773      ;*      AND IR BITS WILL ALWAYS BE SET
3774      ;*      IF SWITCH 14 OR 9 OR 8 ARE SET FOR DEBUGGING
3775      ;*      THEN A RH CLEAR (RHCS2 BIT #5) WILL BE
3776      ;*      GIVEN TO AID SCOPE SYNC'S ON THE CLEAR
3777      ;*      SIGNAL.
3778
3779      ;*****
3780 011722 000004      †ST4: SCOPE
3781 011724 012706 001000      MOV      #STACK,SP      ;RESET STACK
3782 011730 012737 000004 004656      MOV      #4,‡#TSTNM      ;SAVE TEST NUMBER
3783
3784 011736 004737 040312      JSR      PC,‡#CLDISK      ;GIVE RH INITIALIZE
3785                                ;SETUP UNIT NUBER
3786                                ;CLEAR RHWC AND FUNCTION BITS IN RHCS1
3787
3788 011742 012737 177740 004650      MOV      #177740,WRTBIT ;177740 ARE BITS NOT WRITTEN INTO
3789 011750 012737 000100 004652      MOV      #IR,SETBIT      ;IR ARE BITS ALWAYS SET
3790
3791                                ;FLOAT 1'S THRU THE RHCS2 REGISTER
3792 011756 012737 012002 001110      MOV      #2$,‡LPERR      ;SET LOOP ON ERROR ADDRESS
3793 011764 012705 000001      MOV      #1,R5           ;GETTING READY TO FLOAT A ONE
3794 011770 010537 001124      1$: MC / R5,$GDDAT      ;START WITH DATA
3795 011774 042737 177740 001124      BIC      #177740,$GDDAT ;CLEAR BITS NOT WRITTEN INTO
3796 012002 032737 041400 001140      2$: BIT   #BIT14!BIT9!BIT8,SWR ;ARE SWITCHES 14 OR 9 OR 8 SET
3797 012010 001403      BEQ      3$             ;BRANCH IF ANY ONE SET
3798 012012 052777 000040 172050      BIS      #CLR,‡RHCS2    ;GIVE CLEAR FOR SCOPE
3799                                ;SYNC IF IN DEBUG:
3800 012020 013777 001124 172042      3$: MOV      $GDDAT,‡RHCS2 ;WRITE RHCS2 REGISTER
3801 012026 017737 172036 001126      MOV      ‡RHCS2,$BDDAT ;READ RHCS2 REGISTER
3802 012034 053737 004652 001124      BIS      SETBIT,$GDDAT  ;SET ALWAYS SET BITS
3803 012042 023737 001124 001126      CMP      $GDDAT,$BDDAT ;TEST
3804 012050 001404      BEQ      4$             ;BRANCH IF GOOD
3805 012052 013737 004070 001122      MOV      RHCS2,$BDDADR ;GET REGISTER ADDRESS
3806 012060 104137      ERROR   137           ;ONE WAS BEING FLOATED
3807                                ;THRU RHCS2 REGISTER
3808                                ;ON READING RHCS2 BACK
3809                                ;IT DID NOT CONTAIN WHAT
3810                                ;WAS EXPECTED.
3811 012062 000241      4$: CLC                ;CLEAR CARRY
3812 012064 006305      ASL      R5             ;GET 1 ONE LEFT
3813 012066 103340      BCC      1$            ;BRANCH IF 16 NOT DONE
3814
3815                                ;FLOAT A ZERO THRU RHCS2 REGISTER.
3816
3817 012070 012737 012114 001110      MOV      #6$,‡LPERR      ;SET LOOP BACK POINT.
3818 012076 012705 177776      MOV      #177776,R5      ;GET READY TO FLOAT A ZERO
3819 012102 010537 001124      5$: MOV      R5,$GDDAT      ;GET READY TO WRITE DATA
3820 012106 042737 177740 001124      BIC      #177740,$GDDAT ;CLEAR BITS NOT WRITTEN INTO
3821 012114 032737 041400 001140      6$: BIT   #BIT14!BIT9!BIT8,SWR ;ARE SWITCHES 14 OR 9 OR 8 SET
3822 012122 001403      BEQ      7$             ;BRANCH IF ANY OF THE ABOVE SET
3823 012124 012777 000040 171736      MOV      #CLR,‡RHCS2    ;CLEAR FOR SCOPE AID TO

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3824
3825 012132 013777 001124 171730 7$: MOV $GDDAT, @RHCS2 ; SYNC IF IN DEBUG
3826 012140 017737 171724 001126 MOV @RHCS2, $BDDAT ; WRITE INTO RH'XA.
3827 012146 053737 004652 001124 BIS SETBIT, $GDDAT ; READ RHCS2
3828 012154 023737 001124 001126 CMP $GDDAT, $BDDAT ; SET BITS ALWAYS SET
3829 012162 001404 BEQ 9$ ; TEST
3830 012164 013737 004070 001122 MOV RHCS2, $BDADR ; BRANCH IF GOOD
3831 012172 104137 ERROR 137 ; GET REGISTER ADDRESS
3832 ; ZERO WAS BEING FLOATED
3833 ; THRU RHCS2 REGISTER
3834 ; ON READING IT BACK IT
3835 ; DID NOT CONTAIN WHAT
3836 ; WAS EXPECTED
3837 012174 000261 8$: SEC
3838 012176 006105 ROL R5
3839 012200 103740 BCS 5$
3840
3841
3842 ;*****
3843 ;*TEST 5 BIT BANG RHCS3
3844
3845 ;*
3846 ;* TEST LOADING AND READING OF ALL POSSIBLE BITS
3847 ;* IN RHCS3 REGISTER.
3848 ;* USE A PATTERN OF WALKING 1'S (1,2,4,10 ETC)
3849 ;* AND WALKING 0'S (-2,-3,-5 ETC)
3850 ;* IN THIS TEST 177660 BITS WILL NOT BE WRITTEN INTO
3851 ;* AND 0 BITS WILL ALWAYS BE SET
3852 ;* AND BIT9!BIT8!BIT7!BIT5!BIT4 BITS WILL ALWAYS BE CLEARED
3853 ;* IF SWITCH 14 OR 9 OR 8 ARE SET FOR DEBUGGING
3854 ;* THEN A RH CLEAR (RHCS2 BIT #5) WILL BE
3855 ;* GIVEN TO AID SCOPE SYNC'S ON THE CLEAR
3856 ;* SIGNAL.
3857 ;*****
3858 012202 000004 tSTS: SCOPE
3859 012204 012706 001000 MOV #STACK, SP ; RESET STACK
3860 012210 012737 000005 004656 MOV #5, @#TSTNM ; SAVE TEST NUMBER
3861
3862 012216 004737 040312 JSR PC, @#CLDISK ; GIVE RH INITIALIZE
3863 ; SETUP UNIT NUMBER
3864 ; CLEAR RHWC AND FUNCTION BITS IN RHCS1
3865
3866 012222 012737 177660 004650 MOV #177660, WRTBIT ; 177660 ARE BITS NOT WRITTEN INTO
3867 012230 012737 000000 004652 MOV #0, SETBIT ; 0 ARE BITS ALWAYS SET
3868 012236 012737 001660 004654 MOV #BIT9!BIT8!BIT7!BIT5!BIT4, CLRBIT ; BIT9!BIT8!BIT7!BIT5!BIT4 ARE BITS ALWAYS
3869
3870 ; FLOAT 1'S THRU THE RHCS3 REGISTER
3871 012244 012737 012270 001110 MOV #2$, $LPERR ; SET LOOP ON ERROR ADDRESS
3872 012252 012705 000001 MOV #1, R5 ; GETTING READY TO FLOAT A ONE
3873 012256 010537 001124 1$: MOV R5, $GDDAT ; START WITH DATA
3874 012262 042737 177660 001124 BIC #177660, $GDDAT ; CLEAR BITS NOT WRITTEN INTO
3875 012270 032737 041400 001140 2$: BIT #BIT14!BIT9!BIT8, SWR ; ARE SWITCHES 14 OR 9 OR 8 SET
3876 012276 001403 BEQ 3$ ; BRANCH IF ANY ONE SET
3877 012300 052777 000040 171562 BIS #CLR, @RHCS2 ; GIVE CLEAR FOR SCOPE
3878 ; SYNC IF IN DEBUG:
3879 012306 013777 001124 171616 3$: MOV $GDDAT, @RHCS3 ; WRITE RHCS3 REGISTER

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3890 012314 017737 171612 001126      MOV      @RHCS3,$BDDAT      ;READ RHCS3 REGISTER
3891 012322 043737 004654 001124      BIC      CLRBIT,$GDDAT     ;CLEAR ALWAYS CLEARED BITS
3892 012330 053737 004652 001124      BIS      SETBIT,$GDDAT     ;SET ALWAYS SET BITS
3893 012336 023737 001124 001126      CMP      $GDDAT,$BDDAT     ;TEST
3894 012344 001404                BEQ      4$                ;BRANCH IF GOOD
3895 012346 013737 004132 001122      MOV      RHCS3,$BDADR      ;GET REGISTER ADDRESS
3896 012354 104137                ERROR    137              ;ONE WAS BEING FLOATED
3897                                ;THRU RHCS3 REGISTER
3898                                ;ON READING RHCS3 BACK
3899                                ;IT DID NOT CONTAIN WHAT
3900                                ;WAS EXPECTED.
3891 012356 000241                4$:    CLC                  ;CLEAR CARRY
3892 012350 006305                ASL      R5                ;GET 1 ONE LEFT
3893 012362 103335                BCC      1$                ;BRANCH IF 16 NOT DONE
3895                                ;FLOAT A ZERO THRU RHCS3 REGISTER.
3897 012364 012737 012410 001110      MOV      #6$,$LPERR        ;SET LOOP BACK POINT.
3898 012372 012705 177776                MOV      #177776,R5        ;GET READY TO FLOAT A ZERO
3899 012376 010537 001124                5$:    MOV      R5,$GDDAT     ;GET READY TO WRITE DATA
3900 012402 042737 177660 001124      BIC      #177660,$GDDAT    ;CLEAR BITS NOT WRITTEN INTO
3901 012410 032737 041400 001140      6$:    BIT      #BIT14!BIT9!BIT8,SWR ;ARE SWITCHES 14 OR 9 OR 8 SET
3902 012416 001403                BEQ      7$                ;BRANCH IF ANY OF THE ABOVE SET
3903 012420 012777 000040 171442      MOV      #CLR,@RHCS2       ;CLEAR FOR SCOPE AID TO
3904                                ;SYNC IF IN DEBUG
3905 012426 013777 001124 171476      7$:    MOV      $GDDAT,@RHCS3 ;WRITE INTO RH'XA.
3906 012434 017737 171472 001126      MOV      @RHCS3,$BDDAT     ;READ RHCS3
3907 012442 053737 004652 001124      BIS      SETBIT,$GDDAT     ;SET BITS ALWAYS SET
3908 012450 043737 004654 001124      BIC      CLRBIT,$GDDAT     ;CLEAR BITS ALWAYS 0
3909 012456 023737 001124 001126      CMP      $GDDAT,$BDDAT     ;TEST
3910 012464 001404                BEQ      8$                ;BRANCH IF GOOD
3911 012466 013737 004132 001122      MOV      RHCS3,$BDADR      ;GET REGISTER ADDRESS
3912 012474 104137                ERROR    137              ;ZERO WAS BEING FLOATED
3913                                ;THRU RHCS3 REGISTER
3914                                ;ON READING IT BACK IT
3915                                ;DID NOT CONTAIN WHAT
3916                                ;WAS EXPECTED
3918 012476 000261                8$:    SEC                  ;
3919 012500 006105                ROL      R5                ;
3920 012502 103735                BCS      5$                ;
3923                                ;*****
3924                                ;*TEST 6          BIT BANG RHW
3926                                ;*
3927                                ;* TEST LOADING AND READING OF ALL POSSIBLE BITS
3928                                ;* IN RHW REGISTER.
3929                                ;* USE A PATTERN OF WALKING 1'S (1,2,4,10 ETC)
3930                                ;* AND WALKING 0'S (-2,-3,-5 ETC)
3931                                ;* IN THIS TEST 0 BITS WILL NOT BE WRITTEN INTO
3932                                ;* AND 0 BITS WILL ALWAYS BE SET
3933                                ;* IF SWITCH 14 OR 9 OR 8 ARE SET FOR DEBUGGING
3934                                ;* THEN A RH CLEAR (RHCS2 BIT #5) WILL BE
3935                                ;* GIVEN TO AID SCOPE SYNC ON THE CLEAR
3936                                ;* SIGNAL.

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;DID NOT CONTAIN WHAT
;WAS EXPECTED

012756 000261
012750 006435
012752 103740

8\$: SEC
ROL RS
BCS SS

;*TEST 7 BIT BANG RHBA

;* TEST LOADING AND READING OF ALL POSSIBLE BITS
;* IN RHBA REGISTER.
;* USE A PATTERN OF WALKING 1'S (1,2,4,10 ETC)
;* AND WALKING 0'S (-2,-3,-5 ETC)
;* IN THIS TEST 0 BITS WILL NOT BE WRITTEN INTO
;* AND 0 BITS WILL ALWAYS BE SET
;* AND BIT00 BITS WILL ALWAYS BE CLEARED
;* IF SWITCH 14 OR 9 OR 8 ARE SET FOR DEBUGGING
;* THEN A RH CLEAR (RHCS2 BIT #5) WILL BE
;* GIVEN TO AID SCOPE SYNC'S ON THE CLEAR
;* SIGNAL.

4016 012764 000004
4017 012766 012706 001000
4018 012772 012737 000007 004656
4019
4020 013000 004737 040312
4021
4022
4023
4024 013004 012737 000000 004650
4025 013012 012737 000000 004652
4026 013020 012737 000001 004654
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4029 013026 012737 013052 001110
4030 013034 012705 000001
4031 013040 010537 001124
4032 013044 042737 000000 001124
4033 013052 032737 041400 001140
4034 013060 001403
4035 013062 052777 000040 171000
4036
4037 013070 013777 001124 170766
4038 013076 017737 170762 001126
4039 013104 043737 004654 001124
4040 013112 053737 004652 001124
4041 013120 023737 001124 001126
4042 013126 001404
4043 013130 013737 004064 001122
4044 013136 104137

ST7: SCOPE
MOV #STACK, SP ;RESET STACK
MOV #7, 2#STNM ;SAVE TEST NUMBER
JSR PC, 2#CLDISK ;GIVE RH INITIALIZE
;SETUP UNIT NUMBER
;CLEAR RHWC AND FUNCTION BITS IN RHCS!
MOV #0, WRTBIT ;0 ARE BITS NOT WRITTEN INTO
MOV #0, SETBIT ;0 ARE BITS ALWAYS SET
MOV #BIT00, CLRBIT ;BIT00 ARE BITS ALWAYS CLEARED
;FLOAT 1'S THRU THE RHBA REGISTER
MOV #25, \$LPERR ;SET LOOP ON ERROR ADDRESS
MOV #1, R5 ;GETTING READY TO FLOAT A ONE
1\$: MOV R5, \$GDDAT ;START WITH DATA
BIC #0, \$GDDAT ;CLEAR BITS NOT WRITTEN INTO
2\$: BIT #BIT14!BIT9!BIT8, SWR ;ARE SWITCHES 14 OR 9 OR 8 SET
BEQ 3\$;BRANCH IF ANY ONE SET
BIS #CLR, 2#RHCS2 ;GIVE CLEAR FOR SCOPE
;SYNC IF IN DEBUG:
3\$: MOV \$GDDAT, 2#RHBA ;WRITE RHBA REGISTER
MOV 2#RHBA, \$BDDAT ;READ RHBA REGISTER
BIC CLRBIT, \$GDDAT ;CLEAR ALWAYS CLEARED BITS
BIS SETBIT, \$GDDAT ;SET ALWAYS SET BITS
CMP \$GDDAT, \$BDDAT ;TEST
BEQ 4\$;BRANCH IF GOOD
MOV RHBA, \$BDADR ;GET REGISTER ADDRESS
ERROR 137 ;ONE WAS BEING FLOATED
;THRU RHBA REGISTER
;ON READING RHBA BACK
;IT DID NOT CONTAIN WHAT




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4048                                     ; WAS EXPECTED.
4049 013140 000241 45: CLC ; CLEAR CARRY
4050 013142 006305 ASL R5 ; GET 1 ONE LEFT
4051 013144 103335 BCC 15 ; BRANCH IF 16 NOT DONE
4052                                     ; FLOAT A ZERO THRU RHBA REGISTER.
4053
4054
4055 013146 012737 013172 001110 MOV #65,$LPERR ; SET LOOP BACK POINT.
4056 013154 012705 177776 MOV #177776,R5 ; GET READY TO FLOAT A ZERO
4057 013160 010537 001124 55: MOV R5,$GDDAT ; GET READY TO WRITE DATA
4058 013164 042737 000000 001124 BIC #0,$GDDAT ; CLEAR BITS NOT WRITTEN INTO
4059 013172 032737 041400 001140 65: BIT #BIT14!BIT9!BIT8,SWR ; ARE SWITCHES 14 OR 9 OR 8 SET
4060 013200 001403 75 ; BRANCH IF ANY OF THE ABOVE SET
4061 013202 012777 000040 170660 MOV #CLR,$RHCS2 ; CLEAR FOR SCOPE AID TO
4062                                     ; SYNC IF IN DEBUG
4063 013210 013777 001124 170646 75: MOV $GDDAT,$RHBA ; WRITE INTO RH'XA.
4064 013216 017737 170642 001126 MOV $RHBA,$BDDAT ; READ RHBA
4065 013224 053737 004652 001124 BIS SETBIT,$GDDAT ; SET BITS ALWAYS SET
4066 013232 043737 004654 001124 BIC CLRBIT,$GDDAT ; CLEAR BITS ALWAYS 0
4067 013240 023737 001124 001126 CMP $GDDAT,$BDDAT ; TEST
4068 013246 001404 BEQ 85 ; BRANCH IF GOOD
4069 013250 013737 004064 001122 MOV RHBA,$BDDADR ; GET REGISTER ADDRESS
4070 013256 104137 ERROR 137 ; ZERO WAS BEING FLOATED
4071                                     ; THRU RHBA REGISTER
4072                                     ; ON READING IT BACK IT
4073                                     ; DID NOT CONTAIN WHAT
4074                                     ; WAS EXPECTED
4075
4076 013260 000261 85: SEC
4077 013262 006105 ROL R5
4078 013264 103735 BCS 55
4079
4080
4081 ::*****
4082 : *TEST 10 BIT BANG RHBAE
4083
4084 : * TEST LOADING AND READING OF ALL POSSIBLE BITS
4085 : * IN RHBAE REGISTER.
4086 : * USE A PATTERN OF WALKING 1'S (1,2,4,10 ETC)
4087 : * AND WALKING 0'S (-2,-3,-5 ETC)
4088 : * IN THIS TEST 17700 BITS WILL NOT BE WRITTEN INTO
4089 : * AND 0 BITS WILL ALWAYS BE SET
4090 : * AND 17700 BITS WILL ALWAYS BE CLEARED
4091 : * IF SWITCH 14 OR 9 OR 8 ARE SET FOR DEBUGGING
4092 : * THEN A RH CLEAR (RHCS2 BIT #5) WILL BE
4093 : * GIVEN TO AID SCOPE SYNC'S ON THE CLEAR
4094 : * SIGNAL.
4095
4096 ::*****
4097 013266 000004 †ST10: SCOPE
4098 013270 012706 001000 MOV #STACK,SP ; RESET STACK
4099 013274 012737 000010 004656 MOV #10,$†STNM ; SAVE TEST NUMBER
4100
4101 013302 004737 040312 JSR PC,$#CLDISK ; GIVE RH INITIALIZE
4102                                     ; SETUP UNIT NUBER
4103                                     ; CLEAR RHWC AND FUNCTION BITS IN RHCS1

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4211
4212
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4214
4215

013570 000004
013572 012706 001000
013576 012737 000011 004656

013604 004737 040312

013610 012737 000100 001124
013616 053737 005010 001124

013624 017737 170240 001126
013632 023737 001124 001126

013640 001401
013642 104006

013644

013644 005077 170232

013650 104411
013652 004070

```
*****
*TEST 11 SILO TEST 1 (ONE WORD WRITE)
* AFTER A RH CLEAR IS GIVEN (SET BIT #5 IN RHCS2)
* RHCS2 IS CHECKED TO HAVE "IR" (BIT #6) HIGH
* TOGETHER WITH UNIT NUMBER
* ONE WORD OF ALL ZEROS IS WRITTEN INTO RHDB
* BY "CLR"
* "OR" (BIT #7) IS WAITED FOR BY A TRAP INSTRUCTION
* CALLED "WAT" (NO TIMING IS DONE)
* HOWEVER IF "OR" DOES NOT SET WITHIN "WAT" COUNT
* DOWN AN ERROR IS REPORTED
* RHDB IS READ AND CHECKED TO CONTAIN ZEROS
* RHCS2 WILL BE CHECKED TO HAVE "IR" AND UNIT NUMBER
* RHCS1, RHCS3, RHBA, RHBAE, RHWC, WILL BE CHECKED TO HAVE
* APPROPRIATE VALUES
*****
*ST11: SCOPE
MOV #STACK.SP ;RESET STACK
MOV #11,2*†STNM ;SAVE TEST NUMBER
JSR PC,2*CLDISK ;GIVE RH INITIALIZE
;SETUP UNIT NUBER
;CLEAR RHWC AND FUNCTION BITS IN RHCS1
;CHECK THAT RHCS2 HAS IR
MOV #IR,$GDDAT ;GET GOOD = 100
BIS UNIT,$GDDAT ;INCLUDE UNIT NUMBER
MOV 2RHCS2,$BDDAT ;READ RHCS2 FOR COMPARISON
CMP $GDDAT,$BDDAT ;COMPARE EXPECTED
;DATA WITH DATA READ FROM
;RHCS2
;BRANCH IF GOOD
BEQ 65$
ERROR 6
;AFTER SETTING CLR BIT #5
;IN RHCS2 TO INIT THE RH
;AND HAVING DONE NOTHING ELSE
;RHCS2 SHOULD HAVE IR
;=100
;TOGETHER WITH UNIT NUMBER
;BUT CONTAINED WHAT IS
;GIVEN IN BAD RHCS2
65$:
;WRITE ONE WORD OF ALL ZEROS INTO RHDB
CLR 2RHDB ;WRITE 0 IN RHDB
;WAIT FOR OR BIT IN RHCS2 REGISTER TO SET
WAT ;TRAP TO WAIT.T SUBROUTINE
RHCS2 ;AND WAIT FOR OR BIT IN
```

```

4216 013654 000200 OR ;RHCS2 REGISTER
4217 ;IF ERROR OCCURS HERE
4218 ;IT MEANS "OR" DID NOT
4219 ;SET FOR THE FULL COUNT
4220 ;DOWN OF THE WAIT.T SUBROUTINE
4221 ;THIS TIME IS APPROXIMATELY
4222 ;LARGER THAN 500 MILLISECONDS
4223
4224
4225 ;CHECK THAT RHDB HAS 0
4226 013656 012737 000000 001124 MOV #0,$GDDAT ;GET GOOD =
4227
4228 013654 017737 170212 001126 MOV @RHDB,$BDDAT ;READ RHDB FOR COMPARISON
4229 013672 023737 001124 001126 CMP $GDDAT,$BDDAT ;COMPARE EXPECTED
4230 ;DATA WITH DATA READ FROM
4231 ;RHDB
4232 013700 001401 BEQ 67$ ;BRANCH IF GOOD
4233 013702 104007 ERROR 7
4234
4235 ;AFTER CLEARING THE RH
4236 ;AND WRITING ALL ZEROS
4237 ;INTO RHDB
4238 ;THEN READING IT BACK
4239 ;RHDB SHOULD HAVE 0
4240 ;=
4241 ;BUT CONTAINED WHAT IS
4242 ;GIVEN IN BAD RHDB
4243
4244 013704 67$:
4245 013704 012737 000100 001124 MOV #IR,$GDDAT ;CHECK THAT RHCS2 HAS IR
4246 013712 053737 005010 001124 BIS UNIT,$GDDAT ;GET GOOD = 100
4247 ;INCLUDE UNIT NUMBER
4248 013720 017737 170144 001126 MOV @RHCS2,$BDDAT ;READ RHCS2 FOR COMPARISON
4249 013726 023737 001124 001126 CMP $GDDAT,$BDDAT ;COMPARE EXPECTED
4250 ;DATA WITH DATA READ FROM
4251 ;RHCS2
4252 013734 001401 BEQ 69$ ;BRANCH IF GOOD
4253 013736 104010 ERROR 10
4254
4255 ;AFTER CLEARING THE RH
4256 ;AND WRITING ALL ZEROS
4257 ;INTO RHDB
4258 ;THEN READING IT BACK
4259 ;RHCS2 SHOULD HAVE IR
4260 ;=100
4261 ;TOGETHER WITH UNIT NUMBER
4262 ;BUT CONTAINED WHAT IS
4263 ;GIVEN IN BAD RHCS2
4264 013740 69$:
4265 013740 012737 004200 001124 MOV #DVA!RDY,$GDDAT ;CHECK THAT RHCS1 HAS DVA!RDY
4266 ;GET GOOD = 4200
4267 013746 017737 170106 001126 MOV @RHCS1,$BDDAT ;READ RHCS1 FOR COMPARISON
4268 013754 023737 001124 001126 CMP $GDDAT,$BDDAT ;COMPARE EXPECTED
4269 ;DATA WITH DATA READ FROM
4270 ;RHCS1
4271 013762 001401 BEQ 71$ ;BRANCH IF GOOD
4272 013764 104011 ERROR 11

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Handwritten mark resembling a stylized 'H' or 'N'.

Handwritten marks including 'Y I', a signature, and 'P.S.'.

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4272
4273
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4279
4280 013766          71$:
4281
4282 013766 012737 000000 001124      ;CHECK THAT RHCS3 HAS 0
4283
4284 013774 017737 170132 001126      MOV      #0,$GDDAT      ;GET GOOD = 0
4285 014002 023737 001124 001126      MOV      @RHCS3,$BDDAT  ;READ RHCS3 FOR COMPARISON
4286
4287
4288 014010 001401          BEQ      73$            ;COMPARE EXPECTED
4289 014012 104012          ERROR    12            ;DATA WITH DATA READ FROM
4290
4291
4292
4293
4294
4295
4296
4297 014014          73$:
4298
4299 014014 012737 000000 001124      ;CHECK THAT RHBA HAS 0
4300
4301 014022 017737 170036 001126      MOV      @RHBA,$BDDAT  ;GET GOOD =
4302 014030 023737 001124 001126      MOV      @RHBA,$BDDAT  ;READ RHBA FOR COMPARISON
4303
4304
4305 014036 001401          BEQ      75$            ;COMPARE EXPECTED
4306 014040 104013          ERROR    13            ;DATA WITH DATA READ FROM
4307
4308
4309
4310
4311
4312
4313
4314
4315 014042          75$:
4316
4317 014042 012737 000000 001124      ;CHECK THAT RHBAE HAS 0
4318
4319 014050 017737 170054 001126      MOV      @RHBAE,$BDDAT ;GET GOOD =
4320 014056 023737 001124 001126      MOV      @RHBAE,$BDDAT ;READ RHBAE FOR COMPARISON
4321
4322
4323 014064 001401          BEQ      77$            ;COMPARE EXPECTED
4324 014066 104014          ERROR    14            ;DATA WITH DATA READ FROM
4325
4326
4327

```

```

;AFTER CLEARING THE RH
;AND WRITING ALL ZEROS
;INTO RHDB
;THEN READING IT BACK
;RHCS1 SHOULD HAVE DVA!RDY
;=4200
;BUT CONTAINED WHAT IS
;GIVEN IN BAD RHCS1

```

```

;CHECK THAT RHCS3 HAS 0
;GET GOOD = 0
;READ RHCS3 FOR COMPARISON
;COMPARE EXPECTED
;DATA WITH DATA READ FROM
;RHCS3
;BRANCH IF GOOD

```

```

;AFTER CLEARING THE RH
;AND WRITING ALL ZEROS
;INTO RHDB
;THEN READING IT BACK
;RHCS3 SHOULD HAVE 0
;BUT CONTAINED WHAT IS
;GIVEN IN BAD RHCS3

```

```

;CHECK THAT RHBA HAS 0
;GET GOOD =
;READ RHBA FOR COMPARISON
;COMPARE EXPECTED
;DATA WITH DATA READ FROM
;RHBA
;BRANCH IF GOOD

```

```

;AFTER CLEARING THE RH
;AND WRITING ALL ZEROS
;INTO RHDB
;THEN READING IT BACK
;RHBA SHOULD HAVE 0
;=
;BUT CONTAINED WHAT IS
;GIVEN IN BAD RHBA

```

```

;CHECK THAT RHBAE HAS 0
;GET GOOD =
;READ RHBAE FOR COMPARISON
;COMPARE EXPECTED
;DATA WITH DATA READ FROM
;RHBAE
;BRANCH IF GOOD

```

```

;AFTER CLEARING THE RH
;AND WRITING ALL ZEROS
;INTO RHDB

```

```

4328                                     ; THEN READING IT BACK
4329                                     ; RHB AE SHOULD HAVE 0
4330                                     ;
4331                                     ; BUT CONTAINED WHAT IS
4332                                     ; GIVEN IN BAD RHB AE
4333 014070                               77$:
4334                                     ; CHECK THAT RHWC HAS 0
4335 014070 012737 000000 001124        MOV     #0,$GDDAT      ; GET GOOD =
4336
4337 014076 017737 167760 001126        MOV     @RHWC,$BDDAT   ; READ RHWC FOR COMPARISON
4338 014104 023737 001124 001126        CMP     $GDDAT,$BDDAT ; COMPARE EXPECTED
4339                                     ; DATA WITH DATA READ FROM
4340                                     ; RHWC
4341 014112 001401                        BEQ     79$           ; BRANCH IF GOOD
4342 014114 134015
4343                                     ; AFTER CLEARING THE RH
4344                                     ; AND WRITING ALL ZEROS
4345                                     ; INTO RHDB
4346                                     ; THEN READING IT BACK
4347                                     ; RHWC SHOULD HAVE 0
4348                                     ;
4349                                     ; BUT CONTAINED WHAT IS
4350                                     ; GIVEN IN BAD RHWC
4351 014116                               79$:
4352
4353
4354
4355
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4366
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4371 014116 000004
4372 014120 012706 001000
4373 014124 012737 000012 004656
4374
4375 014132 004737 040312
4376
4377
4378
4379
4380
4381 014136 012737 000100 001124
4382 014144 053737 005010 001124
4383

```

```

*****
*TEST 12      SILO TEST 2 (ONE WORD WRITE)
* AFTER A RH CLEAR IS GIVEN (SET BIT #5 IN RHCS2)
* RHCS2 IS CHECKED TO HAVE "IR" (BIT #6) HIGH
* TOGETHER WITH UNIT NUMBER
* ONE WORD OF ALL ONES IS WRITTEN INTO RHDB
* BY "CLR"
* "OR" (BIT #7) IS WAITED FOR BY A TRAP INSTRUCTION
* CALLED "WAT" (NO TIMING IS DONE)
* HOWEVER IF "OR" DOES NOT SET WITHIN "WAT" COUNT
* DOWN AN ERROR IS REPORTED
* RHDB IS READ AND CHECKED TO CONTAIN ALL ONES
* RHCS2 WILL BE CHECKED TO HAVE "IR" AND UNIT NUMBER
* RHCS1,RHCS3,RHBA,RHBAE,RHWC, WILL BE CHECKED TO HAVE
* APPROPRIATE VALUES
*****

```

```

†ST12: SCOPE
MOV     #STACK,SP      ; RESET STACK
MOV     #12,@†STNM    ; SAVE TEST NUMBER
JSR     PC,@#CLDISK   ; GIVE RH INITIALIZE
                          ; SETUP UNIT NUBER
                          ; CLEAR RHWC AND FUNCTION BITS IN RHCS1

; CHECK THAT RHCS2 HAS IR
MOV     #IR,$GDDAT     ; GET GOOD = 100
BIS     UNIT,$GDDAT    ; INCLUDE UNIT NUMBER

```

```

4384 014152 017737 167712 001126      MOV      @RHCS2,$BDDAT ;READ RHCS2 FOR COMPARISON
4385 014160 023737 001124 001126      CMP      $GDDAT,$BDDAT ;COMPARE EXPECTED
4386                                     ;DATA WITH DATA READ FROM
4387                                     ;RHCS2
4388 014166 001401      BEQ      65$          ;BRANCH IF GOOD
4389 014170 104006      ERROR    6
4390                                     ;AFTER SETTING CLR BIT #5
4391                                     ;IN RHCS2 TO INIT THE RH
4392                                     ;AND HAVING DONE NOTHING ELSE
4393                                     ;RHCS2 SHOULD HAVE IR
4394                                     ;=100
4395                                     ;TOGETHER WITH UNIT NUMBER
4396                                     ;BUT CONTAINED WHAT IS
4397                                     ;GIVEN IN BAD RHCS2
4398 014172                                     65$:
4399
4400                                     ;WRITE ONE WORD OF ALL ONES INTO RHDB
4401 014172 012777 177777 167702      MOV      #177777,@RHDB ;WRITE 1 IN RHDB
4402
4403                                     ;WAIT FOR OR BIT IN RHCS2 REGISTER TO SET
4404 014200 104411      WAT                                     ;TRAP TO WAIT.T SUBROUTINE
4405 014202 004070      RHCS2                                     ;AND WAIT FOR OR BIT IN
4406 014204 000200      OR                                     ;RHCS2 REGISTER
4407                                     ;IF ERROR OCCURS HERE
4408                                     ;IT MEANS "OR" DID NOT
4409                                     ;SET FOR THE FULL COUNT
4410                                     ;DOWN OF THE WAIT.T SUBROUTINE
4411                                     ;THIS TIME IS APPROXIMATELY
4412                                     ;LARGER THAN 500 MILLISECONDS
4413
4414
4415                                     ;CHECK THAT RHDB HAS 177777
4416 014206 012737 177777 001124      MOV      #177777,$GDDAT ;GET GOOD = 0
4417
4418 014214 017737 167662 001126      MOV      @RHDB,$BDDAT ;READ RHDB FOR COMPARISON
4419 014222 023737 001124 001126      CMP      $GDDAT,$BDDAT ;COMPARE EXPECTED
4420                                     ;DATA WITH DATA READ FROM
4421                                     ;RHDB
4422 014230 001401      BEQ      67$          ;BRANCH IF GOOD
4423 014232 104007      ERROR    7
4424                                     ;AFTER CLEARING THE RH
4425                                     ;AND WRITING ALL ONES
4426                                     ;INTO RHDB
4427                                     ;THEN READING IT BACK
4428                                     ;RHDB SHOULD HAVE 177777
4429                                     ;BUT CONTAINED WHAT IS
4430                                     ;GIVEN IN BAD RHDB
4431 014234                                     67$:
4432
4433                                     ;CHECK THAT RHCS2 HAS IR
4434 014234 012737 000100 001124      MOV      #IR,$GDDAT ;GET GOOD = 100
4435 014242 053737 005010 001124      BIS      UNIT,$GDDAT ;INCLUDE UNIT NUMBER
4436 014250 017737 167614 001126      MOV      @RHCS2,$BDDAT ;READ RHCS2 FOR COMPARISON
4437 014256 023737 001124 001126      CMP      $GDDAT,$BDDAT ;COMPARE EXPECTED
4438                                     ;DATA WITH DATA READ FROM
4439                                     ;RHCS2

```

K07

MAINDEC-11-DERHAB-A
DERHAB.SRC T12

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SILO TEST 2 (ONE WORD WRITE)

```

4440 014264 001401          BEQ      69$      ;BRANCH IF GOOD
4441 014266 104010          ERROR    10
4442                                     ;AFTER CLEARING THE RH
4443                                     ;AND WRITING ALL ONES
4444                                     ;INTO RHDB
4445                                     ;THEN READING IT BACK
4446                                     ;RHCS2 SHOULD HAVE IR
4447                                     ;=100
4448                                     ;TOGETHER WITH UNIT NUMBER
4449                                     ;BUT CONTAINED WHAT IS
4450                                     ;GIVEN IN BAD RHCS2
4451 014270          69$:
4452                                     ;CHECK THAT RHCS3 HAS 0
4453 014270 012737 000000 001124  MOV      #0,$GDDAT ;GET GOOD =
4454
4455 014276 017737 167630 001126  MOV      @RHCS3,$BDDAT ;READ RHCS3 FOR COMPARISON
4456 014304 023737 001124 001126  CMP      $GDDAT,$BDDAT ;COMPARE EXPECTED
4457                                     ;DATA WITH DATA READ FROM
4458                                     ;RHCS3
4459 014312 001401          BEQ      71$      ;BRANCH IF GOOD
4460 014314 104012          ERROR    12
4461                                     ;AFTER CLEARING THE RH
4462                                     ;AND WRITING ALL ONES
4463                                     ;INTO RHDB
4464                                     ;THEN READING IT BACK
4465                                     ;RHCS3 SHOULD HAVE 0
4466                                     ;=
4467                                     ;BUT CONTAINED WHAT IS
4468                                     ;GIVEN IN BAD RHCS3
4469 014316          71$:
4470                                     ;CHECK THAT RHBA HAS 0
4471 014316 012737 000000 001124  MOV      #0,$GDDAT ;GET GOOD =
4472
4473 014324 017737 167534 001126  MOV      @RHBA,$BDDAT ;READ RHBA FOR COMPARISON
4474 014332 023737 001124 001126  CMP      $GDDAT,$BDDAT ;COMPARE EXPECTED
4475                                     ;DATA WITH DATA READ FROM
4476                                     ;RHBA
4477 014340 001401          BEQ      73$      ;BRANCH IF GOOD
4478 014342 104013          ERROR    13
4479                                     ;AFTER CLEARING THE RH
4480                                     ;AND WRITING ALL ONES
4481                                     ;INTO RHDB
4482                                     ;THEN READING IT BACK
4483                                     ;RHBA SHOULD HAVE 0
4484                                     ;=
4485                                     ;BUT CONTAINED WHAT IS
4486                                     ;GIVEN IN BAD RHBA
4487 014344          73$:
4488                                     ;CHECK THAT RHBAE HAS 0
4489 014344 012737 000000 001124  MOV      #0,$GDDAT ;GET GOOD =
4490
4491 014352 017737 167552 001126  MOV      @RHBAE,$BDDAT ;READ RHBAE FOR COMPARISON
4492 014360 023737 001124 001126  CMP      $GDDAT,$BDDAT ;COMPARE EXPECTED
4493                                     ;DATA WITH DATA READ FROM
4494                                     ;RHBAE
4495 014366 001401          BEQ      75$      ;BRANCH IF GOOD

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4496 014370 104014          ERROR 14          ; AFTER CLEARING THE RH
4497                                     ; AND WRITING ALL ONES
4498                                     ; INTO RHDB
4499                                     ; THEN READING IT BACK
4500                                     ; RHBAE SHOULD HAVE 0
4501                                     ; =
4502                                     ; BUT CONTAINED WHAT IS
4503                                     ; GIVEN IN BAD RHBAE
4504
4505 014372          75$:
4506                                     ; CHECK THAT RHWC HAS 0
4507 014372 012737 000000 001124      MOV      #0,$GDDAT      ; GET GOOD =
4508
4509 014400 017737 167456 001126      MOV      @RHWC,$BDDAT   ; READ RHWC FOR COMPARISON
4510 014406 023737 001124 001126      CMP      $GDDAT,$BDDAT ; COMPARE EXPECTED
4511                                     ; DATA WITH DATA READ FROM
4512                                     ; RHWC
4513 014414 001401          BEQ      77$          ; BRANCH IF GOOD
4514 014416 104015          ERROR 15
4515
4516                                     ; AFTER CLEARING THE RH
4517                                     ; AND WRITING ALL ONES
4518                                     ; INTO RHDB
4519                                     ; THEN READING IT BACK
4520                                     ; RHWC SHOULD HAVE 0
4521                                     ; =
4522                                     ; BUT CONTAINED WHAT IS
4523                                     ; GIVEN IN BAD RHWC
4524
4525 014420          77$:
4526
4527 ;*****
4528 ;*TEST 13          SILO TEST 3 (TWO WORD WRITE)
4529
4530 ;*          AFTER A RH CLEAR IS GIVEN (SET BIT #5 IN RHCS2)
4531 ;*          RHCS2 IS CHECKED TO HAVE "IR" (BIT #6) HIGH,
4532 ;*          TOGETHER WITH UNIT NUMBER
4533 ;*          ONE WORD = 52525 IS WRITTEN INTO RHDB
4534 ;*          RHCS2 IS CHECKED TO HAVE "IR" AND UNIT NUMBER
4535 ;*          A SECOND WORD = 12525 IS WRITTEN INTO RHDB
4536 ;*          "OR" (BIT #7 IN RHCS2) IS WAITED FOR BY A TRAP
4537 ;*          INSTRUCTION CALLED "WAT"
4538 ;*          RHDB IS READ AND CHECKED TO CONTAIN 52525
4539 ;*          RHCS2 IS CHECKED TO HAVE "IR", "OR" AND UNIT NUMBER
4540 ;*          RHDB IS READ A SECOND TIME AND CHECKED TO
4541 ;*          CONTAIN 12525
4542 ;*          RHCS2 IS CHECKED TO HAVE "IR" AND UNIT NUMBER
4543 ;*          THEN ALL REGISTERS RHCS1, RHCS3, RHBA, RHBAE, RHWC
4544 ;*          ARE CHECKED TO HAVE APPROPRIATE VALUE
4545
4546 ;*****
4547 014420 000004          TST13: SCOPE
4548 014422 012706 001000      MOV      #STACK,SP      ; RESET STACK
4549 014426 012737 000013 004656      MOV      #13,@TSTNM    ; SAVE TEST NUMBER
4550
4551 014434 004737 040312      JSR      PC,@CLDISK    ; GIVE RH INITIALIZE

```



```

4608                                     ;BUT CONTAINED WHAT IS
4609                                     ;GIVEN IN BAD RHCS2
4610 014544                               67$:
4611
4612                                     ;WRITE A SECOND WORD = 125252 INTO RHDB
4613 014544 012777 125252 167330        MOV #125252, @RHDB ;WRITE 125252 INTO RHDB
4614
4615                                     ;WAIT FOR OR BIT IN RHCS2 REGISTER TO SET
4616 014552 104411                        WAT ;TRAP TO WAIT.T SUBROUTINE
4617 014554 004070                        RHCS2 ;AND WAIT FOR OR BIT IN
4618 014556 000200                        OR ;RHCS2 REGISTER
4619                                     ;IF ERROR OCCURS HERE
4620                                     ;IT MEANS "OR" DID NOT
4621                                     ;SET FOR THE FULL COUNT
4622                                     ;DOWN OF THE WAIT.T SUBROUTINE
4623                                     ;THIS TIME IS APPROXIMATELY
4624                                     ;LARGER THAN 500 MILLISECONDS
4625
4626                                     ;CHECK THAT RHDB HAS 52525
4627 014560 012737 052525 001124        MOV #52525, $GDDAT ;GET GOOD = 0
4628
4629 014566 017737 167310 001126        MOV @RHDB, $BDDAT ;READ RHDB FOR COMPARISON
4630 014574 023737 001124 001126        CMP $GDDAT, $BDDAT ;COMPARE EXPECTED
4631                                     ;DATA WITH DATA READ FROM
4632                                     ;RHDB
4633 014602 001401                        BEQ 69$ ;BRANCH IF GOOD
4634 014604 104017                        ERROR 17
4635
4636                                     ;AFTERCLEARING THE RH THEN
4637                                     ;WRITING TWO WORDS INTO
4638                                     ;RHDB 52525 ND 125252
4639                                     ;THEN READING RHDB ONCE
4640                                     ;RHDB SHOULD HAVE 52525
4641                                     ;BUT CONTAINED WHAT IS
4642 014606                               69$:
4643                                     ;CHECK THAT RHCS2 HAS IR!OR
4644 014606 012737 000300 001124        MOV #IR!OR, $GDDAT ;GET GOOD = 300
4645 014614 053737 005010 001124        BIS UNIT, $GDDAT ;INCLUDE UNIT NUMBER
4646
4647 014622 017737 167242 001126        MOV @RHCS2, $BDDAT ;READ RHCS2 FOR COMPARISON
4648 014630 023737 001124 001126        CMP $GDDAT, $BDDAT ;COMPARE EXPECTED
4649                                     ;DATA WITH DATA READ FROM
4650                                     ;RHCS2
4651 014636 001401                        BEQ 71$ ;BRANCH IF GOOD
4652 014640 104020                        ERROR 20
4653
4654                                     ;AFTERCLEARING THE RH THEN
4655                                     ;WRITING TWO WORDS INTO
4656                                     ;RHDB 52525 ND 125252
4657                                     ;THEN READING RHDB ONCE
4658                                     ;RHCS2 SHOULD HAVE IR!OR
4659                                     ;=300
4660                                     ;TOGETHER WITH UNIT NUMBER
4661                                     ;BUT CONTAINED WHAT IS
4662 014642                               71$:
4663

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014642 012737 125252 001124
014650 017737 167226 001126
014656 023737 001124 001126
014664 001401
014666 104021

```

:READ RHOB A SECOND TIME
:WRITE RHOB 52525
:GET GOOD = 0
:READ RHOB 52525
:WRITE RHOB 125252
:GET GOOD = 100
:INCLUDE UNIT NUMBER
:READ RHCS2 FOR COMPARISON
:COMPARE EXPECTED
:DATA WITH DATA READ FROM
:RHCS2
:BRANCH IF GOOD
:
:CHECK THAT RHCS2 HAS IR
:MOV #IR,$GDDAT
:BIS UNIT,$GDDAT
:READ RHCS2 FOR COMPARISON
:COMPARE EXPECTED
:DATA WITH DATA READ FROM
:RHCS2
:BRANCH IF GOOD
:
:AFTER CLEARING RH THEN
:WRITING TWO WORDS INTO
:RHOB 52525 AND 125252
:THEN READING RHOB THE
:SECOND TIME
:RHOB SHOULD HAVE 125252
:BT CONTAINED WHAT IS
:GIVEN IN BAD RHOB
    
```

014670 738:
014670 012737 000100 001124
014676 053737 005010 001124
014704 017737 167160 001126
014712 023737 001124 001126
014720 001401
014722 104022

```

:CHECK THAT RHCS2 HAS IR
:MOV #IR,$GDDAT
:BIS UNIT,$GDDAT
:INCLUDE UNIT NUMBER
:READ RHCS2 FOR COMPARISON
:COMPARE EXPECTED
:DATA WITH DATA READ FROM
:RHCS2
:BRANCH IF GOOD
:
:AFTER CLEARING RH THEN
:WRITING TWO WORDS INTO
:RHOB 52525 AND 125252
:THEN READING RHOB THE
:SECOND TIME
:RHCS2 SHOULD HAVE IR
:=100
:TOGETHER WITH UNIT NUMBER
:BT CONTAINED WHAT IS
:GIVEN IN BAD RHCS2
    
```

014724 758:
014724 012737 004200 001124
014732 017737 167122 001126
014740 023737 001124 001126
014746 001401
014750 104141

```

:CHECK THAT RHCS1 HAS DVA!RDY
:MOV #DVA!RDY,$GDDAT
:GET GOOD = 4200
:READ RHCS1 FOR COMPARISON
:COMPARE EXPECTED
:DATA WITH DATA READ FROM
:RHCS1
:BRANCH IF GOOD
:
:AFTER CLEARING RH THEN
:WRITING TWO WORDS INTO
:RHOB 52525 AND 125252
:THEN READING RHOB THE
:SECOND TIME
:RHCS1 SHOULD HAVE DVA!RDY
    
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014752
014752 012737 000000 001124
014760 017737 167145 001126
014766 023737 001124 001126
014774 001401
014776 104142
015000
015000 012737 000000 001124
015006 017737 167052 001126
015014 023737 001124 001126
015022 001401
015024 104143
015026
015026 012737 000000 001124
015034 017737 167070 001126
015042 023737 001124 001126
015050 001401
015052 104144

775:
795:
815:

:CHECK THAT RHCS3 HAS 0
MOV #0,\$GDDAT
MOV 3RHCS3,\$BDDAT
CMP \$GDDAT,\$BDDAT
BEG 795
ERROR 142
:CHECK THAT RHBA HAS 0
MOV #0,\$GDDAT
MOV 3RHBA,\$BDDAT
CMP \$GDDAT,\$BDDAT
BEG 815
ERROR 143
:CHECK THAT RHBAE HAS 0
MOV #0,\$GDDAT
MOV 3RHBAE,\$BDDAT
CMP \$GDDAT,\$BDDAT
BEG 835
ERROR 144

=4200
:BUT CONTAINED WHAT IS
:GIVEN IN BAD RHCS1
:GET GOOD = 0
:READ RHCS3 FOR COMPARISON
:COMPARE EXPECTED
:DATA WITH DATA READ FROM
:RHCS3
:BRANCH IF GOOD
:AFTER CLEARING RH THEN
:WRITING TWO WORDS INTO
:RHDB 52525 AND 125252
:THEN READING RHDB THE
:SECOND TIME
:RHCS3 SHOULD HAVE 0
:BUT CONTAINED WHAT IS
:GIVEN IN BAD RHCS3
:GET GOOD = 0
:READ RHBA FOR COMPARISON
:COMPARE EXPECTED
:DATA WITH DATA READ FROM
:RHBA
:BRANCH IF GOOD
:AFTER CLEARING RH THEN
:WRITING TWO WORDS INTO
:RHDB 52525 AND 125252
:THEN READING RHDB THE
:SECOND TIME
:RHBA SHOULD HAVE 0
:BUT CONTAINED WHAT IS
:GIVEN IN BAD RHBA
:GET GOOD = 0
:READ RHBAE FOR COMPARISON
:COMPARE EXPECTED
:DATA WITH DATA READ FROM
:RHBAE
:BRANCH IF GOOD
:AFTER CLEARING RH THEN
:WRITING TWO WORDS INTO
:RHDB 52525 AND 125252
:THEN READING RHDB THE
:SECOND TIME
:RHBAE SHOULD HAVE 0
:BUT CONTAINED WHAT IS

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4776 015054 83$: ;GIVEN IN BAD RHBAE
4777 ;CHECK THAT RHWC HAS 0
4778 015054 012737 000000 001124 MOV #0,$GDDAT ;GET GOOD = 0
4779 ;READ RHWC FOR COMPARISON
4780 015062 017737 166774 001126 MOV #RHWC,$BDDAT
4781 015070 023737 001124 001126 CMP $GDDAT,$BDDAT ;COMPARE EXPECTED
4782 ;DATA WITH DATA READ FROM
4783 ;RHWC
4784 ;BRANCH IF GOOD
4785 015076 001401 BEQ 95$
4786 015100 104145 ERROR 145
4787 ;AFTER CLEARING RH THEN
4788 ;WRITING TWO WORDS INTO
4789 ;RHDB 52525 AND 125252
4790 ;THEN READING RHDB THE
4791 ;SECOND TIME
4792 ;RHWC SHOULD HAVE 0
4793 ;BUT CONTAINED WHAT IS
4794 ;GIVEN IN BAD RHWC
4795 015102 95$:
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4799
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*TEST 14 SILO TEST 4 (COUNT PATTERN)

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* AFTER A RH CLEAR IS GIVEN (SET BIT #5 IN RHCS2)
* RHCS2 IS CHECKED TO HAVE "IR" (BIT #6) HIGH, TOGETHER
* WITH UNIT NUMBER
* EIGHT WORDS, A COUNT PATTERN 0 THRU 7 IS WRITTEN
* INTO RHDB
* "OR" (BIT #7 IN RHCS2) IS WAITED FOR BY A TRAP
* INSTRUCTION CALLED "WAT"
* THEN RHCS2 IS CHECKED TO HAVE "IR" LOW AND
* "OR" HIGH TOGETHER WITH THE UNIT NUMBER
* THEN RHBS READ AND COMPARED TO HAVE THE RIGHT VALUE
* EIGHT TIES
* THEN RHCS2, RHCS1, RHCS3, RHBA, RHBAE, RHWC ARE
* CHECKED TO HAVE THE APPROPRIATE VALUE
*****
†ST14: SCOPE
MOV #STACK,SP ;RESET STACK
MOV #14,†STNM ;SAVE TEST NUMBER
JSR PC,†CLDISK ;GIVE RH INITIALIZE
;SETUP UNIT NUMBER
;CLEAR RHWC AND FUNCTION BITS IN RHCS1
;CHECK THAT RHCS2 HAS IR
015122 012737 000100 001124 MOV #IR,$GDDAT ;GET GOOD = 100
015130 053737 005010 001124 BIS UNIT,$GDDAT ;INCLUDE UNIT NUMBER
015136 017737 166726 001126 MOV #RHCS2,$BDDAT ;READ RHCS2 FOR COMPARISON
015144 023737 001124 001126 CMP $GDDAT,$BDDAT ;COMPARE EXPECTED
;DATA WITH DATA READ FROM
;RHCS2
015152 001401 BEQ 65$ ;BRANCH IF GOOD

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4898 ;CHECK THAT RHDB HAS 0
4899 015246 012737 000000 001124 MOV #0,$GDDAT ;GET GOOD = 0
4899
4891 015254 017737 166622 001126 MOV 2RHDB,$BDDAT ;READ RHDB FOR COMPARISON
4892 015262 023737 001124 001126 CMP $GDDAT,$BDDAT ;COMPARE EXPECTED
4893 ;DATA WITH DATA READ FROM
4894 ;RHDB
4895 015270 001401 BEQ 69$ ;BRANCH IF GOOD
4896 015272 104024 ERROR 24
4897
4898 ;AFTER SETTING "CLR" BIT #5
4899 ;IN RHCS2 TO INIT THE RH
4900 ;AND WRITING A
4901 ;COUNT PATTERN 0 THRU 7
4902 ;INTO RHDB TO FILL IT
4903 ;THEN ON READING RHDB THE
4904 ;FIRST TIME
4905 ;RHDB SHOULD HAVE 0
4906 ;BUT CONTAINED WHAT IS
4907 015274 69$:
4908
4909 015274 012737 000002 004664 MOV #2,SILONM ;SECOND WORD TO BE READ
4910 ;CHECK THAT RHDB HAS 1
4911 015302 012737 000001 001124 MOV #1,$GDDAT ;GET GOOD = 0
4912
4913 015310 017737 166566 001126 MOV 2RHDB,$BDDAT ;READ RHDB FOR COMPARISON
4914 015316 023737 001124 001126 CMP $GDDAT,$BDDAT ;COMPARE EXPECTED
4915 ;DATA WITH DATA READ FROM
4916 ;RHDB
4917 015324 001401 BEQ 71$ ;BRANCH IF GOOD
4918 015326 104024 ERROR 24
4919
4920 ;AFTER SETTING "CLR" BIT #5
4921 ;IN RHCS2 TO INIT THE RH
4922 ;AND WRITING A
4923 ;COUNT PATTERN 0 THRU 7
4924 ;INTO RHDB TO FILL IT
4925 ;THEN ON READING RHDB THE
4926 ;SECOND TIME
4927 ;RHDB SHOULD HAVE 1
4928 ;BUT CONTAINED WHAT IS
4929 015330 71$:
4930
4931 015330 012737 000003 004664 MOV #3,SILONM ;THIRD WORD TO BE READ
4932 ;CHECK THAT RHDB HAS 2
4933 015336 012737 000002 001124 MOV #2,$GDDAT ;GET GOOD = 0
4934
4935 015344 017737 166532 001126 MOV 2RHDB,$BDDAT ;READ RHDB FOR COMPARISON
4936 015352 023737 001124 001126 CMP $GDDAT,$BDDAT ;COMPARE EXPECTED
4937 ;DATA WITH DATA READ FROM
4938 ;RHDB
4939 015360 001401 BEQ 73$ ;BRANCH IF GOOD
4940 015362 104024 ERROR 24
4941
4942 ;AFTER SETTING "CLR" BIT #5
4943 ;IN RHCS2 TO INIT THE RH
4944 ;AND WRITING A

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4944 ;COUNT PATTERN 0 THRU 7
4945 ;INTO RHDB TO FILL IT
4946 ;THEN ON READING RHDB THE
4947 ;THIRD TIME
4948 ;RHDB SHOULD HAVE 2
4949 ;BUT CONTAINED WHAT IS
4950 ;GIVEN IN BAD RHDB
4951 015364 73$:
4952
4953 015364 012737 000004 004664 MOV #4,SILONM ;FOURTH WORD TO BE READ
4954 ;CHECK THAT RHDB HAS 3
4955 015372 012737 000003 001124 MOV #3,$GDDAT ;GET GOOD = 0
4956
4957 015400 017737 166476 001126 MOV @RHDB,$BDDAT ;READ RHDB FOR COMPARISON
4958 015406 023737 001124 001126 CMP $GDDAT,$BDDAT ;COMPARE EXPECTED
4959 ;DATA WITH DATA READ FROM
4960 ;RHDB
4961 015414 001401 BEQ 75$ ;BRANCH IF GOOD
4962 015416 104024 ERROR 24
4963
4964 ;AFTER SETTING "CLR" BIT #5
4965 ;IN RHCS2 TO INIT THE RH
4966 ;AND WRITING A
4967 ;COUNT PATTERN 0 THRU 7
4968 ;INTO RHDB TO FILL IT
4969 ;THEN ON READING RHDB THE
4970 ;FOURTH TIME
4971 ;RHDB SHOULD HAVE 3
4972 ;BUT CONTAINED WHAT IS
4973 ;GIVEN IN BAD RHDB
4974 015420 75$:
4975
4976 015420 012737 000005 004664 MOV #5,SILONM ;FIFTH WORD TO BE READ
4977 ;CHECK THAT RHDB HAS 4
4978 015426 012737 000004 001124 MOV #4,$GDDAT ;GET GOOD = 0
4979
4980 015434 017737 166442 001126 MOV @RHDB,$BDDAT ;READ RHDB FOR COMPARISON
4981 015442 023737 001124 001126 CMP $GDDAT,$BDDAT ;COMPARE EXPECTED
4982 ;DATA WITH DATA READ FROM
4983 ;RHDB
4984 015450 001401 BEQ 77$ ;BRANCH IF GOOD
4985 015452 104024 ERROR 24
4986
4987 ;AFTER SETTING "CLR" BIT #5
4988 ;IN RHCS2 TO INIT THE RH
4989 ;AND WRITING A
4990 ;COUNT PATTERN 0 THRU 7
4991 ;INTO RHDB TO FILL IT
4992 ;THEN ON READING RHDB THE
4993 ;FIFTH TIME
4994 ;RHDB SHOULD HAVE 4
4995 ;BUT CONTAINED WHAT IS
4996 ;GIVEN IN BAD RHDB
4997 015454 77$:
4998
4999 015454 012737 000006 004664 MOV #6,SILONM ;SIXTH WORD TO BE READ
5000 ;CHECK THAT RHDB HAS 5
5001 015462 012737 000005 001124 MOV #5,$GDDAT ;GET GOOD = 0

```

H08

MAINDEC-11-DERHAB-A
DERHAB.SRC T14

MACY11 27(732) 22-SEP-76 15:11 PAGE 100
SILO TEST 4 (COUNT PATTERN)

5000							
5001	015470	017737	166406	001126	MOV	DRHDB,\$BDDAT	:READ RHDB FOR COMPARISON
5002	015476	023737	001124	001126	CMP	\$GDDAT,\$BDDAT	:COMPARE EXPECTED
5003							:DATA WITH DATA READ FROM
5004							:RHDB
5005	015504	001401			BEG	79\$:BRANCH IF GOOD
5006	015506	104024			ERROR	24	
5007							:AFTER SETTING "CLR" BIT #5
5008							:IN RHCS2 TO INIT THE RH
5009							:AND WRITING A
5010							:COUNT PATTERN 0 THRU 7
5011							:INTO RHDB TO FILL IT
5012							:THEN ON READING RHDB THE
5013							:SIXTH TIME
5014							:RHDB SHOULD HAVE 5
5015							:BUT CONTAINED WHAT IS
5016							:GIVEN IN BAD RHDB
5017	015510					79\$:	
5018							
5019	015510	012737	000007	004664	MOV	#7,SILONM	:SEVENTH WORD TO BE READ
5020						:CHECK THAT RHDB HAS 6	
5021	015516	012737	000006	001124	MOV	#6,\$GDDAT	:GE GOOD = 0
5022							
5023	015524	017737	166352	001126	MOV	DRHDB,\$BDDAT	:READ RHDB FOR COMPARISON
5024	015532	023737	001124	001126	CMP	\$GDDAT,\$BDDAT	:COMPARE EXPECTED
5025							:DATA WITH DATA READ FROM
5026							:RHDB
5027	015540	001401			BEG	81\$:BRANCH IF GOOD
5028	015542	104024			ERROR	24	
5029							:AFTER SETTING "CLR" BIT #5
5030							:IN RHCS2 TO INIT THE RH
5031							:AND WRITING A
5032							:COUNT PATTERN 0 THRU 7
5033							:INTO RHDB TO FILL IT
5034							:THEN ON READING RHDB THE
5035							:SEVENTH TIME
5036							:RHDB SHOULD HAVE 6
5037							:BUT CONTAINED WHAT IS
5038							:GIVEN IN BAD RHDB
5039	015544					81\$:	
5040							
5041	015544	012737	000010	004664	MOV	#8,SILONM	:EIGHTH WORD TO BE READ
5042						:CHECK THAT RHDB HAS 7	
5043	015552	012737	000007	001124	MOV	#7,\$GDDAT	:GET GOOD = 0
5044							
5045	015560	017737	166316	001126	MOV	DRHDB,\$BDDAT	:READ RHDB FOR COMPARISON
5046	015566	023737	001124	001126	CMP	\$GDDAT,\$BDDAT	:COMPARE EXPECTED
5047							:DATA WITH DATA READ FROM
5048							:RHDB
5049	015574	001401			BEG	83\$:BRANCH IF GOOD
5050	015576	104024			ERROR	24	
5051							:AFTER SETTING "CLR" BIT #5
5052							:IN RHCS2 TO INIT THE RH
5053							:AND WRITING A
5054							:COUNT PATTERN 0 THRU 7
5055							:INTO RHDB TO FILL IT

; THEN ON READING RHDB THE
; EIGHTH TIME
; RHDB SHOULD HAVE 7
; BUT CONTAINED WHAT IS
; GIVEN IN BAD RHDB

5056
5057
5058
5059
5060
5061 015600 83\$:

5062
5063
5064 ;CHECK THAT RHCS2 HAS IR
5065 015600 012737 000100 001124 MOV #IR,\$GDDAT
5066 015606 053737 005010 001124 BIS UNIT,\$GDDAT
5067
5068 015614 017737 166250 001126 MOV @RHCS2,\$BDDAT
5069 015622 023737 001124 001126 CMP \$GDDAT,\$BDDAT
5070
5071
5072 015630 001401 BEQ 85\$
5073 015632 104025 ERROR 25

; GET GOOD = 100
; INCLUDE UNIT NUMBER
; READ RHCS2 FOR COMPARISON
; COMPARE EXPECTED
; DATA WITH DATA READ FROM
; RHCS2
; BRANCH IF GOOD

; AFTER SETTING "CLR" BIT #5
; IN RHCS2 TO INIT THE RH
; AND WRITING A
; COUNT PATTERN 0 THRU 7
; INTO RHDB TO FILL IT
; THEN ON READING RHDB THE
; TOTAL 8 TIMES
; RHCS2 SHOULD HAVE IR
; =100
; TOGETHER WITH UNIT NUMBER
; BUT CONTAINED WHAT IS
; GIVEN IN BAD RHCS2

5085
5086 015634 85\$:

5087 ;CHECK THAT RHCS1 HAS DVA!RDY
5088 015634 012737 004200 001124 MOV #DVA!RDY,\$GDDAT
5089
5090 015642 017737 166212 001126 MOV @RHCS1,\$BDDAT
5091 015650 023737 001124 001126 CMP \$GDDAT,\$BDDAT
5092
5093
5094 015656 001401 BEQ 87\$
5095 015660 104026 ERROR 26

; AFTER SETTING "CLR" BIT #5
; IN RHCS2 TO INIT THE RH
; AND WRITING A
; COUNT PATTERN 0 THRU 7
; INTO RHDB TO FILL IT
; THEN ON READING RHDB THE
; TOTAL 8 TIMES
; RHCS1 SHOULD HAVE DVA!RDY
; =4200
; BUT CONTAINED WHAT IS
; GIVEN IN BAD RHCS1

5106
5107 015662 87\$:

5108 ;CHECK THAT RHCS3 HAS 0
5109 015662 012737 000000 001124 MOV #0,\$GDDAT
5110
5111 015670 017737 166236 001126 MOV @RHCS3,\$BDDAT

; GET GOOD = 0
; READ RHCS3 FOR COMPARISON

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5112 015676 023737 001124 001126      CMP      $GDDAT,$BDDAT      ;COMPARE EXPECTED
5113                                     ;DATA WITH DATA READ FROM
5114                                     ;RHCS3
5115 015704 001401      BEQ      89$              ;BRANCH IF GOOD
5116 015706 104027      ERROR    27
5117                                     ;AFTER SETTING "CLR" BIT #5
5118                                     ;IN RHCS2 TO INIT THE RH
5119                                     ;AND WRITING A
5120                                     ;COUNT PATTERN 0 THRU 7
5121                                     ;INTO RHDB TO FILL IT
5122                                     ;THEN ON READING RHDB THE
5123                                     ;TOTAL 8 TIMES
5124                                     ;RHCS3 SHOULD HAVE 0
5125                                     ;BUT CONTAINED WHAT IS
5126                                     ;GIVEN IN BAD RHCS3
5127 015710                                     89$:
5128                                     ;CHECK THAT RHBA HAS 0
5129 015710 012737 000000 001124      MOV      #0,$GDDAT      ;GET GOOD = 0
5130
5131 015716 017737 166142 001126      MOV      @RHBA,$BDDAT   ;READ RHBA FOR COMPARISON
5132 015724 023737 001124 001126      CMP      $GDDAT,$BDDAT ;COMPARE EXPECTED
5133                                     ;DATA WITH DATA READ FROM
5134                                     ;RHBA
5135 015732 001401      BEQ      91$              ;BRANCH IF GOOD
5136 015734 104030      ERROR    30
5137                                     ;AFTER SETTING "CLR" BIT #5
5138                                     ;IN RHCS2 TO INIT THE RH
5139                                     ;AND WRITING A
5140                                     ;COUNT PATTERN 0 THRU 7
5141                                     ;INTO RHDB TO FILL IT
5142                                     ;THEN ON READING RHDB THE
5143                                     ;TOTAL 8 TIMES
5144                                     ;RHBA SHOULD HAVE 0
5145                                     ;BUT CONTAINED WHAT IS
5146                                     ;GIVEN IN BAD RHBA
5147 015736                                     91$:
5148                                     ;CHECK THAT RHBAE HAS 0
5149 015736 012737 000000 001124      MOV      #0,$GDDAT      ;GET GOOD = 0
5150
5151 015744 017737 166160 001126      MOV      @RHBAE,$BDDAT ;READ RHBAE FOR COMPARISON
5152 015752 023737 001124 001126      CMP      $GDDAT,$BDDAT ;COMPARE EXPECTED
5153                                     ;DATA WITH DATA READ FROM
5154                                     ;RHBAE
5155 015760 001401      BEQ      93$              ;BRANCH IF GOOD
5156 015762 104031      ERROR    31
5157                                     ;AFTER SETTING "CLR" BIT #5
5158                                     ;IN RHCS2 TO INIT THE RH
5159                                     ;AND WRITING A
5160                                     ;COUNT PATTERN 0 THRU 7
5161                                     ;INTO RHDB TO FILL IT
5162                                     ;THEN ON READING RHDB THE
5163                                     ;TOTAL 8 TIMES
5164                                     ;RHBAE SHOULD HAVE 0
5165                                     ;BUT CONTAINED WHAT IS
5166                                     ;GIVEN IN BAD RHBAE
5167 015764                                     93$:

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5168                                     ;CHECK THAT RHWC HAS 0
5169 015764 012737 000000 001124      MOV      #0,$GDDAT      ;GET GOOD = 0
5170
5171 015772 017737 166064 001126      MOV      @RHWC,$BDDAT   ;READ RHWC FOR COMPARISON
5172 016000 023737 001124 001126      CMP      $GDDAT,$BDDAT ;COMPARE EXPECTED
5173                                     ;DATA WITH DATA READ FROM
5174                                     ;RHWC
5175 016006 001401                      BEQ      95$            ;BRANCH IF GOOD
5176 016010 104032
5177
5178                                     ;AFTER SETTING "CLR" BIT #5
5179                                     ;IN RHCS2 TO INIT THE RH
5180                                     ;AND WRITING A
5181                                     ;COUNT PATTERN 0 THRU 7
5182                                     ;INTO RHDB TO FILL IT
5183                                     ;THEN ON READING RHDB THE
5184                                     ;TOTAL 8 TIMES
5185                                     ;RHWC SHOULD HAVE 0
5186                                     ;BUT CONTAINED WHAT IS
5187 016012 95$:
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5192
5193
5194
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5206
5207
5208
5209 016012 000004
5210 016014 012706 001000
5211 016020 012737 000015 004656
5212
5213 016026 004737 040312
5214
5215
5216
5217
5218
5219 016032 012737 000100 001124      ;CHECK THAT RHCS2 HAS IR
5220 016040 053737 005010 001124      MOV      #IR,$GDDAT    ;GET GOOD = 100
5221
5222 016046 017737 166016 001126      BIS      UNIT,$GDDAT   ;INCLUDE UNIT NUMBER
5223 016054 023737 001124 001126      MOV      @RHCS2,$BDDAT ;READ RHCS2 FOR COMPARISON
                                     CMP      $GDDAT,$BDDAT ;COMPARE EXPECTED

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```

*****
*TEST 15      SILO TEST 5 (FLOATING ONES)

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```

*
* AFTER ARH CLEAR IS GIVEN (SET BIT #5 IN RHCS2)
* RHCS2 IS CHECKED TO HAVE "IR" (BIT #6) HIGH,
* TOGETHER WITH UNIT NUMBER
* EIGHT WORDS, A PATTERN OF FLOATING ONES (1,2,4,10,20,40,100,200)
* IS WRITTEN INTO RHDB
* "OR" (BIT #7 IN RHCS2) IS WAITED FOR BY A TRAP
* INSTRUCTION CALLED "WAT"
* THEN RHCS2 IS CHECKED TO HAVE "IR" LOW AND
* "OR" HIGH TOGETHER WITH THE UNIT NUMBER
* THEN RHDB IS READ AND COMPARED TO HAVE THE
* RIGHT VALUE AFTER EACH OF THE EIGHT READS.
* THEN RHCS2 IS CHECKED TO HAVE "IR"
* AND UNIT NUMBER
* THEN RHCS1, RHCS3, RHBA, RHBAE, RHWC ARE
* CHECKED TO HAVE THE APPROPRIATE VALUE

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*****
*ST15: SCOPE
MOV      #STACK_SP      ;RESET STACK
MOV      #15,@#TSTNM    ;SAVE TEST NUMBER
JSR      PC,@#CLDISK    ;GIVE RH INITIALIZE
                                     ;SETUP UNIT NUBER
                                     ;CLEAR RHWC AND FUNCTION BITS IN RHCS1

```

3

```

5224 ;DATA WITH DATA READ FROM
5225 ;RHCS2
5226 016062 001401 BEQ 65$ ;BRANCH IF GOOD
5227 016064 104006 ERROR 6
5228 ;AFTER SETTING "CLR" BIT #5
5229 ;IN RHCS2 TO INIT THE RH
5230 ;AND HAVING DONE NOTHING
5231 ;ELSE
5232 ;RHCS2 SHOULD HAVE IR
5233 ;=100
5234 ;TOGETHER WITH UNIT NUMBER
5235 ;BUT CONTAINED WHAT IS
5236 ;GIVEN IN BAD RHCS2
5237 016066 65$:
5238
5239 ;WRITE EIGHT WORDS INTO RHDB TO FILL IT
5240 ;DATA IS 1,2,4,10,20,40,100,200
5241 016066 012701 000001 MOV #1,R1 ;GETTING READY TO FLOAT ONES
5242 016072 012702 000010 MOV #8,R2 ;COUNTER -8 DATA WORDS
5243 016076 010177 166000 1$: MOV R1,RHDB ;WRITE INTO RHDB
5244 016102 006301 ASL R1 ;SHIFT 1 ONE POSITION LEFT
5245 016104 005302 DEC R2 ;COUNT TO 8
5246 016106 001373 BNE 1$ ;BRANCH IF 8 NOT DONE
5247 ;WAIT FOR OR BIT IN RHCS2 REGISTER TO SET
5248 016110 104411 WAT ;TRAP TO WAIT.T SUBROUTINE
5249 016112 004070 RHCS2 ;AND WAIT FOR OR BIT IN
5250 016114 000200 OR ;RHCS2 REGISTER
5251 ;IF ERROR OCCURS HERE
5252 ;IT MEANS "OR" DID NOT
5253 ;SET FOR THE FULL COUNT
5254 ;DOWN OF THE WAIT.T SUBROUTINE
5255 ;THIS TIME IS APPROXIMATELY
5256 ;LARGER THAN 500 MILLISECONDS
5257
5258
5259 ;CHECK THAT RHCS2 HAS OR
5260 016116 012737 000200 001124 MOV #OR,$GDDAT ;GET GOOD = 200
5261 016124 053737 005010 001124 BIS UNIT,$GDDAT ;INCLUDE UNIT NUMBER
5262
5263 016132 017737 165732 001126 MOV RHCS2,$BDDAT ;READ RHCS2 FOR COMPARISON
5264 016140 023737 001124 001126 CMP $GDDAT,$BDDAT ;COMPARE EXPECTED
5265 ;DATA WITH DATA READ FROM
5266 ;RHCS2
5267 016146 001401 BEQ 67$ ;BRANCH IF GOOD
5268 016150 104023 ERROR 23
5269 ;AFTER SETTING "CLR" BIT #5
5270 ;IN RHCS2 TO INIT THE RH
5271 ;AND HAVING WRITTEN A
5272 ;FLOATING ONES PATTERN
5273 ;1,2,4,10,20,40,100,200
5274 ;INTO RHDB TO FILL IT
5275 ;RHCS2 SHOULD HAVE OR
5276 ;=200
5277 ;TOGETHER WITH UNIT NUMBER
5278 ;BUT CONTAINED WHAT IS
5279 ;GIVEN IN BAD RHCS2

```



```

5280 016152          67$:
5281
5282
5283
5284 016152 012737 000001 004664      MOV      #1,SILONM      ;FIRST WORD TO BE READ
5285          :CHECK THAT RHDB HAS 1
5286 016160 012737 000001 001124      MOV      #1,$GDDAT      ;GET GOOD = 0
5287
5288 016166 017737 165710 001126      MOV      @RHDB,$BDDAT    ;READ RHDB FOR COMPARISON
5289 016174 023737 001124 001126      CMP      $GDDAT,$BDDAT  ;COMPARE EXPECTED
5290          :DATA WITH DATA READ FROM
5291          :RHDB
5292 016202 001401          BEQ      69$            ;BRANCH IF GOOD
5293 016204 :04024          ERROR    24
5294
5295          :AFTER SETTING "CLR" BIT #5
5296          :IN RHCS2 TO INIT THE RH
5297          :AND WRITING A PATTERN OF
5298          :FLOATING ONES (WHICH IS
5299          :1,2,4,10,20,40,100,200)
5300          :INTO RHDB TO FILL IT
5301          :THEN ON READING RHDB THE
5302          :FIRST TIME
5303          :RHDB SHOULD HAVE 1
5304          :BUT CONTAINED WHAT IS
5305          :GIVEN IN BAD RHDB
5306
5307
5308 016206 012737 000002 004664      MOV      #2,SILONM      ;SECOND WORD TO BE READ
5309          :CHECK THAT RHDB HAS 2
5310 016214 012737 000002 001124      MOV      #2,$GDDAT      ;GET GOOD = 0
5311
5312 016222 017737 165654 001126      MOV      @RHDB,$BDDAT    ;READ RHDB FOR COMPARISON
5313 016230 023737 001124 001126      CMP      $GDDAT,$BDDAT  ;COMPARE EXPECTED
5314          :DATA WITH DATA READ FROM
5315          :RHDB
5316 016236 001401          BEQ      71$            ;BRANCH IF GOOD
5317 016240 104024          ERROR    24
5318
5319          :AFTER SETTING "CLR" BIT #5
5320          :IN RHCS2 TO INIT THE RH
5321          :AND WRITING A PATTERN OF
5322          :FLOATING ONES (WHICH IS
5323          :1,2,4,10,20,40,100,200)
5324          :INTO RHDB TO FILL IT
5325          :THEN ON READING RHDB THE
5326          :SECOND TIME
5327          :RHDB SHOULD HAVE 2
5328          :BUT CONTAINED WHAT IS
5329          :GIVEN IN BAD RHDB
5330
5331
5332 016242          71$:
5333 016242 012737 000003 004664      MOV      #3,SILONM      ;THIRD WORD TO BE READ
5334          :CHECK THAT RHDB HAS 4
5335 016250 012737 000004 001124      MOV      #4,$GDDAT      ;GET GOOD = 0

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Vertical text on the left margin, possibly a page number or identifier, appearing as a column of characters.

...ING ONES...
...RHOB TO FILL IT...
...THEN ON READING RHOB THE...
...FIFTH TIME...
...RHOB SHOULD HAVE 20...
...BUT CONTAINED WHAT IS...
...GIVEN IN BAD RHOB

016386 795:

016386 012737 000006 004664

MOV #6,SILONM
:CHECK THAT RHOB HAS 40
MOV #40,\$GDDAT

:SIXTH WORD TO BE READ
:GET GOOD = 0

016394 012737 000040 001124

MOV 2RHOB,\$BDDAT
CMP \$GDDAT,\$BDDAT

:READ RHOB FOR COMPARISON
:COMPARE EXPECTED
:DATA WITH DATA READ FROM
:RHOB
:BRANCH IF GOOD

016402 017737 165474 001126
016410 023737 001124 001126

016416 001401
016420 104024

BEG 795
ERROR 24

:AFTER SETTING "CLR" BIT #5
:IN RHCS2 TO INIT THE RH
:AND WRITING A PATTERN OF
:FLOATING ONES (WHICH IS
:1 2 4 10 20 40 100 200)
:INTO RHOB TO FILL IT
:THEN ON READING RHOB THE
:SIXTH TIME
:RHOB SHOULD HAVE 40
:BUT CONTAINED WHAT IS
:GIVEN IN BAD RHOB

016422 795:

016422 012737 000007 004664

MOV #7,SILONM
:CHECK THAT RHOB HAS 100
MOV #100,\$GDDAT

:SEVENTH WORD TO BE READ
:GET GOOD = 0

016430 012737 000100 001124

MOV 2RHOB,\$BDDAT
CMP \$GDDAT,\$BDDAT

:READ RHOB FOR COMPARISON
:COMPARE EXPECTED
:DATA WITH DATA READ FROM
:RHOB
:BRANCH IF GOOD

016436 017737 165440 001126
016444 023737 001124 001126

016452 001401
016454 104024

BEG 815
ERROR 24

:AFTER SETTING "CLR" BIT #5
:IN RHCS2 TO INIT THE RH
:AND WRITING A PATTERN OF
:FLOATING ONES (WHICH IS
:1 2 4 10 20 40 100 200)
:INTO RHOB TO FILL IT
:THEN ON READING RHOB THE
:SEVENTH TIME
:RHOB SHOULD HAVE 100
:BUT CONTAINED WHAT IS


```

016562 023737 001124 001126      CMP      $GDDAT,$BDDAT      ;COMPARE EXPECTED
                                ;DATA WITH DATA READ FROM
                                ;RHCS1
                                ;BRANCH IF GOOD
016570 001401      BEQ      87$
016572 104026      ERROR    26
                                ;AFTER SETTING "CLR" BIT #5
                                ;IN RHCS2 TO INIT THE RH
                                ;AND WRITING A PATTERN OF
                                ;FLOATING ONES (WHICH IS
                                ;1,2,4,10,20,40,100,200)
                                ;INTO RHDB TO FILL IT
                                ;THEN ON READING RHDB THE
                                ;TOTAL 8 TIMES
                                ;RHCS1 SHOULD HAVE DVA!RDY
                                ;+200
                                ;JT CONTAINED WHAT IS
                                ;GIVEN IN BAD RHCS1
016574      87$:
                                ;CHECK THAT RHCS3 HAS 0
016574 012737 000000 001124      MOV      #0,$GDDAT      ;GET GOOD = 0
016602 017737 165324 001126      MOV      3RHCS3,$BDDAT  ;READ RHCS3 FOR COMPARISON
016610 023737 001124 001126      CMP      $GDDAT,$BDDAT  ;COMPARE EXPECTED
                                ;DATA WITH DATA READ FROM
                                ;RHCS3
                                ;BRANCH IF GOOD
016616 001401      BEQ      89$
016620 104027      ERROR    27
                                ;AFTER SETTING "CLR" BIT #5
                                ;IN RHCS2 TO INIT THE RH
                                ;AND WRITING A PATTERN OF
                                ;FLOATING ONES (WHICH IS
                                ;1,2,4,10,20,40,100,200)
                                ;INTO RHDB TO FILL IT
                                ;THEN ON READING RHDB THE
                                ;TOTAL 8 TIMES
                                ;RHCS3 SHOULD HAVE 0
                                ;BUT CONTAINED WHAT IS
                                ;GIVEN IN BAD RHCS3
016622      89$:
                                ;CHECK THAT RHBA HAS 0
016622 012737 000000 001124      MOV      #0,$GDDAT      ;GET GOOD = 0
016630 017737 165230 001126      MOV      3RHBA,$BDDAT  ;READ RHBA FOR COMPARISON
016636 023737 001124 001126      CMP      $GDDAT,$BDDAT  ;COMPARE EXPECTED
                                ;DATA WITH DATA READ FROM
                                ;RHBA
                                ;BRANCH IF GOOD
016644 001401      BEQ      91$
016646 104030      ERROR    30
                                ;AFTER SETTING "CLR" BIT #5
                                ;IN RHCS2 TO INIT THE RH
                                ;AND WRITING A PATTERN OF
                                ;FLOATING ONES (WHICH IS
                                ;1,2,4,10,20,40,100,200)
                                ;INTO RHDB TO FILL IT
                                ;THEN ON READING RHDB THE
                                ;TOTAL 8 TIMES

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016650          91$:
016650 012737 000000 001124      MOV      #0,$GDDAT      ;CHECK THAT RHBAE HAS 0
                                ;GET GOOD = 0
016656 017737 165246 001126      MOV      @RHBAE,$BDDAT   ;READ RHBAE FOR COMPARISON
016664 023737 001124 001126      CMP      $GDDAT,$BDDAT  ;COMPARE EXPECTED
                                ;DATA WITH DATA READ FROM
                                ;RHBAE
                                ;BRANCH IF GOOD
016672 001401      BEQ      93$          ;AFTER SETTING "CLR" BIT #5
016674 104031      ERROR    31          ;IN RHCS2 TO INIT THE RH
                                ;AND WRITING A PATTERN OF
                                ;FLOATING ONES (WHICH IS
                                ;1 2 4 10 20 40 100 200)
                                ;INTO RHDB TO FILL IT
                                ;THEN ON READING RHDB THE
                                ;TOTAL 8 TIMES
                                ;RHBAE SHOULD HAVE 0
                                ;BUT CONTAINED WHAT IS
                                ;GIVEN IN BAD RHBAE

016676          93$:
016676 012737 000000 001124      MOV      #0,$GDDAT      ;CHECK THAT RHWC HAS 0
                                ;GET GOOD = 0
016704 017737 165152 001126      MOV      @RHWC,$BDDAT   ;READ RHWC FOR COMPARISON
016712 023737 001124 001126      CMP      $GDDAT,$BDDAT  ;COMPARE EXPECTED
                                ;DATA WITH DATA READ FROM
                                ;RHWC
                                ;BRANCH IF GOOD
016720 001401      BEQ      95$          ;AFTER SETTING "CLR" BIT #5
016722 104032      ERROR    32          ;IN RHCS2 TO INIT THE RH
                                ;AND WRITING A PATTERN OF
                                ;FLOATING ONES (WHICH IS
                                ;1 2 4 10 20 40 100 200)
                                ;INTO RHDB TO FILL IT
                                ;THEN ON READING RHDB THE
                                ;TOTAL 8 TIMES
                                ;RHWC SHOULD HAVE 0
                                ;BUT CONTAINED WHAT IS
                                ;GIVEN IN BAD RHWC

016724          95$:

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;*****
;*TEST 16      SILO TEST 6 (FLOATING ONES IN UPPER BYTE)
;
;*      AFTER A RH CLEAR IS GIVEN (SET BIT #5 IN RHCS2)
;*      RHCS2 IS CHECKED TO HAVE "IR" (BIT #6) HIGH,
;*      TOGETHER WITH UNIT NUMBER
;*      EIGHT WORDS, A PATTERN OF FLOATING ONES IN UPPER BYTE

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5616          : *      400,1000,2000,4000,10000,20000,40000,100000
5617          : *      IS WRITTEN INTO RHDB
5618          : *      "OR" (BIT #7 IN RHCS2) IS WAITED FOR BY A TRAP
5619          : *      INSTRUCTION CALLED "WAT"
5620          : *      THEN RHCS2 IS CHECKED TO HAVE "IR" LOW AND
5621          : *      "OR" HIGH TOGETHER WITH THE UNIT NUMBER
5622          : *      THEN RHDB IS READ AND COMPARED TO HAVE THE
5623          : *      RIGHT VALUE AFTER EACH OF THE EIGHT READS.
5624          : *      THEN RHCS2 IS CHECKED TO HAVE "IR"
5625          : *      AND UNIT NUMBER
5626          : *      THEN RHCS1, RHCS3, RHBA, RHBAE, RHWC ARE
5627          : *      CHECKED TO HAVE THE APPROPRIATE VALUE
5628          : *      *****
5629 016724 000004          †ST16: SCOPE
5630 016726 012706 001000      MOV      #STACK,SP      ;RESET STACK
5631 016732 012737 000016 004656  MOV      #16,2#TSTNM    ;SAVE TEST NUMBER
5632
5633 016740 004737 040312      JSR      PC,2#CLDISK    ;GIVE RH INITIALIZE
5634                                ;SETUP UNIT NUBER
5635                                ;CLEAR RHWC AND FUNCTION BITS IN RHCS1
5636
5637
5638                                ;CHECK THAT RHCS2 HAS IR
5639 016744 012737 000100 001124  MOV      #IR,$GDDAT     ;GET GOOD = 100
5640 016752 053737 005010 001124  BIS      UNIT,$GDDAT    ;INCLUDE UNIT NUMBER
5641
5642 016760 017737 165104 001126  MOV      2#RHCS2,$BDDAT ;READ RHCS2 FOR COMPARISON
5643 016766 023737 001124 001126  CMP      $GDDAT,$BDDAT  ;COMPARE EXPECTED
5644                                ;DATA WITH DATA READ FROM
5645                                ;RHCS2
5646 016774 001401          BEQ      65$            ;BRANCH IF GOOD
5647 016776 104006          ERROR    6
5648
5649                                ;AFTER SETTING "CLR" BIT #5
5650                                ;IN RHCS2 TO INIT THE RH
5651                                ;AND HAVING DONE NOTHING
5652                                ;ELSE
5653                                ;RHCS2 SHOULD HAVE IR
5654                                ;=100
5655                                ;TOGETHER WITH UNIT NUMBER
5656                                ;BUT CONTAINED WHAT IS
5657                                ;GIVEN IN BAD RHCS2
5657 017000          65$:
5658
5659                                ;WRITE EIGHT WORDS INTO RHDB TO FILL IT
5660                                ;DATA IS 400,1000,2000,4000,10000,20000,40000
5661                                ;100000
5662 017000 012701 000400      MOV      #400,R1        ;GETTING READY TO FLOAT ONES
5663                                ;IN UPPER BYTE
5664 017004 012702 000010      MOV      #8,R2          ;COUNTER -8 DATA WORDS
5665 017010 010177 165066 1$:  MOV      R1,2#RHDB      ;WRITE INTO RHDB
5666 017014 006301          ASL      R1              ;SHIFT 1 ONE POSITION LEFT
5667 017016 005302          DEC      R2              ;COUNT TO 8
5668 017020 001373          BNE     1$              ;BRANCH IF 8 NOT DONE
5669                                ;WAIT FOR OR BIT IN RHCS2 REGISTER TO SET
5670 017022 104411          WAT                      ;TRAP TO WAIT.T SUBROUTINE
5671 017024 004070          RHCS2                   ;AND WAIT FOR OR BIT IN

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5728 017120          69$:
5729
5730
5731 017120 012737 003002 004664      MOV      #2,SILONM      ;SECOND WORD TO BE READ
5732      ;CHECK THAT RHDB HAS 1000
5733 017126 012737 001000 001124      MOV      #1000,$GDDAT   ;GET GOOD = 0
5734
5735 017134 017737 164742 001126      MOV      @RHDB,$BDDAT   ;READ RHDB FOR COMPARISON
5736 017142 023737 001124 001126      CMP      $GDDAT,$BDDAT  ;COMPARE EXPECTED
5737      ;DATA WITH DATA READ FROM
5738      ;RHDB
5739 017150 001401      BEQ      71$           ;BRANCH IF GOOD
5740 017152 104024      ERROR    24
5741
5742      ;AFTER SETTING "CLR" BIT #5
5743      ;IN RHCS2 TO INIT THE RH
5744      ;AND WRITING A PATTERN OF FLOATING
5745      ;ONES (WHICH IS 400,1000,2000,4000,
5746      ;10000,20000,40000,100000)
5747      ;INTO RHDB TO FILL IT
5748      ;THEN ON READING RHDB THE
5749      ;SECOND TIME
5750      ;RHDB SHOULD HAVE 1000
5751      ;BUT CONTAINED WHAT IS
5752      ;GIVEN IN BAD RHDB
5753 017154          71$:
5754
5755 017154 012737 000003 004664      MOV      #3,SILONM      ;THIRD WORD TO BE READ
5756      ;CHECK THAT RHDB HAS 2000
5757 017162 012737 002000 001124      MOV      #2000,$GDDAT  ;GET GOOD = 0
5758
5759 017170 017737 164706 001126      MOV      @RHDB,$BDDAT   ;READ RHDB FOR COMPARISON
5760 017176 023737 001124 001126      CMP      $GDDAT,$BDDAT  ;COMPARE EXPECTED
5761      ;DATA WITH DATA READ FROM
5762      ;RHDB
5763 017204 001401      BEQ      73$           ;BRANCH IF GOOD
5764 017206 104024      ERROR    24
5765
5766      ;AFTER SETTING "CLR" BIT #5
5767      ;IN RHCS2 TO INIT THE RH
5768      ;AND WRITING A PATTERN OF FLOATING
5769      ;ONES (WHICH IS 400,1000,2000,4000,
5770      ;10000,20000,40000,100000)
5771      ;INTO RHDB TO FILL IT
5772      ;THEN ON READING RHDB THE
5773      ;THIRD TIME
5774      ;RHDB SHOULD HAVE 2000
5775      ;BUT CONTAINED WHAT IS
5776      ;GIVEN IN BAD RHDB
5777 017210          73$:
5778
5779 017210 012737 000004 004664      MOV      #4,SILONM      ;FOURTH WORD TO BE READ
5780      ;CHECK THAT RHDB HAS 4000
5781 017216 012737 004000 001124      MOV      #4000,$GDDAT  ;GET GOOD = 0
5782
5783 017224 017737 164652 001126      MOV      @RHDB,$BDDAT   ;READ RHDB FOR COMPARISON

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MAINDEC-11-DERHAB-A
DERHAB.SRC T16

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SILO TEST 6 (FLOATING ONES IN UPPER BYTE)

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5784 017232 023737 001124 001126      CMP      $GDDAT,$BDDAT      ;COMPARE EXPECTED
5785                                     ;DATA WITH DATA READ FROM
5786                                     ;RHDB
5787 017240 001401      BEQ      75$                ;BRANCH IF GOOD
5788 017242 104024      ERROR    24
5789                                     ;AFTER SETTING "CLR" BIT #5
5790                                     ;IN RHCS2 TO INIT THE RH
5791                                     ;AND WRITING A PATTERN OF FLOATING
5792                                     ;ONES (WHICH IS 400,1000,2000,4000,
5793                                     ;10000,20000,40000,100000)
5794                                     ;INTO RHDB TO FILL IT
5795                                     ;THEN ON READING RHDB THE
5796                                     ;FOURTH TIME
5797                                     ;RHDB SHOULD HAVE 4000
5798                                     ;BUT CONTAINED WHAT IS
5799                                     ;GIVEN IN BAD RHDB
5800 017244                                     75$:
5801
5802
5803 017244 012737 000005 004664      MOV      #5,SILONM          ;FIFTH WORD TO BE READ
5804                                     ;CHECK THAT RHDB HAS 10000
5805 017252 012737 010000 001124      MOV      #10000,$GDDAT     ;GET GOOD = 0
5806
5807 017260 017737 164616 001126      MOV      @RHDB,$BDDAT      ;READ RHDB FOR COMPARISON
5808 017266 023737 001124 001126      CMP      $GDDAT,$BDDAT     ;COMPARE EXPECTED
5809                                     ;DATA WITH DATA READ FROM
5810                                     ;RHDB
5811 017274 001401      BEQ      77$                ;BRANCH IF GOOD
5812 017276 104024      ERROR    24
5813                                     ;AFTER SETTING "CLR" BIT #5
5814                                     ;IN RHCS2 TO INIT THE RH
5815                                     ;AND WRITING A PATTERN OF FLOATING
5816                                     ;ONES (WHICH IS 400,1000,2000,4000,
5817                                     ;10000,20000,40000,100000)
5818                                     ;INTO RHDB TO FILL IT
5819                                     ;THEN ON READING RHDB THE
5820                                     ;FIFTH TIME
5821                                     ;RHDB SHOULD HAVE 10000
5822                                     ;BUT CONTAINED WHAT IS
5823                                     ;GIVEN IN BAD RHDB
5824 017300                                     77$:
5825
5826
5827 017300 012737 000006 004664      MOV      #6,SILONM          ;SIXTH WORD TO BE READ
5828                                     ;CHECK THAT RHDB HAS 20000
5829 017306 012737 020000 001124      MOV      #20000,$GDDAT    ;GET GOOD = 0
5830
5831 017314 017737 164562 001126      MOV      @RHDB,$BDDAT      ;READ RHDB FOR COMPARISON
5832 017322 023737 001124 001126      CMP      $GDDAT,$BDDAT     ;COMPARE EXPECTED
5833                                     ;DATA WITH DATA READ FROM
5834                                     ;RHDB
5835 017330 001401      BEQ      79$                ;BRANCH IF GOOD
5836 017332 104024      ERROR    24
5837                                     ;AFTER SETTING "CLR" BIT #5
5838                                     ;IN RHCS2 TO INIT THE RH
5839                                     ;AND WRITING A PATTERN OF FLOATING

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017334

79\$:

017334 012737 000007 004664

MOV #7,SILONM ;SEVENTH WORD TO BE READ

;CHECK THAT RHDB HAS 40000

017342 012737 040000 001124

MOV #40000,\$GDDAT ;GET GOOD = 0

017350 017737 164526 001126

MOV 2RHDB,\$BDDAT ;READ RHDB FOR COMPARISON

017356 023737 001124 001126

CMP \$GDDAT,\$BDDAT ;COMPARE EXPECTED

;DATA WITH DATA READ FROM

;RHDB

BEQ 81\$;BRANCH IF GOOD

017366 104024

ERROR 24

;AFTER SETTING "CLR" BIT #5
;IN RHCS2 TO INIT THE RH
;AND WRITING A PATTERN OF FLOATING
;ONES (WHICH IS 400,1000,2000,4000,
;10000,20000,40000,100000)
;INTO RHDB TO FILL IT
;THEN ON READING RHDB THE
;SEVENTH TIME
;RHDB SHOULD HAVE 40000
;BUT CONTAINED WHAT IS
;GIVEN IN BAD RHDB

017370

81\$:

017370 012737 000010 004664

MOV #8,SILONM ;EIGHTH WORD TO BE READ

;CHECK THAT RHDB HAS 100000

017376 012737 100000 001124

MOV #100000,\$GDDAT ;GET GOOD = 0

017404 017737 164472 001126

MOV 2RHDB,\$BDDAT ;READ RHDB FOR COMPARISON

017412 023737 001124 001126

CMP \$GDDAT,\$BDDAT ;COMPARE EXPECTED

;DATA WITH DATA READ FROM

;RHDB

BEQ 83\$;BRANCH IF GOOD

017422 104024

ERROR 24

;AFTER SETTING "CLR" BIT #5
;IN RHCS2 TO INIT THE RH
;AND WRITING A PATTERN OF FLOATING
;ONES (WHICH IS 400,1000,2000,4000,
;10000,20000,40000,100000)
;INTO RHDB TO FILL IT
;THEN ON READING RHDB THE
;EIGHTH TIME
;RHDB SHOULD HAVE 100000
;BUT CONTAINED WHAT IS
;GIVEN IN BAD RHDB

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5896 017424          83$:
5897
5898
5899          ;CHECK THAT RHCS2 HAS IR
5900 017424 012737 000100 001124  MOV    #IR,$GDDAT  ;GET GOOD = 100
5901 017432 053737 005010 001124  BIS    UNIT,$GDDAT ;INCLUDE UNIT NUMBER
5902
5903 017440 017737 164424 001126  MOV    @RHCS2,$BDDAT ;READ RHCS2 FOR COMPARISON
5904 017446 023737 001124 001126  CMP    $GDDAT,$BDDAT ;COMPARE EXPECTED
5905                          ;DATA WITH DATA READ FROM
5906                          ;RHCS2
5907 017454 001401          BEQ    85$          ;BRANCH IF GOOD
5908 017456 104025          ERROR   25
5909
5910                          ;AFTER SETTING "CLR" BIT #5
5911                          ;IN RHCS2 TO INIT THE RH
5912                          ;AND WRITING A PATTERN OF FLOATING
5913                          ;ONES (WHICH IS 400,1000,2000,4000,
5914                          ;10000,20000,40000,100000)
5915                          ;INTO RHDB TO FILL IT
5916                          ;THEN ON READING RHDB THE
5917                          ;TOTAL 8 TIMES
5918                          ;RHCS2 SHOULD HAVE IR
5919                          ;=100
5920                          ;TOGETHER WITH UNIT NUMBER
5921                          ;BUT CONTAINED WHAT IS
5922                          ;GIVEN IN BAD RHCS2
5923
5924 017460          85$:
5925          ;CHECK THAT RHCS1 HAS DVA!RDY
5926 017460 012737 004200 001124  MOV    #DVA!RDY,$GDDAT ;GET GOOD = 4200
5927 017466 017737 164366 001126  MOV    @RHCS1,$BDDAT ;READ RHCS1 FOR COMPARISON
5928 017474 023737 001124 001126  CMP    $GDDAT,$BDDAT ;COMPARE EXPECTED
5929                          ;DATA WITH DATA READ FROM
5930                          ;RHCS1
5931 017502 001401          BEQ    87$          ;BRANCH IF GOOD
5932 017504 104026          ERROR   26
5933
5934                          ;AFTER SETTING "CLR" BIT #5
5935                          ;IN RHCS2 TO INIT THE RH
5936                          ;AND WRITING A PATTERN OF FLOATING
5937                          ;ONES (WHICH IS 400,1000,2000,4000,
5938                          ;10000,20000,40000,100000)
5939                          ;INTO RHDB TO FILL IT
5940                          ;THEN ON READING RHDB THE
5941                          ;TOTAL 8 TIMES
5942                          ;RHCS1 SHOULD HAVE DVA!RDY
5943                          ;=4200
5944                          ;BUT CONTAINED WHAT IS
5945                          ;GIVEN IN BAD RHCS1
5946 017506          87$:
5947          ;CHECK THAT RHCS3 HAS 0
5948 017506 012737 000000 001124  MOV    #0,$GDDAT    ;GET GOOD = 0
5949 017514 017737 164412 001126  MOV    @RHCS3,$BDDAT ;READ RHCS3 FOR COMPARISON
5950 017522 023737 001124 001126  CMP    $GDDAT,$BDDAT ;COMPARE EXPECTED
5951                          ;DATA WITH DATA READ FROM
5952                          ;RHCS3

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5952 017530 001401      BEQ      89$      ;BRANCH IF GOOD
5953 017532 104027      ERROR    27
5954                                     ;AFTER SETTING "CLR" BIT #5
5955                                     ;IN RHCS2 TO INIT THE RH
5956                                     ;AND WRITING A PATTERN OF FLOATING
5957                                     ;ONES (WHICH IS 400,1000,2000,4000,
5958                                     ;10000,20000,40000,100000)
5959                                     ;INTO RHDB TO FILL IT
5960                                     ;THEN ON READING RHDB THE
5961                                     ;TOTAL 8 TIMES
5962                                     ;RHCS3 SHOULD HAVE 0
5963                                     ;BUT CONTAINED WHAT IS
5964                                     ;GIVEN IN BAD RHCS3
5965 017534                                     89$:
5966                                     ;CHECK THAT RHBA HAS 0
5967 017534 012737 000000 001124      MOV      #0,$GDDAT ;GET GOOD = 0
5968
5969 017542 017737 164316 001126      MOV      @RHBA,$BDDAT ;READ RHBA FOR COMPARISON
5970 017550 023737 001124 001126      CMP      $GDDAT,$BDDAT ;COMPARE EXPECTED
5971                                     ;DATA WITH DATA READ FROM
5972                                     ;RHBA
5973 017556 001401      BEQ      91$      ;BRANCH IF GOOD
5974 017560 104030      ERROR    30
5975                                     ;AFTER SETTING "CLR" BIT #5
5976                                     ;IN RHCS2 TO INIT THE RH
5977                                     ;AND WRITING A PATTERN OF FLOATING
5978                                     ;ONES (WHICH IS 400,1000,2000,4000,
5979                                     ;10000,20000,40000,100000)
5980                                     ;INTO RHDB TO FILL IT
5981                                     ;THEN ON READING RHDB THE
5982                                     ;TOTAL 8 TIMES
5983                                     ;RHBA SHOULD HAVE 0
5984                                     ;BUT CONTAINED WHAT IS
5985                                     ;GIVEN IN BAD RHBA
5986 017562                                     91$:
5987                                     ;CHECK THAT RHBAE HAS 0
5988 017562 012737 000000 001124      MOV      #0,$GDDAT ;GET GOOD = 0
5989
5990 017570 017737 164334 001126      MOV      @RHBAE,$BDDAT ;READ RHBAE FOR COMPARISON
5991 017576 023737 001124 001126      CMP      $GDDAT,$BDDAT ;COMPARE EXPECTED
5992                                     ;DATA WITH DATA READ FROM
5993                                     ;RHBAE
5994 017604 001401      BEQ      93$      ;BRANCH IF GOOD
5995 017606 104031      ERROR    31
5996                                     ;AFTER SETTING "CLR" BIT #5
5997                                     ;IN RHCS2 TO INIT THE RH
5998                                     ;AND WRITING A PATTERN OF FLOATING
5999                                     ;ONES (WHICH IS 400,1000,2000,4000,
6000                                     ;10000,20000,40000,100000)
6001                                     ;INTO RHDB TO FILL IT
6002                                     ;THEN ON READING RHDB THE
6003                                     ;TOTAL 8 TIMES
6004                                     ;RHBAE SHOULD HAVE 0
6005                                     ;BUT CONTAINED WHAT IS
6006                                     ;GIVEN IN BAD RHBAE
6007 017610                                     93$:

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6008 ;CHECK THAT RHWC HAS 0
6009 017610 012737 000000 001124 MOV #0,$GDDAT ;GET GOOD = 0
6010
6011 017616 017737 164240 001126 MOV @RHWC,$BDDAT ;READ RHWC FOR COMPARISON
6012 017624 023737 001124 001126 CMP $GDDAT,$BDDAT ;COMPARE EXPECTED
6013 ;DATA WITH DATA READ FROM
6014 ;RHWC
6015 017632 001401 BEQ 95$ ;BRANCH IF GOOD
6016 017634 104032 ERROR 32
6017 ;AFTER SETTING "CLR" BIT #5
6018 ;IN RHCS2 TO INIT THE RH
6019 ;AND WRITING A PATTERN OF FLOATING
6020 ;ONES (WHICH IS 400,1000,2000,4000,
6021 ;10000,20000,40000,100000)
6022 ;INTO RHDB TO FILL IT
6023 ;THEN ON READING RHDB THE
6024 ;TOTAL 8 TIMES
6025 ;RHWC SHOULD HAVE 0
6026 ;BUT CONTAINED WHAT IS
6027 ;GIVEN IN BAD RHWC
6028 017636 95$:
6029
6030
6031 ;*****
6032 ;*TEST 17 SILO TEST 7 (FLOATING 0 IN LOWER BYTE)
6033
6034 ;* AFTER A RH CLEAR IS GIVEN (SET BIT #5 IN RHCS2)
6035 ;* RHCS2 IS CHECKED TO HAVE "IR" (BIT #6) HIGH,
6036 ;* TOGETHER WITH UNIT NUMBER
6037 ;* EIGHT WORDS OF FLOATING ZEROS 177776, 177775, 177773,
6038 ;* 177767, 177757, 177737, 177677, 177577
6039 ;* IS WRITTEN INTO RHDB
6040 ;* "OR" (BIT #7 IN RHCS2) IS WAITED FOR BY A TRAP
6041 ;* INSTRUCTION CALLED "WAT"
6042 ;* THEN RHCS2 IS CHECKED TO HAVE "IR" LOW AND
6043 ;* "OR" HIGH TOGETHER WITH THE UNIT NUMBER
6044 ;* THEN RHCS3 IS READ AND COMPARED TO HAVE THE
6045 ;* RIGHT VALUE AFTER EACH OF THE EIGHT READS.
6046 ;* THEN RHCS2 IS CHECKED TO HAVE "IR"
6047 ;* AND UNIT NUMBER
6048 ;* THEN RHCS1, RHCS3, RHBA, RHBAE, RHWC ARE
6049 ;* CHECKED TO HAVE THE APPROPRIATE VALUE
6050 ;*****
6051 017636 000004 TST17: SCOPE
6052 017640 012706 001000 MOV #STACK,SP ;RESET STACK
6053 017644 012737 000017 004656 MOV #17,@TSTNM ;SAVE TEST NUMBER
6054
6055 017652 004737 040312 JSR PC,@CLDISK ;GIVE RH INITIALIZE
6056 ;SETUP UNIT NUMBER
6057 ;CLEAR RHWC AND FUNCTION BITS IN RHCS1
6058
6059
6060 ;CHECK THAT RHCS2 HAS IR
6061 017656 012737 000100 001124 MOV #IR,$GDDAT ;GET GOOD = 100
6062 017664 053737 005010 001124 BIS UNIT,$GDDAT ;INCLUDE UNIT NUMBER
6063

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020000

675:

020000 012737 000001 004664

MOV #1,SILONM ;FIRST WORD TO BE READ

020006 012737 177776 001124

;CHECK THAT RHDB HAS 177776
MOV #177776,\$GDDAT ;GET GOOD = 0

020014 017737 164062 001126

MOV 2RHDB,\$BDDAT ;READ RHDB FOR COMPARISON

020022 023737 001124 001126

CMP \$GDDAT,\$BDDAT ;COMPARE EXPECTED
;DATA WITH DATA READ FROM
;RHDB

020030 001401

BEO 695 ;BRANCH IF GOOD

020032 104024

ERROR 24

;AFTER SETTING "CLR" BIT #5
;IN RHCS2 TO INIT THE RH
;AND WRITING A PATTERN OF FLOATING
;ZEROS - 177776, 177775, 177773, 177767,
;177757, 177737, 177677, 177577
;INTO RHDB TO FILL IT
;THEN ON READING RHDB THE
;FIRST TIME
;RHDB SHOULD HAVE 177776
;BUT CONTAINED WHAT IS
;GIVEN IN BAD RHDB

020034

695:

020034 012737 000002 004664

MOV #2,SILONM ;SECOND WORD TO BE READ

020042 012737 177775 001124

;CHECK THAT RHDB HAS 177775
MOV #177775,\$GDDAT ;GET GOOD = 0

020050 017737 164026 001126

MOV 2RHDB,\$BDDAT ;READ RHDB FOR COMPARISON

020056 023737 001124 001126

CMP \$GDDAT,\$BDDAT ;COMPARE EXPECTED
;DATA WITH DATA READ FROM
;RHDB

020054 001401

BEO 715 ;BRANCH IF GOOD

020056 104024

ERROR 24

;AFTER SETTING "CLR" BIT #5
;IN RHCS2 TO INIT THE RH
;AND WRITING A PATTERN OF FLOATING
;ZEROS - 177776, 177775, 177773, 177767,
;177757, 177737, 177677, 177577
;INTO RHDB TO FILL IT
;THEN ON READING RHDB THE
;SECOND TIME
;RHDB SHOULD HAVE 177775
;BUT CONTAINED WHAT IS
;GIVEN IN BAD RHDB

020070

715:

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6176 020070 012737 000003 004664      MOV      #3,SILONM      :THIRD WORD TO BE READ
6177      :CHECK THAT RHDB HAS 177773
6178 020076 012737 177773 001124      MOV      #177773,$GDDAT :GET GOOD = 0
6179 020104 017737 163772 001126      MOV      @RHDB,$BDDAT   :READ RHDB FOR COMPARISON
6180 020112 023737 001124 001126      CMP      $GDDAT,$BDDAT  :COMPARE EXPECTED
6181      :DATA WITH DATA READ FROM
6182      :RHDB
6183 020120 001401      BEQ      73$           :BRANCH IF GOOD
6184 020122 104024      ERROR   24
6185
6186      :AFTER SETTING "CLR" BIT #5
6187      :IN RHCS2 TO INIT THE RH
6188      :AND WRITING A PATTERN OF FLOATING
6189      :ZEROS - 177776, 177775, 177773, 177767,
6190      :177757, 177737, 177677, 177577
6191      :INTO RHDB TO FILL IT
6192      :THEN ON READING RHDB THE
6193      :THIRD TIME
6194      :RHDB SHOULD HAVE 177773
6195      :BUT CONTAINED WHAT IS
6196      :GIVEN IN BAD RHDB
6197
6198 020124      73$:
6199
6200 020124 012737 000004 004664      MOV      #4,SILONM      :FOURTH WORD TO BE READ
6201      :CHECK THAT RHDB HAS 177767
6202 020132 012737 177767 001124      MOV      #177767,$GDDAT :GET GOOD = 0
6203 020140 017737 163736 001126      MOV      @RHDB,$BDDAT   :READ RHDB FOR COMPARISON
6204 020146 023737 001124 001126      CMP      $GDDAT,$BDDAT  :COMPARE EXPECTED
6205      :DATA WITH DATA READ FROM
6206      :RHDB
6207 020154 001401      BEQ      75$           :BRANCH IF GOOD
6208 020156 104024      ERROR   24
6209
6210      :AFTER SETTING "CLR" BIT #5
6211      :IN RHCS2 TO INIT THE RH
6212      :AND WRITING A PATTERN OF FLOATING
6213      :ZEROS - 177776, 177775, 177773, 177767,
6214      :177757, 177737, 177677, 177577
6215      :INTO RHDB TO FILL IT
6216      :THEN ON READING RHDB THE
6217      :FOURTH TIME
6218      :RHDB SHOULD HAVE 177767
6219      :BUT CONTAINED WHAT IS
6220      :GIVEN IN BAD RHDB
6221
6222 020150      75$:
6223
6224 020160 012737 000005 004664      MOV      #5,SILONM      :FIFTH WORD TO BE READ
6225      :CHECK THAT RHDB HAS 177757
6226 020166 012737 177757 001124      MOV      #177757,$GDDAT :GET GOOD = 0
6227 020174 017737 163702 001126      MOV      @RHDB,$BDDAT   :READ RHDB FOR COMPARISON
6228 020202 023737 001124 001126      CMP      $GDDAT,$BDDAT  :COMPARE EXPECTED
6229      :DATA WITH DATA READ FROM
6230      :RHDB

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6232 020210 001401          BEQ      77$          ;BRANCH IF GOOD
6233 020212 104024          ERROR    24
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6245 020214          77$:
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6248 020214 012737 000006 004664      MOV      #6,SILONM      ;SIXTH WORD TO BE READ
6249
6250 020222 012737 177737 001124      ;CHECK THAT RHCB HAS 177737
6251
6252 020230 017737 163646 001126      MOV      #177737,$GDDAT ;GET GOOD = 0
6253 020236 023737 001124 001126      MOV      @RHCB,$BDDAT   ;READ RHCB FOR COMPARISON
6254
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6256 020244 001401          BEQ      79$          ;COMPARE EXPECTED
6257 020246 104024          ERROR    24          ;DATA WITH DATA READ FROM
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6271 020250          79$:
6272 020250 012737 000007 004664      MOV      #7,SILONM      ;SEVENTH WORD TO BE READ
6273
6274 020256 012737 177677 001124      ;CHECK THAT RHCB HAS 177677
6275
6276 020264 017737 163612 001126      MOV      #177677,$GDDAT ;GET GOOD = 0
6277 020272 023737 001124 001126      MOV      @RHCB,$BDDAT   ;READ RHCB FOR COMPARISON
6278
6279
6280 020300 001401          BEQ      81$          ;COMPARE EXPECTED
6281 020302 104024          ERROR    24          ;DATA WITH DATA READ FROM
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;AFTER SETTING "CLR" BIT #5
;IN RHCS2 TO INIT THE RH
;AND WRITING A PATTERN OF FLOATING
;ZEROS - 177776, 177775, 177773, 177757,
;177757, 177737, 177677, 177577
;INTO RHDB TO FILL IT
;THEN ON READING RHDB THE
;FIFTH TIME
;RHDB SHOULD HAVE 177757
;BUT CONTAINED WHAT IS
;GIVEN IN BAD RHDB

;AFTER SETTING "CLR" BIT #5
;IN RHCS2 TO INIT THE RH
;AND WRITING A PATTERN OF FLOATING
;ZEROS - 177776, 177775, 177773, 177757,
;177757, 177737, 177677, 177577
;INTO RHDB TO FILL IT
;THEN ON READING RHDB THE
;SIXTH TIME
;RHDB SHOULD HAVE 177737
;BUT CONTAINED WHAT IS
;GIVEN IN BAD RHDB

;AFTER SETTING "CLR" BIT #5
;IN RHCS2 TO INIT THE RH
;AND WRITING A PATTERN OF FLOATING
;ZEROS - 177776, 177775, 177773, 177757,
;177757, 177737, 177677, 177577
;INTO RHDB TO FILL IT

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020304

81\$:

020304 012737 000010 004664

MOV #8, SILJNM ;EIGHTH WORD TO BE READ

020312 012737 177577 001124

;CHECK THAT RHDB HAS 177577
MOV #177577,\$GDDAT ;GET GOOD = 0

020320 017737 163556 001126

MOV @RHDB,\$BDDAT ;READ RHDB FOR COMPARISON

020326 023737 001124 001126

CMP \$GDDAT,\$BDDAT ;COMPARE EXPECTED

020334 001401

BEG 83\$;BRANCH IF GOOD

020336 104024

ERROR 24

;AFTER SETTING "CLR" BIT #5
;IN RHCS2 TO INIT THE RH
;AND WRITING A PATTERN OF FLOATING
;ZEROS - 177776, 177775, 177773, 177767,
;177757, 177737, 177677, 177577
;INTC RHDB TO FILL IT
;THEN ON READING RHDB THE
;EIGHTH TIME
;RHDB SHOULD HAVE 177577
;BUT CONTAINED WHAT IS
;GIVEN IN BAD RHDB

020340

83\$:

020340 012737 000100 001124

;CHECK THAT RHCS2 HAS IR
MOV #IR,\$GDDAT ;GET GOOD = 100

020346 053737 005010 001124

BIS UNIT,\$GDDAT ;INCLUDE UNIT NUMBER

020354 017737 163510 001126

MOV @RHCS2,\$BDDAT ;READ RHCS2 FOR COMPARISON

020362 023737 001124 001126

CMP \$GDDAT,\$BDDAT ;COMPARE EXPECTED

020370 001401

BEG 85\$;BRANCH IF GOOD

020372 104025

ERROR 25

;AFTER SETTING "CLR" BIT #5
;IN RHCS2 TO INIT THE RH
;AND WRITING A PATTERN OF FLOATING
;ZEROS - 177776, 177775, 177773, 177767,
;177757, 177737, 177677, 177577
;INTC RHDB TO FILL IT
;THEN ON READING RHDB THE
;TOTAL 8 TIMES
;RHCS2 SHOULD HAVE IR
;=100
;TOGETHER WITH UNIT NUMBER
;BUT CONTAINED WHAT IS
;GIVEN IN BAD RHCS2

020374

85\$:

F10

MAINDEC-11-DERHAB-A
DERHAB.SRC T17

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SILO TEST 7 (FLOATING 0 IN LOWER BYTE)

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6344      ;CHECK THAT RHCS1 HAS DVA!RDY
6345 020374 012737 004200 001124  MOV      #DVA!RDY,$GDDAT ;GET GOOD = 4200
6346
6347 020402 017737 163452 001126  MOV      @RHCS1,$BDDAT ;READ RHCS1 FOR COMPARISON
6348 020410 023737 001124 001126  CMP      $GDDAT,$BDDAT ;COMPARE EXPECTED
6349                                     ;DATA WITH DATA READ FROM
6350                                     ;RHCS1
6351 020416 001401  BEQ      87$ ;BRANCH IF GOOD
6352 020420 104026  ERROR   26
6353                                     ;AFTER SETTING "CLR" BIT #5
6354                                     ;IN RHCS2 TO INIT THE RH
6355                                     ;AND WRITING A PATTERN OF FLOATING
6356                                     ;ZEROS - 177776, 177775, 177773, 177767,
6357                                     ;177757, 177737, 177677, 177577
6358                                     ;INTO RHDB TO FILL IT
6359                                     ;THEN ON READING RHDB THE
6360                                     ;TOTAL 8 TIMES
6361                                     ;RHCS1 SHOULD HAVE DVA!RDY
6362                                     ;=4200
6363                                     ;BUT CONTAINED WHAT IS
6364                                     ;GIVEN IN BAD RHCS1
6365 020422      87$:
6366      ;CHECK THAT RHCS3 HAS 0
6367 020422 012737 000000 001124  MOV      #0,$GDDAT ;GET GOOD = 0
6368
6369 020430 017737 163476 001126  MOV      @RHCS3,$BDDAT ;READ RHCS3 FOR COMPARISON
6370 020436 023737 001124 001126  CMP      $GDDAT,$BDDAT ;COMPARE EXPECTED
6371                                     ;DATA WITH DATA READ FROM
6372                                     ;RHCS3
6373 020444 001401  BEQ      89$ ;BRANCH IF GOOD
6374 020446 104027  ERROR   27
6375                                     ;AFTER SETTING "CLR" BIT #5
6376                                     ;IN RHCS2 TO INIT THE RH
6377                                     ;AND WRITING A PATTERN OF FLOATING
6378                                     ;ZEROS - 177776, 177775, 177773, 177767,
6379                                     ;177757, 177737, 177677, 177577
6380                                     ;INTO RHDB TO FILL IT
6381                                     ;THEN ON READING RHDB THE
6382                                     ;TOTAL 8 TIMES
6383                                     ;RHCS3 SHOULD HAVE 0
6384                                     ;BUT CONTAINED WHAT IS
6385                                     ;GIVEN IN BAD RHCS3
6386 020450      89$:
6387      ;CHECK THAT RHBA HAS 0
6388 020450 012737 000000 001124  MOV      #0,$GDDAT ;GET GOOD = 0
6389
6390 020456 017737 163402 001126  MOV      @RHBA,$BDDAT ;READ RHBA FOR COMPARISON
6391 020464 023737 001124 001126  CMP      $GDDAT,$BDDAT ;COMPARE EXPECTED
6392                                     ;DATA WITH DATA READ FROM
6393                                     ;RHBA
6394 020472 001401  BEQ      91$ ;BRANCH IF GOOD
6395 020474 104030  ERROR   30
6396                                     ;AFTER SETTING "CLR" BIT #5
6397                                     ;IN RHCS2 TO INIT THE RH
6398                                     ;AND WRITING A PATTERN OF FLOATING
6399                                     ;ZEROS - 177776, 177775, 177773, 177767.

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020476 012737 000000 001124
020504 017737 163420 001126
020512 023737 001124 001126
020520 001401
020522 104031
020524
020524 012737 000000 001124
020532 017737 163324 001126
020540 023737 001124 001126
020546 001401
020550 104032
020552

91\$:
93\$:
95\$:

;CHECK THAT RHBAE HAS 0
MOV #0,\$GDDAT
MOV @RHBAE,\$BDDAT
CMP \$GDDAT,\$BDDAT
BEQ 95\$
ERROR 31
;CHECK THAT RHWC HAS 0
MOV #0,\$GDDAT
MOV @RHWC,\$BDDAT
CMP \$GDDAT,\$BDDAT
BEQ 95\$
ERRJR 32

;177757, 177737, 177677, 177577
;INTO RHDB TO FILL IT
;THEN ON READING RHDB THE
;TOTAL 8 TIMES
;RHBA SHOULD HAVE 0
;BUT CONTAINED WHAT IS
;GIVEN IN BAD RHBA
;GET GOOD = 0
;READ RHBAE FOR COMPARISON
;COMPARE EXPECTED
;DATA WITH DATA READ FROM
;RHBAE
;BRANCH IF GOOD
;AFTER SETTING "CLR" BIT #5
;IN RHCS2 TO INIT THE RH
;AND WRITING A PATTERN OF FLOATING
;ZEROS - 177776, 177775, 177773, 177767,
;177757, 177737, 177677, 177577
;INTO RHDB TO FILL IT
;THEN ON READING RHDB THE
;TOTAL 8 TIMES
;RHBAE SHOULD HAVE 0
;BUT CONTAINED WHAT IS
;GIVEN IN BAD RHBAE
;GET GOOD 0
;READ RHWC FOR COMPARISON
;COMPARE EXPECTED
;DATA WITH DATA READ FROM
;RHWC
;BRANCH IF GOOD
;AFTER SETTING "CLR" BIT #5
;IN RHCS2 TO INIT THE RH
;AND WRITING A PATTERN OF FLOATING
;ZEROS - 177776, 177775, 177773, 177757,
;177757, 177737, 177677, 177577
;INTO RHDB TO FILL IT
;THEN ON READING RHDB THE
;TOTAL 8 TIMES
;RHWC SHOULD HAVE 0
;BUT CONTAINED WHAT IS
;GIVEN IN BAD RHWC

;*TEST 20 SILO TEST 9 (FLOATING ZEROS IN UPPER BYTE)
;* AFTER A RH CLEAR IS GIVEN (SET BIT #5 IN RHCS2)

H10

MAINDEC-11-DERHAB-A
DERHAB.SRC T20

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SILO TEST 8 (FLOATING ZEROS IN UPPER BYTE)

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6456 : * RHCS2 IS CHECKED TO HAVE "IR" (BIT #5) HIGH,
6457 : * TOGETHER WITH UNIT NUMBER
6458 : * EIGHT WORDS, A PATTERN OF FLOATING ZEROS IN UPPER BYTE
6459 : * 177377, 177677, 175777, 173777, 167777, 157777, 137777, 77777
6460 : * IS WRITTEN INTO RHDB
6461 : * "OR" (BIT #7 IN RHCS2) IS WAITED FOR BY A TRAP
6462 : * INSTRUCTION CALLED "WAT"
6463 : * THEN RHCS2 IS CHECKED TO HAVE "IR" LOW AND
6464 : * "OR" HIGH TOGETHER WITH THE UNIT NUMBER
6465 : * THEN RHDB IS READ AND COMPARED TO HAVE THE
6466 : * RIGHT VALUE AFTER EACH OF THE EIGHT READS.
6467 : * THEN RHCS2 IS CHECKED TO HAVE "IR"
6468 : * AND UNIT NUMBER
6469 : * THEN RHCS1, RHCS3, RHBA, RHBAE, RHWC ARE
6470 : * CHECKED TO HAVE THE APPROPRIATE VALUE

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6471 :
6472 020552 000004 ST20: SCOPE
6473 020554 012706 MOV #STACK,SP ;RESET STACK
6474 020560 012737 000020 004656 MOV #20,@#TSTNM ;SAVE TEST NUMBER
6475 :
6476 020566 004737 040312 JSR PC,@#CLDISK ;GIVE RH INITIALIZE
6477 : ;SETUP UNIT NUMBER
6478 : ;CLEAR RHWC AND FUNCTION BITS IN RHCS1
6479 :
6480 :
6481 :

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6482 020572 012737 000100 001124 ;CHECK THAT RHCS2 HAS IR
6483 020600 053737 005010 001124 MOV #IR,$GDDAT ;GET GOOD = 100
6484 : BIS UNIT,$GDDAT ;INCLUDE UNIT NUMBER
6485 020606 017737 163256 001126 MOV @RHCS2,$BDDAT ;READ RHCS2 FOR COMPARISON
6486 020614 023737 001124 001126 CMP $GDDAT,$BDDAT ;COMPARE EXPECTED
6487 : ;DATA WITH DATA READ FROM
6488 : ;RHCS2
6489 020622 001401 BEQ 65$ ;BRANCH IF GOOD
6490 020624 104006 ERROR 6

```

```

6491 : ;AFTER SETTING "CLR" BIT #5
6492 : ;IN RHCS2 TO INIT THE RH
6493 : ;AND HAVING DONE NOTHING
6494 : ;ELSE
6495 : ;RHCS2 SHOULD HAVE IR
6496 : ;=100
6497 : ;TOGETHER WITH UNIT NUMBER
6498 : ;BUT CONTAINED WHAT IS
6499 : ;GIVEN IN BAD RHCS2

```

```

6500 020626 65$:
6501 :
6502 : ;WRITE EIGHT WORDS INTO RHDB TO FILL IT
6503 : ;A PATTERN OF FLOATING ZEROS IN UPPER BYTE
6504 : ;DATA IS - 177377, 176777, 175777, 173777, 167777,
6505 : ;157777, 137777, 77777

```

```

6506 020626 012701 177377 MOV #177377,R1 ;GETTING READY TO FLOAT ZEROS
6507 : ;IN UPPER BYTE
6508 020632 012702 000010 MOV #8,R2 ;COUNTER -8 DA A WORDS
6509 020636 010177 163240 1$: MOV R1,@RHDB ;WRITE INTO RHDB
6510 020642 000261 SEC ;SET CARRY
6511 020644 006101 ROL R1 ;GET ZERO ONE BIT LEFT

```



```

6568 ; INTO RHDB TO FILL IT
6569 ; THEN ON READING RHDB THE
6570 ; FIRST TIME
6571 ; RHDB SHOULD HAVE 177377
6572 ; BUT CONTAINED WHAT IS
6573 ; GIVEN IN BAD RHDB
6574 020750          69$:
6575
6576
6577 020750 012737 000002 004664      MOV    #2,SILONM      ;SECOND WORD TO BE READ
6578 ;CHECK THAT RHDB HAS 176777
6579 020756 012737 176777 001124      MOV    #176777,$GDDAT ;GET GOOD = 0
6580
6581 020764 017737 163112 001126      MOV    @RHDB,$BDDAT   ;READ RHDB FOR COMPARISON
6582 020772 023737 001124 001126      CMP    $GDDAT,$BDDAT ;COMPARE EXPECTED
6583 ;DATA WITH DATA READ FROM
6584 ;RHDB
6585 021000 001401          BEQ    71$           ;BRANCH IF GOOD
6586 021002 104034          ERROR  34
6587
6588 ; AFTER SETTING "CLR" BIT #5
6589 ; IN RHCS2 TO INIT THE RH
6590 ; AND WRITING A PATTERN OF FLOATING
6591 ; ZEROS IN UPPER BYTE
6592 ; 177377, 176777, 175777, 173777,
6593 ; 167777, 157777, 137777, 77777
6594 ; INTO RHDB TO FILL IT
6595 ; THEN ON READING RHDB THE
6596 ; SECOND TIME
6597 ; RHDB SHOULD HAVE 176777
6598 ; BUT CONTAINED WHAT IS
6599 ; GIVEN IN BAD RHDB
6600 021004          71$:
6601
6602 021004 012737 000003 004664      MOV    #3,SILONM      ;THIRD WORD TO BE READ
6603 ;CHECK THAT RHDB HAS 175777
6604 021012 012737 175777 001124      MOV    #175777,$GDDAT ;GET GOOD = 0
6605
6606 021020 017737 163056 001126      MOV    @RHDB,$BDDAT   ;READ RHDB FOR COMPARISON
6607 021026 023737 001124 001126      CMP    $GDDAT,$BDDAT ;COMPARE EXPECTED
6608 ;DATA WITH DATA READ FROM
6609 ;RHDB
6610 021034 001401          BEQ    73$           ;BRANCH IF GOOD
6611 021036 104024          ERROR  24
6612
6613 ; AFTER SETTING "CLR" BIT #5
6614 ; IN RHCS2 TO INIT THE RH
6615 ; AND WRITING A PATTERN OF FLOATING
6616 ; ZEROS IN UPPER BYTE
6617 ; 177377, 176777, 175777, 173777,
6618 ; 167777, 157777, 137777, 77777
6619 ; INTO RHDB TO FILL IT
6620 ; THEN ON READING RHDB THE
6621 ; THIRD TIME
6622 ; RHDB SHOULD HAVE 175777
6623 ; BUT CONTAINED WHAT IS
        ; GIVEN IN BAD RHDB

```

K10

MAINDEC-11-DERHAB-A
DERHAB.SRC T20

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SILO TEST 8 (FLOATING ZEROS IN UPPER BYTE)

```

6624 021040          73$:
6625
6626
6627 021040 012737 000004 004664      MOV    #4,SILONM      ;FOURTH WORD TO BE READ
6628                                ;CHECK THAT RHDB HAS 173777
6629 021046 012737 173777 001124      MOV    #173777,$GDDAT ;GET GOOD = 0
6630
6631 021054 017737 163022 001126      MOV    @RHDB,$BDDAT  ;READ RHDB FOR COMPARISON
6632 021062 023737 001124 001126      CMP    $GDDAT,$BDDAT ;COMPARE EXPECTED
6633                                ;DATA WITH DATA READ FROM
6634                                ;RHDB
6635 021070 001401          BEQ    75$           ;BRANCH IF GOOD
6636 021072 104024          ERROR   24
6637
6638                                ;AFTER SETTING "CLR" BIT #5
6639                                ;IN RHCS2 TO INIT THE RH
6640                                ;AND WRITING A PATTERN OF FLOATING
6641                                ;ZEROS IN UPPER BYTE
6642                                ;177377, 176777, 175777, 173777,
6643                                ;167777, 157777, 137777, 77777
6644                                ;INTO RHDB TO FILL IT
6645                                ;THEN ON READING RHDB THE
6646                                ;FOURTH TIME
6647                                ;RHDB SHOULD HAVE 173777
6648                                ;BUT CONTAINED WHAT IS
6649                                ;GIVEN IN BAD RHDB
6649 021074          75$:
6650
6651
6652 021074 012737 000005 004664      MOV    #5,SILONM      ;FIFTH WORD TO BE READ
6653                                ;CHECK THAT RHDB HAS 167777
6654 021102 012737 167777 001124      MOV    #167777,$GDDAT ;GET GOOD = 0
6655
6656 021110 017737 162766 001126      MOV    @RHDB,$BDDAT  ;READ RHDB FOR COMPARISON
6657 021116 023737 001124 001126      CMP    $GDDAT,$BDDAT ;COMPARE EXPECTED
6658                                ;DATA WITH DATA READ FROM
6659                                ;RHDB
6660 021124 001401          BEQ    77$           ;BRANCH IF GOOD
6661 021126 104024          ERROR   24
6662
6663                                ;AFTER SETTING "CLR" BIT #5
6664                                ;IN RHCS2 TO INIT THE RH
6665                                ;AND WRITING A PATTERN OF FLOATING
6666                                ;ZEROS IN UPPER BYTE
6667                                ;177377, 176777, 175777, 173777,
6668                                ;167777, 157777, 137777, 77777
6669                                ;INTO RHDB TO FILL IT
6670                                ;THEN ON READING RHDB THE
6671                                ;FIFTH TIME
6672                                ;RHDB SHOULD HAVE 167777
6673                                ;BUT CONTAINED WHAT IS
6674                                ;GIVEN IN BAD RHDB
6674 021130          77$:
6675
6676
6677 021130 012737 000006 004664      MOV    #6,SILONM      ;SIXTH WORD TO BE READ
6678                                ;CHECK THAT RHDB HAS 157777
6679 021136 012737 157777 001124      MOV    #157777,$GDDAT ;GET GOOD = 0

```

```

6680
6681 021144 017737 162732 001126      MOV      @RHDB,$BDDAT      ;READ RHDB FOR COMPARISON
6682 021152 023737 001124 001126      CMP      $GDDAT,$BDDAT    ;COMPARE EXPECTED
6683                                     ;DATA WITH DATA READ FROM
6684                                     ;RHDB
6685 021160 001401      BEQ      79$              ;BRANCH IF GOOD
6686 021162 104024      ERROR    24
6687                                     ;AFTER SETTING "CLR" BIT #5
6688                                     ;IN RHCS2 TO INIT THE RH
6689                                     ;AND WRITING A PATTERN OF FLOATING
6690                                     ;ZEROS IN UPPER BYTE
6691                                     ;177377, 176777, 175777, 173777,
6692                                     ;167777, 157777, 137777, 77777
6693                                     ;INTO RHDB TO FILL IT
6694                                     ;THEN ON READING RHDB THE
6695                                     ;SIXTH TIME
6696                                     ;RHDB SHOULD HAVE 157777
6697                                     ;BUT CONTAINED WHAT IS
6698                                     ;GIVEN IN BAD RHDB
6699 021164          79$:
6700
6701
6702 021164 012737 000007 004664      MOV      #7,SILONM        ;SEVENTH WORD TO BE READ
6703                                     ;CHECK THAT RHDB HAS 137777
6704 021172 012737 137777 001124      MOV      #137777,$GDDAT   ;GET GOOD = 0
6705
6706 021200 017737 162676 001126      MOV      @RHDB,$BDDAT    ;READ RHDB FOR COMPARISON
6707 021206 023737 001124 001126      CMP      $GDDAT,$BDDAT    ;COMPARE EXPECTED
6708                                     ;DATA WITH DATA READ FROM
6709                                     ;RHDB
6710 021214 001401      BEQ      81$              ;BRANCH IF GOOD
6711 021216 104024      ERROR    24
6712                                     ;AFTER SETTING "CLR" BIT #5
6713                                     ;IN RHCS2 TO INIT THE RH
6714                                     ;AND WRITING A PATTERN OF FLOATING
6715                                     ;ZEROS IN UPPER BYTE
6716                                     ;177377, 176777, 175777, 173777,
6717                                     ;167777, 157777, 137777, 77777
6718                                     ;INTO RHDB TO FILL IT
6719                                     ;THEN ON READING RHDB THE
6720                                     ;SEVENTH TIME
6721                                     ;RHDB SHOULD HAVE 137777
6722                                     ;BUT CONTAINED WHAT IS
6723                                     ;GIVEN IN BAD RHDB
6724 021220          81$:
6725
6726
6727 021220 012737 000010 004664      MOV      #8.,SILONM       ;EIGHTH WORD TO BE READ
6728                                     ;CHECK THAT RHDB HAS 77777
6729 021226 012737 077777 001124      MOV      #77777,$GDDAT   ;GET GOOD = 0
6730
6731 021234 017737 162642 001126      MOV      @RHDB,$BDDAT    ;READ RHDB FOR COMPARISON
6732 021242 023737 001124 001126      CMP      $GDDAT,$BDDAT    ;COMPARE EXPECTED
6733                                     ;DATA WITH DATA READ FROM
6734                                     ;RHDB
6735 021250 001401      BEQ      83$              ;BRANCH IF GOOD

```



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6736 021252 104024          ERROR 24
6737
6738
6739
6740
6741
6742
6743
6744
6745
6746
6747
6748
6749 021254          83$:
6750
6751
6752
6753 021254 012737 000100 001124      ;CHECK THAT RHCS2 HAS IR
6754 021262 053737 005010 001124      MOV #IR,$GDDAT ;GET GOOD = 100
6755
6756 021270 017737 162574 001126      BIS UNIT,$GDDAT ;INCLUDE UNIT NUMBER
6757 021276 023737 001124 001126      MOV @RHCS2,$BDDAT ;READ RHCS2 FOR COMPARISON
6758
6759
6760 021304 001401
6761 021306 104025          BEQ 85$ ;COMPARE EXPECTED
6762
6763
6764
6765
6766
6767
6768
6769
6770
6771
6772
6773
6774
6775
6776 021310          85$:
6777
6778 021310 012737 004200 001124      ;CHECK THAT RHCS1 HAS DVA!RDY
6779
6780 021316 017737 162536 001126      MOV @RHCS1,$BDDAT ;GET GOOD = 4200
6781 021324 023737 001124 001126      CMP $GDDAT,$BDDAT ;READ RHCS1 FOR COMPARISON
6782
6783
6784 021332 001401
6785 021334 104026          BEQ 87$ ;COMPARE EXPECTED
6786
6787
6788
6789
6790
6791

```

```

;AFTER SETTING "CLR" BIT #5
;IN RHCS2 TO INIT THE RH
;AND WRITING A PATTERN OF FLOATING
;ZEROS IN UPPER BYTE
;177377, 176777, 175777, 173777,
;167777, 157777, 137777, 77777
;INTO RHDB TO FILL IT
;THEN ON READING RHDB THE
;EIGHTH TIME
;RHDB SHOULD HAVE 77777
;BUT CONTAINED WHAT IS
;GIVEN IN BAD RHDB

```

```

;AFTER SETTING "CLR" BIT #5
;IN RHCS2 TO INIT THE RH
;AND WRITING A PATTERN OF FLOATING
;ZEROS IN UPPER BYTE
;177377, 176777, 175777, 173777,
;167777, 157777, 137777, 77777
;INTO RHDB TO FILL IT
;THEN ON READING RHDB THE
;TOTAL 8 TIMES
;RHCS2 SHOULD HAVE IR
;=100
;TOGETHER WITH UNIT NUMBER
;BUT CONTAINED WHAT IS
;GIVEN IN BAD RHCS2

```

```

;AFTER SETTING "CLR" BIT #5
;IN RHCS2 TO INIT THE RH
;AND WRITING A PATTERN OF FLOATING
;ZEROS IN UPPER BYTE
;177377, 176777, 175777, 173777,
;167777, 157777, 137777, 77777

```

```

6792 ; INTO RHDB TO FILL IT
6793 ; THEN ON READING RHDB THE
6794 ; TOTAL 8 TIMES
6795 ; RHCS1 SHOULD HAVE DVA!RDY
6796 ; =4200
6797 ; BUT CONTAINED WHAT IS
6798 ; GIVEN IN BAD RHCS1
6799 021336 87$:
6800 ; CHECK THAT RHCS3 HAS 0
6801 021336 012737 000000 001124 MOV #0,$GDDAT ; GET GOOD = 0
6802
6803 021344 017737 162562 001126 MOV @RHCS3,$BDDAT ; READ RHCS3 FOR COMPARISON
6804 021352 023737 001124 001126 CMP $GDDAT,$BDDAT ; COMPARE EXPECTED
6805 ; DATA WITH DATA READ FROM
6806 ; RHCS3
6807 021360 001401 BEQ 99$ ; BRANCH IF GOOD
6808 021362 104027 ERROR 27
6809 ; AFTER SETTING "CLR" BIT #5
6810 ; IN RHCS2 TO INIT THE RH
6811 ; AND WRITING A PATTERN OF FLOATING
6812 ; ZEROS IN UPPER BYTE
6813 ; 177377, 176777, 175777, 173777,
6814 ; 167777, 157777, 137777, 77777
6815 ; INTO RHDB TO FILL IT
6816 ; THEN ON READING RHDB THE
6817 ; TOTAL 8 TIMES
6818 ; RHCS3 SHOULD HAVE 0
6819 ; BUT CONTAINED WHAT IS
6820 ; GIVEN IN BAD RHCS3
6821 021364 89$:
6822 ; CHECK THAT RHBA HAS 0
6823 021364 012737 000000 001124 MOV #0,$GDDAT ; GET GOOD = 0
6824
6825 021372 017737 162466 001126 MOV @RHBA,$BDDAT ; READ RHBA FOR COMPARISON
6826 021400 023737 001124 001126 CMP $GDDAT,$BDDAT ; COMPARE EXPECTED
6827 ; DATA WITH DATA READ FROM
6828 ; RHBA
6829 021406 001401 BEQ 91$ ; BRANCH IF GOOD
6830 021410 104030 ERROR 30
6831 ; AFTER SETTING "CLR" BI #5
6832 ; IN RHCS2 TO INIT THE RH
6833 ; AND WRITING A PATTERN OF FLOATING
6834 ; ZEROS IN UPPER BYTE
6835 ; 177377, 176777, 175777, 173777,
6836 ; 167777, 157777, 137777, 77777
6837 ; INTO RHDB TO FILL IT
6838 ; THEN ON READING RHDB THE
6839 ; TOTAL 8 TIMES
6840 ; RHBA SHOULD HAVE 0
6841 ; BUT CONTAINED WHAT IS
6842 ; GIVEN IN BAD RHBA
6843 021412 91$:
6844 ; CHECK THAT RHBAE HAS 0
6845 021412 012737 000000 001124 MOV #0,$GDDAT ; GET GOOD = 0
6846
6847 021420 017737 162504 001126 MOV @RHBAE,$BDDAT ; READ RHBAE FOR COMPARISON

```


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6956
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6958
6959

```

:* CHECK RHCS1 TO HAVE RDY
:* CHECK RHCS2 TO HAVE ONLY IR AND UNIT NUMBER
:* CHECK RHCS3, RHBA, RHBAE, RHWC TO HAVE APPROPRIATE
:* VALUES.
*****
TST2: SCOPE
MOV #STACK, SP ;RESET STACK
MOV #21, #TSTNM ;SAVE TEST NUMBER

JSR PC, #CLDISK ;GIVE RH INITIALIZE
;SETUP UNIT NUMBER
;CLEAR RHWC AND FUNCTION BITS IN RHCS1

;CHECK THAT RHER1 HAS 0
MOV #0, #SGDDAT ;GET GOOD = 0

MOV #RHER1, #SBDDAT ;READ RHER1 FOR COMPARISON
CMP #SGDDAT, #SBDDAT ;COMPARE EXPECTED
;DATA WITH DATA READ FROM
;RHER1
;BRANCH IF GOOD

;AFTER SETTING "CLR" BIT #15
;IN RHCS2 TO INIT THE RH
;AND HAVING DONE NOTHING ELSE
;RHER1 SHOULD HAVE 0
;BUT CONTAINED WHAT IS
;GIVEN IN BAD RHER1

655:

;TO INVERT THE PARITY CHECKING ON THE CONTROL BUS
;"PAT" BIT #4 IN RHCS2 IS SET
BIS #PAT, #RHCS2 ;SET PAT = 20 IN RHCS2

;CHECK THAT RHER1 HAS 0
MOV #0, #SGDDAT ;GET GOOD = 0

MOV #RHER1, #SBDDAT ;READ RHER1 FOR COMPARISON
CMP #SGDDAT, #SBDDAT ;COMPARE EXPECTED
;DATA WITH DATA READ FROM
;RHER1
;BRANCH IF GOOD

;AFTER CLEARING THE RH AND
;DEVICE BY AN RH CLEAR
;AND READING RHER1 WITH
;WRONG PARITY CHECKING
;RHER1 SHOULD HAVE 0
;BUT CONTAINED WHAT IS
;GIVEN IN BAD RHER1

675:

;HAVING READ RHER1 WITH WRONG PARITY BEING
;CHECKED MCPE - BIT #13 AND SC BIT #15 IN RHCS1 SHOULD BE SET

```

021466 000004
021470 012706 001000
021474 012737 000021 004656

021502 004737 040312

021506 012737 000000 001124

021514 017737 162354 001126
021522 023737 001124 001126

021530 001401
021532 104033

021534

021534 052777 000020 162326

021542 012737 000000 001124

021550 017737 162320 001126
021556 023737 001124 001126

021564 001401
021566 104034

021570

```

6960
6961
6962 021570 012737 004200 001124      ;CHECK THAT RHCSI HAS 4200
        MOV      #4200,$GDDAT      ;GET GOOD = 124200
6963
6964 021576 017737 162256 001126      MOV      @RHCSI,$BDDAT      ;READ RHCSI FOR COMPARISON
6965 021604 023737 001124 001126      CMP      $GDDAT,$BDDAT      ;COMPARE EXPECTED
6966                                     ;DATA WITH DATA READ FROM
6967                                     ;RHCSI
6968 021612 001401      BEQ      699      ;BRANCH IF GOOD
6969 021614 104035      ERROR    35
6970                                     ;AFTER CLEARING THE RH AND
6971                                     ;DEVICE BY AN RH CLEAR
6972                                     ;AND READING RHERI WITH
6973                                     ;WRONG PARITY CHECKING
6974                                     ;RHCSI SHOULD HAVE 4200
6975                                     ;=124200
6976                                     ;BUT CONTAINED WHAT IS
6977                                     ;GIVEN IN BAD RHCSI
6978 021616                                     699:
6979
6980                                     ;WRITE A ONE INTO TRE (RHCSI - BIT #14)
6981                                     ;THIS SHOULD NOT CLEAR MOPE OR SC
6982 021616 012777 040000 162234      MOV      @TRE,@RHCSI      ;WRITE "1" INTO TRE IN RHCSI
6983
6984
6985
6986                                     ;CHECK THAT RHCSI HAS 104200
6987 021624 012737 104200 001124      MOV      #104200,$GDDAT      ;GET GOOD = 124200
6988
6989 021632 017737 162222 001126      MOV      @RHCSI,$BDDAT      ;READ RHCSI FOR COMPARISON
6990 021640 023737 001124 001126      CMP      $GDDAT,$BDDAT      ;COMPARE EXPECTED
6991                                     ;DATA WITH DATA READ FROM
6992                                     ;RHCSI
6993 021646 001401      BEQ      715      ;BRANCH IF GOOD
6994 021650 104036      ERROR    36
6995                                     ;AFTER CLEARING THE RH AND
6996                                     ;DEVICE REGISTERS BY AN
6997                                     ;RH CLEAR
6998                                     ;RHERI WAS CHECKED TO HAVE
6999                                     ;ZEROS
7000                                     ;WRONG PARITY CHECKING WAS
7001                                     ;SET BY "PAT" BIT IN RHCS2
7002                                     ;RHERI WAS READ TO SET
7003                                     ;MOPE AND SC IN RHCSI
7004                                     ;ON WRITING "1" INTO TRE
7005                                     ;RHCSI SHOULD HAVE 104200
7006                                     ;=124200
7007                                     ;BUT CONTAINED WHAT IS
7008                                     ;GIVEN IN BAD RHCSI
7009 021652                                     715:
7010
7011                                     ;NOW ERRORS WILL BE CLEARED BY RH CLEAR
7012                                     ;SET "CLR" BIT #5 IN RHCS2
7013 021652 012777 000340 162210      MOV      #CLR,@RHCS2      ;CLEAR BY RH INIT
7014 021660 013777 005010 162202      MOV      UNIT,@RHCS2      ;REINSTATE UNIT NUMBER
7015

```

```

7016
7017
7018 021666 012737 004200 001124      ;CHECK THAT RHCSI HAS DVA!RDY
MOV      #DVA!RDY,$GDDAT ;GET GOOD = 4200
7019
7020 021674 017737 162150 001126      MOV      @RHCSI,$BDDAT ;READ RHCSI FOR COMPARISON
7021 021702 023737 001124 001126      CMP      $GDDAT,$BDDAT ;COMPARE EXPECTED
;DATA WITH DATA READ FROM
;RHCSI
7022
7023
7024 021710 001401      BEQ      73$           ;BRANCH IF GOOD
7025 021712 104037      ERROR    37
7026
7027
7028
7029
7030
7031
7032
7033
7034
7035
7036
7037
7038
7039
7040
7041 021714      73$:
7042
7043 021714 012737 000100 001124      ;CHECK THAT RHCS2 HAS IR
MOV      #IR,$GDDAT ;GET GOOD = 100
7044 021722 053737 005010 001124      BIS      UNIT,$GDDAT ;INCLUDE UNIT NUMBER
7045
7046 021730 017737 162134 001126      MOV      @RHCS2,$BDDAT ;READ RHCS2 FOR COMPARISON
7047 021736 023737 001124 001126      CMP      $GDDAT,$BDDAT ;COMPARE EXPECTED
;DATA WITH DATA READ FROM
;RHCS2
7048
7049
7050 021744 001401      BEQ      75$           ;BRANCH IF GOOD
7051 021746 104040      ERROR    40
7052
7053
7054
7055
7056
7057
7058
7059
7060
7061
7062
7063
7064
7065
7066
7067
7068 021750      75$:
7069
7070 021750 012737 000000 001124      ;CHECK THAT RHCS3 HAS 0
MOV      #0,$GDDAT ;GET GOOD = 0
7071

```



7072	021756	017737	162150	001126	MOV	DRHCS3,\$BDDAT	; READ RHCS3 FOR COMPARISON
7073	021764	023737	001124	001126	CMP	\$GDDAT,\$BDDAT	; COMPARE EXPECTED
7074							; DATA WITH DATA READ FROM
7075							; RHCS3
7076	021772	001401			BEQ	77\$; BRANCH IF GOOD
7077	021774	104041			ERROR	41	
7078							; AFTER CLEARING THE RH AND
7079							; DEVICE REGISTERS BY AN
7080							; RH CLEAR
7081							; RHER1 WAS CHECKED TO HAVE
7082							; ZEROS
7083							; WRONG PARITY CHECKING WAS
7084							; SET BY "PAT" BIT IN RHCS2
7085							; RHER1 WAS READ TO SET
7086							; MCPE AND SC IN RHCS1
7087							; ON WRITING "1" INTO TRE
7088							; THEN SETTING "CLR" IN RHCS1
7089							; RHCS3 SHOULD HAVE 0
7090							; BUT CONTAINED WHAT IS
7091							; GIVEN IN BAD RHCS3
7092	021776						77\$:
7093							; CHECK THAT RHBA HAS 0
7094	021776	012737	000000	001124	MOV	#0,\$GDDAT	; GET GOOD = 0
7095							
7096	022004	017737	162054	001126	MOV	DRHBA,\$BDDAT	; READ RHBA FOR COMPARISON
7097	022012	023737	001124	001126	CMP	\$GDDAT,\$BDDAT	; COMPARE EXPECTED
7098							; DATA WITH DATA READ FROM
7099							; RHBA
7100	022020	001401			BEQ	79\$; BRANCH IF GOOD
7101	022022	104042			ERROR	42	
7102							; AFTER CLEARING THE RH AND
7103							; DEVICE REGISTERS BY AN
7104							; RH CLEAR
7105							; RHER1 WAS CHECKED TO HAVE
7106							; ZEROS
7107							; WRONG PARITY CHECKING WAS
7108							; SET BY "PAT" BIT IN RHCS2
7109							; RHER1 WAS READ TO SET
7110							; MCPE AND SC IN RHCS1
7111							; ON WRITING "1" INTO TRE
7112							; THEN SETTING "CLR" IN RHCS1
7113							; RHBA SHOULD HAVE 0
7114							; BUT CONTAINED WHAT IS
7115							; GIVEN IN BAD RHBA
7116	022024						79\$:
7117							; CHECK THAT RHBAE HAS 0
7118	022024	012737	000000	001124	MOV	#0,\$GDDAT	; GET GOOD = 0
7119							
7120	022032	017737	162072	001126	MOV	DRHBAE,\$BDDAT	; READ RHBAE FOR COMPARISON
7121	022040	023737	001124	001126	CMP	\$GDDAT,\$BDDAT	; COMPARE EXPECTED
7122							; DATA WITH DATA READ FROM
7123							; RHBAE
7124	022046	001401			BEQ	81\$; BRANCH IF GOOD
7125	022050	104043			ERROR	43	
7126							; AFTER CLEARING THE RH AND
7127							; DEVICE REGISTERS BY AN

```

7128 ;RH CLEAR
7129 ;RHER1 WAS CHECKED TO HAVE
7130 ;ZEROS
7131 ;WRONG PARITY CHECKING WAS
7132 ;SET BY "PAT" BIT IN RHCS2
7133 ;RHER1 WAS READ TO SET
7134 ;MCPE AND SC IN RHCS1
7135 ;ON WRITING "1" INTO TRE
7136 ;THEN SETTING "CLR" IN RHCS1
7137 ;RHBAE SHOULD HAVE 0
7138 ;BUT CONTAINED WHAT IS
7139 ;GIVEN IN BAD RHBAE

```

```

7140 022052      81$:
7141
7142 022052 012737 000000 001124  :CHECK THAT RHWC HAS 0
7143      MOV      #0,$GDDAT
7144 022060 017737 161776 001126  MOV      @RHWC,$BDDAT
7145 022066 023737 001124 001126  CMP      $GDDAT,$BDDAT
7146
7147
7148 022074 001401      BEQ      83$
7149 022076 104044      ERROR   44

```

```

;GET GOOD = 0
;READ RHWC FOR COMPARISON
;COMPARE EXPECTED
;DATA WITH DATA READ FROM
;RHWC
;BRANCH IF GOOD

```

```

7150 ;AFTER CLEARING THE RH AND
7151 ;DEVICE REGISTERS BY AN
7152 ;RH CLEAR
7153 ;RHER1 WAS CHECKED TO HAVE
7154 ;ZEROS
7155 ;WRONG PARITY CHECKING WAS
7156 ;SET BY "PAT" BIT IN RHCS2
7157 ;RHER1 WAS READ TO SET
7158 ;MCPE AND SC IN RHCS1
7159 ;ON WRITING "1" INTO TRE
7160 ;THEN SETTING "CLR" IN RHCS1
7161 ;RHWC SHOULD HAVE 0
7162 ;BUT CONTAINED WHAT IS
7163 ;GIVEN IN BAD RHWC

```

```

7164 022100      83$:
7165
7166

```

```

;*****
;TEST 22      RHCS1 - MCPE BIT #13 (PARITY LINE = 1)

```

```

7170 ;*      AN RH CLEAR (SET BIT #5 IN RHCS2) IS GIVEN TO CLEAR
7171 ;*      ALL DEVICE REGISTERS
7172 ;*      WRITE A ONE INTO DISK ADDRESS REGISTER (RHDA)
7173 ;*      (IN TAPE DRIVES CALLED REGISTER)
7174 ;*      SET "PAT" (RHCS2 BIT #4) THIS WILL INVERT THE PARITY CHECKING
7175 ;*      READ RHDA AND COMPARE IT HAS ONE IN IT
7176 ;*      THIS READING SHOULD SET SC, MCPE IN RHCS1
7177 ;*      CHECK RHCS1 TO HAVE ONLY RDY SET
7178 ;*      CHECK RHCS2, RHCS3, RHBA, RHBAE, RHWC TO HAVE APPROPRIATE
7179 ;*      VALUES

```

```

7180 ;*****
7181 022100 000004      ST22: SCOPE
7182 022102 012706 001000      MOV      #STACK,SP      ;RESET STACK
7183 022106 012737 000022 004656      MOV      #22,@STNM      ;SAVE TEST NUMBER

```



```

7184
7185 022114 004737 040312 JSR PC, @CLDISK ;GIVE RH INITIALIZE
7186 ;SETUP UNIT NUMBER
7187 ;CLEAR RHWC AND FUNCTION BITS IN RHCS1
7188
7189 ;WRITE A ONE INTO RHDA - DISK ADDRESS REGISTER
7190 ;THIS REGISTER IN TAPE DRIVES IS CALLED "RHFC FRAME COUNT"
7191 ;AFTER WRITING THIS "1" THEN ON READING THIS REGISTER
7192 ;THE CONTROL PARITY LINE WILL HAVE ZERO
7193 022120 012777 000001 161740 MOV #BIT0, @RHDA ;WRITE "1" INTO RHDA
7194 ;IN TAPE DRIVES CALLED RHFC
7195
7196 ;INVERT THE PARITY TO BE CHECKED BY SETTING "PAT" BIT #4 IN RHCS2
7197 022126 052777 000020 161734 BIS #PAT, @RHCS2 ;SET PAT IN RHCS2
7198
7199
7200 ;CHECK THAT RHDA HAS BIT0
7201 022134 012737 000001 001124 MOV #BIT0, $GDDAT ;GET GOOD = 1
7202
7203 022142 017737 161720 001126 MOV @RHDA, $BDDAT ;READ RHDA FOR COMPARISON
7204 022150 023737 001124 001126 CMP $GDDAT, $BDDAT ;COMPARE EXPECTED
7205 ;DATA WITH DATA READ FROM
7206 ;RHDA
7207 022156 001401 BEQ 65$ ;BRANCH IF GOOD
7208 022160 104045 ERROR 45
7209 ;AFTER AN RH CLEAR "1"
7210 ;WAS WRITTEN INTO ADDRESS REGISTER
7211 ;(FRAME COUNT IN TAPE)
7212 ;PAT IN RHCS2 WAS SET
7213 ;THEN THE ADDRESS REGISTER
7214 ;WAS READ BACK
7215 ;RHDA SHOULD HAVE BIT0
7216 ;=1
7217 ;BUT CONTAINED WHAT IS
7218 ;GIVEN IN BAD RHDA
7219 022162 65$:
7220
7221 ;CHECK THAT RHCS1 HAS 4200
7222 022162 012737 004200 001124 MOV #4200, $GDDAT ;GET GOOD = .24000
7223
7224 022170 017737 161664 001126 MOV @RHCS1, $BDDAT ;READ RHCS1 FOR COMPARISON
7225 022176 023737 001124 001126 CMP $GDDAT, $BDDAT ;COMPARE EXPECTED
7226 ;DATA WITH DATA READ FROM
7227 ;RHCS1
7228 022204 001401 BEQ 67$ ;BRANCH IF GOOD
7229 022206 104046 ERROR 46
7230 ;AFTER AN RH CLEAR "1"
7231 ;WAS WRITTEN INTO ADDRESS REGISTER
7232 ;(FRAME COUNT IN TAPE)
7233 ;PAT IN RHCS2 WAS SET
7234 ;THEN THE ADDRESS REGISTER
7235 ;WAS READ BACK
7236 ;RHCS1 SHOULD HAVE 4200
7237 ;=124000
7238 ;BUT CONTAINED WHAT IS
7239 ;GIVEN IN BAD RHCS1

```

```

7240 022210          67$:
7241          ;CHECK THAT RHCS2 HAS IR!PAT
7242 022210 012737 000120 001124  MOV    #IR!PAT,$GDDAT ;GET GOOD = 120
7243 022216 053737 005010 001124  BIS    UNIT,$GDDAT    ;INCLUDE UNIT NUMBER
7244
7245 022224 017737 161640 001126  MOV    @RHCS2,$BDDAT  ;READ RHCS2 FOR COMPARISON
7246 022232 023737 001124 001126  CMP    $GDDAT,$BDDAT ;COMPARE EXPECTED
7247          ;DATA WITH DATA READ FROM
7248          ;RHCS2
7249 022240 001401          BEQ    69$             ;BRANCH IF GOOD
7250 022242 104047          ERROR  47
7251          ;AFTER AN RH CLEAR "1"
7252          ;WAS WRITTEN INTO ADDRESS REGISTER
7253          ;(FRAME COUNT IN TAPE)
7254          ;PAT IN RHCS2 WAS SET
7255          ;THEN THE ADDRESS REGISTER
7256          ;WAS READ BACK
7257          ;RHCS2 SHOULD HAVE IR!PAT
7258          ;=120
7259          ;TOGETHER WITH UNIT NUMBER
7260          ;BUT CONTAINED WHAT IS
7261          ;GIVEN IN BAD RHCS2
7262 022244          69$:
7263          ;CHECK THAT RHCS3 HAS 0
7264 022244 012737 000000 001124  MOV    #0,$GDDAT    ;GET GOOD = 0
7265
7266 022252 017737 161654 001126  MOV    @RHCS3,$BDDAT ;READ RHCS3 FOR COMPARISON
7267 022260 023737 001124 001126  CMP    $GDDAT,$BDDAT ;COMPARE EXPECTED
7268          ;DATA WITH DATA READ FROM
7269          ;RHCS3
7270 022266 001401          BEQ    71$             ;BRANCH IF GOOD
7271 022270 104050          ERROR  50
7272          ;AFTER AN RH CLEAR "1"
7273          ;WAS WRITTEN INTO ADDRESS REGISTER
7274          ;(FRAME COUNT IN TAPE)
7275          ;PAT IN RHCS2 WAS SET
7276          ;THEN THE ADDRESS REGISTER
7277          ;WAS READ BACK
7278          ;RHCS3 SHOULD HAVE 0
7279          ;BUT CONTAINED WHAT IS
7280          ;GIVEN IN BAD RHCS3
7281 022272          71$:
7282          ;CHECK THAT RHBA HAS 0
7283 022272 012737 000000 001124  MOV    #0,$GDDAT    ;GET GOOD = 0
7284
7285 022300 017737 161560 001126  MOV    @RHBA,$BDDAT ;READ RHBA FOR COMPARISON
7286 022306 023737 001124 001126  CMP    $GDDAT,$BDDAT ;COMPARE EXPECTED
7287          ;DATA WITH DATA READ FROM
7288          ;RHBA
7289 022314 001401          BEQ    73$             ;BRANCH IF GOOD
7290 022316 104051          ERROR  51
7291          ;AFTER AN RH CLEAR "1"
7292          ;WAS WRITTEN INTO ADDRESS REGI. ER
7293          ;(FRAME COUNT IN TAPE)
7294          ;PAT IN RHCS2 WAS SET
7295          ;THEN THE ADDRESS REGISTER

```


K11

MAINDEC-11-DERHAB-A
DERHAB.SRC T23

MACY11 27(732) 22-SEP-76 15:11 PAGE 142
TEST DUPLICATED A16 (RHCS1 BIT #8)

```

7352          ;*      READ RHCS1 TO CONTAIN ONLY RDY (BIT #8 IN RHCS1 IS ZERO)
7353          ;*
7354          ;*
7355          ;* *****
7356 022374 000004          †ST23: SCOPE
7357 022376 012706 001000  MOV      #STACK,SP      ;RESET STACK
7358 022402 012737 000023 004656  MOV      #23,#STNM      ;SAVE TEST NUMBER
7359
7360 022410 004737 040312          JSR      PC,#CLDISK      ;GIVE RH INITIALIZE
7361                                     ;SETUP UNIT NUBER
7362                                     ;CLEAR RHWG AND FUNCTION BITS IN RHCS1
7363
7364          ;WRITE "1" INTO A16 IN RHCS1
7365 022414 012777 000400 161436  MOV      #A16,#RHCS1    ;MOVE 400 INTO RHCS1 .
7366
7367
7368
7369          ;CHECK THAT RHCS1 HAS A16!DVA!RDY
7370 022422 012737 004600 001124  MOV      #A16!DVA!RDY,$GDDAT ;GET GOOD = 4600
7371
7372 022430 017737 161424 001126  MOV      @RHCS1,$BDDAT    ;READ RHCS1 FOR COMPARISON
7373 022436 023737 001124 001126  CMP      $GDDAT,$BDDAT    ;COMPARE EXPECTED
7374                                     ;DATA WITH DATA READ FROM
7375                                     ;RHCS1
7376 022444 001401          BEQ      65$              ;BRANCH IF GOOD
7377 022446 104054          ERROR    54
7378                                     ;AFTER SETTING "CLR" BIT #5
7379                                     ;IN RHCS2 TO INIT THE RH
7380                                     ;A16 WAS WRITTEN INTO RHCS1
7381                                     ;RHCS1 WAS READ
7382                                     ;RHCS1 SHOULD HAVE A16!DVA!RDY
7383                                     ;=4600
7384                                     ;BUT CONTAINED WHAT IS
7385                                     ;GIVEN IN BAD RHCS1
7386 022450          65$:
7387
7388
7389          ;CHECK THAT RHBAE HAS BIT0
7390 022450 012737 000001 001124  MOV      #BIT0,$GDDAT    ;GET GOOD = 1
7391
7392 022456 017737 161446 001126  MOV      @RHBAE,$BDDAT    ;READ RHBAE FOR COMPARISON
7393 022464 023737 001124 001126  CMP      $GDDAT,$BDDAT    ;COMPARE EXPECTED
7394                                     ;DATA WITH DATA READ FROM
7395                                     ;RHBAE
7396 022472 001401          BEQ      67$              ;BRANCH IF GOOD
7397 022474 104055          ERROR    55
7398                                     ;AFTER SETTING "CLR" BIT #5
7399                                     ;IN RHCS2 TO INIT THE RH
7400                                     ;A16 WAS WRITTEN INTO RHCS1
7401                                     ;RHCS1 WAS READ
7402                                     ;THEN RHBAE WAS READ
7403                                     ;RHBAE SHOULD HAVE BIT0
7404                                     ;=1
7405                                     ;BUT CONTAINED WHAT IS
7406                                     ;GIVEN IN BAD RHBAE
7407 022476          67$:

```

```

7408
7409
7410 022476 005077 161426      ;NOW MOVE 0 INTO RHBAE (WRITING 0 INTO RHBAE BIT #0)
7411      CLR      @RHBAE      ;WRITE ZERO INTO RHBAE
7412
7413
7414 022502 012737 000000 001124 ;CHECK THAT RHBAE HAS 0
7415      MOV      #0,$GDDAT ;GET GOOD = 0
7416 022510 017737 161414 001126 ;READ RHBAE FOR COMPARISON
7417 022516 023737 001124 001126 ;COMPARE EXPECTED
7418      MOV      @RHBAE,$BDDAT ;DATA WITH DATA READ FROM
7419      CMP      $GDDAT,$BDDAT ;RHBAE
7420 022524 001401      BEQ      69$ ;BRANCH IF GOOD
7421 022526 104056      ERROR    56
7422
7423      ;AFTER SETTING "CLR" BIT #5
7424      ;IN RHCS2 TO INIT THE RH
7425      ;A16 WAS WRITTEN INTO RHCS1
7426      ;RHCS1 WAS READ
7427      ;RHBAE WAS READ
7428      ;THEN ZERO WAS WRITTEN
7429      ;INTO RHBAE
7430      ;ON READING RHBAE
7431      ;RHBAE SHOULD HAVE 0
7432      ;BUT CONTAINED WHAT IS
7433      ;GIVEN IN BAD RHBAE

```

69\$:

```

7434
7435
7436      ;CHECK THAT RHCS1 HAS DVA!RDY
7437 022530 012737 004200 001124 ;DVA!RDY,$GDDAT ;GET GOOD = 4200
7438      MOV      #DVA!RDY,$GDDAT
7439 022536 017737 161316 001126 ;READ RHCS1 FOR COMPARISON
7440 022544 023737 001124 001126 ;COMPARE EXPECTED
7441      MOV      @RHCS1,$BDDAT ;DATA WITH DATA READ FROM
7442      CMP      $GDDAT,$BDDAT ;RHCS1
7443 022552 001401      BEQ      71$ ;BRANCH IF GOOD
7444 022554 104057      ERROR    57
7445
7446      ;AFTER SETTING "CLR" BIT #5
7447      ;IN RHCS2 TO INIT THE RH
7448      ;A16 WAS WRITTEN INTO RHCS1
7449      ;RHCS1 WAS READ
7450      ;RHBAE WAS READ
7451      ;THEN ZERO WAS WRITTEN
7452      ;INTO RHBAE
7453      ;RHBAE WAS READ
7454      ;ON READING RHCS1
7455      ;RHCS1 SHOULD HAVE DVA!RDY
7456      ;=4200
7457      ;BUT CONTAINED WHAT IS
7458      ;GIVEN IN BAD RHCS1

```

71\$:

022556

```

7461 ;*****
7462 ;*TEST 24      TEST DUPLICATED A17 (RHCS1 BIT #9)
7463

```



```

7520 ;A17 WAS WRITTEN INTO RHCS1
7521 ;RHCS1 WAS READ
7522 ;THEN RHBAE WAS READ
7523 ;RHBAE SHOULD HAVE BIT1
7524 ;=2
7525 ;BUT CONTAINED WHAT IS
7526 ;GIVEN IN BAD RHBAE
7527 022660          67$:
7528
7529 ;NOW MOVE 0 INTO RHBAE (WRITING 0 INTO RHBAE BIT #0)
7530 022660 005077 161244 CLR @RHBAE ;WRITE ZERO INTO RHBAE
7531
7532
7533 ;CHECK THAT RHBAE HAS 0
7534 022664 012737 000000 001124 MOV #0,$GDDAT ;GET GOOD = 0
7535
7536 022672 017737 161232 001126 MOV @RHBAE,$BDDAT ;READ RHBAE FOR COMPARISON
7537 022700 023737 001124 001126 CMP $GDDAT,$BDDAT ;COMPARE EXPECTED
7538 ;DATA WITH DATA READ FROM
7539 ;RHBAE
7540 022706 001401 BEQ 69$ ;BRANCH IF GOOD
7541 022710 104062 ERROR 62
7542
7543 ;AFTER SETTING "CLR" BIT #5
7544 ;IN RHCS2 TO INIT THE RH
7545 ;A17 WAS WRITTEN INTO RHCS1
7546 ;RHCS1 WAS READ
7547 ;RHBAE WAS READ
7548 ;THEN ZERO WAS WRITTEN
7549 ;INTO RHBAE
7550 ;ON READING RHBAE
7551 ;RHBAE SHOULD HAVE 0
7552 ;BUT CONTAINED WHAT IS
7553 ;GIVEN IN BAD RHBAE
7553 022712          69$:
7554
7555
7556 ;CHECK THAT RHCS1 HAS DVA!RDY
7557 022712 012737 004200 001124 MOV #DVA!RDY,$GDDAT ;GET GOOD = 4200
7558
7559 022720 017737 161134 001126 MOV @RHCS1,$BDDAT ;READ RHCS1 FOR COMPARISON
7560 022726 023737 001124 001126 CMP $GDDAT,$BDDAT ;COMPARE EXPECTED
7561 ;DATA WITH DATA READ FROM
7562 ;RHCS1
7563 022734 001401 BEQ 71$ ;BRANCH IF GOOD
7564 022736 104063 ERROR 63
7565
7566 ;AFTER SETTING "CLR" BIT #5
7567 ;IN RHCS2 TO INIT THE RH
7568 ;A17 WAS WRITTEN INTO RHCS1
7569 ;RHCS1 WAS READ
7570 ;RHBAE WAS READ
7571 ;THEN ZERO WAS WRITTEN
7572 ;INTO RHBAE
7573 ;RHBAE WAS READ
7574 ;ON READING RHCS1
7575 ;RHCS1 SHOULD HAVE DVA!RDY
;=4200

```



:BUT CONTAINED WHAT IS
:GIVEN IN BAD RHCS1

022740

718:

:TEST 25 TEST DUPLICATED IE (RHCS1 BIT #5)

: CLEAR THE SUBSYSTEM BY A CONTROLLER CLEAR (RHCS2 BIT #5)
: MOVE 100 (ONE INTO IE) IN RHCS1
: READ RHCS1 TO CONTAIN 300 (BIT #6 AND RDY)
: READ RHCS3 TO CONTAIN "100" (BIT #6 HIGH)

: MOVE 0 INTO RHCS3 (WRITING 0 INTO RHCS3 BIT #6)
: READ RHCS3 TO CONTAIN 100
: READ RHCS1 TO CONTAIN ONLY RDY (BIT #6 IN RHCS1 IS ZERO)

:TEST25: SCOPE

022740 000004
022742 012706 001000
022746 012737 000025 004656

MOV #STACK, SP :RESET STACK
MOV #25, #STNM :SAVE TEST NUMBER

022754 004737 040312

JSR PC, #CLDISK :GIVE RH INITIALIZE
:SETUP UNIT NUMBER
:CLEAR RHWC AND FUNCTION BITS IN RHCS1

022760 012777 000100 161072

:WRITE "1" INTO IE IN RHCS1
MOV #IE, #RHCS1 :MOVE 100 INTO RHCS1

022766 012737 004300 001124

:CHECK THAT RHCS1 HAS IE!DVA!RDY
MOV #IE!DVA!RDY, #GDDAT ;GET GOOD = 4300

022774 017737 161060 001126
023002 023737 001124 001126

MOV #RHCS1, #BDDAT :READ RHCS1 FOR COMPARISON
CMP #GDDAT, #BDDAT :COMPARE EXPECTED
:DATA WITH DATA READ FROM
:RHCS1

023010 001401
023012 104064

BEQ 655 :BRANCH IF GOOD
ERROR 64

023014

655:

:AFTER SETTING "CLR" BIT #5
:IN RHCS2 TO INIT THE RH
:IE WAS WRITTEN INTO RHCS1
:RHCS1 WAS READ
:RHCS1 SHOULD HAVE IE!DVA!RDY
:=4300
:BUT CONTAINED WHAT IS
:GIVEN IN BAD RHCS1

023014 012737 000100 001124

:CHECK THAT RHCS3 HAS IE
MOV #IE, #GDDAT :GET GOOD = 1

7631


```

7632 023022 017737 161104 001126      MOV      @RHCS3,$BDDAT      ;READ RHCS3 FOR COMPARISON
7633 023030 023737 001124 001126      CMP      $GDDAT,$BDDAT    ;COMPARE EXPECTED
7634                                     ;DATA WITH DATA READ FROM
7635                                     ;RHCS3
7636 023036 001401      BEQ      67$              ;BRANCH IF GOOD
7637 023040 104065      ERROR    65
7638                                     ;AFTER SETTING "CLR" BIT #5
7639                                     ;IN RHCS2 TO INIT THE RH
7640                                     ;IE WAS WRITTEN INTO RHCS1
7641                                     ;RHCS1 WAS READ
7642                                     ;THEN RHCS3 WAS READ
7643                                     ;RHCS3 SHOULD HAVE IE
7644                                     ;=1
7645                                     ;BUT CONTAINED WHAT IS
7646                                     ;GIVEN IN BAD RHCS3
7647 023042                                     67$:
7648
7649                                     ;NOW MOVE 0 INTO RHCS3 (WRITING 0 INTO RHCS3 BIT #1)
7650 023042 005077 161064      CLR      @RHCS3          ;WRITE ZERO INTO RHCS3
7651
7652                                     ;CHECK THAT RHCS3 HAS 0
7653 023046 012737 000000 001124      MOV      #0,$GDDAT      ;GET GOOD = 0
7654
7655 023054 017737 161052 001126      MOV      @RHCS3,$BDDAT  ;READ RHCS3 FOR COMPARISON
7656 023062 023737 001124 001126      CMP      $GDDAT,$BDDAT  ;COMPARE EXPECTED
7657                                     ;DATA WITH DATA READ FROM
7658                                     ;RHCS3
7659                                     ;BRANCH IF GOOD
7660 023070 001401      BEQ      69$              ;BRANCH IF GOOD
7661 023072 104066      ERROR    66
7662                                     ;AFTER SETTING "CLR" BIT #5
7663                                     ;IN RHCS2 TO INIT THE RH
7664                                     ;IE WAS WRITTEN INTO RHCS1
7665                                     ;RHCS1 WAS READ
7666                                     ;RHCS3 WAS READ
7667                                     ;THEN ZERO WAS WRITTEN
7668                                     ;INTO RHCS3
7669                                     ;ON READING RHCS3
7670                                     ;RHCS3 SHOULD HAVE 0
7671                                     ;BUT CONTAINED WHAT IS
7672                                     ;GIVEN IN BAD RHCS3
7673 023074                                     69$:
7674
7675                                     ;CHECK THAT RHCS1 HAS DVA!RDY
7676 023074 012737 004200 001124      MOV      #DVA!RDY,$GDDAT ;GET GOOD = 4200
7677
7678 023102 017737 160752 001126      MOV      @RHCS1,$BDDAT  ;READ RHCS1 FOR COMPARISON
7679 023110 023737 001124 001126      CMP      $GDDAT,$BDDAT  ;COMPARE EXPECTED
7680                                     ;DATA WITH DATA READ FROM
7681                                     ;RHCS1
7682                                     ;BRANCH IF GOOD
7683 023116 001401      BEQ      71$              ;BRANCH IF GOOD
7684 023120 104067      ERROR    67
7685                                     ;AFTER SETTING "CLR" BIT #5
7686                                     ;IN RHCS2 TO INIT THE RH
7687                                     ;IE WAS WRITTEN INTO RHCS1

```

:RHCSI WAS READ
:RHCS3 WAS READ
:THEN ZERO WAS WRITTEN
:INTO RHCS3
:RHCS3 WAS READ
:ON READING RHCSI
:RHCSI SHOULD HAVE DVA!RDY
:=4200
:BUT CONTAINED WHAT IS
:GIVEN IN BAD RHCSI

023122

715:

::*****
:*TEST 26 RHCSI PROGRAM ERROR (PGE BIT #10)

::* CLEAR THE SUBSYSTEM BY A CONTROLLER CLEAR (RHCS2 BIT #5)
:* SET UP FOR A 10 WORD WRITE
:* SET "GO" (RHCSI BIT #0) TWICE IN TWO SUCCESSIVE
:* INSTRUCTIONS
:* THIS SHOULD SET SC AND TRE IN RHCSI
:* AND PGE SHOULD BE SET IN RHCS2
:* RHCS3, ARE CHECKED
:* THE NUMBER OF WORDS ARE LARGE ENOUGH SO THAT AFTER
:* ONE "BIS" INSTRUCTION TO SET "GO" IN RHCSI
:* THERE IS SUFFICIENT TIME TO GIVE ANOTHER "BIS" INSTRUCTION
:* TO SET "GO" BEFORE THE FIRST "GO" HAS TIME TO COMPLETE
:*

::*****
*ST26: SCOPE

7700
7701
7702
7703
7704
7705
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7708
7709
7710
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7712
7713
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7743

023122 000004
023124 012706 001000
023130 012737 000026 004656
023136 004737 040312
023142 012777 177766 160712
023150 012777 004210 160706
023156 012737 177777 004644
023164 004077 161450
023170 004154
023172 052777 000001 160660
023200 052777 000001 160652
023206 012737 144061 001124
023214 017737 160640 001126
023222 023737 001124 001126

MOV #STACK,SP :RESET STACK
MOV #26,#STNM :SAVE TEST NUMBER
JSR PC,#CLDISK :GIVE RH INITIALIZE
:SETUP UNIT NUBER
:CLEAR RHWC AND FUNCTION BITS IN RHCSI
:SET UP RH FOR A 10 WORD WRITE
MOV #-10,#RHWC :WORD COUNT REGISTER=10
MOV #WRFROM,#RHBA :BUS ADDRESS REGISTER=WRITBUF
:WRITE 10 WORDS FROM "WRFROM" INTO
:WHAT EVER RH DEVICE IS AVAILABLE
MOV #-1,NOGO :DO NOT GIVE GO IN COMMAND ROUTINE
JSR RO,#COMND :GO TO DO COMMAND
WRIDAT :WRITE DATA
BIS #GO,#RHCSI :SET GO
BIS #GO,#RHCSI :SET GO WITHOUT TIME TO COMPLET LAST GO
:CHECK THAT RHCSI HAS SC!DVA!TRE!61
MOV #SC!DVA!TRE!61,#GDDAT :GET GOOD = 144000
MOV #RHCSI,#BDDAT :READ RHCSI FOR COMPARISON
CMP #GDDAT,#BDDAT :COMPARE EXPECTED
:DATA WITH DATA READ FROM
:RHCSI

```

7744 023230 001401          BEQ      65$      ;BRANCH IF GOOD
7745 023232 104070          ERROR    70
7746
7747
7748
7749
7750
7751
7752
7753
7754
7755 023234          65$:
7756
7757 023234 012737 002200 001124  MOV     #PGE!OR,$GDDAT ;GET GOOD = 2000
7758 023242 053737 005010 001124  BIS     LUNIT,$GDDAT   ;INCLUDE UNIT NUMBER
7759
7760 023250 017737 160614 001126  MOV     2RHCS2,$BDDAT ;READ RHCS2 FOR COMPARISON
7761 023256 023737 001124 001126  CMP     $GDDAT,$BDDAT ;COMPARE EXPECTED
7762
7763
7764 023264 001401          BEQ      67$      ;BRANCH IF GOOD
7765 023266 104071          ERROR    71
7766
7767
7768
7769
7770
7771
7772
7773
7774
7775
7776 023270          67$:
7777
7778 023270 012737 002000 001124  MOV     #DBL,$GDDAT   ;GET GOOD = 0
7779
7780 023276 017737 160630 001126  MOV     2RHCS3,$BDDAT ;READ RHCS3 FOR COMPARISON
7781 023304 023737 001124 001126  CMP     $GDDAT,$BDDAT ;COMPARE EXPECTED
7782
7783
7784
7785 023312 001401          BEQ      69$      ;BRANCH IF GOOD
7786 023314 104072          ERROR    72
7787
7788
7789
7790
7791
7792
7793
7794 023316          69$:
7795
7796
7797
7798
7799

```

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:*****
:*TEST 27          RHCS2 - BUS ADDRESS INHIBIT BIT #3

```

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7800 : * CLEAR THE SUBSYSTEM BY A CONTROLLER CLEAR (RHCS2 BIT #6)
7801 : * SET UP FOR A 2 WORD WRITE FROM A BUFFER TAGGED "WRFROM"
7802 : * SET BUS ADDRESS INHIBIT RHCS2 BIT #3 "BAI"
7803 : * AT THE END OF WRITE CHECK
7804 : * RHCS1 TO HAVE ONLY RDY AND COMMAND
7805 : * RHCS2 TO HAVE BAI
7806 : * RHCS3 TO HAVE 0
7807 : * RHBA TO HAVE ADDRESS OF "WRFROM"
7808 : * RHBAE AND RHWC TO HAVE ZEROS
7809 : *
7810 : * NOW SET UP READ FOR THE SAME DATA WITH
7811 : * BAI SET TO READ INTO A BUFFER TAGGED "REINTO"
7812 : * AFTER READ CHECK
7813 : * RHCS1 TO HAVE ONLY RDY AND COMMAND
7814 : * RHCS2 TO HAVE BAI
7815 : * RHCS3 TO HAVE 0
7816 : * RHBA TO HAVE ADDRESS OF "REINTO"
7817 : * RHBAE AND RHWC TO HAVE ZEROS
7818 : * DATA IN REINTO BUFFER IS CHECKED WITH DATA IN
7819 : * WRFROM BUFFER
7820 : * *****
7821 023316 000004 ST27: SCOPE
7822 023320 012706 001000 MOV #STACK.SP ;RESET STACK
7823 023324 012737 000027 004656 MOV #27,#STNM ;SAVE TEST NUMBER
7824
7825 023332 004737 040312 JSR PC,#CLDISK ;GIVE RH INITIALIZE
7826 ;SETUP UNIT NUBER
7827 ;CLEAR RHWC AND FUNCTION BITS IN RHCS1
7828
7829 ;SET UP 10 WORD WRITE FROM BUFFER TAGGED "WRFROM"
7830 ;WITH BAI IN RHCS2
7831 023336 012777 177766 160516 MOV #-10,#RHWC ;WORD COUNT REGISTER=10
7832 023344 052777 000010 160516 BIS #BAI,#RHCS2 ;BUS ADDRESS INHIBIT IN RHCS2
7833 023352 012777 004210 160504 MOV #WRFROM,#RHBA ;BUS ADDRESS REGISTER=WRFROM
7834 023360 004077 161254 JSR R0,#COMND ;GO TO DO COMMAND
7835 023364 004154 WRIDAT ;WRITE DATA
7836
7837
7838 ;CHECK THAT RHCS1 HAS DVA!RDY!60
7839 023366 012737 004260 001124 MOV #DVA!RDY!60,#GDDAT ;GET GOOD = 4252
7840
7841 023374 017737 160460 001126 MOV #RHCS1,#SDDAT ;READ RHCS1 FOR COMPARISON
7842 023402 023737 001124 001126 CMP #GDDAT,#SDDAT ;COMPARE EXPECTED
7843 ;DATA WITH DATA READ FROM
7844 ;RHCS1
7845 023410 001401 BEQ 65$ ;BRANCH IF GOOD
7846 023412 104076 ERROR 76
7847 ;AFTER SETTING "CLR" BIT #5
7848 ;IN RHCS2 TO INIT THE RH
7849 ;A 2 WORD WRITE WAS DONE
7850 ;WITH BAI BIT IN RHCS2 SET
7851 ;AT END OF WRITE
7852 ;RHCS1 SHOULD HAVE DVA!RDY!60
7853 ;=4252
7854 ;BUT CONTAINED WHAT IS
7855 ;GIVEN IN BAD RHCS1

```



```

7912                                     ;GIVEN IN BAD RHBA
7913 023524 71$:
7914                                     ;CHECK THAT RHBAE HAS 0
7915 023524 012737 000000 001124 MOV #0,$GDDAT ;GET GOOD = 0
7916
7917 023532 017737 160372 001126 MOV JRHBAE,$BDDAT ;READ RHBAE FOR COMPARISON
7918 023540 023737 001124 001126 CMP $GDDAT,$BDDAT ;COMPARE EXPECTED
7919                                     ;DATA WITH DATA READ FROM
7920                                     ;RHBAE
7921 023546 001401 BEQ 73$ ;BRANCH IF GOOD
7922 023550 104102 ERROR 102
7923                                     ;AFTER SETTING "CLR" BIT #5
7924                                     ;IN RHCS2 TO INIT THE RH
7925                                     ;A 2 WORD WRITE WAS DONE
7926                                     ;WITH BAI BIT IN RHCS2 SET
7927                                     ;AT END OF WRITE
7928                                     ;RHBAE SHOULD HAVE 0
7929                                     ;BUT CONTAINED WHAT IS
7930                                     ;GIVEN IN BAD RHBAE
7931 023552 73$:
7932                                     ;CHECK THAT RHW C HAS 0
7933 023552 012737 000000 001124 MOV #0,$GDDAT ;GET GOOD = 0
7934
7935 023560 017737 160276 001126 MOV JRHWC,$BDDAT ;READ RHW C FOR COMPARISON
7936 023566 023737 001124 001126 CMP $GDDAT,$BDDAT ;COMPARE EXPECTED
7937                                     ;DATA WITH DATA READ FROM
7938                                     ;RHW C
7939 023574 001401 BEQ 75$ ;BRANCH IF GOOD
7940 023576 104103 ERROR 103
7941                                     ;AFTER SETTING "CLR" BIT #5
7942                                     ;IN RHCS2 TO INIT THE RH
7943                                     ;A 2 WORD WRITE WAS DONE
7944                                     ;WITH BAI BIT IN RHCS2 SET
7945                                     ;AT END OF WRITE
7946                                     ;RHW C SHOULD HAVE 0
7947                                     ;BUT CONTAINED WHAT IS
7948                                     ;GIVEN IN BAD RHW C
7949 023600 75$:
7950
7951
7952
7953 *****
7954 *TEST 30 RHCS2 MDPE BIT #8 AND RHCS3 IPCKD BIT #0
7955 * CLEAR THE SUBSYSTEM BY A CONTROLLER CLEAR (RHCS2 BIT #5)
7956 * SET IPCKD (RHCS3 BIT #0)
7957 * MOVE ALL ZEROS INTO RHDB ONCE
7958 * THIS SHOULD SET TRE SC IN RHCS1
7959 * READ RHDB ONCE THIS SHOULD SET MDPE (RHCS2 BIT #8)
7960 * CHECK RHCS1 FOR RDY (BIT #7), TRE (BIT #14), SC (BIT #15)
7961 * "CLR" (RHCS2 BIT #5) IS GIVEN
7962 * ALL ERRORS ARE CLEARED
7963 * CHECK RHCS1, RHCS2, RHCS3, RHBA, RHBAE, RHW C
7964 *****
7965
7966 023600 000304 TEST30: SCOPE
7967 023602 012736 001000 MOV #STACK,SP ;RESET STACK

```

MAINDEC-11-DERHAB-A
DERHAB.SRC T30MACY11 27(732) 22-SEP-76 15:11 PAGE 153
RHSC2 MDPE BIT #8 AND RHCS3 IPCKO BIT #0

```

7968 023606 012737 000030 004656      MOV      #30, @#TSTNM      ;SAVE TEST NUMBER
7969
7970 023614 004737 040312              JSR      PC, @#CLDISK      ;GIVE RH INITIALIZE
7971                                ;SETUP UNIT NUMBER
7972                                ;CLEAR RHWC AND FUNCTION BITS IN RHCS1
7973 023620 012737 000000 004662      MOV      #0, IP           ;IPCK BIT NO. FOR PRINTOUT
7974
7975 023626 052777 000001 160276      BIS      #IPCKO, @RHCS3 ;SET IPCKO IN RHCS3-BIT #0
7976 023634 005037 004210              CLR      WRFROM           ;WRITE ZERO INTO LOCATION
7977 023640 013777 004210 160234      MOV      WRFROM, @RHDB    ;WRITE ZEROS INTO RHDB ONCE
7978
7979                                ;CHECK THAT RHCS1 HAS SC!TRE!DVA!RDY
7980 023646 012737 144200 001124      MOV      #SC!TRE!DVA!RDY, $GDDAT ;GET GOOD = 144200
7981
7982 023654 017737 160200 001126      MOV      @RHCS1, $BDDAT   ;READ RHCS1 FOR COMPARISON
7983 023662 023737 001124 001126      CMP      $GDDAT, $BDDAT   ;COMPARE EXPECTED
7984                                ;DATA WITH DATA READ FROM
7985                                ;RHCS1
7986 023670 001401                      BEQ      65$              ;BRANCH IF GOOD
7987 023672 104140                      ERROR    140
7988
7989                                ;AFTER SETTING "CLR" BIT #5
7990                                ;IN RHCS2 TO INIT THE RH
7991                                ;IPCKO BIT #0 RHCS3 WAS SET
7992                                ;ZEROS WERE MOVED INTO RHDB
7993                                ;RHCS1 SHOULD HAVE SC!TRE!DVA!RDY
7994                                ;=144200
7995                                ;BUT CONTAINED WHAT IS
7996                                ;GIVEN IN BAD RHCS1
7996 023674                      65$:
7997 023674 017737 160202 001200      MOV      @RHDB, $TMP1     ;READ RHDB ONCE
7998
7999
8000                                ;CHECK THAT RHCS3 HAS 1
8001 023702 012737 000001 001124      MOV      #1, $GDDAT       ;GET GOOD = 1
8002
8003 023710 017737 160216 001126      MOV      @RHCS3, $BDDAT   ;READ RHCS3 FOR COMPARISON
8004 023716 023737 001124 001126      CMP      $GDDAT, $BDDAT   ;COMPARE EXPECTED
8005                                ;DATA WITH DATA READ FROM
8006                                ;RHCS3
8007 023724 001401                      BEQ      67$              ;BRANCH IF GOOD
8008 023726 104104                      ERROR    104
8009
8010                                ;AFTER SETTING "CLR" BIT #5
8011                                ;IN RHCS2 TO INIT THE RH
8012                                ;IPCKO BIT #0 RHCS3 WAS SET
8013                                ;ZEROS WERE MOVED INTO
8014                                ;RHDB
8015                                ;RHDB WAS READ THEN
8016                                ;RHCS3 SHOULD HAVE 1
8017                                ;=1
8018                                ;BUT CONTAINED WHAT IS
8019                                ;GIVEN IN BAD RHCS3
8019 023730                      67$:
8020
8021                                ;CHECK THAT RHCS2 HAS MDPE!IR
8022 023730 012737 000500 001124      MOV      #MDPE!IR, $GDDAT ;GET GOOD = 500
8023 023736 053737 005010 001124      BIS      UNIT, $GDDAT     ;INCLUDE UNIT NUMBER

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8080 ;RHCS2
8081 024046 001401 BEQ 73$ ;BRANCH IF GOOD
8082 024050 104107 ERROR 107
8083 ;AFTER SETTING "CLR" BIT #5
8084 ;IN RHCS2 TO INIT THE RH
8085 ;IPCKO BIT #0 RHCS3 WAS SET
8086 ;ZEROS WERE MOVED INTO
8087 ;RHDB
8088 ;RHDB WAS READ THEN
8089 ;AN RH CLEAR (RHCS2 BIT #5)
8090 ;WAS GIVEN THIS SHOULD
8091 ;CLEAR ALL ERRORS
8092 ;RHCS2 SHOULD HAVE IR
8093 ;=100
8094 ;TOGETHER WITH UNIT NUMBER
8095 ;BUT CONTAINED WHAT IS
8096 ;GIVEN IN BAD RHCS2
8097 024052 73$:
8098 ;CHECK THAT RHCS3 HAS 0
8099 024052 012737 000000 001124 MOV #0,$GDDAT ;GET GOOD = 0
8100
8101 024060 017737 160046 001126 MOV @RHCS3,$BDDAT ;READ RHCS3 FOR COMPARISON
8102 024066 023737 001124 001126 CMP $GDDAT,$BDDAT ;COMPARE EXPECTED
8103 ;DATA WITH DATA READ FROM
8104 ;RHCS3
8105 024074 001401 BEQ 75$ ;BRANCH IF GOOD
8106 024076 104110 ERROR 110
8107 ;AFTER SETTING "CLR" BIT #5
8108 ;IN RHCS2 TO INIT THE RH
8109 ;IPCKO BIT #0 RHCS3 WAS SET
8110 ;ZEROS WERE MOVED INTO
8111 ;RHDB
8112 ;RHDB WAS READ THEN
8113 ;AN RH CLEAR (RHCS2 BIT #5)
8114 ;WAS GIVEN THIS SHOULD
8115 ;CLEAR ALL ERRORS
8116 ;RHCS3 SHOULD HAVE 0
8117 ;BUT CONTAINED WHAT IS
8118 ;GIVEN IN BAD RHCS3
8119 024100 75$:
8120 ;CHECK THAT RHBA HAS 0
8121 024100 012737 000000 001124 MOV #0,$GDDAT ;GET GOOD = 0
8122
8123 024106 017737 157752 001126 MOV @RHBA,$BDDAT ;READ RHBA FOR COMPARISON
8124 024114 023737 001124 001126 CMP $GDDAT,$BDDAT ;COMPARE EXPECTED
8125 ;DATA WITH DATA READ FROM
8126 ;RHBA
8127 024122 001401 BEQ 77$ ;BRANCH IF GOOD
8128 024124 104111 ERROR 111
8129 ;AFTER SETTING "CLR" BIT #5
8130 ;IN RHCS2 TO INIT THE RH
8131 ;IPCKO BIT #0 RHCS3 WAS SET
8132 ;ZEROS WERE MOVED INTO
8133 ;RHDB
8134 ;RHDB WAS READ THEN
8135 ;AN RH CLEAR (RHCS2 BIT #5)

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8136                                     ; WAS GIVEN THIS SHOULD
8137                                     ; CLEAR ALL ERRORS
8138                                     ; RHBA SHOULD HAVE 0
8139                                     ; BUT CONTAINED WHAT IS
8140                                     ; GIVEN IN BAD RHBA
8141 024126                               77$:
8142                                     ; CHECK THAT RHBAE HAS 0
8143 024126 012737 000000 001124      MOV    #0,$GDDAT    ; GET GOOD = 0
8144
8145 024134 017737 157770 001126      MOV    @RHBAE,$BDDAT ; READ RHBAE FOR COMPARISON
8146 024142 023737 001124 001126      CMP    $GDDAT,$BDDAT ; COMPARE EXPECTED
8147                                     ; DATA WITH DATA READ FROM
8148                                     ; RHBAE
8149 024150 001401                       BEQ    79$          ; BRANCH IF GOOD
8150 024152 104112                       ERROR  112
8151                                     ; AFTER SETTING "CLR" BIT #5
8152                                     ; IN RHCS2 TO INIT THE RH
8153                                     ; IPCKO BIT #0 RHCS3 WAS SET
8154                                     ; ZEROS WERE MOVED INTO
8155                                     ; RHDB
8156                                     ; RHDB WAS READ THEN
8157                                     ; AN RH CLEAR (RHCS2 BIT #5)
8158                                     ; WAS GIVEN THIS SHOULD
8159                                     ; CLEAR ALL ERRORS
8160                                     ; RHBAE SHOULD HAVE 0
8161                                     ; BUT CONTAINED WHAT IS
8162                                     ; GIVEN IN BAD RHBAE
8163 024154                               79$:
8164                                     ; CHECK THAT RHWC HAS 0
8165 024154 012737 000000 001124      MOV    #0,$GDDAT    ; GET GOOD = 0
8166
8167 024162 017737 157674 001126      MOV    @RHWC,$BDDAT ; READ RHWC FOR COMPARISON
8168 024170 023737 001124 001126      CMP    $GDDAT,$BDDAT ; COMPARE EXPECTED
8169                                     ; DATA WITH DATA READ FROM
8170                                     ; RHWC
8171 024176 001401                       BEQ    81$          ; BRANCH IF GOOD
8172 024200 104113                       ERROR  113
8173                                     ; AFTER SETTING "CLR" BIT #5
8174                                     ; IN RHCS2 TO INIT THE RH
8175                                     ; IPCKO BIT #0 RHCS3 WAS SET
8176                                     ; ZEROS WERE MOVED INTO
8177                                     ; RHDB
8178                                     ; RHDB WAS READ THEN
8179                                     ; AN RH CLEAR (RHCS2 BIT #5)
8180                                     ; WAS GIVEN THIS SHOULD
8181                                     ; CLEAR ALL ERRORS
8182                                     ; RHWC SHOULD HAVE 0
8183                                     ; BUT CONTAINED WHAT IS
8184                                     ; GIVEN IN BAD RHWC
8185 024202                               81$:
8186
8187
8188
8189                                     ; *****
8190                                     ; *TEST 31      RHSC2 MDPE BIT #8 AND RHCS3 IPCK1 BIT #1
8191                                     ; *      CLEAR THE SUBSYSTEM BY A CONTROLLER CLEAR (RHCS2 BIT #5)

```

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8192          : *      SET IPCK1 (RHCS3 BIT #1)
8193          : *      MOVE ALL ZEROS INTO RHDB ONCE
8194          : *      THIS SHOULD SET TRE SC IN RHCS1
8195          : *      READ RHDB ONCE THIS SHOULD SET MDPE (RHCS2 BIT #8)
8196          : *      CHECK RHCS1 FOR RDY (BIT #7), TRE (BIT #14), SC (BIT #15)
8197          : *      "CLR" (RHCS2 BIT #5) IS GIVEN
8198          : *      ALL ERRORS ARE CLEARED
8199          : *      CHECK RHCS1, RHCS2, RHCS3, RHBA, RHBAE, RHWC
8200
8201          : *****
8202 024202 000004          †ST31: SCOPE
8203 024204 012706 001000      MOV      #STACK,SP      ;RESET STACK
8204 024210 012737 000031 004656  MOV      #31,#TSTNM      ;SAVE TEST NUMBER
8205
8206 024216 004737 040312      JSR      PC,#CLDISK      ;GIVE RH INITIALIZE
8207                          ;SETUP UNIT NUBER
8208                          ;CLEAR RHWC AND FUNCTION BITS IN RHCS1
8209 024222 012737 000001 004662  MOV      #1,IP           ;IPCK BIT NO. FOR PRINTOUT
8210
8211 024230 052777 000002 157674  BIS      #IPCK1,#RHCS3 ;SET IPCK1 IN RHCS3-BIT #1
8212 024236 005037 004210      CLR      WRFROM          ;WRITE ZERO INTO LOCATION
8213 024242 013777 004210 157632  MOV      WRFROM,#RHDB    ;WRITE ZEROS INTO RHDB ONCE
8214
8215          ;CHECK THAT RHCS1 HAS SC!TRE!DVA!RDY
8216 024250 012737 144200 001124  MOV      #SC!TRE!DVA!RDY,$GDDAT ;GET GOOD = 144200
8217
8218 024256 017737 157576 001126  MOV      #RHCS1,$BDDAT    ;READ RHCS1 FOR COMPARISON
8219 024264 023737 001124 001126  CMP      $GDDAT,$BDDAT    ;COMPARE EXPECTED
8220                          ;DATA WITH DATA READ FROM
8221                          ;RHCS1
8222 024272 001401          BEQ      65$              ;BRANCH IF GOOD
8223 024274 104140          ERROR    140
8224
8225          ;AFTER SETTING "CLR" BIT #5
8226          ;IN RHCS2 TO INIT THE RH
8227          ;IPCK1 BIT #1 RHCS3 WAS SET
8228          ;ZEROS WERE MOVED INTO RHDB
8229          ;RHCS1 SHOULD HAVE SC!TRE!DVA!RDY
8230          ;=144200
8231          ;BUT CONTAINED WHAT IS
8232          ;GIVEN IN BAD RHCS1
8232 024276          65$:
8233 024276 017737 157600 001200      MOV      #RHDB,$TMP1      ;READ RHDB ONCE
8234
8235
8236          ;CHECK THAT RHCS3 HAS 2
8237 024304 012737 000002 001124  MOV      #2,$GDDAT        ;GET GOOD = 2
8238
8239 024312 017737 157614 001126  MOV      #RHCS3,$BDDAT    ;READ RHCS3 FOR COMPARISON
8240 024320 023737 001124 001126  CMP      $GDDAT,$BDDAT    ;COMPARE EXPECTED
8241                          ;DATA WITH DATA READ FROM
8242                          ;RHCS3
8243 024326 001401          BEQ      67$              ;BRANCH IF GOOD
8244 024330 104104          ERROR    104
8245
8246          ;AFTER SETTING "CLR" BIT #5
8247          ;IN RHCS2 TO INIT THE RH
8248          ;IPCK1 BIT #1 RHCS3 WAS SET

```


: WAS GIVEN THIS SHOULD
: CLEAR ALL ERRORS
: RHCW SHOULD HAVE 0
: BUT CONTAINED WHAT IS
: GIVEN IN BAD RHCW

024604

815:

: TEST 32 RHSC2 MDPE BIT #8 AND RHCS3 IPCK2 BIT #2
: CLEAR THE SUBSYSTEM BY A CONTROLLER CLEAR (RHCS2 BIT #5)
: SET IPCK2 (RHCS3 BIT #2)
: MOVE ALL ZEROS INTO RHDB TWICE
: THIS SHOULD NOT SET TRE SC IN RHCS1
: READ RHDB ONCE THIS SHOULD SET MDPE (RHCS2 BIT #8)
: CHECK RHCS1 FOR RDY (BIT #7), TRE (BIT #14), SC (BIT #15)
: READ RHDB A SECOND TIME. CHECK RHCS1, RHCS2, RHCS3
: "CLR" (RHCS2 BIT #5) IS GIVEN
: ALL ERRORS ARE CLEARED
: CHECK RHCS1, RHCS2, RHCS3, RHBA, RHBAE, RHCW

: ST32: SCOPE
: MOV #STACK, SP ; RESET STACK
: MOV #32, @STNM ; SAVE TEST NUMBER
: JSR PC, @CLDISK ; GIVE RH INITIALIZE
: ; SETUP UNIT NUMBER
: ; CLEAR RHCW AND FUNCTION BITS IN RHCS1
: ; IPCK BIT NO. FOR PRINTOUT
: MOV #2, IP
: BIS #IPCK2, @RHCS3 ; SET IPCK2 IN RHCS3-BIT #2
: CLR WRFROM ; WRITE ZERO INTO LOCATION
: MOV WRFROM, @RHDB ; WRITE ZEROS INTO RHDB ONCE
: MOV WRFROM, @RHDB ; WRITE ZERO SECOND TIME
: ; CHECK THAT RHCS1 HAS DVA!RDY
: MOV #DVA!RDY, \$GDDAT ; GET GOOD = 4200
: MOV @RHCS1, \$BDDAT ; READ RHCS1 FOR COMPARISON
: CMP \$GDDAT, \$BDDAT ; COMPARE EXPECTED
: ; DATA WITH DATA READ FROM
: ; RHCS1
: ; BRANCH IF GOOD
: BEQ 655
: ERROR 140

: AFTER SETTING "CLR" BIT #5
: IN RHCS2 TO INIT THE RH
: IPCK2 BIT #2 RHCS3 WAS SET
: ZEROS WERE MOVED INTO RHDB
: TWICE
: RHCS1 SHOULD HAVE DVA!RDY
: =4200
: BUT CONTAINED WHAT IS
: GIVEN IN BAD RHCS1

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024604 000004
024606 012736 001000
024612 012737 000032 004656
024620 004737 040312
024624 012737 000002 004662
024632 052777 000004 157272
024640 005037 004210
024644 013777 004210 157230
024652 013777 004210 157222
024660 012737 004200 001124
024666 017737 157166 001126
024674 023737 001124 001126
024702 001401
024704 104140

024706

655:

```

00000000 024706 017737 157170 001200      MOV      @RHDB,$TMP1      ;READ RHDB ONCE
00000001 024714 012737 000004 001124      ;CHECK THAT RHCS3 HAS 4
00000002 024714 012737 000004 001124      MOV      #4,$GDDAT      ;GET GOOD = 4
00000003 024732 017737 157204 001126      MOV      @RHCS3,$BDDAT  ;READ RHCS3 FOR COMPARISON
00000004 024730 023737 001124 001126      CMP      $GDDAT,$BDDAT  ;COMPARE EXPECTED
00000005 024736 001401 104104      BEQ      67$           ;DATA WITH DATA READ FROM
00000006 024740 104104 104104      ERROR   104           ;RHCS3
00000007 024742 104104 104104      ;BRANCH IF GOOD
00000008 024742 104104 104104      ;AFTER SETTING "CLR" BIT #5
00000009 024742 104104 104104      ;IN RHCS2 TO INIT THE RH
00000010 024742 104104 104104      ;IPCK2 BIT #2 RHCS3 WAS SET
00000011 024742 104104 104104      ;ZEROS WERE MOVED INTO
00000012 024742 104104 104104      ;RHDB
00000013 024742 104104 104104      ;RHDB WAS READ THEN
00000014 024742 104104 104104      ;RHCS3 SHOULD HAVE 4
00000015 024742 104104 104104      ;=4
00000016 024742 104104 104104      ;BUT CONTAINED WHAT IS
00000017 024742 104104 104104      ;GIVEN IN BAD RHCS3
00000018 024742 104104 104104      67$:
00000019 024742 012737 000700 001124      ;CHECK THAT RHCS2 HAS MDPE!OR!IR
00000020 024750 053737 005010 001124      MOV      #MDPE!OR!IR,$GDDAT ;GET GOOD = 70C
00000021 024750 053737 005010 001124      BIS      UNIT,$GDDAT      ;INCLUDE UNIT NUMBER
00000022 024756 017737 157106 001126      MOV      @RHCS2,$BDDAT  ;READ RHCS2 FOR COMPARISON
00000023 024764 023737 001124 001126      CMP      $GDDAT,$BDDAT  ;COMPARE EXPECTED
00000024 024772 001401 104104      BEQ      69$           ;DATA WITH DATA READ FROM
00000025 024774 104104 104104      ERROR   105           ;RHCS2
00000026 024774 104104 104104      ;BRANCH IF GOOD
00000027 024776 104104 104104      ;AFTER SETTING "CLR" BIT #5
00000028 024776 104104 104104      ;IN RHCS2 TO INIT THE RH
00000029 024776 104104 104104      ;IPCK2 BIT #2 RHCS3 WAS SET
00000030 024776 104104 104104      ;ZEROS WERE MOVED INTO
00000031 024776 104104 104104      ;RHDB
00000032 024776 104104 104104      ;RHDB WAS READ THEN
00000033 024776 104104 104104      ;RHCS2 SHOULD HAVE MDPE!OR!IR
00000034 024776 104104 104104      ;=700
00000035 024776 104104 104104      ;TOGETHER WITH UNIT NUMBER
00000036 024776 104104 104104      ;BUT CONTAINED WHAT IS
00000037 024776 104104 104104      ;GIVEN IN BAD RHCS2
00000038 024776 017737 157100 001202      MOV      @RHDB,$TMP2      ;READ RHDB SECOND TIME
00000039 025004 012737 144200 001124      ;CHECK THAT RHCS1 HAS SC!TRE!DVA!RDY
00000040 025004 012737 144200 001124      MOV      #SC!TRE!DVA!RDY,$GDDAT ;GET GOOD = 144200
00000041 025012 017737 157042 001126      MOV      @RHCS1,$BDDAT  ;READ RHCS1 FOR COMPARISON
00000042 025020 023737 001124 001126      CMP      $GDDAT,$BDDAT  ;COMPARE EXPECTED
00000043 025026 001401 104104      BEQ      71$           ;DATA WITH DATA READ FROM
00000044 025026 001401 104104      ;RHCS1
00000045 025026 001401 104104      ;BRANCH IF GOOD

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0528 025030 104140          ERROR 140          ;AFTER SETTING "CLR" BIT #5
0529                                     ;IN RHCS2 TO INIT THE RH
0530                                     ;IPCK2 BIT #2 RHCS3 WAS SET
0531                                     ;ZEROS WERE MOVED INTO
0532                                     ;RHDB
0533                                     ;RHDB WAS READ THEN
0534                                     ;RHCS1 SHOULD HAVE SC!TRE!DVA!RDY
0535                                     ;=144200
0536                                     ;BUT CONTAINED WHAT IS
0537                                     ;GIVEN IN BAD RHCS1
0538
0539 025032          71$:
0540                                     ;CHECK THAT RHCS2 HAS MDPE!IR
0541 025032 012737 000500 001124      MOV      #MDPE!IR,$GDDAT ;GET GOOD = 500
0542 025040 053737 005010 001124      BIS      UNIT,$GDDAT    ;INCLUDE UNIT NUMBER
0543
0544 025046 017737 157016 001126      MOV      @RHCS2,$BDDAT  ;READ RHCS2 FOR COMPARISON
0545 025054 023737 001124 001126      CMP      $GDDAT,$BDDAT ;COMPARE EXPECTED
0546                                     ;DATA WITH DATA READ FROM
0547                                     ;RHCS2
0548 025062 001401          BEQ      73$          ;BRANCH IF GOOD
0549 025064 104105          ERROR 105
0550
0551                                     ;AFTER SETTING "CLR" BIT #5
0552                                     ;IN RHCS2 TO INIT THE RH
0553                                     ;IPCK2 BIT #2 RHCS3 WAS SET
0554                                     ;ZEROS WERE MOVED INTO
0555                                     ;RHDB
0556                                     ;RHDB WAS READ THEN
0557                                     ;RHCS2 SHOULD HAVE MDPE!IR
0558                                     ;=500
0559                                     ;TOGETHER WITH UNIT NUMBER
0560                                     ;BUT CONTAINED WHAT IS
0561                                     ;GIVEN IN BAD RHCS2
0562
0563 025066          73$:
0564                                     ;CHECK THAT RHCS3 HAS 4
0565 025066 012737 000004 001124      MOV      #4,$GDDAT    ;GET GOOD = 4
0566 025074 017737 157032 001126      MOV      @RHCS3,$BDDAT ;READ RHCS3 FOR COMPARISON
0567 025102 023737 001124 001126      CMP      $GDDAT,$BDDAT ;COMPARE EXPECTED
0568                                     ;DATA WITH DATA READ FROM
0569                                     ;RHCS3
0570 025110 001401          BEQ      75$          ;BRANCH IF GOOD
0571 025112 104104          ERROR 104
0572
0573                                     ;AFTER SETTING "CLR" BIT #5
0574                                     ;IN RHCS2 TO INIT THE RH
0575                                     ;IPCK2 BIT #2 RHCS3 WAS SET
0576                                     ;ZEROS WERE MOVED INTO
0577                                     ;RHDB
0578                                     ;RHDB WAS READ THEN
0579                                     ;RHCS3 SHOULD HAVE 4
0580                                     ;=4
0581                                     ;BUT CONTAINED WHAT IS
0582                                     ;GIVEN IN BAD RHCS3
0583
0584 025114          75$:
0585 025114 004737 040312          JSR      PC,@#CLDISK  ;GIVE RH INITIALIZE

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8584                                     :SETUP UNIT NUBER
8585                                     :CLEAR RHWC AND FUNCTION BITS IN RHCS1
8586
8587
8588                                     ;CHECK THAT RHCS1 HAS RDY!DVA
8589 025120 012737 004200 001124      MOV      #RDY!DVA,$GDDAT ;GET GOOD = 4200
8590
8591 025126 017737 156726 001126      MOV      @RHCS1,$BDDAT ;READ RHCS1 FOR COMPARISON
8592 025134 023737 001124 001126      CMP      $GDDAT,$BDDAT ;COMPARE EXPECTED
8593                                     ;DATA WITH DATA READ FROM
8594                                     ;RHCS1
8595 025142 001401                      BEQ      77$ ;BRANCH IF GOOD
8596 025144 104106                      ERROR   106
8597
8598                                     ;AFTER SETTING "CLR" BIT #5
8599                                     ;IN RHCS2 TO INIT THE RH
8600                                     ;IPCK2 BIT #2 RHCS3 WAS SET
8601                                     ;ZEROS WERE MOVED INTO
8602                                     ;RHDB
8603                                     ;RHDB WAS READ THEN
8604                                     ;AN RH CLEAR (RHCS2 BIT #5)
8605                                     ;WAS GIVEN THIS SHOULD
8606                                     ;CLEAR ALL ERRORS
8607                                     ;RHCS1 SHOULD HAVE RDY!DVA
8608                                     ;=4200
8609                                     ;BUT CONTAINED WHAT IS
8610                                     ;GIVEN IN BAD RHCS1
8611 025146                               77$:
8612                                     ;CHECK THAT RHCS2 HAS IR
8613 025146 012737 000100 001124      MOV      #IR,$GDDAT ;GET GOOD = 100
8614 025154 053737 005010 001124      BIS      UNIT,$GDDAT ;INCLUDE UNIT NUMBER
8615
8616 025162 017737 156702 001126      MOV      @RHCS2,$BDDAT ;READ RHCS2 FOR COMPARISON
8617 025170 023737 001124 001126      CMP      $GDDAT,$BDDAT ;COMPARE EXPECTED
8618                                     ;DATA WITH DATA READ FROM
8619                                     ;RHCS2
8620 025176 001401                      BEQ      79$ ;BRANCH IF GOOD
8621 025200 104107                      ERROR   107
8622
8623                                     ;AFTER SETTING "CLR" BIT #5
8624                                     ;IN RHCS2 TO INIT THE RH
8625                                     ;IPCK2 BIT #2 RHCS3 WAS SET
8626                                     ;ZEROS WERE MOVED INTO
8627                                     ;RHDB
8628                                     ;RHDB WAS READ THEN
8629                                     ;AN RH CLEAR (RHCS2 BIT #5)
8630                                     ;WAS GIVEN THIS SHOULD
8631                                     ;CLEAR ALL ERRORS
8632                                     ;RHCS2 SHOULD HAVE IR
8633                                     ;=100
8634                                     ;TOGETHER WITH UNIT NUMBER
8635                                     ;BUT CONTAINED WHAT IS
8636                                     ;GIVEN IN BAD RHCS2
8637 025202                               79$:
8638                                     ;CHECK THAT RHCS3 HAS 0
8639 025202 012737 000000 001124      MOV      #0,$GDDAT ;GET GOOD = 0
8640
8641 025210 017737 156716 001126      MOV      @RHCS3,$BDDAT ;READ RHCS3 FOR COMPARISON

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MAINDEC-11-DERHAB-A
DERHAB.SRC T32

MACY11 27(732) 22-SEP-76 15:11 PAGE 166
RHSC2 MDPE BIT #8 AND RHCS3 IPCK2 BIT #2

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8696 ;WAS GIVEN THIS SHOULD
8697 ;CLEAR ALL ERRORS
8698 ;RHBAE SHOULD HAVE 0
8699 ;BUT CONTAINED WHAT IS
8700 ;GIVEN IN BAD RHBAE
8701 025304 85$:
8702 ;CHECK THAT RHWC HAS 0
8703 025304 012737 000000 001124 MOV #0,$GDDAT ;GET GOOD = 0
8704
8705 025312 017737 156544 001126 MOV JRHWC,$BDDAT ;READ RHWC FOR COMPARISON
8706 025320 023737 001124 001126 CMP $GDDAT,$BDDAT ;COMPARE EXPECTED
8707 ;DATA WITH DATA READ FROM
8708 ;RHWC
8709 025326 001401 BEQ 87$ ;BRANCH IF GOOD
8710 025330 104113 ERROR 113
8711 ;AFTER SETTING "CLR" BIT #5
8712 ;IN RHCS2 TO INIT THE RH
8713 ;IPCK2 BIT #2 RHCS3 WAS SET
8714 ;ZEROS WERE MOVED INTO
8715 ;RHDB
8716 ;RHDB WAS READ THEN
8717 ;AN RH CLEAR (RHCS2 BIT #5)
8718 ;WAS GIVEN THIS SHOULD
8719 ;CLEAR ALL ERRORS
8720 ;RHWC SHOULD HAVE 0
8721 ;BUT CONTAINED WHAT IS
8722 ;GIVEN IN BAD RHWC
8723 025332 87$:
8724
8725
8726
8727
8728 ;*****
8729 *TEST 33 RHSC2 MDPE BIT #8 AND RHCS3 IPCK3 BIT #3
8730 * CLEAR THE SUBSYSTEM BY A CONTROLLER CLEAR (RHCS2 BIT #5)
8731 * SET IPCK3 (RHCS3 BIT #3)
8732 * MOVE ALL ZEROS INTO RHDB TWICE
8733 * THIS SHOULD NOT SET TRE SC IN RHCS1
8734 * READ RHDB ONCE THIS SHOULD SET MDPE (RHCS2 BIT #8)
8735 * CHECK RHCS1 FOR RDY (BIT #7), TRE (BIT #14), SC (BIT #15)
8736 * READ RHDB A SECOND TIME. CHECK RHCS1, RHCS2, RHCS3
8737 * "CLR" (RHCS2 BIT #5) IS GIVEN
8738 * ALL ERRORS ARE CLEARED
8739 * CHECK RHCS1, RHCS2, RHCS3, RHBA, RHBAE, RHWC
8740 ;*****
8741 025332 000004 †ST33: SCOPE
8742 025334 012706 001000 MOV #STACK,SP ;RESET STACK
8743 025340 012737 000033 004656 MOV #33,‡†STNM ;SAVE TEST NUMBER
8744
8745 025346 004737 040312 JSR PC,‡#CLDISK ;GIVE RH INITIALIZE
8746 ;SETUP UNIT NUBER
8747 ;CLEAR RHWC AND FUNCTION BITS IN RHCS1
8748 025352 012737 000003 004662 MOV #3,IP ;IPCK BIT NO. FOR PRINTOUT
8749
8750 025360 052777 000010 156544 BIS #IPCK3,‡RHCS3 ;SET IPCK3 IN RHCS3-BIT #3
8751 025366 005037 004210 CLR WRFROM ;WRITE ZERO INTO LOCATION

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MAINDEC-11-DERHAB-A
DERHAB.SRC T33

MACY11 27(732) 22-SEP-76 15:11 PAGE 167
RHSC2 MDPE BIT #8 AND RHCS3 IPCK3 BIT #3

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8752 025372 013777 004210 156502      MOV      WRFROM,DRHDB      ;WRITE ZEROS INTO RHDB ONCE
8753 025400 013777 004210 156474      MOV      WRFROM,DRHDB      ;WRITE ZERO SECOND TIME
8754
8755                                     ;CHECK THAT RHCS1 HAS DVA!RDY
8756 025406 012737 004200 001124      MOV      #DVA!RDY,$GDDAT  ;GET GOOD = 4200
8757
8758 025414 017737 156440 001126      MOV      DRHCS1,$BDDAT    ;READ RHCS1 FOR COMPARISON
8759 025422 023737 001124 001126      CMP      $GDDAT,$BDDAT    ;COMPARE EXPECTED
8760                                     ;DATA WITH DATA READ FROM
8761                                     ;RHCS1
8762 025430 001401                                     BEQ      65$              ;BRANCH IF GOOD
8763 025432 104140                                     ERROR    140
8764                                     ;AFTER SETTING "CLR" BIT #5
8765                                     ;IN RHCS2 TO INIT THE RH
8766                                     ;IPCK3 BIT #3 RHCS3 WAS SET
8767                                     ;ZEROS WERE MOVED INTO RHDB
8768                                     ;TWICE
8769                                     ;RHCS1 SHOULD HAVE DVA!RDY
8770                                     ;=4200
8771                                     ;BUT CONTAINED WHAT IS
8772                                     ;GIVEN IN BAD RHCS1
8773 025434                                     65$:
8774 025434 017737 156442 001200      MOV      DRHDB,$TMP1      ;READ RHDB ONCE
8775
8776
8777                                     ;CHECK THAT RHCS3 HAS 10
8778 025442 012737 000010 001124      MOV      #10,$GDDAT      ;GET GOOD = 10
8779
8780 025450 017737 156456 001126      MOV      DRHCS3,$BDDAT    ;READ RHCS3 FOR COMPARISON
8781 025456 023737 001124 001126      CMP      $GDDAT,$BDDAT    ;COMPARE EXPECTED
8782                                     ;DATA WITH DATA READ FROM
8783                                     ;RHCS3
8784 025464 001401                                     BEQ      67$              ;BRANCH IF GOOD
8785 025466 104104                                     ERROR    104
8786                                     ;AFTER SETTING "CLR" BIT #5
8787                                     ;IN RHCS2 TO INIT THE RH
8788                                     ;IPCK3 BIT #3 RHCS3 WAS SET
8789                                     ;ZEROS WERE MOVED INTO
8790                                     ;RHDB
8791                                     ;RHDB WAS READ THEN
8792                                     ;RHCS3 SHOULD HAVE 10
8793                                     ;=10
8794                                     ;BUT CONTAINED WHAT IS
8795                                     ;GIVEN IN BAD RHCS3
8796 025470                                     67$:
8797
8798                                     ;CHECK THAT RHCS2 HAS MDPE!OR!IR
8799 025470 012737 000700 001124      MOV      #MDPE!OR!IR,$GDDAT ;GET GOOD = 700
8800 025476 053737 005010 001124      BIS      UNIT,$GDDAT      ;INCLUDE UNIT NUMBER
8801
8802 025504 017737 156360 001126      MOV      DRHCS2,$BDDAT    ;READ RHCS2 FOR COMPARISON
8803 025512 023737 001124 001126      CMP      $GDDAT,$BDDAT    ;COMPARE EXPECTED
8804                                     ;DATA WITH DATA READ FROM
8805                                     ;RHCS2
8806 025520 001401                                     BEQ      69$              ;BRANCH IF GOOD
8807 025522 104105                                     ERROR    105

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8808 ;AFTER SETTING "CLR" BIT #5
8809 ;IN RHCS2 TO INIT THE RH
8810 ;IPCK3 BIT #3 RHCS3 WAS SET
8811 ;ZEROS WERE MOVED INTO
8812 ;RHDB
8813 ;RHDB WAS READ THEN
8814 ;RHCS2 SHOULD HAVE MDPE!OR!IR
8815 ;=700
8816 ;TOGETHER WITH UNIT NUMBER
8817 ;BUT CONTAINED WHAT IS
8818 ;GIVEN IN BAD RHCS2
8819 025524 69$:
8820
8821 025524 017737 156352 001202 MOV @RHDB,$TMP2 ;READ RHDB SECOND TIME
8822 ;CHECK THAT RHCS1 HAS SC!TRE!DVA!RDY
8823 025532 012737 144200 001124 MOV #SC!TRE!DVA!RDY,$GDDAT ;GET GOOD = 144200
8824
8825 025540 017737 156314 001126 MOV @RHCS1,$BDDAT ;READ RHCS1 FOR COMPARISON
8826 025546 023737 001124 001126 CMP $GDDAT,$BDDAT ;COMPARE EXPECTED
8827 ;DATA WITH DATA READ FROM
8828 ;RHCS1
8829 025554 001401 BEQ 71$ ;BRANCH IF GOOD
8830 025556 104140 ERROR 140
8831 ;AFTER SETTING "CLR" BIT #5
8832 ;IN RHCS2 TO INIT THE RH
8833 ;IPCK3 BIT #3 RHCS3 WAS SET
8834 ;ZEROS WERE MOVED INTO
8835 ;RHDB
8836 ;RHDB WAS READ THEN
8837 ;RHCS1 SHOULD HAVE SC!TRE!DVA!RDY
8838 ;=144200
8839 ;BUT CONTAINED WHAT IS
8840 ;GIVEN IN BAD RHCS1
8841 025560 71$:
8842 ;CHECK THAT RHCS2 HAS MDPE!IR
8843 025560 012737 000500 001124 MOV #MDPE!IR,$GDDAT ;GET GOOD = 500
8844 025566 053737 005010 001124 BIS UNIT,$GDDAT ;INCLUDE UNIT NUMBER
8845
8846 025574 017737 156270 001126 MOV @RHCS2,$BDDAT ;READ RHCS2 FOR COMPARISON
8847 025602 023737 001124 001126 CMP $GDDAT,$BDDAT ;COMPARE EXPECTED
8848 ;DATA WITH DATA READ FROM
8849 ;RHCS2
8850 025610 001401 BEQ 73$ ;BRANCH IF GOOD
8851 025612 104105 ERROR 105
8852 ;AFTER SETTING "CLR" BIT #5
8853 ;IN RHCS2 TO INIT THE RH
8854 ;IPCK3 BIT #3 RHCS3 WAS SET
8855 ;ZEROS WERE MOVED INTO
8856 ;RHDB
8857 ;RHDB WAS READ THEN
8858 ;RHCS2 SHOULD HAVE MDPE!IR
8859 ;=500
8860 ;TOGETHER WITH UNIT NUMBER
8861 ;BUT CONTAINED WHAT IS
8862 ;GIVEN IN BAD RHCS2
8863 025614 73$:

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8864 ;CHECK THAT RHCS3 HAS 10
8865 025614 012737 000010 001124 MOV #10,$GDDAT ;GET GOOD = 10
8866
8867 025622 017737 156304 001126 MOV @RHCS3,$BDDAT ;READ RHCS3 FOR COMPARISON
8868 025630 023737 001124 001126 CMP $GDDAT,$BDDAT ;COMPARE EXPECTED
8869 ;DATA WITH DATA READ FROM
8870 ;RHCS3
8871 025636 001401 BEQ 75$ ;BRANCH IF GOOD
8872 025640 104104 ERROR 104
8873 ;AFTER SETTING "CLR" BIT #5
8874 ;IN RHCS2 TO INIT THE RH
8875 ;IPCK3 BIT #3 RHCS3 WAS SET
8876 ;ZEROS WERE MOVED INTO
8877 ;RHDB
8878 ;RHDB WAS READ THEN
8879 ;RHCS3 SHOULD HAVE 10
8880 ;=10
8881 ;BUT CONTAINED WHAT IS
8882 ;GIVEN IN BAD RHCS3
8883 025642 75$:
8884
8885 025642 004737 040312 JSR PC,@#CLDISK ;GIVE RH INITIALIZE
8886 ;SETUP UNIT NUBER
8887 ;CLEAR RHWC AND FUNCTION BITS IN RHCS1
8888
8889
8890 ;CHECK THAT RHCS1 HAS RDY!DVA
8891 025646 012737 004200 001124 MOV #RDY!DVA,$GDDAT ;GET GOOD = 4200
8892
8893 025654 017737 156200 001126 MOV @RHCS1,$BDDAT ;READ RHCS1 FOR COMPARISON
8894 025662 023737 001124 001126 CMP $GDDAT,$BDDAT ;COMPARE EXPECTED
8895 ;DATA WITH DATA READ FROM
8896 ;RHCS1
8897 025670 001401 BEQ 77$ ;BRANCH IF GOOD
8898 025672 104106 ERROR 106
8899 ;AFTER SETTING "CLR" BIT #5
8900 ;IN RHCS2 TO INIT THE RH
8901 ;IPCK3 BIT #3 RHCS3 WAS SET
8902 ;ZEROS WERE MOVED INTO
8903 ;RHDB
8904 ;RHDB WAS READ THEN
8905 ;AN RH CLEAR (RHCS2 BIT #5)
8906 ;WAS GIVEN THIS SHOULD
8907 ;CLEAR ALL ERRORS
8908 ;RHCS1 SHOULD HAVE RDY!DVA
8909 ;=4200
8910 ;BUT CONTAINED WHAT IS
8911 ;GIVEN IN BAD RHCS1
8912 025674 77$:
8913
8914 025674 012737 000100 001124 ;CHECK THAT RHCS2 HAS IR
8915 025702 053737 005010 001124 MOV #IR,$GDDAT ;GET GOOD = 100
8916 BIS UNIT,$GDDAT ;INCLUDE UNIT NUMBER
8917
8918 025710 017737 156154 001126 MOV @RHCS2,$BDDAT ;READ RHCS2 FOR COMPARISON
8919 025716 023737 001124 001126 CMP $GDDAT,$BDDAT ;COMPARE EXPECTED
;DATA WITH DATA READ FROM

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8976                                     ;WAS GIVEN THIS SHOULD
8977                                     ;CLEAR ALL ERORS
8978                                     ;RHBA SHOULD HAVE 0
8979                                     ;BUT CONTAINED WHAT IS
8980                                     ;GIVEN IN BAD RHBA
8981 026004                               83$:
8982                                     ;CHECK THAT RHBAE HAS 0
8983 026004 012737 000000 001124         MOV    #0,$GDDAT    ;GET GOOD = 0
8984                                     ;
8985 026012 017737 156112 001126         MOV    @RHBAE,$BDDAT ;READ RHBAE FOR COMPARISON
8986 026020 023737 001124 001126         CMP    $GDDAT,$BDDAT ;COMPARE EXPECTED
8987                                     ;DATA WITH DATA READ FROM
8988                                     ;RHBAE
8989 026026 001401                         BEQ    85$          ;BRANCH IF GOOD
8990 026030 104112
8991                                     ;AFTER SETTING "CLR" BIT #5
8992                                     ;IN RHCS2 TO INIT THE RH
8993                                     ;IPCK3 BIT #3 RHCS3 WAS SET
8994                                     ;ZEROS WERE MOVED INTO
8995                                     ;RHDB
8996                                     ;RHDB WAS READ THEN
8997                                     ;AN RH CLEAR (RHCS2 BIT #5)
8998                                     ;WAS GIVEN THIS SHOULD
8999                                     ;CLEAR ALL ERRORS
9000                                     ;RHBAE SHOULD HAVE 0
9001                                     ;BUT CONTAINED WHAT IS
9002                                     ;GIVEN IN BAD RHBAE
9003 026032                               85$:
9004                                     ;CHECK THAT RHWC HAS 0
9005 026032 012737 000000 001124         MOV    #0,$GDDAT    ;GET GOOD = 0
9006                                     ;
9007 026040 017737 156016 001126         MOV    @RHWC,$BDDAT ;READ RHWC FOR COMPARISON
9008 026046 023737 001124 001126         CMP    $GDDAT,$BDDAT ;COMPARE EXPECTED
9009                                     ;DATA WITH DATA READ FROM
9010                                     ;RHWC
9011 026054 001401                         BEQ    87$          ;BRANCH IF GOOD
9012 026056 104113
9013                                     ;AFTER SETTING "CLR" BIT #5
9014                                     ;IN RHCS2 TO INIT THE RH
9015                                     ;IPCK3 BIT #3 RHCS3 WAS SET
9016                                     ;ZEROS WERE MOVED INTO
9017                                     ;RHDB
9018                                     ;RHDB WAS READ THEN
9019                                     ;AN RH CLEAR (RHCS2 BIT #5)
9020                                     ;WAS GIVEN THIS SHOULD
9021                                     ;CLEAR ALL ERRORS
9022                                     ;RHWC SHOULD HAVE 0
9023                                     ;BUT CONTAINED WHAT IS
9024                                     ;GIVEN IN BAD RHWC
9025 026060                               87$:
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:*****
;*TEST 34          RHCS2-WCE BIT #14 AND RHCS3-WCE OW BIT #12

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..* CLEAR THE SUBSYSTEM BY A CONTROLLER CLEAR (RHCS2 BIT #5)
..* WRITE NINE WORD OF ALL NINES ON TO THE DEVICE
..* SET UP TO DO WRITE CHECK ON THAT WORD FROM AN
..* ODD WORD BOUNDARY
..* DO A ONE WORD WRITE CHECK WITH RHBA FROM AN
..* ODD WORD BOUNDARY
..* THIS SHOULD SET WCE OW (RHCS3 BIT #12)
..* AND WCE (RHCS2 BIT #14)
..* "CLR" (RHCS2 BIT #5) IS GIVEN
..* ALL ERRORS ARE CLEARED
..* CHECK RHCS1, RHCS2, RHCS3, RHBA, RHBAE, RHWC

```

+ST34: SCOPE
MOV #STACK, SP ;RESET STACK
MOV #34, #STNM ;SAVE TEST NUMBER
JSR PC, #CLDISK ;GIVE RH INITIALIZE
;SETUP UNIT NUMBER
;CLEAR RHWC AND FUNCTION BITS IN RHCS:
;WRITE ONE WORD OF ALL ONES ON THE DEVICE
MOV #-1, WRFROM ;ALL ONES INTO WRITE FROM
MOV #-9, #RHWC ;NINE WORD FOR WORD COUNT REGISTER
MOV #WRFROM, #RHBA ;WRITE FROM BUFFER INTO BUS ADDRESS
JSR RD, #COMND ;GO TO DO COMMAND
WRIDAT ;WRITE DATA
;SET UP FOR WRITE CHECK
CLR BUFOW ;WRITE DATA FOR WRITE CHECK ERROR
MOV #-9, #RHWC ;NINE WORD FOR WORD COUNT REGISTER
MOV #BLFOW, #RHBA ;WRITE FROM BUFFER INTO BUS ADDRESS
;FROM ODD WORD BOUNDARY
;WRITE CHECK ON ODD WORD BOUNDARY
JSR RD, #COMND ;GO TO DO COMMAND
WRCHK ;WRITE CHECK DATA
;CHECK THAT RHCS2 HAS WCE!OR!IR
MOV #WCE!OR!IR, #GDDAT ;GET GOOD = 40300
BIS UNIT, #GDDAT ;INCLUDE UNIT NUMBER
MOV #RHCS2, #BDDAT ;READ RHCS2 FOR COMPARISON
CMP #GDDAT, #BDDAT ;COMPARE EXPECTED
;DATA WITH DATA READ FROM
;RHCS2
;BRANCH IF GOOD
BEQ 655
ERROR 114
;AFTER SETTING "CLR" BIT #5
;IN RHCS2 TO INIT THE RH
;ONE WORD OF ALL ONES IS
;WRITTEN ON THE DEVICE

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026212 655:

026212 012737 012000 001124
026220 017737 155706 001126
026226 023737 001124 001126

:CHECK THAT RHCS3 HAS DBL!WCEOW
MOV #DBL!WCEOW,\$GDDAT ;GET GOOD = 12000
MOV @RHCS3,\$BDDAT ;READ RHCS3 FOR COMPARISON
CMP \$GDDAT,\$BDDAT ;COMPARE EXPECTED

:A WRITE CHECK WAS DONE
:ON AN ODD WORD BOUNDARY THEN
:RHCS2 SHOULD HAVE WCE!OR!IR
:=40300
:TOGETHER WITH UNIT NUMBER
:BUT CONTAINED WHAT IS
:GIVEN IN BAD RHCS2

026234 001401
026236 104115

BEG 675
ERROR 115

:DATA WITH DATA READ FROM
:RHCS3
:BRANCH IF GOOD

026240 675:

026240 004737 040312

JSR PC.@CLDISK

:AFTER SETTING "CLR" BIT #5
:IN RHCS2 TO INIT THE RH
:ONE WORD OF ALL ONES IS
:WRITTEN ON THE DEVICE
:A WRITE CHECK WAS DONE
:ON AN ODD WORD BOUNDARY THEN
:RHCS3 SHOULD HAVE DBL!WCEOW
:=12000
:BUT CONTAINED WHAT IS
:GIVEN IN BAD RHCS3

:GIVE RH INITIALIZE
:SETUP UNIT NUBER
:CLEAR RHWC AND FUNCTION BITS IN RHCS1

026244 012737 004200 001124
026252 017737 155602 001126
026260 023737 001124 001126

:CHECK THAT RHCS1 HAS RDY!DVA
MOV #RDY!DVA,\$GDDAT ;GET GOOD = 4200
MOV @RHCS1,\$BDDAT ;READ RHCS1 FOR COMPARISON
CMP \$GDDAT,\$BDDAT ;COMPARE EXPECTED

:DATA WITH DATA READ FROM
:RHCS1
:BRANCH IF GOOD

026266 001401
026270 104116

BEG 695
ERROR 116

:AFTER SETTING "CLR" BIT #5
:IN RHCS2 TO INIT THE RH
:ONE WORD OF ALL ONES IS
:WRITTEN ON THE DEVICE
:A WRITE CHECK WAS DONE
:ON AN ODD WORD BOUNDARY THEN
:AN RH CLEAR (RHCS2 BIT #5)
:WAS GIVEN THIS SHOULD
:CLEAR ALL ERRORS
:RHCS1 SHOULD HAVE RDY!DVA
:=4200


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9200 ;RHBA
9201 026376 001401 BEQ 75$ ;BRANCH IF GOOD
9202 026400 104121 ERROR 121
9203
9204 ;AFTER SETTING "CLR" BIT #5
9205 ;IN RHCS2 TO INIT THE RH
9206 ;ONE WORD OF ALL ONES IS
9207 ;WRITTEN ON THE DEVICE
9208 ;A WRITE CHECK WAS DONE
9209 ;ON AN ODD WORD BOUNDARY THEN
9210 ;AN RH CLEAR (RHCS2 BIT #5)
9211 ;WAS GIVEN THIS SHOULD
9212 ;CLEAR ALL ERRORS
9213 ;RHBA SHOULD HAVE 0
9214 ;BUT CONTAINED WHAT IS
9215 ;GIVEN IN BAD RHBA
9215 026402 75$:
9216
9217 026402 012737 000000 001124 ;CHECK THAT RHBAE HAS 0
9218 MOV #0,$GDDAT ;GET GOOD = 0
9219
9220 026410 017737 155514 001126 MOV RHBAE,$BDDAT ;READ RHBAE FOR COMPARISON
9221 026416 023737 001124 001126 CMP $GDDAT,$BDDAT ;COMPARE EXPECTED
9222 ;DATA WITH DATA READ FROM
9223 ;RHBAE
9224 BEQ 77$ ;BRANCH IF GOOD
9225 026424 001401 ERROR 122
9226 026426 104122
9227
9228 ;AFTER SETTING "CLR" BIT #5
9229 ;IN RHCS2 TO INIT THE RH
9230 ;ONE WORD OF ALL ONES IS
9231 ;WRITTEN ON THE DEVICE
9232 ;A WRITE CHECK WAS DONE
9233 ;ON AN ODD WORD BOUNDARY THEN
9234 ;AN RH CLEAR (RHCS2 BIT #5)
9235 ;WAS GIVEN THIS SHOULD
9236 ;CLEAR ALL ERRORS
9237 ;RHBAE SHOULD HAVE 0
9238 ;BUT CONTAINED WHAT IS
9239 ;GIVEN IN BAD RHBAE
9240 026430 77$:
9241
9242 026430 012737 000000 001124 ;CHECK THAT RHWC HAS 0
9243 MOV #0,$GDDAT ;GET GOOD = 0
9244
9245 026436 017737 155420 001126 MOV RHWC,$BDDAT ;READ RHWC FOR COMPARISON
9246 026444 023737 001124 001126 CMP $GDDAT,$BDDAT ;COMPARE EXPECTED
9247 ;DATA WITH DATA READ FROM
9248 ;RHWC
9249 BEQ 79$ ;BRANCH IF GOOD
9250 026452 001401 ERROR 123
9251 026454 104123
9252
9253 ;AFTER SETTING "CLR" BIT #5
9254 ;IN RHCS2 TO INIT THE RH
9255 ;ONE WORD OF ALL ONES IS
9256 ;WRITTEN ON THE DEVICE
9257 ;A WRITE CHECK WAS DONE
9258 ;ON AN ODD WORD BOUNDARY THEN
9259 ;AN RH CLEAR (RHCS2 BIT #5)
9260 ;WAS GIVEN THIS SHOULD
9261 ;CLEAR ALL ERRORS

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;RHC SHOULD HAVE 0
;BUT CONTAINED WHAT IS
;GIVEN IN BAD RHC

9256
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9259 026456 795:

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::*****
:*TEST 35 RHCS2-WCE BIT #14 AND RHCS3-WCE OW BIT #12

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;* CLEAR THE SUBSYSTEM BY A CONTROLLER CLEAR (RHCS2 BIT #5)
;* WRITE NINE WORD OF ALL NINES ON TO THE DEVICE
;* SET UP TO DO WRITE CHECK ON THAT WORD FROM AN
;* ODD WORD BOUNDARY
;* DO A ONE WORD WRITE CHECK WITH RHBA FROM AN
;* ODD WORD BOUNDARY
;* THIS SHOULD SET WCE OW (RHCS3 BIT #12)
;* AND WCE (RHCS2 BIT #14)
;* "CLR" (RHCS2 BIT #5) IS GIVEN
;* ALL ERRORS ARE CLEARED
;* CHECK RHCS1, RHCS2, RHCS3, RHBA, RHBAE, RHC

9279 026456 000004
9280 026460 012706 001003
9281 026464 012737 000035 004656
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9283 026472 004737 040312
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9288 026476 012737 177777 004210
9289 026504 012777 177767 155350
9290 026512 012777 004210 155344
9291
9292 026520 004077 156114
9293 026524 004154
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9296 026526 005037 003002
9297 026532 012777 177767 155322
9298 026540 012777 003002 155316
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9301 026546 004077 156066
9302 026552 004150
9303
9304
9305
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9307
9308 026554 012737 040300 001124
9309 026562 053737 005010 001124
9310
9311 026570 017737 155274 001126

::*****
*ST35: SCOPE
MOV #STACK, SP ;RESET STACK
MOV #35, @#STNM ;SAVE TEST NUMBER

JSR PC, @#CLDISK ;GIVE RH INITIALIZE
;SETUP UNIT NUBER
;CLEAR RHC AND FUNCTION BITS IN RHCS1

;WRITE ONE WORD OF ALL ONES ON THE DEVICE
MOV #-1, WRFROM ;ALL ONES INTO WRITE FROM
MOV #-9, @RHWC ;NINE WORD FOR WORD COUNT REGISTER
MOV #WRFROM, @RHBA ;WRITE FROM BUFFER INTO BUS ADDRESS

JSR RC, @COMND ;GO TO DO COMMAND
WRIDAT ;WRITE DATA

;SET UP FOR WRITE CHECK
CLR BUFOW ;WRITE DATA FOR WRITE CHECK ERROR
MOV #-9, @RHWC ;NINE WORD FOR WORD COUNT REGISTER
MOV #BUFOW, @RHBA ;WRITE FROM BUFFER INTO BUS ADDRESS
;FROM ODD WORD BOUNDARY

;WRITE CHECK ON ODD WORD BOUNDARY
JSR RC, @COMND ;GO TO DO COMMAND
WRCHK ;WRITE CHECK DATA

;CHECK THAT RHCS2 HAS WCE!OR!IR
MOV #WCE!OR!IR, \$GDDAT ;GET GOOD = 40300
BIS UNIT, \$GDDAT ;INCLUDE UNIT NUMBER

MOV @RHCS2, \$BDDAT ;READ RHCS2 FOR COMPARISON


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9368 ; ONE WORD OF ALL ONES IS
9369 ; WRITTEN ON THE DEVICE
9370 ; A WRITE CHECK WAS DONE
9371 ; ON AN ODD WORD BOUNDARY THEN
9372 ; AN RH CLEAR (RHCS2 BIT #5)
9373 ; WAS GIVEN THIS SHOULD
9374 ; CLEAR ALL ERRORS
9375 ; RHCS1 SHOULD HAVE RDY!DVA
9376 ; =4200
9377 ; BUT CONTAINED WHAT IS
9378 ; GIVEN IN BAD RHCS1
9379 026670          69$:
9380 ; CHECK THAT RHCS2 HAS IR
9381 026670 012737 000100 001124  MOV    #IR,$GDDAT  ; GET GOOD = 100
9382 026676 053737 005010 001124  BIS    UNIT,$GDDAT ; INCLUDE UNIT NUMBER
9383
9384 026704 017737 1551E0 001126  MOV    @RHCS2,$BDDAT ; READ RHCS2 FOR COMPARISON
9385 026712 023737 001124 001126  CMP    $GDDAT,$BDDAT ; COMPARE EXPECTED
9386 ; DATA WITH DATA READ FROM
9387 ; RHCS2
9388 026720 001401          BEQ    71$          ; BRANCH IF GOOD
9389 026722 104117          ERROR  117
9390
9391 ; AFTER SETTING "CLR" BIT #5
9392 ; IN RHCS2 TO INIT THE RH
9393 ; ONE WORD OF ALL ONES IS
9394 ; WRITTEN ON THE DEVICE
9395 ; A WRITE CHECK WAS DONE
9396 ; ON AN ODD WORD BOUNDARY THEN
9397 ; AN RH CLEAR (RHCS2 BIT #5)
9398 ; WAS GIVEN THIS SHOULD
9399 ; CLEAR ALL ERRORS
9400 ; RHCS2 SHOULD HAVE IR
9401 ; =100
9402 ; TOGETHER WITH UNIT NUMBER
9403 ; EJT CONTAINED WHAT IS
9404 ; GIVEN IN BAD RHCS2
9404 026724          71$:
9405 ; CHECK THAT RHCS3 HAS 0
9406 026724 012737 000000 001124  MOV    #0,$GDDAT  ; GET GOOD = 0
9407
9408 026732 017737 155174 001126  MOV    @RHCS3,$BDDAT ; READ RHCS3 FOR COMPARISON
9409 026740 023737 001124 001126  CMP    $GDDAT,$BDDAT ; COMPARE EXPECTED
9410 ; DATA WITH DATA READ FROM
9411 ; RHCS3
9412 026746 001401          BEQ    73$          ; BRANCH IF GOOD
9413 026750 104120          ERROR  120
9414
9415 ; AFTER SETTING "CLR" BIT #5
9416 ; IN RHCS2 TO INIT THE RH
9417 ; ONE WORD OF ALL ONES IS
9418 ; WRITTEN ON THE DEVICE
9419 ; A WRITE CHECK WAS DONE
9420 ; ON AN ODD WORD BOUNDARY THEN
9421 ; AN RH CLEAR (RHCS2 BIT #5)
9422 ; WAS GIVEN THIS SHOULD
9423 ; CLEAR ALL ERRORS
9423 ; RHCS3 SHOULD HAVE 0

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9424                                     ;BUT CONTAINED WHAT IS
9425                                     ;GIVEN IN BAD RHCS3
9426 026752                               73$:
9427                                     ;CHECK THAT RHBA HAS 0
9428 026752 012737 000000 001124        MOV     #0,$GDDAT      ;GET GOOD = 0
9429
9430 026750 017737 155100 001126        MOV     @RHBA,$BDDAT   ;READ RHBA FOR COMPARISON
9431 026756 023737 001124 001126        CMP     $GDDAT,$BDDAT ;COMPARE EXPECTED
9432                                     ;DATA WITH DATA READ FROM
9433                                     ;RHBA
9434 026774 001401                          BEQ     75$            ;BRANCH IF GOOD
9435 026776 104121                          ERROR   121
9436                                     ;AFTER SETTING "CLR" BIT #5
9437                                     ;IN RHCS2 TO INIT THE RH
9438                                     ;ONE WORD OF ALL ONES IS
9439                                     ;WRITTEN ON THE DEVICE
9440                                     ;A WRITE CHECK WAS DONE
9441                                     ;ON AN ODD WORD BOUNDARY THEN
9442                                     ;AN RH CLEAR (RHCS2 BIT #5)
9443                                     ;WAS GIVEN THIS SHOULD
9444                                     ;CLEAR ALL ERRORS
9445                                     ;RHBA SHOULD HAVE 0
9446                                     ;BUT CONTAINED WHAT IS
9447                                     ;GIVEN IN BAD RHBA
9448 027000                               75$:
9449                                     ;CHECK THAT RHBAE HAS 0
9450 027000 012737 000000 001124        MOV     #0,$GDDAT      ;GET GOOD = 0
9451
9452 027006 017737 155116 001126        MOV     @RHBAE,$BDDAT ;READ RHBAE FOR COMPARISON
9453 027014 023737 001124 001126        CMP     $GDDAT,$BDDAT ;COMPARE EXPECTED
9454                                     ;DATA WITH DATA READ FROM
9455                                     ;RHBAE
9456 027022 001401                          BEQ     77$            ;BRANCH IF GOOD
9457 027024 104122                          ERROR   122
9458                                     ;AFTER SETTING "CLR" BIT #5
9459                                     ;IN RHCS2 TO INIT THE RH
9460                                     ;ONE WORD OF ALL ONES IS
9461                                     ;WRITTEN ON THE DEVICE
9462                                     ;A WRITE CHECK WAS DONE
9463                                     ;ON AN ODD WORD BOUNDARY THEN
9464                                     ;AN RH CLEAR (RHCS2 BIT #5)
9465                                     ;WAS GIVEN THIS SHOULD
9466                                     ;CLEAR ALL ERRORS
9467                                     ;RHBAE SHOULD HAVE 0
9468                                     ;BUT CONTAINED WHAT IS
9469                                     ;GIVEN IN BAD RHBAE
9470 027026                               77$:
9471                                     ;CHECK THAT RHWC HAS 0
9472 027026 012737 000000 001124        MOV     #0,$GDDAT      ;GET GOOD = 0
9473
9474 027034 017737 155022 001126        MOV     @RHWC,$BDDAT  ;READ RHWC FOR COMPARISON
9475 027042 023737 001124 001126        CMP     $GDDAT,$BDDAT ;COMPARE EXPECTED
9476                                     ;DATA WITH DATA READ FROM
9477                                     ;RHWC
9478 027050 001401                          BEQ     79$            ;BRANCH IF GOOD
9479 027052 104123                          ERROR   123

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795:

; AFTER SETTING "CLR" BIT #5
; IN RHCS2 TO INIT THE RH
; ONE WORD OF ALL ONES IS
; WRITTEN ON THE DEVICE
; A WRITE CHECK WAS DONE
; ON AN ODD WORD BOUNDARY THEN
; AN RH CLEAR (RHCS2 BIT #5)
; WAS GIVEN THIS SHOULD
; CLEAR ALL ERRORS
; RHC SHOULD HAVE 0
; BUT CONTAINED WHAT IS
; GIVEN IN BAD RHC

; *TEST 36 RHCS2-WCE BIT #14 AND RHCS3-WCE EW BIT #11

; * CLEAR THE SUBSYSTEM BY A CONTROLLER CLEAR (RHCS2 BIT #5)
; * WRITE NINE WORD OF ALL NINES ON TO THE DEVICE
; * SET UP TO DO WRITE CHECK ON THAT WORD FROM AN
; * EVEN WORD BOUNDARY
; * DO A ONE WORD WRITE CHECK WITH RHBA FROM AN
; * EVEN WORD BOUNDARY
; * THIS SHOULD SET WCE EW (RHCS3 BIT #11)
; * AND WCE (RHCS2 BIT #14)
; * "CLR" (RHCS2 BIT #5) IS GIVEN
; * ALL ERRORS ARE CLEARED
; * CHECK RHCS1, RHCS2, RHCS3, RHBA, RHBAE, RHC

; *T36: SCOPE

027054 000004
027056 012706 001000
027062 012737 000036 004656

027070 004737 040312

027074 012737 177777 004210
027102 012777 177767 154752
027110 012777 004210 154746

027116 004077 155516
027122 004154

027124 005037 003000
027130 012777 177767 154724
027136 012777 003000 154720

027144 004077 155470
027150 004154

MOV #STACK, SP ; RESET STACK
MOV #36, #TSTNM ; SAVE TEST NUMBER

JSR PC, #CLDISK ; GIVE RH INITIALIZE
; SETUP UNIT NUMBER
; CLEAR RHC AND FUNCTION BITS IN RHCS1

; WRITE ONE WORD OF ALL ONES ON THE DEVICE
MOV #-1, WRFROM ; ALL ONES INTO WRITE FROM
MOV #-9, #RHWC ; NINE WORD FOR WORD COUNT REGISTER
MOV #WRFROM, #RHBA ; WRITE FROM BUFFER INTO BUS ADDRESS

JSR RO, #COMND ; GO TO DO COMMAND
WRIDAT ; WRITE DATA

; SET UP FOR WRITE CHECK
CLR BUFEW ; WRITE DATA FOR WRITE CHECK ERROR
MOV #-9, #RHWC ; NINE WORD FOR WORD COUNT REGISTER
MOV #BUFEW, #RHBA ; WRITE FROM BUFFER INTO BUS ADDRESS
; FROM EVEN WORD BOUNDARY

; WRITE CHECK ON EVEN WORD BOUNDARY
JSR RO, #COMND ; GO TO DO COMMAND
WRCHK ; WRITE CHECK DATA

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RHCS2-WCE BIT #14 AND RHCS3-WCE EW BIT #11

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9541 027152 012737 040300 001124      ;CHECK THAT RHCS2 HAS WCE!OR!IR
9542 027160 053737 005010 001124      MOV      #WCE!OR!IR,$GDDAT      ;GET GOOD = 40300
9543                                     BIS      UNIT,$GDDAT      ;INCLUDE UNIT NUMBER
9544 027166 017737 154676 001126      MOV      @RHCS2,$BDDAT      ;READ RHCS2 FOR COMPARISON
9545 027174 023737 001124 001126      CMP      $GDDAT,$BDDAT      ;COMPARE EXPECTED
9546                                     ;DATA WITH DATA READ FROM
9547                                     ;RHCS2
9548 027202 001401                                     BEQ      65$      ;BRANCH IF GOOD
9549 027204 104114                                     ERROR    114
9550                                     ;AFTER SETTING "CLR" BIT #5
9551                                     ;IN RHCS2 TO INIT THE RH
9552                                     ;ONE WORD OF ALL ONES IS
9553                                     ;WRITTEN ON THE DEVICE
9554                                     ;A WRITE CHECK WAS DONE
9555                                     ;ON AN EVEN WORD BOUNDARY THEN
9556                                     ;RHCS2 SHOULD HAVE WCE!OR!IR
9557                                     ;=40300
9558                                     ;TOGETHER WITH UNIT NUMBER
9559                                     ;BUT CONTAINED WHAT IS
9560                                     ;GIVEN IN BAD RHCS2
9561 027206                                     65$:
9562
9563                                     ;CHECK THAT RHCS3 HAS DBL!WCEEW
9564 027206 012737 006000 001124      MOV      #DBL!WCEEW,$GDDAT      ;GET GOOD = 6000
9565
9566 027214 017737 154712 001126      MOV      @RHCS3,$BDDAT      ;READ RHCS3 FOR COMPARISON
9567 027222 023737 001124 001126      CMP      $GDDAT,$BDDAT      ;COMPARE EXPECTED
9568                                     ;DATA WITH DATA READ FROM
9569                                     ;RHCS3
9570 027230 001401                                     BEQ      67$      ;BRANCH IF GOOD
9571 027232 104115                                     ERROR    115
9572                                     ;AFTER SETTING "CLR" BIT #5
9573                                     ;IN RHCS2 TO INIT THE RH
9574                                     ;ONE WORD OF ALL ONES IS
9575                                     ;WRITTEN ON THE DEVICE
9576                                     ;A WRITE CHECK WAS DONE
9577                                     ;ON AN EVEN WORD BOUNDARY THEN
9578                                     ;RHCS3 SHOULD HAVE DBL!WCEEW
9579                                     ;=6000
9580                                     ;BUT CONTAINED WHAT IS
9581                                     ;GIVEN IN BAD RHCS3
9582 027234                                     67$:
9583
9584
9585 027234 004737 040312      JSR      PC,@#CLDISK      ;GIVE RH INITIALIZE
9586                                     ;SETUP UNIT NUBER
9587                                     ;CLEAR RHWC AND FUNCTION BITS IN RHCS1
9588
9589
9590                                     ;CHECK THAT RHCS1 HAS RDY!DVA
9591 027240 012737 004200 001124      MOV      #RDY!DVA,$GDDAT      ;GET GOOD = 4200

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9592
9593 027246 017737 154606 001126      MOV      @RHCS1,$BDDAT      ;READ RHCS1 FOR COMPARISON
9594 027254 023737 001124 001126      CMP      $GDDAT,$BDDAT    ;COMPARE EXPECTED
9595                                     ;DATA WITH DATA READ FROM
9596                                     ;RHCS1
9597 027262 001401      BEQ      69$              ;BRANCH IF GOOD
9598 027264 104115      ERROR   116
9599                                     ;AFTER SETTING "CLR" BIT #5
9600                                     ;IN RHCS2 TO INIT THE RH
9601                                     ;ONE WORD OF ALL ONES IS
9602                                     ;WRITTEN ON THE DEVICE
9603                                     ;A WRITE CHECK WAS DONE
9604                                     ;ON AN EVEN WORD BOUNDARY THEN
9605                                     ;AN RH CLEAR (RHCS2 BIT #5)
9606                                     ;WAS GIVEN THIS SHOULD
9607                                     ;CLEAR ALL ERRORS
9608                                     ;RHCS1 SHOULD HAVE RDY!JVA
9609                                     ;=4200
9610                                     ;BUT CONTAINED WHAT IS
9611                                     ;GIVEN IN BAD RHCS1
9612 027266                                     69$:
9613                                     ;CHECK THAT RHCS2 HAS IR
9614 027266 012737 000100 001124      MOV      #IR,$GDDAT      ;GET GOOD = 100
9615 027274 053737 005010 001124      BIS      UNIT,$GDDAT    ;INCLUDE UNIT NUMBER
9616
9617 027302 017737 154562 001126      MOV      @RHCS2,$BDDAT  ;READ RHCS2 FOR COMPARISON
9618 027310 023737 001124 001126      CMP      $GDDAT,$BDDAT  ;COMPARE EXPECTED
9619                                     ;DATA WITH DATA READ FROM
9620                                     ;RHCS2
9621 027316 001401      BEQ      71$              ;BRANCH IF GOOD
9622 027320 104117      ERROR   117
9623                                     ;AFTER SETTING "CLR" BIT #5
9624                                     ;IN RHCS2 TO INIT THE RH
9625                                     ;ONE WORD OF ALL ONES IS
9626                                     ;WRITTEN ON THE DEVICE
9627                                     ;A WRITE CHECK WAS DONE
9628                                     ;ON AN EVEN WORD BOUNDARY THEN
9629                                     ;AN RH CLEAR (RHCS2 BIT #5)
9630                                     ;WAS GIVEN THIS SHOULD
9631                                     ;CLEAR ALL ERRORS
9632                                     ;RHCS2 SHOULD HAVE IR
9633                                     ;=100
9634                                     ;TOGETHER WITH UNIT NUMBER
9635                                     ;BUT CONTAINED WHAT IS
9636                                     ;GIVEN IN BAD RHCS2
9637 027322                                     71$:
9638                                     ;CHECK THAT RHCS3 HAS 0
9639 027322 012737 000000 001124      MOV      #0,$GDDAT      ;GET GOOD = 0
9640
9641 027330 017737 154576 001126      MOV      @RHCS3,$BDDAT  ;READ RHCS3 FOR COMPARISON
9642 027336 023737 001124 001126      CMP      $GDDAT,$BDDAT  ;COMPARE EXPECTED
9643                                     ;DATA WITH DATA READ FROM
9644                                     ;RHCS3
9645 027344 001401      BEQ      73$              ;BRANCH IF GOOD
9646 027346 104120      ERROR   120
9647                                     ;AFTER SETTING "CLR" BIT #5

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9648 ; IN RHCS2 TO INIT THE RH
9649 ; ONE WORD OF ALL ONES IS
9650 ; WRITTEN ON THE DEVICE
9651 ; A WRITE CHECK WAS DONE
9652 ; ON AN EVEN WORD BOUNDARY THEN
9653 ; AN RH CLEAR (RHCS2 BIT #5)
9654 ; WAS GIVEN THIS SHOULD
9655 ; CLEAR ALL ERRORS
9656 ; RHCS3 SHOULD HAVE 0
9657 ; BUT CONTAINED WHAT IS
9658 ; GIVEN IN BAD RHCS3
9659 027350 73$:
9660 ; CHECK THAT RHBA HAS 0
9661 027350 012737 000000 001124 MOV #0,$GDDAT ; GET GOOD = 0
9662
9663 027356 017737 154502 001126 MOV @RHBA,$BDDAT ; READ RHBA FOR COMPARISON
9664 027364 023737 001124 001126 CMP $GDDAT,$BDDAT ; COMPARE EXPECTED
9665 ; DATA WITH DATA READ FROM
9666 ; RHBA
9667 027372 001401 BEQ 75$ ; BRANCH IF GOOD
9668 027374 104121 ERROR 121
9669
9670 ; AFTER SETTING "CLR" BIT #5
9671 ; IN RHCS2 TO INIT THE RH
9672 ; ONE WORD OF ALL ONES IS
9673 ; WRITTEN ON THE DEVICE
9674 ; A WRITE CHECK WAS DONE
9675 ; ON AN EVEN WORD BOUNDARY THEN
9676 ; AN RH CLEAR (RHCS2 BIT #5)
9677 ; WAS GIVEN THIS SHOULD
9678 ; CLEAR ALL ERRORS
9679 ; RHBA SHOULD HAVE 0
9680 ; BUT CONTAINED WHAT IS
9681 ; GIVEN IN BAD RHBA
9681 027376 75$:
9682 ; CHECK THAT RHBAE HAS 0
9683 027376 012737 000000 001124 MOV #0,$GDDAT ; GET GOOD = 0
9684
9685 027404 017737 154520 001126 MOV @RHBAE,$BDDAT ; READ RHBAE FOR COMPARISON
9686 027412 023737 001124 001126 CMP $GDDAT,$BDDAT ; COMPARE EXPECTED
9687 ; DATA WITH DATA READ FROM
9688 ; RHBAE
9689 027420 001401 BEQ 77$ ; BRANCH IF GOOD
9690 027422 104122 ERROR 122
9691
9692 ; AFTER SETTING "CLR" BIT #5
9693 ; IN RHCS2 TO INIT THE RH
9694 ; ONE WORD OF ALL ONES IS
9695 ; WRITTEN ON THE DEVICE
9696 ; A WRITE CHECK WAS DONE
9697 ; ON AN EVEN WORD BOUNDARY THEN
9698 ; AN RH CLEAR (RHCS2 BIT #5)
9699 ; WAS GIVEN THIS SHOULD
9700 ; CLEAR ALL ERRORS
9701 ; RHBAE SHOULD HAVE 0
9702 ; BUT CONTAINED WHAT IS
9703 ; GIVEN IN BAD RHBAE
9703 027424 77$:

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DERHAB.SRC T36

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RHCS2-WCE BIT #14 AND RHCS3-WCE EW BIT #11

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9704                                     ;CHECK THAT RHWC HAS 0
9705 027424 012737 000000 001124      MOV      #0,$GDDAT      ;GET GOOD = 0
9706
9707 027432 012737 154424 001126      MOV      @RHWC,$BDDAT   ;READ RHWC FOR COMPARISON
9708 027440 023737 001124 001126      CMP      $GDDAT,$BDDAT ;COMPARE EXPECTED
9709                                     ;DATA WITH DATA READ FROM
9710                                     ;RHWC
9711 027446 001401                      BEQ      79$            ;BRANCH IF GOOD
9712 027450 104123                      ERROR   123
9713                                     ;AFTER SETTING "CLR" BIT #5
9714                                     ;IN RHCS2 TO INIT THE RH
9715                                     ;ONE WORD OF ALL ONES IS
9716                                     ;WRITTEN ON THE DEVICE
9717                                     ;A WRITE CHECK WAS DONE
9718                                     ;ON AN EVEN WORD BOUNDARY THEN
9719                                     ;AN RH CLEAR (RHCS2 BIT #5)
9720                                     ;WAS GIVEN THIS SHOULD
9721                                     ;CLEAR ALL ERRORS
9722                                     ;RHWC SHOULD HAVE 0
9723                                     ;BUT CONTAINED WHAT IS
9724                                     ;GIVEN IN BAD RHWC
9725 027452                               79$:
9726
9727
9728
9729                                     ;*****
9730                                     ;*TEST 37      RHCS2-WCE BIT #14 AND RHCS3-WCE EW BIT #11
9731
9732                                     ;*
9733                                     ;*      CLEAR THE SUBSYSTEM BY A CONTROLLER CLEAR (RHCS2 BIT #5)
9734                                     ;*      WRITE NINE WORD OF ALL NINES ON TO THE DEVICE
9735                                     ;*      SET UP TO DO WRITE CHECK ON THAT WORD FROM AN
9736                                     ;*      EVEN WORD BOUNDARY
9737                                     ;*      DO A ONE WORD WRITE CHECK WITH RHBA FROM AN
9738                                     ;*      EVEN WORD BOUNDARY
9739                                     ;*      THIS SHOULD SET WCE EW (RHCS3 BIT #11)
9740                                     ;*      AND WCE (RHCS2 BIT #14)
9741                                     ;*      "CLR" (RHCS2 BIT #5) IS GIVEN
9742                                     ;*      ALL ERRORS ARE CLEARED
9743                                     ;*      CHECK RHCS1, RHCS2, RHCS3, RHBA, RHBAE, RHWC
9744                                     ;*****
9745 027452 000004      †ST37: SCOPE
9746 027454 012706 001000      MOV      #STACK,SP      ;RESET STACK
9747 027460 012737 000037 004656      MOV      #37,@†STNM    ;SAVE TEST NUMBER
9748
9749 027466 004737 040312      JSR      PC,@#CLDISK   ;GIVE RH INITIALIZE
9750                                     ;SETUP UNIT NUBER
9751                                     ;CLEAR RHWC AND FUNCTION BITS IN RHCS1
9752
9753                                     ;WRITE ONE WORD OF ALL ONES ON THE DEVICE
9754 027472 012737 177777 004210      MOV      #-1,WRFROM    ;ALL ONES INTO WRITE FROM
9755 027500 012777 177767 154354      MOV      #-9,@RHWC     ;NINE WORD FOR WORD COUNT REGISTER
9756 027506 012777 004210 154350      MOV      #WRFROM,@RHBA ;WRITE FROM BUFFER INTO BUS ADDRESS
9757
9758 027514 004077 155120      JSR      R0,@COMND     ;GO TO DO COMMAND
9759 027520 004154      WRIDAT                ;WRITE DATA

```




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9816
9817
9818 027632 004737 040312 JSR PC,0#CLDISK ;GIVE RH INITIALIZE
9819 ;SETUP UNIT NUMBER
9820 ;CLEAR RHWC AND FUNCTION BITS IN RHCS1
9821
9822
9823
9824 ;CHECK THAT RHCS1 HAS RDY!DVA
027636 012737 004200 001124 MOV #RDY!DVA,$GDDAT ;GET GOOD = 4200
9825
9826 027644 017737 154210 001126 MOV @RHCS1,$BDDAT ;READ RHCS1 FOR COMPARISON
9827 027652 023737 001124 001126 CMP $GDDAT,$BDDAT ;COMPARE EXPECTED
9828 ;DATA WITH DATA READ FROM
9829 ;RHCS1
9830 027660 001401 BEQ 69$ ;BRANCH IF GOOD
9831 027662 104116 ERROR 116
9832
9833 ;AFTER SETTING "CLR" BIT #5
9834 ;IN RHCS2 TO INIT THE RH
9835 ;ONE WORD OF ALL ONES IS
9836 ;WRITTEN ON THE DEVICE
9837 ;A WRITE CHECK WAS DONE
9838 ;ON AN EVEN WORD BOUNDARY THEN
9839 ;AN RH CLEAR (RHCS2 BIT #5)
9840 ;WAS GIVEN THIS SHOULD
9841 ;CLEAR ALL ERRORS
9842 ;RHCS1 SHOULD HAVE RDY!DVA
9843 ;=4200
9844 ;BUT CONTAINED WHAT IS
9845 ;GIVEN IN BAD RHCS1
027664 69$:
9846
9847 027664 012737 000100 001124 ;CHECK THAT RHCS2 HAS IR
9848 027672 053737 005010 001124 MOV #IR,$GDDAT ;GET GOOD = 100
9849 BIS UNIT,$GDDAT ;INCLUDE UNIT NUMBER
9850
9851 027700 017737 154164 001126 MOV @RHCS2,$BDDAT ;READ RHCS2 FOR COMPARISON
9852 027706 023737 001124 001126 CMP $GDDAT,$BDDAT ;COMPARE EXPECTED
9853 ;DATA WITH DATA READ FROM
9854 ;RHCS2
9855 027714 001401 BEQ 71$ ;BRANCH IF GOOD
9856 027716 104117 ERROR 117
9857
9858 ;AFTER SETTING "CLR" BIT #5
9859 ;IN RHCS2 TO INIT THE RH
9860 ;ONE WORD OF ALL ONES IS
9861 ;WRITTEN ON THE DEVICE
9862 ;A WRITE CHECK WAS DONE
9863 ;ON AN EVEN WORD BOUNDARY THEN
9864 ;AN RH CLEAR (RHCS2 BIT #5)
9865 ;WAS GIVEN THIS SHOULD
9866 ;CLEAR ALL ERRORS
9867 ;RHCS2 SHOULD HAVE IR
9868 ;=100
9869 ;TOGETHER WITH UNIT NUMBER
9870 ;BUT CONTAINED WHAT IS
9871 ;GIVEN IN BAD RHCS2
027720 71$:
9872 ;CHECK THAT RHCS3 HAS 0

```



```

9928                                     :A WRITE CHECK WAS DONE
9929                                     :ON AN EVEN WORD BOUNDARY THEN
9930                                     :AN RH CLEAR (RHCS2 BIT #5)
9931                                     :WAS GIVEN THIS SHOULD
9932                                     :CLEAR ALL ERRORS
9933                                     :RHBAE SHOULD HAVE 0
9934                                     :BUT CONTAINED WHAT IS
9935                                     :GIVEN IN BAD RHBAE
9936 030022                                77$:
9937                                     :CHECK THAT RHWG HAS 0
9938 030022 012737 000000 001124          MOV      #0,$GDDAT      ;GET GOOD = 0
9939
9940 030033 017737 154026 001126          MOV      @RHWG,$BDDAT  ;READ RHWG FOR COMPARISON
9941 030036 023737 001124 001126          CMP      $GDDAT,$BDDAT ;COMPARE EXPECTED
9942                                     :DATA WITH DATA READ FROM
9943                                     :RHWG
9944 030044 001401                          BEQ      79$           ;BRANCH IF GOOD
9945 030046 104123
9946
9947                                     :AFTER SETTING "CLR" BIT #5
9948                                     :IN RHCS2 TO INIT THE RH
9949                                     :ONE WORD OF ALL ONES IS
9950                                     :WRITTEN ON THE DEVICE
9951                                     :A WRITE CHECK WAS DONE
9952                                     :ON AN EVEN WORD BOUNDARY THEN
9953                                     :AN RH CLEAR (RHCS2 BIT #5)
9954                                     :WAS GIVEN THIS SHOULD
9955                                     :CLEAR ALL ERRORS
9956                                     :RHWG SHOULD HAVE 0
9957                                     :BUT CONTAINED WHAT IS
9958                                     :GIVEN IN BAD RHWG
9959
9960 030050                                79$:
9961
9962
9963
9964
9965
9966
9967
9968
9969
9970
9971
9972
9973
9974 030050 000004                                ;*****
9975 030052 012706 001000                                ;*TEST 40      TEST DBL (RHCS3 BIT #10) TEST A
9976 030056 012737 000040 004656                                ;*
9977                                     ;* CLEAR THE SUBSYSTEM BY A CONTROLLER CLEAR (RHCS2 BIT #5)
9978                                     ;* SET UP FOR A NINE WORD WRITE FROM AN EVEN WORD BOUNDARY
9979                                     ;* DO A NINE WORD WRITE FROM AN EVEN WORD BOUNDARY
9980                                     ;* THIS SHOULD NOT SET RHCS3 BIT #10 DBL
9981                                     ;* CHECK RHCS3
9982                                     ;* CHECK RHCS1,RHCS2,RHBA,RHBAE,RHWG
9983
9984
9985
9986
9987
9988
9989
9990
9991
9992
9993
9994
9995
9996
9997
9998
9999

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9994 030074 012702 003000      MOV      #BFEVEN,R2      ;START THREE WORD BUFFER
9995                                ;FROM AN EVEN WORD BOUNDARY
9996 030100 012722 052525      1$:  MOV      #52525,(R2)+  ;MOVE THREE 52525 INTO BUFFER
9997 030104 005301      DEC      R1              ;COUNT
9998 030106 001374      BNE      1$              ;BRANCH IF THREE NOT DONE
9999 030110 012777 003000 153746  MOV      #BFEVEN,RHBA    ;SET BUS ADDRESS TO START
9990                                ;FROM AN EVEN WORD BOUNDARY
9991 030116 012777 177767 153736  MOV      #-9,RHWC        ;WORD COUNT NINE
9992 030124 004077 154510      JSR      RD,ACOMND       ;GO TO DO COMMAND
9993 030130 004154      WRDAT                       ;WRITE DATA
9994
9995
9996                                ;CHECK THAT RHCS3 HAS 0
9997 030132 012737 000000 001124  MOV      #0,$GDDAT      ;GET GOOD = 0
9998
9999 030140 017737 153766 001126  MOV      RHCS3,$BDDAT    ;READ RHCS3 FOR COMPARISON
10000 030146 023737 001124 001126  CMP      $GDDAT,$BDDAT   ;COMPARE EXPECTED
10001                                ;DATA WITH DATA READ FROM
10002                                ;RHCS3
10003 030154 001401      BEQ      65$            ;BRANCH IF GOOD
10004 030156 104124      ERROR    124
10005                                ;AFTER SETTING "CLR" BIT #5
10006                                ;IN RHCS2 TO INIT THE RH
10007                                ;A NINE WORD WRITE FROM AN
10008                                ;EVEN WORD BOUNDARY WAS DONE
10009                                ;THEN
10010                                ;RHCS3 SHOULD HAVE 0
10011                                ;BUT CONTAINED WHAT IS
10012                                ;GIVEN IN BAD RHCS3
10013 030160      65$:
10014 030160 042777 000076 153672  BIC      #76,RHCS1      ;CLEAR FUNCTION BITS
10015                                ;CHECK THAT RHCS1 HAS RDY!DVA
10016 030166 012737 004200 001124  MOV      #RDY!DVA,$GDDAT ;GET GOOD = 4200
10017
10018 030174 017737 153660 001126  MOV      RHCS1,$BDDAT    ;READ RHCS1 FOR COMPARISON
10019 030202 023737 001124 001126  CMP      $GDDAT,$BDDAT   ;COMPARE EXPECTED
10020                                ;DATA WITH DATA READ FROM
10021                                ;RHCS1
10022 030210 001401      BEQ      67$            ;BRANCH IF GOOD
10023 030212 104125      ERROR    125
10024                                ;AFTER SETTING "CLR" BIT #5
10025                                ;IN RHCS2 TO INIT THE RH
10026                                ;A NINE WORD WRITE FROM AN
10027                                ;EVEN WORD BOUNDARY WAS DONE
10028                                ;THEN
10029                                ;RHCS1 SHOULD HAVE RDY!DVA
10030                                ;=4200
10031                                ;BUT CONTAINED WHAT IS
10032                                ;GIVEN IN BAD RHCS1
10033 030214      67$:
10034                                ;CHECK THAT RHCS2 HAS IR
10035 030214 012737 000100 001124  MOV      #IR,$GDDAT     ;GET GOOD = 100
10036 030222 053737 005010 001124  BIS      UNIT,$GDDAT     ;INCLUDE UNIT NUMBER
10037
10038 030230 017737 153634 001126  MOV      RHCS2,$BDDAT    ;READ RHCS2 FOR COMPARISON
10039 030236 023737 001124 001126  CMP      $GDDAT,$BDDAT   ;COMPARE EXPECTED

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10040 ;DATA WITH DATA READ FROM
10041 ;RHCS2
10042 030244 001401 BEQ 69$
10043 030246 104126 ERROR 126 ;BRANCH IF GOOD
10044
10045 ;AFTER SETTING "CLR" BIT #5
10046 ;IN RHCS2 TO INIT THE RH
10047 ;A NINE WORD WRITE FROM AN
10048 ;EVEN WORD BOUNDARY WAS DONE
10049 ;THEN
10050 ;RHCS2 SHOULD HAVE IR
10051 ;=100
10052 ;TOGETHER WITH UNIT NUMBER
10053 ;BUT CONTAINED WHAT IS
10054 030250 59$:
10055 ;CHECK THAT RHWC HAS 0
10056 030250 012737 000000 001124 MOV #0,$GDDAT ;GET GOOD = 0
10057
10058 030256 017737 153600 001126 MOV @RHWC,$BDDAT ;READ RHWC FOR COMPARISON
10059 030264 023737 001124 001126 CMP $GDDAT,$BDDAT ;COMPARE EXPECTED
10060 ;DATA WITH DATA READ FROM
10061 ;RHWC
10062 030272 001401 BEQ 71$
10063 030274 104131 ERROR 131 ;BRANCH IF GOOD
10064
10065 ;AFTER SETTING "CLR" BIT #5
10066 ;IN RHCS2 TO INIT THE RH
10067 ;A NINE WORD WRITE FROM AN
10068 ;EVEN WORD BOUNDARY WAS DONE
10069 ;THEN
10070 ;RHWC SHOULD HAVE 0
10071 ;BUT CONTAINED WHAT IS
10072 030276 71$:
10073
10074
10075
10076 ;*****
10077 ;*TEST 41 TEST DBL (RHCS3 BIT #10) TEST B
10078
10079 ;* CLEAR THE SUBSYSTEM BY A CONTROLLER CLEAR (RHCS2 BIT #5)
10080 ;* SET UP FOR A TEN WORD WRITE FROM AN EVEN WORD BOUNDARY
10081 ;* DO A TEN WORD WRITE FROM AN EVEN WORD BOUNDARY
10082 ;* THIS SHOULD SET RHCS3 BIT #10 DBL
10083 ;* CHECK RHCS3
10084 ;* CHECK RHCS1,RHCS2,RHBA,RHBAE,RHWC
10085
10086 ;*****
10087 030276 000004 †ST41: SCOPE
10088 030300 012706 001000 MOV #STACK,SP ;RESET STACK
10089 030304 012737 000041 004656 MOV #41,@†STNM ;SAVE TEST NUMBER
10090
10091 030312 004737 040312 JSR PC,@CLDISK ;GIVE RH INITIALIZE
10092 ;SETUP UNIT NUBER
10093 ;CLEAR RHWC AND FUNCTION BITS IN RHCS1
10094
10095 ;SET UP FOR A TEN WORD WRITE FROM AN EVEN WORD BOUNDARY

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10096 030316 012701 000003      MOV      #3,R1      ;COUNT OF THREE
10097 030322 012702 003000      MOV      #BF'EVEN,R2 ;START THREE WORD BUFFER
10098                                     ;FROM AN EVEN WORD BOUNDARY
10099 030326 012722 052525      1$: MOV      #52525,(R2)+ ;MOVE THREE 52525 INTO BUFFER
10100 030332 005301      DEC      R1         ;COUNT
10101 030334 001374      BNE     1$         ;BRANCH IF THREE NOT DONE
10102 030336 012777 003000 153520      MOV      #BF'EVEN,DRHBA ;SET BUS ADDRESS TO START
10103                                     ;FROM AN EVEN WORD BOUNDARY
10104 030344 012777 177766 153510      MOV      #-10,DRHWC ;WORD COUNT TEN
10105 030352 004077 154262      JSR     RD,DCOMND   ;GO TO DO COMMAND
10106 030356 004154      WRIDAT                ;WRITE DATA
10107
10108
10109                                     ;CHECK THAT RHCS3 HAS DBL
10110 030360 012737 002000 001124      MOV      #DBL,$GDDAT ;GET GOOD = 2000
10111
10112 030366 017737 153540 001126      MOV      DRHCS3,$BDDAT ;READ RHCS3 FOR COMPARISON
10113 030374 023737 001124 001126      CMP      $GDDAT,$BDDAT ;COMPARE EXPECTED
10114                                     ;DATA WITH DATA READ FROM
10115                                     ;RHCS3
10116 030402 001401      BEQ     65$        ;BRANCH IF GOOD
10117 030404 104124      ERROR   124
10118
10119                                     ;AFTER SETTING "CLR" BIT #5
10120                                     ;IN RHCS2 TO INIT THE RH
10121                                     ;A TEN WORD WRITE FROM AN
10122                                     ;EVEN WORD BOUNDARY WAS DONE
10123                                     ;THEN
10124                                     ;RHCS3 SHOULD HAVE DBL
10125                                     ;=2000
10126                                     ;BUT CONTAINED WHAT IS
10127 030406                                     65$:
10128 030406 042777 000076 153444      BIC      #76,DRHCS1 ;CLEAR FUNCTION BITS
10129                                     ;CHECK THAT RHCS1 HAS RDY!DVA
10130 030414 012737 004200 001124      MOV      #RDY!DVA,$GDDAT ;GET GOOD = 4200
10131
10132 030422 017737 153432 001126      MOV      DRHCS1,$BDDAT ;READ RHCS1 FOR COMPARISON
10133 030430 023737 001124 001126      CMP      $GDDAT,$BDDAT ;COMPARE EXPECTED
10134                                     ;DATA WITH DATA READ FROM
10135                                     ;RHCS1
10136 030436 001401      BEQ     67$        ;BRANCH IF GOOD
10137 030440 104125      ERROR   125
10138
10139                                     ;AFTER SETTING "CLR" BIT #5
10140                                     ;IN RHCS2 TO INIT THE RH
10141                                     ;A TEN WORD WRITE FROM AN
10142                                     ;EVEN WORD BOUNDARY WAS DONE
10143                                     ;THEN
10144                                     ;RHCS1 SHOULD HAVE RDY!DVA
10145                                     ;=4200
10146                                     ;BUT CONTAINED WHAT IS
10147 030442                                     67$:
10148                                     ;CHECK THAT RHCS2 HAS IR
10149 030442 012737 000100 001124      MOV      #IR,$GDDAT ;GET GOOD = 100
10150 030450 053737 005010 001124      BIS      UNIT,$GDDAT ;INCLUDE UNIT NUMBER
10151

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10152 030456 017737 153406 001126      MOV      @RHCS2,$BDDAT      ;READ RHCS2 FOR COMPARISON
10153 030464 023737 001124 001126      CMP      $GDDAT,$BDDAT     ;COMPARE EXPECTED
10154                                     ;DATA WITH DATA READ FROM
10155                                     ;RHCS2
10156 030472 001401      BEQ      69$               ;BRANCH IF GOOD
10157 030474 104126      ERROR   126
10158                                     ;AFTER SETTING "CLR" BIT #5
10159                                     ;IN RHCS2 TO INIT THE RH
10160                                     ;A TEN WORD WRITE FROM AN
10161                                     ;EVEN WORD BOUNDARY WAS DONE
10162                                     ;THEN
10163                                     ;RHCS2 SHOULD HAVE IR
10164                                     ;=100
10165                                     ;TOGETHER WITH UNIT NUMBER
10166                                     ;BUT CONTAINED WHAT IS
10167                                     ;GIVEN IN BAD RHCS2
10168 030476                                     69$:
10169                                     ;CHECK THAT RHWC HAS 0
10170 030476 012737 000000 001124      MOV      #0,$GDDAT         ;GET GOOD = 0
10171
10172 030504 017737 153352 001126      MOV      @RHWC,$BDDAT     ;READ RHWC FOR COMPARISON
10173 030512 023737 001124 001126      CMP      $GDDAT,$BDDAT     ;COMPARE EXPECTED
10174                                     ;DATA WITH DATA READ FROM
10175                                     ;RHWC
10176 030520 001401      BEQ      71$               ;BRANCH IF GOOD
10177 030522 104131      ERROR   131
10178                                     ;AFTER SETTING "CLR" BIT #5
10179                                     ;IN RHCS2 TO INIT THE RH
10180                                     ;A TEN WORD WRITE FROM AN
10181                                     ;EVEN WORD BOUNDARY WAS DONE
10182                                     ;THEN
10183                                     ;RHWC SHOULD HAVE 0
10184                                     ;BUT CONTAINED WHAT IS
10185                                     ;GIVEN IN BAD RHWC
10186 030524                                     71$:
10187
10188
10189
10190
10191
10192
10193
10194
10195
10196
10197
10198
10199
10200
10201 030524 000004      *ST42: SCOPE
10202 030526 012706 001000      MOV      #STACK,SP        ;RESET STACK
10203 030532 012737 000042 004656      MOV      #42,@#1STNM     ;SAVE TEST NUMBER
10204
10205 030540 004737 040312      JSR      PC,#CLDISK      ;GIVE RH INITIALIZE
10206                                     ;SETUP UNIT NUBER
10207                                     ;CLEAR RHWC AND FUNCTION BITS IN RHCS1

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*TEST 42 TEST DBL (RHCS3 BIT #10) TEST C

* CLEAR THE SUBSYSTEM BY A CONTROLLER CLEAR (RHCS2 BIT #5)
* SET UP FOR A TEN WORD WRITE FROM AN ODD WORD BOUNDARY
* DO A TEN WORD WRITE FROM AN ODD WORD BOUNDARY
* THIS SHOULD NOT SET RHCS3 BIT #10 DBL
* CHECK RHCS3
* CHECK RHCS1, RHCS2, RHBA, RHBAE, RHWC

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10208
10209
10210 030544 012701 000003      ;SET UP FOR A TEN WORD WRITE FROM AN ODD WORD BOUNDARY
10211 030550 012702 003002      MOV #3,R1 ;COUNT OF THREE
10212                                MOV #BF0DD,R2 ;START THREE WORD BUFFER
10213 030554 012722 052525      1$: MOV #52525,(R2)+ ;FROM AN ODD WORD BOUNDARY
10214 030560 005301                                ;MOVE THREE 52525 INTO BUFFER
10215 030562 001374                                DEC R1 ;COUNT
10216 030564 012777 003002 153272 BNE 1$ ;BRANCH IF THREE NOT DONE
10217                                MOV #BF0DD,@RHBA ;SET BUS ADDRESS TO START
10218 030572 012777 177766 153262 MOV #-10,@RHWC ;WORD COUNT TEN
10219 030600 004077 154034      JSR RO,@COMND ;GO TO DO COMMAND
10220 030604 004154      WRIDAT ;WRITE DATA
10221
10222
10223                                ;CHECK THAT RHCS3 HAS 0
10224 030606 012737 000000 001124 MOV #0,$GDDAT ;GET GOOD = 0
10225
10226 030614 017737 153312 001126 MOV @RHCS3,$BDDAT ;READ RHCS3 FOR COMPARISON
10227 030622 023737 001124 001126 CMP $GDDAT,$BDDAT ;COMPARE EXPECTED
10228                                ;DATA WITH DATA READ FROM
10229                                ;RHCS3
10230 030630 001401      BEQ 65$ ;BRANCH IF GOOD
10231 030632 104124      ERROR 124
10232                                ;AFTER SETTING "CLR" BIT #5
10233                                ;IN RHCS2 TO INIT THE RH
10234                                ;A TEN WORD WRITE FROM AN
10235                                ;ODD WORD BOUNDARY WAS DONE
10236                                ;THEN
10237                                ;RHCS3 SHOULD HAVE 0
10238                                ;BUT CONTAINED WHAT IS
10239                                ;GIVEN IN BAD RHCS3
10240 030634      65$:
10241 030634 042777 000076 153216 BIC #76,@RHCS1 ;CLEAR FUNCTION BITS
10242                                ;CHECK THAT RHCS1 HAS RDY!DVA
10243 030642 012737 004200 001124 MOV #RDY!DVA,$GDDAT ;GET GOOD = 4200
10244
10245 030650 017737 153204 001126 MOV @RHCS1,$BDDAT ;READ RHCS1 FOR COMPARISON
10246 030656 023737 001124 001126 CMP $GDDAT,$BDDAT ;COMPARE EXPECTED
10247                                ;DATA WITH DATA READ FROM
10248                                ;RHCS1
10249 030664 001401      BEQ 67$ ;BRANCH IF GOOD
10250 030666 104125      ERROR 125
10251                                ;AFTER SETTING "CLR" BIT #5
10252                                ;IN RHCS2 TO INIT THE RH
10253                                ;A TEN WORD WRITE FROM AN
10254                                ;ODD WORD BOUNDARY WAS DONE
10255                                ;THEN
10256                                ;RHCS1 SHOULD HAVE RDY!DVA
10257                                ;=4200
10258                                ;BUT CONTAINED WHAT IS
10259                                ;GIVEN IN BAD RHCS1
10260 030670      67$:
10261                                ;CHECK THAT RHCS2 HAS IR
10262 030670 012737 000100 001124 MOV #IR,$GDDAT ;GET GOOD = 100
10263 030676 053737 005010 001124 BIS UNIT,$GDDAT ;INCLUDE UNIT NUMBER

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10320                                     ;CLEAR RHWC AND FUNCTION BITS IN RHCS1
10321
10322                                     ;SET UP FOR A ELEVEN WORD WRITE FROM AN EVEN WORD BOUNDARY
10323 030772 012701 000003                MOV #3,R1 ;COUNT OF THREE
10324 030776 012702 003000                MOV #BFEVEN,R2 ;START THREE WORD BUFFER
10325                                     ;FROM AN EVEN WORD BOUNDARY
10326 031002 012722 052525                1$: MOV #52525,(R2)+ ;MOVE THREE 52525 INTO BUFFER
10327 031006 005301                        DEC R1 ;COUNT
10328 031010 001374                        BNE 1$ ;BRANCH IF THREE NOT DONE
10329 031012 012777 003000 153044        MOV #BFEVEN,DRHBA ;SET BUS ADDRESS TO START
10330                                     ;FROM AN EVEN WORD BOUNDARY
10331 031020 012777 177765 153034        MOV #-11,DRHWC ;WORD COUNT ELEVEN
10332 031026 004077 153606                JSR RD,DCOMND ;GO TO DO COMMAND
10333 031032 004154                        WRIDAT ;WRITE DATA
10334
10335
10336                                     ;CHECK THAT RHCS3 HAS 0
10337 031034 012737 000000 001124        MOV #0,$GDDAT ;GET GOOD = 0
10338
10339 031042 017737 153064 001126        MOV DRHCS3,$BDDAT ;READ RHCS3 FOR COMPARISON
10340 031050 023737 001124 001126        CMP $GDDAT,$BDDAT ;COMPARE EXPECTED
10341                                     ;DATA WITH DATA READ FROM
10342                                     ;RHCS3
10343 031056 001401                        BEQ 65$ ;BRANCH IF GOOD
10344 031060 104124                        ERROR 124
10345                                     ;AFTER SETTING "CLR" BIT #5
10346                                     ;IN RHCS2 TO INIT THE RH
10347                                     ;A ELEVEN WORD WRITE FROM AN
10348                                     ;EVEN WORD BOUNDARY WAS DONE
10349                                     ;THEN
10350                                     ;RHCS3 SHOULD HAVE 0
10351                                     ;BUT CONTAINED WHAT IS
10352                                     ;GIVEN IN BAD RHCS3
10353 031062                                     65$:
10354 031062 042777 000076 152770        BIC #76,DRHCS1 ;CLEAR FUNCTION BITS
10355                                     ;CHECK THAT RHCS1 HAS RDY!DVA
10356 031070 012737 004200 001124        MOV #RDY!DVA,$GDDAT ;GET GOOD = 4200
10357
10358 031076 017737 152756 001126        MOV DRHCS1,$BDDAT ;READ RHCS1 FOR COMPARISON
10359 031104 023737 001124 001126        CMP $GDDAT,$BDDAT ;COMPARE EXPECTED
10360                                     ;DATA WITH DATA READ FROM
10361                                     ;RHCS1
10362 031112 001401                        BEQ 67$ ;BRANCH IF GOOD
10363 031114 104125                        ERROR 125
10364                                     ;AFTER SETTING "CLR" BIT #5
10365                                     ;IN RHCS2 TO INIT THE RH
10366                                     ;A ELEVEN WORD WRITE FROM AN
10367                                     ;EVEN WORD BOUNDARY WAS DONE
10368                                     ;THEN
10369                                     ;RHCS1 SHOULD HAVE RDY!DVA
10370                                     ;=4200
10371                                     ;BUT CONTAINED WHAT IS
10372                                     ;GIVEN IN BAD RHCS1
10373 031116                                     67$:
10374                                     ;CHECK THAT RHCS2 HAS IR
10375 031116 012737 000100 001124        MOV #IR,$GDDAT ;GET GOOD = 100

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10376 031124 053737 005010 001124      BIS      UNIT,$GDDAT      ;INCLUDE UNIT NUMBER
10377
10378 031132 017737 152732 001126      MOV      @RHCS2,$BDDAT   ;READ RHCS2 FOR COMPARISON
10379 031140 023737 001124 001126      CMP      $GDDAT,$BDDAT   ;COMPARE EXPECTED
10380                                     ;DATA WITH DATA READ FROM
10381                                     ;RHCS2
10382 031146 001401      BEQ      69$             ;BRANCH IF GOOD
10383 031150 104126      ERROR   126
10384                                     ;AFTER SETTING "CLR" BIT #5
10385                                     ;IN RHCS2 TO INIT THE RH
10386                                     ;A ELEVEN WORD WRITE FROM AN
10387                                     ;EVEN WORD BOUNDARY WAS DONE
10388                                     ;THEN
10389                                     ;RHCS2 SHOULD HAVE IR
10390                                     ;=100
10391                                     ;TOGETHER WITH UNIT NUMBER
10392                                     ;BUT CONTAINED WHAT IS
10393                                     ;GIVEN IN BAD RHCS2
10394 031152      69$:
10395                                     ;CHECK THAT RHWC HAS 0
10396 031152 012737 000000 001124      MOV      #0,$GDDAT      ;GET GOOD = 0
10397
10398 031160 017737 152676 001126      MOV      @RHWC,$BDDAT   ;READ RHWC FOR COMPARISON
10399 031166 023737 001124 001126      CMP      $GDDAT,$BDDAT   ;COMPARE EXPECTED
10400                                     ;DATA WITH DATA READ FROM
10401                                     ;RHWC
10402 031174 001401      BEQ      71$             ;BRANCH IF GOOD
10403 031176 104131      ERROR   131
10404                                     ;AFTER SETTING "CLR" BIT #5
10405                                     ;IN RHCS2 TO INIT THE RH
10406                                     ;A ELEVEN WORD WRITE FROM AN
10407                                     ;EVEN WORD BOUNDARY WAS DONE
10408                                     ;THEN
10409                                     ;RHWC SHOULD HAVE 0
10410                                     ;BUT CONTAINED WHAT IS
10411                                     ;GIVEN IN BAD RHWC
10412 031200      71$:
10413
10414
10415
10416                                     ;*****
10417                                     ;*TEST 44      TEST DBL (RHCS3 BIT #10) TEST E
10418
10419                                     ;*
10420                                     ;*      CLEAR THE SUBSYSTEM BY A CONTROLLER CLEAR (RHCS2 BIT #5)
10421                                     ;*      SET UP FOR A TEN WORD WRITE FROM AN EVEN WORD BOUNDARY
10422                                     ;*      WITH BAI IN RHCS2 BIT #3 SET
10423                                     ;*      DO A TEN WORD WRITE FROM AN EVEN WORD BOUNDARY
10424                                     ;*      THIS SHOULD NOT SET RHCS3 BIT #10 DBL
10425                                     ;*      CHECK RHCS3
10426                                     ;*      CHECK RHCS1,RHCS2,RHBA,RHBAE,RHWC
10427                                     ;*****
10428 031200 000004      TEST44. SCOPE
10429 031202 012706 001000      MOV      @STACK,SP      ;RESET STACK
10430 031206 012737 000044 004656      MOV      #44,@TSTNM     ;SAVE TEST NUMBER
10431

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10432 031214 004737 040312 JSR PC, @CLDISK ;GIVE RH INITIALIZE
10433 ;SETUP UNIT NUBER
10434 ;CLEAR RHWC AND FUNCTION BITS IN RHCS1
10435
10436 ;SET UP FOR A TEN WORD WRITE FROM AN EVEN WORD BOUNDARY
10437 031220 012701 000003 MOV #3, R1 ;COUNT OF THREE
10438 031224 012702 003000 MOV #BF EVEN, R2 ;START THREE WORD BUFFER
10439 ;FROM AN EVEN WORD BOUNDARY
10440 031230 012722 052525 1$: MOV #52525, (R2)+ ;MOVE THREE 52525 INTO BUFFER
10441 031234 005301 DEC R1 ;COUNT
10442 031236 001374 BNE 1$ ;BRANCH IF THREE NOT DONE
10443 031240 012777 003000 152616 MOV #BF EVEN, @RHBA ;SET BUS ADDRESS TO START
10444 ;FROM AN EVEN WORD BOUNDARY
10445 031246 012777 177766 152606 MOV #-10, @RHWC ;WORD COUNT TEN
10446 031254 052777 000010 152606 BIS #BAI, @RHCS2 ;SET BAI IN RHCS2
10447 031262 004077 153352 JSR @COMND ;GO TO DO COMMAND
10448 031266 004154 WRIDAT ;WRITE DATA
10449
10450
10451 ;CHECK THAT RHCS3 HAS 0
10452 031270 012737 000000 001124 MOV #0, $GDDAT ;GET GOOD = 0
10453
10454 031276 017737 152630 001126 MOV @RHCS3, $BDDAT ;READ RHCS3 FOR COMPARISON
10455 031304 023737 001124 001126 CMP $GDDAT, $BDDAT ;COMPARE EXPECTED
10456 ;DATA WITH DATA READ FROM
10457 ;RHCS3
10458 031312 001401 BEQ 65$ ;BRANCH IF GOOD
10459 031314 104124 ERROR 124
10460 ;AFTER SETTING "CLR" BIT #5
10461 ;IN RHCS2 TO INIT THE RH
10462 ;A TEN WORD WRITE FROM AN
10463 ;EVEN WORD BOUNDARY WAS DONE
10464 ;WITH BAI IN RHCS2 SET
10465 ;THEN
10466 ;RHCS3 SHOULD HAVE 0
10467 ;BUT CONTAINED WHAT IS
10468 ;GIVEN IN BAD RHCS3
10469 031316 042777 000076 152534 65$: BIC #76, @RHCS1 ;CLEAR FUNCTION BITS
10470 031316 ;CHECK THAT RHCS1 HAS RDY!DVA
10471 MOV #RDY!DVA, $GDDAT ;GET GOOD = 4200
10472 031324 012737 004200 001124
10473
10474 031332 017737 152522 001126 MOV @RHCS1, $BDDAT ;READ RHCS1 FOR COMPARISON
10475 031340 023737 001124 001126 CMP $GDDAT, $BDDAT ;COMPARE EXPECTED
10476 ;DATA WITH DATA READ FROM
10477 ;RHCS1
10478 031346 001401 BEQ 67$ ;BRANCH IF GOOD
10479 031350 104125 ERROR 125
10480 ;AFTER SETTING "CLR" BIT #5
10481 ;IN RHCS2 TO INIT THE RH
10482 ;A TEN WORD WRITE FROM AN
10483 ;EVEN WORD BOUNDARY WAS DONE
10484 ;WITH BAI IN RHCS2 SET
10485 ;THEN
10486 ;RHCS1 SHOULD HAVE RDY!DVA
10487 ;=4200

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031434 000004
031436 012736 001000
031442 012737 000045 004656
031450 004737 040312
031454 012701 000003
031450 012702 003002
031464 012722 052525
031470 005301
031472 001374
031474 012777 003002 152352
031502 012777 177765 152352
031510 004077 153124
031514 004154
031516 012737 002000 001124
031524 017737 152402 001126
031532 023737 001124 001126
031540 001401
031542 104124
031544 042777 000076 152306
031552 012737 004200 001124
031560 017737 152274 001126
031566 023737 001124 001126
031574 001401
031576 104125

```
*****  
↑ST45: SCOPE  
MOV #STACK,SP :RESET STACK  
MOV #45,2#↑STNM :SAVE TEST NUMBER  
JSR PC,2#CLDISK :GIVE RH INITIALIZE  
:SETUP UNIT NUBER  
:CLEAR RHWC AND FUNCTION BITS IN RHCS!  
:SET UP FOR A ELEVEN WORD WRITE FROM AN ODD WORD BOUNDARY  
MOV #3,R1 :COUNT OF THREE  
MOV #BFODD,R2 :START THREE WORD BUFFER  
:FROM AN ODD WORD BOUNDARY  
15: MOV #52525,(R2)+ :MOVE THREE 52525 INTO BUFFER  
DEC R1 :COUNT  
BNE 15 :BRANCH IF THREE NOT DONE  
MOV #BFODD,2#RHBA :SET BUS ADDRESS TO START  
:FROM AN ODD WORD BOUNDARY  
MOV #-11,2#RHWC :WORD COUNT ELEVEN  
JSR R0,2#COMND :GO TO DO COMMAND  
WRIDAT :WRITE DATA  
:CHECK THAT RHCS3 HAS DBL  
MOV# #DBL,$GDDAT :GET GOOD = 2000  
MOV 2#RHCS3,$BDDAT :READ RHCS3 FOR COMPARISON  
CMP $GDDAT,$BDDAT :COMPARE EXPECTED  
:DATA WITH DATA READ FROM  
:RHCS3  
BEQ 655 :BRANCH IF GOOD  
ERROR 124  
:AFTER SETTING "CLR" BIT #5  
:IN RHCS2 TO INIT THE RH  
:A ELEVEN WORD WRITE FROM AN  
:ODD WORD BOUNDARY WAS DONE  
:THEN  
:RHCS3 SHOULD HAVE DBL  
:=2000  
:BUT CONTAINED WHAT IS  
:GIVEN IN BAD RHCS3  
655: BIC #76,2#RHCS1 :CLEAR FUNCTION BITS  
:CHECK THAT RHCS1 HAS RDY!DVA  
MOV #RDY!DVA,$GDDAT :GET GOOD = 4200  
MOV 2#RHCS1,$BDDAT :READ RHCS1 FOR COMPARISON  
CMP $GDDAT,$BDDAT :COMPARE EXPECTED  
:DATA WITH DATA READ FROM  
:RHCS1  
BEQ 675 :BRANCH IF GOOD  
ERROR 125  
:AFTER SETTING "CLR" BIT #5  
:IN RHCS2 TO INIT THE RH  
:A ELEVEN WORD WRITE FROM AN
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10600 ; ODD WORD BOUNDARY WAS DONE
10601 ; THEN
10602 ; RHCS1 SHOULD HAVE RDY!DVA
10603 ; =4200
10604 ; BUT CONTAINED WHAT IS
10605 ; GIVEN IN BAD RHCS1

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```

10606 031600 675: ; CHECK THAT RHCS2 HAS IR
10607 ; MOV #IR,$GDDAT ; GET GOOD = 100
10608 031600 012737 000100 001124 ; BIS UNIT,$GDDAT ; INCLUDE UNIT NUMBER
10609 031606 053737 005010 001124
10610
10611 031614 017737 152250 001126 ; MOV @RHCS2,$BDDAT ; READ RHCS2 FOR COMPARISON
10612 031622 023737 001124 001126 ; CMP $GDDAT,$BDDAT ; COMPARE EXPECTED
10613 ; DATA WITH DATA READ FROM
10614 ; RHCS2
10615 031630 001401 ; BEQ 695 ; BRANCH IF GOOD
10616 031632 104126 ; ERROR 126

```

```

10617 ; AFTER SETTING "CLR" BIT #5
10618 ; IN RHCS2 TO INIT THE RH
10619 ; A ELEVEN WORD WRITE FROM AN
10620 ; ODD WORD BOUNDARY WAS DONE
10621 ; THEN
10622 ; RHCS2 SHOULD HAVE IR
10623 ; =100
10624 ; TOGETHER WITH UNIT NUMBER
10625 ; BUT CONTAINED WHAT IS
10626 ; GIVEN IN BAD RHCS2

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10627 031634 695: ; CHECK THAT RHWC HAS 0
10628 ; MOV #0,$GDDAT ; GET GOOD = 0
10629 031634 012737 000000 001124
10630
10631 031642 017737 152214 001126 ; MOV @RHWC,$BDDAT ; READ RHWC FOR COMPARISON
10632 031650 023737 001124 001126 ; CMP $GDDAT,$BDDAT ; COMPARE EXPECTED
10633 ; DATA WITH DATA READ FROM
10634 ; RHWC
10635 031656 001401 ; BEQ 715 ; BRANCH IF GOOD
10636 031660 104131 ; ERROR 131

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10637 ; AFTER SETTING "CLR" BIT #5
10638 ; IN RHCS2 TO INIT THE RH
10639 ; A ELEVEN WORD WRITE FROM AN
10640 ; ODD WORD BOUNDARY WAS DONE
10641 ; THEN
10642 ; RHWC SHOULD HAVE 0
10643 ; BUT CONTAINED WHAT IS
10644 ; GIVEN IN BAD RHWC

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10645 031662 715:
10646
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10649
10650 ; *****
10651 ; *TEST 46 TEST DBL (RHCS3 BIT #10) TEST G
10652
10653 ; * CLEAR THE SUBSYSTEM BY A CONTROLLER CLEAR (RHCS2 BIT #5)
10654 ; * SET UP FOR A NINE WORD READ FROM AN EVEN WORD BOUNDARY
10655 ; * DO A NINE WORD READ FROM AN EVEN WORD BOUNDARY

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10712 ; IN RHCS2 TO INIT THE RH
10713 ; A NINE WORD READ FROM AN
10714 ; EVEN WORD BOUNDARY WAS DONE
10715 ; THEN
10716 ; RHCS1 SHOULD HAVE RDY!DVA
10717 ; =4200
10718 ; BUT CONTAINED WHAT IS
10719 ; GIVEN IN BAD RHCS1

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```

10720 032026          67$:
10721 ; CHECK THAT RHCS2 HAS IR
10722 032026 012737 000100 001124  MOV #IR,$GDDAT ; GET GOOD = 100
10723 032034 053737 005010 001124  BIS UNIT,$GDDAT ; INCLUDE UNIT NUMBER
10724
10725 032042 017737 152022 001126  MOV @RHCS2,$BDDAT ; READ RHCS2 FOR COMPARISON
10726 032050 023737 001124 001126  CMP $GDDAT,$BDDAT ; COMPARE EXPECTED
10727 ; DATA WITH DATA READ FROM
10728 ; RHCS2
10729 032056 001401          BEQ 69$ ; BRANCH IF GOOD
10730 032050 104126          ERROR 126

```

```

10731 ; AFTER SETTING "CLR" BIT #5
10732 ; IN RHCS2 TO INIT THE RH
10733 ; A NINE WORD READ FROM AN
10734 ; EVEN WORD BOUNDARY WAS DONE
10735 ; THEN
10736 ; RHCS2 SHOULD HAVE IR
10737 ; =100
10738 ; TOGETHER WITH UNIT NUMBER
10739 ; BUT CONTAINED WHAT IS
10740 ; GIVEN IN BAD RHCS2

```

```

10741 032062          69$:
10742 ; CHECK THAT RHWC HAS 0
10743 032062 012737 000000 001124  MOV #0,$GDDAT ; GET GOOD = 0
10744
10745 032070 017737 151766 001126  MOV @RHWC,$BDDAT ; READ RHWC FOR COMPARISON
10746 032076 023737 001124 001126  CMP $GDDAT,$BDDAT ; COMPARE EXPECTED
10747 ; DATA WITH DATA READ FROM
10748 ; RHWC
10749 032104 001401          BEQ 71$ ; BRANCH IF GOOD
10750 032106 104131          ERROR 131

```

```

10751 ; AFTER SETTING "CLR" BIT #5
10752 ; IN RHCS2 TO INIT THE RH
10753 ; A NINE WORD READ FROM AN
10754 ; EVEN WORD BOUNDARY WAS DONE
10755 ; THEN
10756 ; RHWC SHOULD HAVE 0
10757 ; BUT CONTAINED WHAT IS
10758 ; GIVEN IN BAD RHWC

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10759 032110          71$:
10760
10761
10762

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```

10763 ;*****
10764 ;*TEST 47          TEST DBL (RHCS3 BIT #10) TEST H

```

```

10765 ;*          CLEAR THE SUBSYSTEM BY A CONTROLLER CLEAR (RHCS2 BIT #5)
10766 ;*          SET UP FOR A TEN WORD READ FROM AN EVEN WORD BOUNDARY
10767

```


G16

MAINDEC-11-DERHAB-A
DERHAB.SRC T47

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TEST DBL (RHCS3 BIT #10) TEST H

```

10768      : *      DO A TEN WORD READ FROM AN EVEN WORD BOUNDARY
10769      : *      THIS SHOULD SET RHCS3 BIT #10 DBL
10770      : *      CHECK RHCS3
10771      : *      CHECK RHCS1, RHCS2, RHBA, RHBAE, RHW
10772
10773      : *****
10774 032110 000004          TST47: SCOPE
10775 032112 012706 001000      MOV      #STACK, SP          ;RESET STACK
10776 032116 012737 000047 004656  MOV      #47, @*TSTNM        ;SAVE TEST NUMBER
10777
10778 032124 004737 040312      JSR      PC, @*CLDISK        ;GIVE RH INITIALIZE
10779                                     ;SETUP UNIT NUBER
10790                                     ;CLEAR RHW AND FUNCTION BITS IN RHCS1
10781
10782      :SET UP FOR A TEN WORD READ FROM AN EVEN WORD BOUNDARY
10783 032130 012701 000003      MOV      #3, R1              ;COUNT OF THREE
10784 032134 012702 003000      MOV      #BFEBEN, R2         ;START THREE WORD BUFFER
10785                                     ;FROM AN EVEN WORD BOUNDARY
10786 032140 012722 052525      1$:  MOV      #52525, (R2)+     ;MOVE THREE 52525 INTO BUFFER
10787 032144 005301             DEC      R1                  ;COUNT
10788 032146 001374             BNE     1$                   ;BRANCH IF THREE NOT DONE
10789 032150 012777 003000 151706  MOV      #BFEBEN, @RHBA      ;SET BUS ADDRESS TO START
10790                                     ;FROM AN EVEN WORD BOUNDARY
10791 032156 012777 177766 151676  MOV      #-10, @RHW         ;WORD COUNT TEN
10792 032164 004077 152453      JSR      RC, @COMND         ;GO TO DO COMMAND
10793 032170 004160             READAT                      ;READ DATA
10794
10795
10796      :CHECK THAT RHCS3 HAS DBL
10797 032172 012737 002000 001124  MOV      #DBL, $GDDAT        ;GET GOOD = 2000
10798
10799 032200 017737 151726 001126  MOV      @RHCS3, $BDDAT      ;READ RHCS3 FOR COMPARISON
10800 032206 023737 001124 001126  CMP      $GDDAT, $BDDAT      ;COMPARE EXPECTED
10801                                     ;DATA WITH DATA READ FROM
10802                                     ;RHCS3
10803 032214 001401             BEQ     65$                   ;BRANCH IF GOOD
10804 032216 104124
10805
10806                                     ;AFTER SETTING "CLR" BIT #5
10807                                     ;IN RHCS2 TO INIT THE RH
10808                                     ;A TEN WORD READ FROM AN
10809                                     ;EVEN WORD BOUNDARY WAS DONE
10810                                     ;THEN
10811                                     ;RHCS3 SHOULD HAVE DBL
10812                                     ;=2000
10813                                     ;BUT CONTAINED WHAT IS
10814                                     ;GIVEN IN BAD RHCS3
10814 032220 042777 000076 151632  65$:  BIC      #76, @RHCS1         ;CLEAR FUNCTION BITS
10815 032220 042777 000076 151632  ;CHECK THAT RHCS1 HAS RDY!DVA
10816
10817 032226 012737 004200 001124  MOV      #RDY!DVA, $GDDAT    ;GET GOOD = 4200
10818
10819 032234 017737 151620 001126  MOV      @RHCS1, $BDDAT      ;READ RHCS1 FOR COMPARISON
10820 032242 023737 001124 001126  CMP      $GDDAT, $BDDAT      ;COMPARE EXPECTED
10821                                     ;DATA WITH DATA READ FROM
10822                                     ;RHCS1
10823 032250 001401             BEQ     67$                   ;BRANCH IF GOOD

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10824 032252 104125          ERROR 125
10825
10826
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10833
10834 032254          67$:
10835          ;CHECK THAT RHCS2 HAS IR
10836 032254 012737 000100 001124      MOV      #IR,$GDDAT      ;GET GOOD = 100
10837 032262 053737 005010 001124      BIS      UNIT,$GDDAT     ;INCLUDE UNIT NUMBER
10838
10839 032270 017737 151574 001126      MOV      @RHCS2,$BDDAT   ;READ RHCS2 FOR COMPARISON
10840 032276 023737 001124 001126      CMP      $GDDAT,$BDDAT  ;COMPARE EXPECTED
10841
10842
10843 032304 001401          BEQ      69$             ;BRANCH IF GOOD
10844 032306 104126          ERROR 126
10845
10846
10847
10848
10849
10850
10851
10852
10853
10854
10855 032310          69$:
10856          ;CHECK THAT RHWC HAS 0
10857 032310 012737 000000 001124      MOV      #0,$GDDAT      ;GET GOOD = 0
10858
10859 032316 017737 151540 001126      MOV      @RHWC,$BDDAT   ;READ RHWC FOR COMPARISON
10860 032324 023737 001124 001126      CMP      $GDDAT,$BDDAT  ;COMPARE EXPECTED
10861
10862
10863 032332 001401          BEQ      71$             ;BRANCH IF GOOD
10864 032334 104131          ERROR 131
10865
10866
10867
10868
10869
10870
10871
10872
10873 032336          71$:
10874
10875
10876
10877
10878
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;AFTER SETTING "CLR" BIT #5
;IN RHCS2 TO INIT THE RH
;A TEN WORD READ FROM AN
;EVEN WORD BOUNDARY WAS DONE
;THEN
;RHCS1 SHOULD HAVE RDY!DVA
;=4200
;BUT CONTAINED WHAT IS
;GIVEN IN BAD RHCS1

;AFTER SETTING "CLR" BIT #5
;IN RHCS2 TO INIT THE RH
;A TEN WORD READ FROM AN
;EVEN WORD BOUNDARY WAS DONE
;THEN
;RHCS2 SHOULD HAVE IR
;=100
;TOGETHER WITH UNIT NUMBER
;BUT CONTAINED WHAT IS
;GIVEN IN BAD RHCS2

;AFTER SETTING "CLR" BIT #5
;IN RHCS2 TO INIT THE RH
;A TEN WORD READ FROM AN
;EVEN WORD BOUNDARY WAS DONE
;THEN
;RHWC SHOULD HAVE 0
;BUT CONTAINED WHAT IS
;GIVEN IN BAD RHWC

;*****
;*TEST 50 TEST DBL (RHCS3 BIT #10) TEST I

48 Q 1

MAINDEC-11-DERHAB-A
DERHAB.SRC T50

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TEST DBL (RHCS3 BIT #10) TEST I

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10890          :*      CLEAR THE SUBSYSTEM BY A CONTROLLER CLEAR (RHCS2 BIT #5)
10891          :*      SET UP FOR A TEN WORD READ FROM AN ODD WORD BOUNDARY
10892          :*      DO A TEN WORD READ FROM AN ODD WORD BOUNDARY
10893          :*      THIS SHOULD NOT SET RHCS3 BIT #10 DBL
10894          :*      CHECK RHCS3
10895          :*      CHECK RHCS1, RHCS2, RHBA, RHBAE, RHWC
10896
10897          :*****
10898 032336 000004          †T50: SCOPE
10899 032340 012706 001000      MOV      #STACK SP      ;RESET STACK
10900 032344 012737 000050 004656      MOV      #50,‡#TSTNM    ;SAVE TEST NUMBER
10901
10902 032352 004737 040312          JSR      PC,‡#CLDISK    ;GIVE RH INITIALIZE
10903                                     ;SETUP UNIT NUBER
10904                                     ;CLEAR RHWC AND FUNCTION BITS IN RHCS1
10905
10906          ;SET UP FOR A TEN WORD READ FROM AN ODD WORD BOUNDARY
10907 032356 012701 000003      MOV      #3,R1          ;COUNT OF THREE
10908 032362 012702 003002      MOV      #BFODD,R2      ;START THREE WORD BUFFER
10909                                     ;FROM AN ODD WORD BOUNDARY
10910          1$: MOV      #52525,(R2)+ ;MOVE THREE 52525 INTO BUFFER
10911          DEC      R1          ;COUNT
10912          BNE     1$          ;BRANCH IF THREE NOT DONE
10913          MOV     #BFODD,‡RHBA ;SET BUS ADDRESS TO START
10914          MOV     #-10,‡RHWC ;WORD COUNT TEN
10915          JSR     RD,‡COMND    ;GO TO DO COMMAND
10916          READAT ;READ DATA
10917
10918          ;CHECK THAT RHCS3 HAS 0
10919          MOV     #0,$GDDAT    ;GET GOOD = 0
10920
10921          MOV     ‡RHCS3,$BDDAT ;READ RHCS3 FOR COMPARISON
10922          CMP     $GDDAT,$BDDAT ;COMPARE EXPECTED
10923          ;DATA WITH DATA READ FROM
10924          ;RHCS3
10925          BEQ     65$          ;BRANCH IF GOOD
10926          ERROR  124
10927
10928          ;AFTER SETTING "CLR" BIT #5
10929          ;IN RHCS2 TO INIT THE RH
10930          ;A TEN WORD READ FROM AN
10931          ;ODD WORD BOUNDARY WAS DONE
10932          ;THEN
10933          ;RHCS3 SHOULD HAVE 0
10934          ;BUT CONTAINED WHAT IS
10935          ;GIVEN IN BAD RHCS3
10936
10937 032446 042777 000076 151404      65$: BIC     #76,‡RHCS1    ;CLEAR FUNCTION BITS
10938 032446 042777 000076 151404      ;CHECK THAT RHCS1 HAS RDY!DVA
10939          MOV     #RDY!DVA,$GDDAT ;GET GOOD = 4200
10940
10941          MOV     ‡RHCS1,$BDDAT ;READ RHCS1 FOR COMPARISON
10942          CMP     $GDDAT,$BDDAT ;COMPARE EXPECTED
10943          ;DATA WITH DATA READ FROM
10944          ;RHCS1
10945

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10936 032476 001401          BEQ      67$      ;BRANCH IF GOOD
10937 032500 104125          ERROR    125
10938                                     ;AFTER SETTING "CLR" BIT #5
10939                                     ;IN RHCS2 TO INIT THE RH
10940                                     ;A TEN WORD READ FROM AN
10941                                     ;ODD WORD BOUNDARY WAS DONE
10942                                     ;THEN
10943                                     ;RHCS1 SHOULD HAVE RDY!DVA
10944                                     ;=4200
10945                                     ;BUT CONTAINED WHAT IS
10946                                     ;GIVEN IN BAD RHCS1
10947 032502          67$:
10948                                     ;CHECK THAT RHCS2 HAS IR
10949 032502 012737 000100 001124  MOV      *IR,$GDDAT  ;GET GOOD = 100
10950 032510 053737 005010 001124  BIS      UNIT,$GDDAT ;INCLUDE UNIT NUMBER
10951
10952 032516 017737 151346 001126  MOV      @RHCS2,$BDDAT ;READ RHCS2 FOR COMPARISON
10953 032524 023737 001124 001126  CMP      $GDDAT,$BDDAT ;COMPARE EXPECTED
10954                                     ;DATA WITH DATA READ FROM
10955                                     ;RHCS2
10956 032532 001401          BEQ      69$      ;BRANCH IF GOOD
10957 032534 104126          ERROR    126
10958                                     ;AFTER SETTING "CLR" BIT #5
10959                                     ;IN RHCS2 TO INIT THE RH
10960                                     ;A TEN WORD READ FROM AN
10961                                     ;ODD WORD BOUNDARY WAS DONE
10962                                     ;THEN
10963                                     ;RHCS2 SHOULD HAVE IR
10964                                     ;=100
10965                                     ;TOGETHER WITH UNIT NUMBER
10966                                     ;BUT CONTAINED WHAT IS
10967                                     ;GIVEN IN BAD RHCS2
10968 032536          69$:
10969                                     ;CHECK THAT RHWC HAS 0
10970 032536 012737 000000 001124  MOV      #0,$GDDAT  ;GET GOOD = 0
10971
10972 032544 017737 151312 001126  MOV      @RHWC,$BDDAT ;READ RHWC FOR COMPARISON
10973 032552 023737 001124 001126  CMP      $GDDAT,$BDDAT ;COMPARE EXPECTED
10974                                     ;DATA WITH DATA READ FROM
10975                                     ;RHWC
10976 032560 001401          BEQ      71$      ;BRANCH IF GOOD
10977 032562 104131          ERROR    131
10978                                     ;AFTER SETTING "CLR" BIT #5
10979                                     ;IN RHCS2 TO INIT THE RH
10980                                     ;A TEN WORD READ FROM AN
10981                                     ;ODD WORD BOUNDARY WAS DONE
10982                                     ;THEN
10983                                     ;RHWC SHOULD HAVE 0
10984                                     ;BUT CONTAINED WHAT IS
10985                                     ;GIVEN IN BAD RHWC
10986 032564          71$:
10987
10988
10989
10990
10991
;*****
;*TEST 51          TEST DBL (RHCS3 BIT #10) TEST J

```

K16

MAINDEC-11-DERHAB-A
DERHAB.SRC T51

MACY11 27(732) 22-SEP-76 15:11 PAGE 207
TEST DBL (RHCS3 BIT #10) TEST J

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10992
10993
10994
10995
10996
10997
10998
10999
11000
11001 032564 000004
11002 032566 012706 001000
11003 032572 012737 000051 004656
11004
11005 032600 004737 040312
11006
11007
11008
11009
11010 032604 012701 000003
11011 032610 012702 003000
11012
11013 032614 012722 052525
11014 032620 005301
11015 032622 001374
11016 032624 012777 003000 151232
11017
11018 032632 012777 177765 151222
11019 032640 004077 151774
11020 032644 004160
11021
11022
11023
11024 032646 012737 000000 001124
11025
11026 032654 017737 151252 001126
11027 032662 023737 001124 001126
11028
11029
11030 032670 001401
11031 032672 104124
11032
11033
11034
11035
11036
11037
11038
11039
11040 032674
11041 032674 042777 000076 151156
11042
11043 032702 012737 004200 001124
11044
11045 032710 017737 151144 001126
11046 032716 023737 001124 001126
11047

```

```

:* CLEAR THE SUBSYSTEM BY A CONTROLLER CLEAR (RHCS2 BIT #5)
:* SET UP FOR A ELEVEN WORD READ FROM AN EVEN WORD BOUNDARY
:* DO A ELEVEN WORD READ FROM AN EVEN WORD BOUNDARY
:* THIS SHOULD NOT SET RHCS3 BIT #10 DBL
:* CHECK RHCS3
:* CHECK RHCS1, RHCS2, RHBA, RHBAE, RHWC
:*****
+ST51: SCOPE
MOV #STACK, SP ;RESET STACK
MOV #51, @#TSTNM ;SAVE TEST NUMBER
JSR PC, @#CLDISK ;GIVE RH INITIALIZE
;SETUP UNIT NUBER
;CLEAR RHWC AND FUNCTION BITS IN RHCS1
;SET UP FOR A ELEVEN WORD READ FROM AN EVEN WORD BOUNDARY
MOV #3, R1 ;COUNT OF THREE
MOV #BFEVEN, R2 ;START THREE WORD BUFFER
;FROM AN EVEN WORD BOUNDARY
1$: MOV #52525, (R2)+ ;MOVE THREE 52525 INTO BUFFER
DEC R1 ;COUNT
BNE 1$ ;BRANCH IF THREE NOT DONE
MOV #BFEVEN, @RHBA ;SET BUS ADDRESS TO START
;FROM AN EVEN WORD BOUNDARY
MOV #-11, @RHWC ;WORD COUNT ELEVEN
JSR RD, @COMND ;GO TO DO COMMAND
READAT ;READ DATA
;CHECK THAT RHCS3 HAS 0
MOV #0, $GDDAT ;GET GOOD = 0
MOV @RHCS3, $BDDAT ;READ RHCS3 FOR COMPARISON
CMP $GDDAT, $BDDAT ;COMPARE EXPECTED
;DATA WITH DATA READ FROM
;RHCS3
BEQ 65$ ;BRANCH IF GOOD
ERROR 124
;AFTER SETTING "CLR" BIT #5
;IN RHCS2 TO INIT THE RH
;A ELEVEN WORD READ FROM AN
;EVEN WORD BOUNDARY WAS DONE
;THEN
;RHCS3 SHOULD HAVE 0
;BUT CONTAINED WHAT IS
;GIVEN IN BAD RHCS3
65$: BIC #76, @RHCS1 ;CLEAR FUNCTION BITS
;CHECK THAT RHCS1 HAS RDY!DVA
MOV #RDY!DVA, $GDDAT ;GET GOOD = 4200
MOV @RHCS1, $BDDAT ;READ RHCS1 FOR COMPARISON
CMP $GDDAT, $BDDAT ;COMPARE EXPECTED
;DATA WITH DATA READ FROM

```

24

MAINDEC-11-DERHAB-A
DERHAB.SRC TS1

MACY11 27(732) 22-SEP-76 15:11 PAGE 208
TEST DBL (RHCS3 BIT #10) TEST J

```

11048                                     :RHCS1
11049 032724 001401                       BEQ      67$    ;BRANCH IF GOOD
11050 032726 104125                       ERROR    125
11051                                     ;AFTER SETTING "CLR" BIT #5
11052                                     ;IN RHCS2 TO INIT THE RH
11053                                     ;A ELEVEN WORD READ FROM AN
11054                                     ;EVEN WORD BOUNDARY WAS DONE
11055                                     ;THEN
11056                                     ;RHCS1 SHOULD HAVE RDY!DVA
11057                                     ;=4200
11058                                     ;BUT CONTAINED WHAT IS
11059                                     ;GIVEN IN BAD PHCS1
11060 032730                               67$:
11061                                     ;CHECK THAT RHCS2 HAS IR
11062 032730 012737 000100 001124           MOV      #IR,$GDDAT ;GET GOOD = 10C
11063 032736 053737 005010 001124           BIS      UNIT,$GDDAT ;INCLUDE UNIT NUMBER
11064
11065 032744 017737 151120 001126           MOV      @RHCS2,$BDDAT ;READ RHCS2 FOR COMPARISON
11066 032752 023737 001124 001126           CMP      $GDDAT,$BDDAT ;COMPARE EXPECTED
11067                                     ;DATA WITH DATA READ FROM
11068                                     ;RHCS2
11069 032760 001401                       BEQ      69$    ;BRANCH IF GOOD
11070 032762 104126                       ERROR    126
11071                                     ;AFTER SETTING "CLR" BIT #5
11072                                     ;IN RHCS2 TO INIT THE RH
11073                                     ;A ELEVEN WORD READ FROM AN
11074                                     ;EVEN WORD BOUNDARY WAS DONE
11075                                     ;THEN
11076                                     ;RHCS2 SHOULD HAVE IR
11077                                     ;=100
11078                                     ;TOGETHER WITH UNIT NUMBER
11079                                     ;BUT CONTAINED WHAT IS
11080                                     ;GIVEN IN BAD RHCS2
11081 032764                               69$:
11082                                     ;CHECK THAT RHWC HAS 0
11083 032764 012737 000000 001124           MOV      #0,$GDDAT  ;GET GOOD = 0
11084
11085 032772 017737 151064 001126           MOV      @RHWC,$BDDAT ;READ RHWC FOR COMPARISON
11086 033000 023737 001124 001126           CMP      $GDDAT,$BDDAT ;COMPARE EXPECTED
11087                                     ;DATA WITH DATA READ FROM
11088                                     ;RHWC
11089 033006 001401                       BEQ      71$    ;BRANCH IF GOOD
11090 033010 104131                       ERROR    131
11091                                     ;AFTER SETTING "CLR" BIT #5
11092                                     ;IN RHCS2 TO INIT THE RH
11093                                     ;A ELEVEN WORD READ FROM AN
11094                                     ;EVEN WORD BOUNDARY WAS DONE
11095                                     ;THEN
11096                                     ;RHWC SHOULD HAVE 0
11097                                     ;BUT CONTAINED WHAT IS
11098                                     ;GIVEN IN BAD RHWC
11099 033012                               71$:
11100
11101
11102
11103

```

;*****

M16

MAINDEC-11-DERHAB-A
DERHAB.SRC T52

MACY11 27(732) 22-SEP-76 15:11 PAGE 209
TEST DBL (RHCS3 BIT #10) TEST K

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11104          :*TEST 52          TEST DBL (RHCS3 BIT #10) TEST K
11105
11106          :*          CLEAR THE SUBSYSTEM BY A CONTROLLER CLEAR (RHCS2 BIT #5)
11107          :*          SET UP FOR A ELEVEN WORD READ FROM AN ODD WORD BOUNDARY
11108          :*          DO A ELEVEN WORD READ FROM AN ODD WORD BOUNDARY
11109          :*          THIS SHOULD SET RHCS3 BIT #10 DBL
11110          :*          CHECK RHCS3
11111          :*          CHECK RHCS1, RHCS2, RHBA, RHBAE, RHWC
11112
11113          :*****
11114 033012 000004          †ST52: SCOPE
11115 033014 012706 001000          MOV      #STACK, SP          ;RESET STACK
11116 033020 012737 000052 004656          MOV      #52, †STNM          ;SAVE TEST NUMBER
11117
11118 033026 004737 040312          JSR      PC, †CLDISK          ;GIVE RH INITIALIZE
11119                                     ;SETUP UNIT NUBER
11120                                     ;CLEAR RHWC AND FLNCTION BITS IN RHCS1
11121
11122          ;SET UP FOR A ELEVEN WORD READ FROM AN ODD WORD BOUNDARY
11123 033032 012701 000003          MOV      #3, R1              ;COUNT OF THREE
11124 033036 012702 003002          MOV      #BF0DD, R2          ;START THREE WORD BUFFER
11125                                     ;FROM AN ODD WORD BOUNDARY
11126 033042 012722 052525          1$:  MOV      #52525, (R2)+     ;MOVE THREE 52525 INTO BUFFER
11127 033046 005301          DEC      R1                  ;COUNT
11128 033050 001374          BNE     1$                   ;BRANCH IF THREE NOT DONE
11129 033052 012777 003002 151004          MOV      #BF0DD, †RHBA       ;SET BUS ADDRESS TO START
11130                                     ;FROM AN ODD WORD BOUNDARY
11131 033060 012777 177765 150774          MOV      #-11, †RHWC          ;WORD COUNT ELEVEN
11132 033066 004077 151546          JSR      †RD, †COMND          ;GO TO DO COMMAND
11133 033072 004160          READAT          ;READ DATA
11134
11135
11136          ;CHECK THAT RHCS3 HAS DBL
11137 033074 012737 002000 001124          MOV      #DBL, $GDDAT        ;GET GOOD = 2000
11138
11139 033102 017737 151024 001126          MOV      †RHCS3, $BDDAT      ;READ RHCS3 FOR COMPARISON
11140 033110 023737 001124 001126          CMP      $GDDAT, $BDDAT      ;COMPARE EXPECTED
11141                                     ;DATA WITH DATA READ FROM
11142                                     ;RHCS3
11143 033116 001401          BEQ     65$                   ;BRANCH IF GOOD
11144 033120 104124          ERROR   124
11145
11146          ;AFTER SETTING "CLR" BIT #5
11147          ;IN RHCS2 TO INIT THE RH
11148          ;A ELEVEN WORD READ FROM AN
11149          ;ODD WORD BOUNDARY WAS DONE
11150          ;THEN
11151          ;RHCS3 SHOULD HAVE DBL
11152          ;=2000
11153          ;BUT CONTAINED WHAT IS
11154          ;GIVEN IN BAD RHCS3
11154 033122          65$: BIC      #76, †RHCS1          ;CLEAR FUNCTION BITS
11155 033122 042777 000076 150730          ;CHECK THAT RHCS1 HAS RDY!DVA
11156          MOV      #RDY!DVA, $GDDAT ;GET GOOD = 4200
11157 033130 012737 004200 001124          MOV      †RDY!DVA, $GDDAT
11158
11159 033136 017737 150716 001126          MOV      †RHCS1, $BDDAT      ;READ RHCS1 FOR COMPARISON

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000144 022737 001124 001126      CMP      $GDDAT,$BDDAT      :COMPARE EXPECTED
:DATA WITH DATA READ FROM
:RHCS1
:BRANCH IF GOOD
000150 001401      BEQ      675
000151 104126      ERROR   125
:
: AFTER SETTING "CLR" BIT #5
: IN RHCS2 TO INIT THE RH
: A ELEVEN WORD READ FROM AN
: ODD WORD BOUNDARY WAS DONE
: THEN
: RHCS1 SHOULD HAVE RDY:DVA
: =4200
: BUT CONTAINED WHAT IS
: GIVEN IN BAD RHCS1

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```

033156      675:
033156 012737 000100 001124      MOV      #IR,$GDDAT      :CHECK THAT RHCS2 HAS IR
:GET GOOD = 100
033164 053737 005010 001124      BIS      UNIT,$GDDAT      :INCLUDE UNIT NUMBER
033172 017737 150672 001126      MOV      @RHCS2,$BDDAT    :READ RHCS2 FOR COMPARISON
033200 023737 001124 001126      CMP      $GDDAT,$BDDAT    :COMPARE EXPECTED
:DATA WITH DATA READ FROM
:RHCS2
:BRANCH IF GOOD
033206 001401      BEQ      695
033210 104126      ERROR   125
:
: AFTER SETTING "CLR" BIT #5
: IN RHCS2 TO INIT THE RH
: A ELEVEN WORD READ FROM AN
: ODD WORD BOUNDARY WAS DONE
: THEN
: RHCS2 SHOULD HAVE IR
: =100
: TOGETHER WITH UNIT NUMBER
: BUT CONTAINED WHAT IS
: GIVEN IN BAD RHCS2

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```

033212      695:
033212 012737 000000 001124      MOV      #0,$GDDAT      :CHECK THAT RHWC HAS 0
:GET GOOD = 0
033220 017737 150636 001126      MOV      @RHWC,$BDDAT    :READ RHWC FOR COMPARISON
033226 023737 001124 001126      CMP      $GDDAT,$BDDAT    :COMPARE EXPECTED
:DATA WITH DATA READ FROM
:RHWC
:BRANCH IF GOOD
033234 001401      BEQ      715
033236 104131      ERROR   131
:
: AFTER SETTING "CLR" BIT #5
: IN RHCS2 TO INIT THE RH
: A ELEVEN WORD READ FROM AN
: ODD WORD BOUNDARY WAS DONE
: THEN
: RHWC SHOULD HAVE 0
: BUT CONTAINED WHAT IS
: GIVEN IN BAD RHWC

```

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033240      715:

```

:TEST 53 TEST DBL (RHCS3 BIT #10) TEST L

:* CLEAR THE SUBSYSTEM BY A CONTROLLER CLEAR (RHCS2 BIT #5)
:* SET UP FOR A TEN WORD READ FROM AN EVEN WORD BOUNDARY
:* WITH BAI IN RHCS2 BIT #3 SET
:* DO A TEN WORD READ FROM AN EVEN WORD BOUNDARY
:* THIS SHOULD NOT SET RHCS3 BIT #10 DBL
:* CHECK RHCS3
:* CHECK RHCS1, RHCS2, RHBA, RHBAE, RHWC

033240 000004
033242 012706 001000
033246 012737 000053 004656

033254 004737 040312

033260 012701 000003
03326 012702 000000

033270 012722 052525
033274 005301
033276 001374
033300 012777 003000 150556

033306 012777 177766 150546
033314 052777 000010 150546
033322 004077 151312
033326 004160

5753: S00PE
MOV #STACK, SP ;RESET STACK
MOV #53, #STNM ;SAVE TEST NUMBER

JSR PC, #CLDISK ;GIVE RH INITIALIZE
;SETUP UNIT NUBER
;CLEAR RHWC AND FUNCTION BITS IN RHCS1

;SET UP FOR A TEN WORD READ FROM AN EVEN WORD BOUNDARY
MOV #3, R1 ;COUNT OF THREE
MOV #8FEVEN, R2 ;START THREE WORD BUFFER
;FROM AN EVEN WORD BOUNDARY
15: MOV #52525, (R2)+ ;MOVE THREE 52525 INTO BUFFER
DEC R1 ;COUNT
BNE 15 ;BRANCH IF THREE NOT DONE
MOV #8FEVEN, #RHBA ;SET BUS ADDRESS TO START
;FROM AN EVEN WORD BOUNDARY
MOV #-10, #RHWC ;WORD COUNT TEN
BIS #BAI, #RHCS2 ;SET BAI IN RHCS2
JSR #RD, #COMND ;GO TO DO COMMAND
READAT ;READ DATA

033330 012737 000000 001124
033336 017737 150570 001126
033344 023737 001124 001126

033352 001401
033354 104124

;CHECK THAT RHCS3 HAS 0
MOV #0, #GDDAT ;GET GOOD = 0

MOV #RHCS3, #BDDAT ;READ RHCS3 FOR COMPARISON
CMP #GDDAT, #BDDAT ;COMPARE EXPECTED
;DATA WITH DATA READ FROM
;RHCS3
BEG 655 ;BRANCH IF GOOD
ERROR 124

033356
033356 042777 000076 150474

655: BIC #76, #RHCS1 ;CLEAR FUNCTION BITS

:AFTER SETTING "CLR" BIT #5
:IN RHCS2 TO INIT THE RH
:A TEN WORD READ FROM AN
:EVEN WORD BOUNDARY WAS DONE
:WITH BAI IN RHCS2 SET
:THEN
:RHCS3 SHOULD HAVE 0
:BUT CONTAINED WHAT IS
:GIVEN IN BAD RHCS3

2

: THEN
: RHWC SHOULD HAVE C
: BUT CONTAINED WHAT IS
: GIVEN IN BAD RHWC

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033474

715:

: TEST 54 TEST DBL (RHCS3 BIT #10) TEST M

: * CLEAR THE SUBSYSTEM BY A CONTROLLER CLEAR (RHCS2 BIT #5)
: * SET UP FOR A ONE WORD WRITE REVERSE FROM AN EVEN WORD BOUNDARY
: * DO A ONE WORD WRITE REVERSE FROM AN EVEN WORD BOUNDARY
: * THIS SHOULD NOT SET RHCS3 BIT #10 DBL
: * CHECK RHCS3
: * CHECK RHCS1, RHCS2, RHBA, RHBAE, RHWC

033474 000004
033476 012706 001000
033502 012737 000054 004656
033510 004737 040312
033514 012701 000003
033520 012702 003000
033524 012722 052525
033530 005301
033532 001374
033534 012777 003000 150322
033542 012777 177777 150312
033550 004077 151064
033554 004204
033556 012737 000000 001124
033564 017737 150342 001126
033572 023737 001124 001126
033600 001401
033602 104124

TEST54: SCOPE
MOV #STACK, SP ; RESET STACK
MOV #54, #TSTNM ; SAVE TEST NUMBER
JSR PC, #CLDISK ; GIVE RH INITIALIZE
; SETUP UNIT NUMBER
; CLEAR RHWC AND FUNCTION BITS IN RHCS1
; SET UP FOR A ONE WORD WRITE REVERSE FROM AN EVEN WORD BOUNDARY
MOV #3, R1 ; COUNT OF THREE
MOV #BFEVEN, R2 ; START THREE WORD BUFFER
; FROM AN EVEN WORD BOUNDARY
IS: MOV #52525, (R2)+ ; MOVE THREE 52525 INTO BUFFER
DEC R1 ; COUNT
SNE IS ; BRANCH IF THREE NOT DONE
MOV #BFEVEN, #RHBA ; SET BUS ADDRESS TO START
; FROM AN EVEN WORD BOUNDARY
MOV #-1, #RHWC ; WORD COUNT ONE
JSR RD, #COMND ; GO TO DO COMMAND
REVRT ; REVERSEWRITE DATA
; CHECK THAT RHCS3 HAS 0
MOV #0, #GDDAT ; GET GOOD = 0
MOV #RHCS3, #BDDAT ; READ RHCS3 FOR COMPARISON
CMP #GDDAT, #BDDAT ; COMPARE EXPECTED
; DATA WITH DATA READ FROM
; RHCS3
BEQ ERROR 65\$; BRANCH IF GOOD
ERROR 124
; AFTER SETTING "CLR" BIT #5
; IN RHCS2 TO INIT THE RH
; A ONE WORD WRITE REVERSE FROM AN
; EVEN WORD BOUNDARY WAS DONE
; THEN

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11394 ;RHCS3 SHOULD HAVE 0
11395 ;BUT CONTAINED WHAT IS
11396 ;GIVEN IN BAD RHCS3
11397
11398 033604 042777 000076 150246 655: BIC #76,RHCS1 ;CLEAR FUNCTION BITS
11399 ;CHECK THAT RHCS1 HAS SC!RDY!DVA
11400 033612 012737 104200 001124 MOV #SC!RDY!DVA,$GDDAT ;GET GOOD = 104200
11401
11402 033620 017737 150234 001126 MOV @RHCS1,$BDDAT ;READ RHCS1 FOR COMPARISON
11403 033626 023737 001124 001126 CMP $GDDAT,$BDDAT ;COMPARE EXPECTED
11404 ;DATA WITH DATA READ FROM
11405 ;RHCS1
11406 033634 001401 BEQ 675 ;BRANCH IF GOOD
11407 033636 104125 ERROR 125
11408
11409 ;AFTER SETTING "CLR" BIT #5
11410 ;IN RHCS2 TO INIT THE RH
11411 ;A ONE WORD WRITE REVERSE FROM AN
11412 ;EVEN WORD BOUNDARY WAS DONE
11413 ;THEN
11414 ;RHCS1 SHOULD HAVE SC!RDY!DVA
11415 ;=104200
11416 ;BUT CONTAINED WHAT IS
11417 ;GIVEN IN BAD RHCS1
11418
11419 033640 675: ;CHECK THAT RHCS2 HAS IR!OR
11420 033640 012737 000300 001124 MOV #IR!OR,$GDDAT ;GET GOOD = 300
11421 033646 053737 005010 001124 BIS UNIT,$GDDAT ;INCLUDE UNIT NUMBER
11422
11423 033654 017737 150210 001126 MOV @RHCS2,$BDDAT ;READ RHCS2 FOR COMPARISON
11424 033662 023737 001124 001126 CMP $GDDAT,$BDDAT ;COMPARE EXPECTED
11425 ;DATA WITH DATA READ FROM
11426 ;RHCS2
11427 033670 001401 BEQ 695 ;BRANCH IF GOOD
11428 033672 104126 ERROR 126
11429
11430 ;AFTER SETTING "CLR" BIT #5
11431 ;IN RHCS2 TO INIT THE RH
11432 ;A ONE WORD WRITE REVERSE FROM AN
11433 ;EVEN WORD BOUNDARY WAS DONE
11434 ;THEN
11435 ;RHCS2 SHOULD HAVE IR!OR
11436 ;=300
11437 ;TOGETHER WITH UNIT NUMBER
11438 ;BUT CONTAINED WHAT IS
11439 ;GIVEN IN BAD RHCS2
11440
11441 033674 695: ;CHECK THAT RHWC HAS 0
11442 033674 012737 000000 001124 MOV #0,$GDDAT ;GET GOOD = 0
11443
11444 033702 017737 150154 001126 MOV @RHWC,$BDDAT ;READ RHWC FOR COMPARISON
11445 033710 023737 001124 001126 CMP $GDDAT,$BDDAT ;COMPARE EXPECTED
11446 ;DATA WITH DATA READ FROM
11447 ;RHWC
11448 033716 001401 BEQ 715 ;BRANCH IF GOOD
11449 033720 104131 ERROR 131
11450
11451 ;AFTER SETTING "CLR" BIT #5
11452 ;IN RHCS2 TO INIT THE RH

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11552 ; IN RHCS2 TO INIT THE RH
11553 ; A TWO WORD WRITE REVERSE FROM AN
11554 ; EVEN WORD BOUNDARY WAS DONE
11555 ; THEN
11556 ; RHWC SHOULD HAVE 0
11557 ; BUT CONTAINED WHAT IS
11558 ; GIVEN IN BAD RHWC

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034150

718:

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:*****
:*TEST 56 TEST DBL (RHCS3 BIT #10) TEST 0

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:* CLEAR THE SUBSYSTEM BY A CONTROLLER CLEAR (RHCS2 BIT #5)
:* SET UP FOR A TWO WORD WRITE REVERSE FROM AN ODD WORD BOUNDARY
:* DO A TWO WORD WRITE REVERSE FROM AN ODD WORD BOUNDARY
:* THIS SHOULD SET RHCS3 BIT #10 DBL
:* CHECK RHCS3
:* CHECK RHCS1, RHCS2, RHBA, RHBAE, RHWC

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:*****

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11574 034150 000004 1ST56: SCOPE
11575 034152 012706 001000 MOV #STACK, SP ; RESET STACK
11576 034156 012737 000056 004656 MOV #56, @#TSTNM ; SAVE TEST NUMBER
11577
11578 034164 004737 040312 JSR PC, @#CLDISK ; GIVE RH INITIALIZE
11579 ; SETUP UNIT NUBER
11580 ; CLEAR RHWC AND FUNCTION BITS IN RHCS1
11581
11582 ; SET UP FOR A TWO WORD WRITE REVERSE FROM AN ODD WORD BOUNDARY
11583 034170 012701 000003 MOV #3, R1 ; COUNT OF THREE
11584 034174 012702 003002 MOV #BFODD, R2 ; START THREE WORD BUFFER
11585 ; FROM AN ODD WORD BOUNDARY
11586 034200 012722 052525 18: MOV #52525, (R2)+ ; MOVE THREE 52525 INTO BUFFER
11587 034204 005301 DEC R1 ; COUNT
11588 034206 001374 BNE 18 ; BRANCH IF THREE NOT DONE
11589 034210 012777 003002 147646 MOV #BFODD, @RHBA ; SET BUS ADDRESS TO START
11590 ; FROM AN ODD WORD BOUNDARY
11591 034216 012777 177776 147636 MOV #-2, @RHWC ; WORD COUNT TWO
11592 034224 004077 150410 JSR RD, @COMND ; GO TO DO COMMAND
11593 034230 004204 REVWRT ; REVERSEWRITE DATA
11594
11595
11596 ; CHECK THAT RHCS3 HAS DBL
11597 034232 012737 002000 001124 MOV #DBL, $GDDAT ; GET GOOD = 2000
11598
11599 034240 017737 147666 001126 MOV @RHCS3, $BDDAT ; READ RHCS3 FOR COMPARISON
11600 034246 023737 001124 001126 CMP $GDDAT, $BDDAT ; COMPARE EXPECTED
11601 ; DATA WITH DATA READ FROM
11602 ; RHCS3
11603 034254 001401 BEQ 658 ; BRANCH IF GOOD
11604 034256 104124 ERROR 124
11605 ; AFTER SETTING "CLR" BIT #5
11606 ; IN RHCS2 TO INIT THE RH
11607 ; A TWO WORD WRITE REVERSE FROM AN

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12/8

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11608                                     : ODD WORD BOUNDARY WAS DONE
11609                                     : THEN
11610                                     : RHCS3 SHOULD HAVE DBL
11611                                     : =2000
11612                                     : BUT CONTAINED WHAT IS
11613                                     : GIVEN IN BAD RHCS3
11614 034260                                65$:
11615 034260 042777 000076 147572          BIC      #76, @RHCS1          : CLEAR FUNCTION BITS
11616                                     : CHECK THAT RHCS1 HAS SC:RDY:DVA
11617 034266 012737 104200 001124          MOV      #SC:RDY:DVA, $GDDAT ; GET GOOD = 104200
11618
11619 034274 017737 147560 001126          MOV      @RHCS1, $BDDAT      : READ RHCS1 FOR COMPARISON
11620 034302 023737 001124 001126          CMP      $GDDAT, $BDDAT     : COMPARE EXPECTED
11621                                     : DATA WITH DATA READ FROM
11622                                     : RHCS1
11623 034310 001401                          BEQ      67$                 : BRANCH IF GOOD
11624 034312 104125                          ERROR    125
11625
11626                                     : AFTER SETTING "CLR" BIT #5
11627                                     : IN RHCS2 TO INIT THE RH
11628                                     : A TWO WORD WRITE REVERSE FROM AN
11629                                     : ODD WORD BOUNDARY WAS DONE
11630                                     : THEN
11631                                     : RHCS1 SHOULD HAVE SC:RDY:DVA
11632                                     : =104200
11633                                     : BUT CONTAINED WHAT IS
11634 034314                                67$:
11635                                     : CHECK THAT RHCS2 HAS IR:OR
11636 034314 012737 000300 001124          MOV      #IR:OR, $GDDAT     : GET GOOD = 300
11637 034322 053737 005010 001124          BIS      UNIT, $GDDAT       : INCLUDE UNIT NUMBER
11638
11639 034330 017737 147534 001126          MOV      @RHCS2, $BDDAT     : READ RHCS2 FOR COMPARISON
11640 034336 023737 001124 001126          CMP      $GDDAT, $BDDAT     : COMPARE EXPECTED
11641                                     : DATA WITH DATA READ FROM
11642                                     : RHCS2
11643 034344 001401                          BEQ      69$                 : BRANCH IF GOOD
11644 034346 104126                          ERROR    126
11645
11646                                     : AFTER SETTING "CLR" BIT #5
11647                                     : IN RHCS2 TO INIT THE RH
11648                                     : A TWO WORD WRITE REVERSE FROM AN
11649                                     : ODD WORD BOUNDARY WAS DONE
11650                                     : THEN
11651                                     : RHCS2 SHOULD HAVE IR:OR
11652                                     : =300
11653                                     : TOGETHER WITH UNIT NUMBER
11654                                     : BUT CONTAINED WHAT IS
11655 034350                                69$:
11656                                     : CHECK THAT RHWC HAS 0
11657 034350 012737 000000 001124          MOV      #0, $GDDAT         : GET GOOD = 0
11658
11659 034356 017737 147500 001126          MOV      @RHWC, $BDDAT      : READ RHWC FOR COMPARISON
11660 034364 023737 001124 001126          CMP      $GDDAT, $BDDAT     : COMPARE EXPECTED
11661                                     : DATA WITH DATA READ FROM
11662                                     : RHWC
11663 034372 001401                          BEQ      71$                 : BRANCH IF GOOD

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K01

MAINDEC-11-DERHAB-A
DERHAB.SRC T56

MACY11 27(732) 22-SEP-76 15:11 PAGE 219
TEST DBL (RHCS3 BIT #10) TEST 0

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11664 034374 104131          ERROR 131
11665
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11670
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11673 034376          718:
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11688 034376 000004
11689 034400 012706 001000
11690 034404 012737 000057 004656
11691
11692 034412 004737 040312
11693
11694
11695
11696
11697 034416 012701 000003
11698 034422 012702 003000
11699
11700 034426 012722 052525
11701 034432 005301
11702 034434 001374
11703 034436 012777 003000 147420
11704
11705 034444 012777 177775 147410
11706 034452 004077 150162
11707 034456 004204
11708
11709
11710
11711 034460 012737 002000 001124
11712
11713 034466 017737 147440 001126
11714 034474 023737 001124 001126
11715
11716
11717 034532 001401
11718 034504 104124
11719

;AFTER SETTING "CLR" BIT #5
;IN RHCS2 TO INIT THE RH
;A TWO WORD WRITE REVERSE FROM AN
;ODD WORD BOUNDARY WAS DONE
;THEN
;RHC SHOULD HAVE 0
;BUT CONTAINED WHAT IS
;GIVEN IN BAD RHC

;*****
;*TEST 57 TEST DBL (RHCS3 BIT #10) TEST P
;
; CLEAR THE SUBSYSTEM BY A CONTROLLER CLEAR (RHCS2 BIT #5)
; SET UP FOR A THREE WORD WRITE REVERSE FROM AN EVEN WORD BOUNDARY
; DO A THREE WORD WRITE REVERSE FROM AN EVEN WORD BOUNDARY
; THIS SHOULD SET RHCS3 BIT #10 DBL
; CHECK RHCS3
; CHECK RHCS1, RHCS2, RHBA, RHBAE, RHC

;*****
†ST57: SCOPE
MOV #STACK, SP ;RESET STACK
MOV #57, @†STNM ;SAVE TEST NUMBER
JSR PC, @CLDISK ;GIVE RH INITIALIZE
;SETUP UNIT NUBER
;CLEAR RHC AND FUNCTION BITS IN RHCS1
;SET UP FOR A THREE WORD WRITE REVERSE FROM AN EVEN WORD BOUNDARY
MOV #3, R1 ;COUNT OF THREE
MOV #BFEVEN, R2 ;START THREE WORD BUFFER
;FROM AN EVEN WORD BOUNDARY
1$: MOV #52525, (R2)+ ;MOVE THREE 52525 INTO BUFFER
DEC R1 ;COUNT
BNE 1$ ;BRANCH IF THREE NOT DONE
MOV #-3, @RHC ;WORD COUNT THREE
JSR RO, @COMND ;GO TO DO COMMAND
REVRT ;REVERSEWRITE DATA

;CHECK THAT RHCS3 HAS DBL
MOV #DBL, $GDDAT ;GET GOOD = 2000
MOV @RHCS3, $BDDAT ;READ RHCS3 FOR COMPARISON
CMP $GDDAT, $BDDAT ;COMPARE EXPECTED
;DATA WITH DATA READ FROM
;RHCS3
BEQ 65$ ;BRANCH IF GOOD
ERROR 124
;AFTER SETTING "CLR" BIT #5

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11776 ;RHCW
11777 034620 001401 BEQ 71$ ;BRANCH IF GOOD
11778 034622 104131 ERROR 131
11779 ;AFTER SETTING "CLR" BIT #5
11790 ;IN RHCS2 TO INIT THE RH
11781 ;A THREE WORD WRITE REVERSE FROM AN
11782 ;EVEN WORD BOUNDARY WAS DONE
11783 ;THEN
11784 ;RHCW SHOULD HAVE 0
11785 ;BUT CONTAINED WHAT IS
11786 ;GIVEN IN BAD RHCW
11787 034624 71$:
11788
11789
11790
11791 ;*****
11792 ;*TEST 60 TEST DBL (RHCS3 BIT #10) TEST Q
11793
11794 ;* CLEAR THE SUBSYSTEM BY A CONTROLLER CLEAR (RHCS2 BIT #5)
11795 ;* SET UP FOR A THREE WORD WRITE REVERSE FROM AN ODD WORD BOUNDARY
11796 ;* DO A THREE WORD WRITE REVERSE FROM AN ODD WORD BOUNDARY
11797 ;* THIS SHOULD NOT SET RHCS3 BIT #10 DBL
11798 ;* CHECK RHCS3
11799 ;* CHECK RHCS1, RHCS2, RHBA, RHBAE, RHCW
11800
11801 ;*****
11802 034624 00C004 tST60: SCOPE
11803 034626 012706 001000 MOV #STACK, SP ;RESET STACK
11804 034632 012737 000060 004656 MOV #60, a#tSTNM ;SAVE TEST NUMBER
11805
11806 034640 004737 040312 JSR PC, a#CLDISK ;GIVE RH INITIALIZE
11807 ;SET UP UNIT NUMBER
11808 ;CLEAR RHCW AND FUNCTION BITS IN RHCS1
11809
11810 ;SET UP FOR A THREE WORD WRITE REVERSE FROM AN ODD WORD BOUNDARY
11811 034644 012701 000003 MOV #3, R1 ;COUNT OF THREE
11812 034650 012702 003002 MOV #BFODD, R2 ;START THREE WORD BUFFER
11813 ;FROM AN ODD WORD BOUNDARY
11814 034654 012722 052525 1$: MOV #52525, (R2)+ ;MOVE THREE 52525 INTO BUFFER
11815 034660 005301 DEC R1 ;COUNT
11816 034662 001374 BNE 1$ ;BRANCH IF THREE NOT DONE
11817 034664 012777 003002 147172 MOV #BFODD, aRHBA ;SET BUS ADDRESS TO START
11818 ;FROM AN ODD WORD BOUNDARY
11819 034672 012777 177775 147162 MOV #-3, aRHCW ;WORD COUNT THREE
11820 034700 004077 147734 JSR RD, aCOMND ;GO TO DO COMMAND
11821 034704 004204 REVWRT ;REVERSEWRITE DATA
11822
11823
11824 ;CHECK THAT RHCS3 HAS 0
11825 034706 012737 000000 001124 MOV #0, $GDDAT ;GET GOOD = 0
11826
11827 034714 017737 147212 001126 MOV aRHCS3, $BDDAT ;READ RHCS3 FOR COMPARISON
11828 034722 023737 001124 001126 CMP $GDDAT, $BDDAT ;COMPARE EXPECTED
11829 ;DATA WITH DATA READ FROM
11830 ;RHCS3
11831 034730 001401 BEQ 65$ ;BRANCH IF GOOD

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11832 034732 104124          ERROR 124
11833
11834
11835
11836
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11838
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11840
11841 034734
11842 034734 042777 000076 147116 65$: BIC #76, @RHCS1 ;CLEAR FUNCTION BITS
11843 ;CHECK THAT RHCS1 HAS SC!RDY!DVA ;IN RHCS2 TO INIT THE RH
11844 034742 012737 104200 001124 MOV #SC!RDY!DVA, $GDDAT ;A THREE WORD WRITE REVERSE FROM AN
11845 ;ODD WORD BOUNDARY WAS DONE
11846 034750 017737 147104 001126 MOV @RHCS1, $BDDAT ;THEN
11847 034756 023737 001124 001126 CMP $GDDAT, $BDDAT ;RHCS3 SHOULD HAVE 0
11848 ;COMPARE EXPECTED ;BUT CONTAINED WHAT IS
11849 ;DATA WITH DATA READ FROM ;GIVEN IN BAD RHCS3
11850 034764 001401 BEQ 67$
11851 034766 104125 ERROR 125
11852
11853
11854
11855
11856
11857
11858
11859
11860
11861 034770          67$:
11862 ;CHECK THAT RHCS2 HAS IR!OR
11863 034770 012737 000300 001124 MOV #IR!OR, $GDDAT ;GET GOOD = 300
11864 034776 053737 005010 001124 BIS UNIT, $GDDAT ;INCLUDE UNIT NUMBER
11865
11866 035004 017737 147060 001126 MOV @RHCS2, $BDDAT ;READ RHCS2 FOR COMPARISON
11867 035012 023737 001124 001126 CMP $GDDAT, $BDDAT ;COMPARE EXPECTED
11868 ;DATA WITH DATA READ FROM
11869 ;RHCS2
11870 035020 001401 BEQ 69$
11871 035022 104126 ERROR 126 ;BRANCH IF GOOD
11872
11873
11874
11875
11876
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11881
11882 035024          69$:
11883 ;CHECK THAT RHWC HAS 0
11884 035024 012737 000000 001124 MOV #0, $GDDAT ;GET GOOD = 0
11885
11886 035032 017737 147024 001126 MOV @RHWC, $BDDAT ;READ RHWC FOR COMPARISON
11887 035040 023737 001124 001126 CMP $GDDAT, $BDDAT ;COMPARE EXPECTED

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00000 035260          598:
00001 035260 012737 000000 001124      ;CHECK THAT RHCW HAS 0
00002 035266 017737 146570 001126      MOV      #0,$GDDAT      ;GET GOOD = 0
00003 035274 023737 001124 001126      MOV      $RHCW,$BDDAT   ;READ RHCW FOR COMPARISON
00004 035302 001401          ;COMPARE EXPECTED
00005 035304 104101          ;DATA WITH DATA READ FROM
00006 035306          ;RHCW
00007 035308          ;BRANCH IF GOOD
00008 035310          ;AFTER SETTING "CLR" BIT #5
00009 035312          ;IN RHCS2 TO INIT THE RH
00010 035314          ;A TWO WORD WRITE REVERSE FROM AN
00011 035316          ;EVEN WORD BOUNDARY WAS DONE
00012 035318          ;WITH BAI IN RHCS2 SET
00013 035320          ;THEN
00014 035322          ;RHCW SHOULD HAVE 0
00015 035324          ;BUT CONTAINED WHAT IS
00016 035326          ;GIVEN IN BAD RHCW
00017 035328
00018 035330
00019 035332
00020 035334
00021 035336
00022 035338
00023 035340
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00025 035344
00026 035346
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00031 035356

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00032 035306          718:
00033 035308
00034 035310
00035 035312
00036 035314
00037 035316
00038 035318
00039 035320
00040 035322
00041 035324
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00043 035328
00044 035330
00045 035332
00046 035334
00047 035336
00048 035338
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00051 035344
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00053 035348
00054 035350
00055 035352
00056 035354
00057 035356

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*****
*TEST 62      TEST DBL (RHCS3 BIT #10) TEST S
*****
*
*   CLEAR THE SUBSYSTEM BY A CONTROLLER CLEAR (RHCS2 BIT #5)
*   SET UP FOR A NINE WORD READ REVERSE FROM AN EVEN WORD BOUNDARY
*   DO A NINE WORD READ REVERSE FROM AN EVEN WORD BOUNDARY
*   THIS SHOULD SET RHCS3 BIT #10 DBL
*   CHECK RHCS3
*   CHECK RHCS1,RHCS2,RHBA,RHBAE,RHCW
*   IF MAG. TAPE NOT USED, THIS TEST IS NOT DONE
*****

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00058 035306 000004          TST62:  SCOPE
00059 035310 012706 001000      MOV      #STACK,SP      ;RESET STACK
00060 035314 012737 000062 004656      MOV      #62,$#TSTNM    ;SAVE TEST NUMBER
00061 035322 004737 040312      JSR      PC,$#CLDISK    ;GIVE RH INITIALIZE
00062 035326 022737 041710 004640      CMP      #CMNDTM,COMND  ;SETUP UNIT NUBER
00063 035334 001103          ;CLEAR RHCW AND FUNCTION BITS IN RHCS1
00064 035336          ;IS TU THERE
00065 035338          BNE      TST63      ;BRANCH IF TU NOT THERE
00066 035340          ;SET UP FOR A NINE WORD READ REVERSE FROM AN EVEN WORD BOUNDARY
00067 035342 012701 000003      MOV      #3,R1          ;COUNT OF THREE
00068 035344 012702 003000      MOV      #BFEVEN,R2     ;START THREE WORD BUFFER
00069 035346 012722 052525          ;FROM AN EVEN WORD BOUNDARY
00070 035348 005301          ;MOVE THREE 52525 INTO BUFFER
00071 035350 001374          ;COUNT
00072 035352 012777 003000 146500      MOV      #52525,(R2)+   ;BRANCH IF THREE NOT DONE
00073 035354          ;COUNT
00074 035356          ;SET BUS ADDRESS TO STAR

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120056 035364 012777 177767 146470      MOV #9.,DRHWC ;WORD COUNT NINE      ;FROM AN EVEN WORD BOUNDARY
120057 035372 004077 147242      JSR   RO,DCOMND ;GO TO DO COMMAND
120058 035376 004200      REVRED ;REVERSE READ
120059 035400 012737 002000 001124      ;CHECK THAT RHCS3 HAS DBL
120060 035406 017737 146520 001126      MOV   DRHCS3,$BDDAT ;READ RHCS3 FOR COMPARISON
120061 035414 023737 001124 001126      CMP   $GDDAT,$BDDAT ;COMPARE EXPECTED
120062 035422 001401      BEQ   65$ ;BRANCH IF GOOD
120063 035424 104124      ERROR 124 ;DATA WITH DATA READ FROM
120064 035426 042777 000076 146424      ;RHCS3
120065 035426 042777 000076 146424      ;AFTER SETTING "CLR" BIT #5
120066 035434 012737 004200 001124      BIC   #76,DRHCS1 ;CLEAR FUNCTION BITS
120067 035442 017737 146412 001126      ;CHECK THAT RHCS1 HAS RDY!DVA
120068 035450 023737 001124 001126      MOV   #RDY!DVA,$GDDAT ;GET GOOD = 4200
120069 035456 001401      BEQ   67$ ;BRANCH IF GOOD
120070 035460 104125      ERROR 125 ;RHCS1 SHOULD HAVE DBL
120071 035462 012737 000100 001124      ;=2000
120072 035470 053737 005010 001124      ;BUT CONTAINED WHAT IS
120073 035476 017737 146366 001126      ;GIVEN IN BAD RHCS3
120074 035504 023737 001124 001126      ;CHECK THAT RHCS2 HAS IR
120075 035512 001401      BEQ   69$ ;BRANCH IF GOOD
120076 035514 104126      ERROR 126 ;RHCS1 SHOULD HAVE RDY!DVA
120077 035512 001401      BEQ   69$ ;BRANCH IF GOOD
120078 035514 104126      ERROR 126 ;GIVEN IN BAD RHCS1
120079 035512 001401      BEQ   69$ ;BRANCH IF GOOD
120080 035514 104126      ERROR 126 ;AFTER SETTING "CLR" BIT #5

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; IN RHCS2 TO INIT THE RH
; A NINE WORD READ REVERSE FROM AN
; EVEN WORD BOUNDARY WAS DONE
; THEN
; RHCS2 SHOULD HAVE IR
; =100
; TOGETHER WITH UNIT NUMBER
; BUT CONTAINED WHAT IS
; GIVEN IN BAD RHCS2

035516          69$:
; CHECK THAT RHC HAS 0
MOV             #0,$GDDAT      ; GET GOOD = 0

035516 012737 000000 001124
MOV            @RHC,$BDDAT     ; READ RHC FOR COMPARISON
035524 017737 146332 001126   CMP            $GDDAT,$BDDAT  ; COMPARE EXPECTED
035532 023737 001124 001126   ; DATA WITH DATA READ FROM
; RHC
; BRANCH IF GOOD

035540 001401          BEQ      71$
035542 104131          ERROR   131

; AFTER SETTING "CLR" BIT #5
; IN RHCS2 TO INIT THE RH
; A NINE WORD READ REVERSE FROM AN
; EVEN WORD BOUNDARY WAS DONE
; THEN
; RHC SHOULD HAVE 0
; BUT CONTAINED WHAT IS
; GIVEN IN BAD RHC

035544          71$:

;*****
;*TEST 63      TEST DBL (RHCS3 BIT #10) TEST T
;*****
;*
;* CLEAR THE SUBSYSTEM BY A CONTROLLER CLEAR (RHCS2 BIT #5)
;* SET UP FOR A TEN WORD READ REVERSE FROM AN EVEN WORD BOUNDARY
;* DO A TEN WORD READ REVERSE FROM AN EVEN WORD BOUNDARY
;* THIS SHOULD NOT SET RHCS3 BIT #10 DBL
;* CHECK RHCS3
;* CHECK RHCS1,RHCS2,RHBA,RHBAE,RHC
;* IF MAG. TAPE NOT USED. THIS TEST IS NOT DONE
;*****

†T63: SCOPE
035544 000004          MOV      #STACK,SP      ; RESET STACK
035546 012706 001000   MOV      #63,@†TSTNM   ; SAVE TEST NUMBER
035552 012737 000063 004656   JSR     PC,@†CLDISK   ; GIVE RH INITIALIZE
; SETUP UNIT NUMBER
035560 004737 040312   CMP     #CMNDTM,COMND ; CLEAR RHC AND FUNCTION BITS IN RHCS1
; IS TU THERE
035564 022737 041710 004640   BNE    T64           ; BRANCH IF TU NOT THERE
035572 001103

; SET UP FOR A TEN WORD READ REVERSE FROM AN EVEN WORD BOUNDARY

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MAINDEC-11-DERHAB-A
DERHAB.SRC T63

MACY11 27(732) 22-SEP-76 15:11 PAGE 218
TEST DBL (RHCS3 BIT #10) TEST T

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12168 035574 012701 000003      MOV      #3,R1      ;COUNT OF THREE
12169 035600 012702 003000      MOV      #BFEBVEN,R2 ;START THREE WORD BUFFER
12170 035604 012722 052525      1$: MOV      #52525,(R2)+ ;MOVE THREE 52525 INTO BUFFER
12171 035610 005301      DEC      R1          ;COUNT
12172 035612 001374      BNE      1$         ;BRANCH IF THREE NOT DONE
12173 035614 012777 003000 146242      MOV      #BFEBVEN,@RHBA ;SET BUS ADDRESS TO START
12174 035622 012777 177766 146232      MOV      #-10,@RHWC ;WORD COUNT TEN
12175 035630 004077 147004      JSR      RD,@COMND   ;GO TO DO COMMAND
12176 035634 004200      REVRED          ;REVERSE READ
12177 035636 012737 000000 001124      ;CHECK THAT RHCS3 HAS 0
12178 035644 017737 146262 001126      MOV      @RHCS3,$BDDAT ;READ RHCS3 FOR COMPARISON
12179 035652 023737 001124 001126      CMP      $GDDAT,$BDDAT ;COMPARE EXPECTED
12180 035660 001401      BEQ      65$        ;DATA WITH DATA READ FROM
12181 035662 104124      ERROR      124      ;RHCS3
12182 035664 042777 000076 146166      65$: BIC      #76,@RHCS1 ;BRANCH IF GOOD
12183 035672 012737 004200 001124      ;CHECK THAT RHCS1 HAS RDY!DVA
12184 035700 017737 146154 001126      MOV      @RDY!DVA,$GDDAT ;GET GOOD = 4200
12185 035706 023737 001124 001126      MOV      @RHCS1,$BDDAT ;READ RHCS1 FOR COMPARISON
12186 035714 001401      BEQ      67$        ;COMPARE EXPECTED
12187 035716 104125      ERROR      125      ;DATA WITH DATA READ FROM
12188 035720 017737 146130 001126      67$: MOV      @RHCS2,$BDDAT ;RHCS1
12189 035726 053737 005010 001124      ;BRANCH IF GOOD
12190 035734 017737 146130 001126      ;AFTER SETTING "CLR" BIT #5
12191 035734 017737 146130 001126      ;IN RHCS2 TO INIT THE RH
12192 035734 017737 146130 001126      ;A TEN WORD READ REVERSE FROM AN
12193 035734 017737 146130 001126      ;EVEN WORD BOUNDARY WAS DONE
12194 035734 017737 146130 001126      ;THEN
12195 035734 017737 146130 001126      ;RHCS1 SHOULD HAVE RDY!DVA
12196 035734 017737 146130 001126      ;=4200
12197 035734 017737 146130 001126      ;BUT CONTAINED WHAT IS
12198 035734 017737 146130 001126      ;GIVEN IN BAD RHCS1
12199 035720 017737 146130 001126      ;CHECK THAT RHCS2 HAS IR
12200 035720 017737 146130 001126      MOV      #IR,$GDDAT   ;GET GOOD = 100
12201 035726 053737 005010 001124      BIS      UNIT,$GDDAT  ;INCLUDE UNIT NUMBER
12202 035734 017737 146130 001126      MOV      @RHCS2,$BDDAT ;READ RHCS2 FOR COMPARISON

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Handwritten notes:
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12234 035742 023737 001124 001126      CMP      $GDDAT,$BDDAT      ;COMPARE EXPECTED
12235                                     ;DATA WITH DATA READ FROM
12236                                     ;RHCS2
12237 035750 001401      BEQ      69$                ;BRANCH IF GOOD
12238 035752 104126      ERROR    126
12239                                     ;AFTER SETTING "CLR" BIT #5
12240                                     ;IN RHCS2 TO INIT THE RH
12241                                     ;A TEN WORD READ REVERSE FROM AN
12242                                     ;EVEN WORD BOUNDARY WAS DONE
12243                                     ;THEN
12244                                     ;RHCS2 SHOULD HAVE IR
12245                                     ;=100
12246                                     ;TOGETHER WITH UNIT NUMBER
12247                                     ;BUT CONTAINED WHAT IS
12248                                     ;GIVEN IN BAD RHCS2
12249 035754                                     69$:
12250                                     ;CHECK THAT RHWC HAS 0
12251 035754 012737 000000 001124      MOV      #0,$GDDAT          ;GET GOOD = 0
12252 035762 017737 146074 001126      MOV      @RHWC,$BDDAT      ;READ RHWC FOR COMPARISON
12253 035770 023737 001124 001126      CMP      $GDDAT,$BDDAT      ;COMPARE EXPECTED
12254                                     ;DATA WITH DATA READ FROM
12255                                     ;RHWC
12256                                     ;BRANCH IF GOOD
12257 035776 001401      BEQ      71$                ;AFTER SETTING "CLR" BIT #5
12258 035000 104131      ERROR    131                ;IN RHCS2 TO INIT THE RH
12259                                     ;A TEN WORD READ REVERSE FROM AN
12260                                     ;EVEN WORD BOUNDARY WAS DONE
12261                                     ;THEN
12262                                     ;RHWC SHOULD HAVE 0
12263                                     ;BUT CONTAINED WHAT IS
12264                                     ;GIVEN IN BAD RHWC
12265 036002                                     71$:
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*****
*TEST 64      TEST DBL (RHCS3 BIT #10) TEST U

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* CLEAR THE SUBSYSTEM BY A CONTROLLER CLEAR (RHCS2 BIT #5)
* SET UP FOR A TEN WORD READ REVERSE FROM AN ODD WORD BOUNDARY
* DO A TEN WORD READ REVERSE FROM AN ODD WORD BOUNDARY
* THIS SHOULD SET RHCS3 BIT #10 DBL
* CHECK RHCS3
* CHECK RHCS1,RHCS2,RHBA,RHBAE,RHWC
* IF MAG. TAPE NOT USED. THIS TEST IS NOT DONE

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TST64: SCOPE
MOV      #STACK,SP      ;RESET STACK
MOV      #64,@TSTNM     ;SAVE TEST NUMBER
JSR      PC,@CLDISK     ;GIVE RH INITIALIZE
;SETUP UNIT NUBER
;CLEAR RHWC AND FUNCTION BITS IN RHCS1

```

MAINDEC-11-DERHAB-A
DERHAB.SRC T64

MACY11 27(732) 22-SEP-76 15:11 PAGE 230
TEST DBL (RHCS3 BIT #10) TEST U

```

12280 036022 022737 041710 004640      CMP      #CMNDTM,COMND      ;IS TU THERE
12281
12282 036030 001103                      BNE      TST65      ;BRANCH IF TU NOT THERE
12283
12284
12285
12286 036032 012701 000003      :SET UP FOR A TEN WORD READ REVERSE FROM AN ODD WORD BOUNDARY
12287 036036 012702 003002      MOV      #3,R1      ;COUNT OF THREE
12288
12289
12290 036042 012722 052525      1$:      MOV      #52525.(R2)+    ;START THREE WORD BUFFER
12291 036046 005301      ;FROM AN ODD WORD BOUNDARY
12292 036050 001374      ;MOVE THREE 52525 INTO BUFFER
12293 036052 012777 003002 146004      DEC      R1      ;COUNT
12294
12295
12296 036060 012777 177766 145774      BNE      1$      ;BRANCH IF THREE NOT DONE
12297 036066 004077 146546      MOV      #BFODD,ARHBA    ;SET BUS ADDRESS TO START
12298
12299
12300 036074 012737 002000 001124      MOV      #-10.,ARHWC    ;WORD COUNT TEN
12301
12302 036102 017737 146024 001126      JSR      RD,ACOMND      ;GO TO DO COMMAND
12303 036110 023737 001124 001126      REVRED                      ;REVERSE READ
12304
12305
12306 036116 001401      :CHECK THAT RHCS3 HAS DBL
12307 036120 104124      MOV      #DBL,$GDDAT    ;GET GOOD = 2000
12308
12309
12310 036102 017737 146024 001126      MOV      ARHCS3,$BDDAT  ;READ RHCS3 FOR COMPARISON
12311 036110 023737 001124 001126      CMP      $GDDAT,$BDDAT  ;COMPARE EXPECTED
12312
12313
12314
12315
12316
12317 036116 001401      ;DATA WITH DATA READ FROM
12318 036120 104124      ;RHCS3
12319
12320
12321
12322
12323
12324
12325
12326 036116 001401      BEQ      65$           ;BRANCH IF GOOD
12327 036120 104124      ERROR    124
12328
12329
12330
12331
12332
12333
12334
12335

```

;AFTER SETTING "CLR" BIT #5
 ;IN RHCS2 TO INIT THE RH
 ;A TEN WORD READ REVERSE FROM AN
 ;ODD WORD BOUNDARY WAS DONE
 ;THEN
 ;RHCS3 SHOULD HAVE DBL
 ;=2000
 ;BUT CONTAINED WHAT IS
 ;GIVEN IN BAD RHCS3

55\$:
 BIC #76,ARHCS1 ;CLEAR FUNCTION BITS
 ;CHECK THAT RHCS1 HAS RDY!DVA
 MOV #RDY!DVA,\$GDDAT ;GET GOOD = 4200
 MOV ARHCS1,\$BDDAT ;READ RHCS1 FOR COMPARISON
 CMP \$GDDAT,\$BDDAT ;COMPARE EXPECTED
 ;DATA WITH DATA READ FROM
 ;RHCS1
 ;BRANCH IF GOOD

;AFTER SETTING "CLR" BIT #5
 ;IN RHCS2 TO INIT THE RH
 ;A TEN WORD READ REVERSE FROM AN
 ;ODD WORD BOUNDARY WAS DONE
 ;THEN
 ;RHCS1 SHOULD HAVE RDY!DVA
 ;=4200
 ;BUT CONTAINED WHAT IS

```

12336                                     :GIVEN IN BAD RHCS1
12337 036156                               67$:
12338                                     :CHECK THAT RHCS2 HAS IR
12339 036156 012737 000100 001124      MOV  #IR,$GDDAT      :GET GOOD = 100
12340 036164 053737 005010 001124      BIS  UNIT,$GDDAT    ;INCLUDE UNIT NUMBER
12341
12342 036172 017737 145672 001126      MOV  @RHCS2,$BDDAT  :READ RHCS2 FOR COMPARISON
12343 036200 023737 001124 001126      CMP  $GDDAT,$BDDAT ;COMPARE EXPECTED
12344                                     ;DATA WITH DATA READ FROM
12345                                     ;RHCS2
12346 036206 001401                          BEQ  69$            ;BRANCH IF GOOD
12347 036210 104126                          ERROR 126
12348                                     ;AFTER SETTING "CLR" BIT #5
12349                                     ;IN RHCS2 TO INIT THE RH
12350                                     ;A TEN WORD READ REVERSE FROM AN
12351                                     ;ODD WORD BOUNDARY WAS DONE
12352                                     ;THEN
12353                                     ;RHCS2 SHOULD HAVE IR
12354                                     ;=100
12355                                     ;TOGETHER WITH UNIT NUMBER
12356                                     ;BUT CONTAINED WHAT IS
12357                                     ;GIVEN IN BAD RHCS2

```

```

12358 036212                               69$:
12359                                     :CHECK THAT RHWC HAS 0
12360 036212 012737 000000 001124      MOV  #0,$GDDAT     ;GET GOOD = 0
12361
12362 036220 017737 145636 001126      MOV  @RHWC,$BDDAT  :READ RHWC FOR COMPARISON
12363 036226 023737 001124 001126      CMP  $GDDAT,$BDDAT ;COMPARE EXPECTED
12364                                     ;DATA WITH DATA READ FROM
12365                                     ;RHWC
12366 036234 001401                          BEQ  71$            ;BRANCH IF GOOD
12367 036236 104131                          ERROR 131
12368                                     ;AFTER SETTING "CLR" BIT #5
12369                                     ;IN RHCS2 TO INIT THE RH
12370                                     ;A TEN WORD READ REVERSE FROM AN
12371                                     ;ODD WORD BOUNDARY WAS DONE
12372                                     ;THEN
12373                                     ;RHWC SHOULD HAVE 0
12374                                     ;BUT CONTAINED WHAT IS
12375                                     ;GIVEN IN BAD RHWC

```

```

12376 036240                               71$:
12377
12378
12379

```

```

;*****
;*TEST 55      TEST DBL (RHCS3 BIT #10) TEST V

```

```

;*
;* CLEAR THE SUBSYSTEM BY A CONTROLLER CLEAR (RHCS2 BIT #5)
;* SET UP FOR A ELEVEN WORD READ REVERSE FROM AN EVEN WORD BOUNDARY
;* DO A ELEVEN WORD READ REVERSE FROM AN EVEN WORD BOUNDARY
;* THIS SHOULD SET RHCS3 BIT #10 DBL
;* CHECK RHCS3
;* CHECK RHCS1,RHCS2,RHBA,RHBAE,RHWC
;* IF MAG. TAPE NOT USED.THIS TEST IS NOT DONE

```

```

;*****

```

```

12380
12381
12382
12383
12384
12385
12386
12387
12388
12389
12390
12391

```



```

12448 ; IN RHCS2 TO INIT THE RH
12449 ; A ELEVEN WORD READ REVERSE FROM AN
12450 ; EVEN WORD BOUNDARY WAS DONE
12451 ; THEN
12452 ; RHCS1 SHOULD HAVE RDY!DVA
12453 ; =4200
12454 ; BUT CONTAINED WHAT IS
12455 ; GIVEN IN BAD RHCS1
12456 036414 67$: ; CHECK THAT RHCS2 HAS IR
12457 ; MOV #IR,$GDDAT ; GET GOOD = 100
12458 036414 012737 000100 001124 ; BIS UNIT,$GDDAT ; INCLUDE UNIT NUMBER
12459 036422 053737 005010 001124
12460
12461 036430 017737 145434 001126 ; MOV @RHCS2,$BDDAT ; READ RHCS2 FOR COMPARISON
12462 036436 023737 001124 001126 ; CMP $GDDAT,$BDDAT ; COMPARE EXPECTED
12463 ; DATA WITH DATA READ FROM
12464 ; RHCS2
12465 036444 001401 ; BEQ 69$ ; BRANCH IF GOOD
12466 036446 104126 ; ERROR 126
12467 ; AFTER SETTING "CLR" BIT #5
12468 ; IN RHCS2 TO INIT THE RH
12469 ; A ELEVEN WORD READ REVERSE FROM AN
12470 ; EVEN WORD BOUNDARY WAS DONE
12471 ; THEN
12472 ; RHCS2 SHOULD HAVE IR
12473 ; =100
12474 ; TOGETHER WITH UNIT NUMBER
12475 ; BUT CONTAINED WHAT IS
12476 ; GIVEN IN BAD RHCS2
12477 036450 69$: ; CHECK THAT RHWC HAS 0
12478 ; MOV #0,$GDDAT ; GET GOOD = 0
12479 036450 012737 000000 001124
12480
12481 036456 017737 145400 001126 ; MOV @RHWC,$BDDAT ; READ RHWC FOR COMPARISON
12482 036464 023737 001124 001126 ; CMP $GDDAT,$BDDAT ; COMPARE EXPECTED
12483 ; DATA WITH DATA READ FROM
12484 ; RHWC
12485 036472 001401 ; BEQ 71$ ; BRANCH IF GOOD
12486 036474 104131 ; ERROR 131
12487 ; AFTER SETTING "CLR" BIT #5
12488 ; IN RHCS2 TO INIT THE RH
12489 ; A ELEVEN WORD READ REVERSE FROM AN
12490 ; EVEN WORD BOUNDARY WAS DONE
12491 ; THEN
12492 ; RHWC SHOULD HAVE 0
12493 ; BUT CONTAINED WHAT IS
12494 ; GIVEN IN BAD RHWC
12495 036476 71$:
12496
12497
12498
12499 ;*****
12500 ;*TEST 66 TEST DBL (RHCS3 BIT #10) TEST W
12501
12502 ;* CLEAR THE SUBSYSTEM BY A CONTROLLER CLEAR (RHCS2 BIT #5)
12503 ;* SET UP FOR A ELEVEN WORD READ REVERSE FROM AN ODD WORD BOUNDARY

```

M02

MAINDEC-11-DERHAB-A
DERHAB.SRC T66

MACY11 27(732) 22-SEP-76 15:11 PAGE 234
TEST DBL (RHCS3 BIT #10) TEST W

```

12504      : *      DO A ELEVEN WORD READ REVERSE FROM AN ODD WORD BOUNDARY
12505      : *      THIS SHOULD NOT SET RHCS3 BIT #10 DBL
12506      : *      CHECK RHCS3
12507      : *      CHECK RHCS1, RHCS2, RHBA, RHBAE, RHWC
12508      : *      IF MAG. TAPE NOT USED. THIS TEST IS NOT DONE
12509
12510      : *****
12511 036476 000004      TST66: SCOPE
12512 036500 012706 001000      MOV      #STACK, SP      ; RESET STACK
12513 036504 012737 000066 004656      MOV      #66, @#TSTNM    ; SAVE TEST NUMBER
12514
12515 036512 004737 040312      JSR      PC, @#CLDISK    ; GIVE RH INITIALIZE
12516                                ; SETUP UNIT NUMBER
12517                                ; CLEAR RHWC AND FUNCTION BITS IN RHCS1
12518 036516 022737 041710 004640      CMP      #CMNDTM, COMND  ; IS TU THERE
12519
12520 036524 001103      BNE      TST67      ; BRANCH IF TU NOT THERE
12521
12522
12523                                ; SET UP FOR A ELEVEN WORD READ REVERSE FROM AN ODD WORD BOUNDARY
12524 036526 012701 000003      MOV      #3, R1          ; COUNT OF THREE
12525 036532 012702 003002      MOV      #BFODD, R2      ; START THREE WORD BUFFER
12526                                ; FROM AN ODD WORD BOUNDARY
12527 036536 012722 052525      1$: MOV      #52525, (R2)+  ; MOVE THREE 52525 INTO BUFFER
12528 036542 005301      DEC      R1              ; COUNT
12529 036544 001374      BNE      1$             ; BRANCH IF THREE NOT DONE
12530 036546 012777 003002 145310      MOV      #BFODD, @RHBA   ; SET BUS ADDRESS TO START
12531                                ; FROM AN ODD WORD BOUNDARY
12532 036554 012777 177765 145300      MOV      #-11., @RHWC    ; WORD COUNT ELEVEN
12533
12534 036562 004077 146052      JSR      RD, @COMND      ; GO TO DO COMMAND
12535 036566 004200      REVRED                                ; REVERSE READ
12536
12537                                ; CHECK THAT RHCS3 HAS 0
12538 036570 012737 000800 001124      MOV      #0, $GDDAT      ; GET GOOD = 0
12539
12540 036576 017737 145320 001126      MOV      @RHCS3, $BDDAT  ; READ RHCS3 FOR COMPARISON
12541 036504 023737 001124 001126      CMP      $GDDAT, $BDDAT  ; COMPARE EXPECTED
12542                                ; DATA WITH DATA READ FROM
12543                                ; RHCS3
12544 036612 001401      BEQ      65$            ; BRANCH IF GOOD
12545 036614 104124      ERROR   124
12546
12547                                ; AFTER SETTING "CLR" BIT #5
12548                                ; IN RHCS2 TO INIT THE RH
12549                                ; A ELEVEN WORD READ REVERSE FROM AN
12550                                ; ODD WORD BOUNDARY WAS DONE
12551                                ; THEN
12552                                ; RHCS3 SHOULD HAVE 0
12553                                ; BUT CONTAINED WHAT IS
12554                                ; GIVEN IN BAD RHCS3
12554 036616      65$: BIC      #76, @RHCS1     ; CLEAR FUNCTION BITS
12555 036616 042777 000076 145234      ; CHECK THAT RHCS1 HAS RDY!DVA
12556                                ;
12557 036624 012737 004200 001124      MOV      #RDY!DVA, $GDDAT ; GET GOOD = 4200
12558
12559 036632 017737 145222 001126      MOV      @RHCS1, $BDDAT  ; READ RHCS1 FOR COMPARISON

```



```

12560 036640 023737 001124 001126      CMP      $GDDAT,$BDDAT      ;COMPARE EXPECTED
12561                                     ;DATA WITH DATA READ FROM
12562                                     ;RHCS1
12563 036646 001401      BEQ      67$                ;BRANCH IF GOOD
12564 036650 104125      ERROR    125
12565                                     ;AFTER SETTING "CLR" BIT #5
12566                                     ;IN RHCS2 TO INIT THE RH
12567                                     ;A ELEVEN WORD READ REVERSE FROM AN
12568                                     ;ODD WORD BOUNDARY WAS DONE
12569                                     ;THEN
12570                                     ;RHCS1 SHOULD HAVE RDY!DVA
12571                                     ;=4200
12572                                     ;BUT CONTAINED WHAT IS
12573                                     ;GIVEN IN BAD RHCS1
12574 036652                67$:
12575                                     ;CHECK THAT RHCS2 HAS IR
12576 036652 012737 000100 001124      MOV      #IR,$GDDAT        ;GET GOOD = 100
12577 036660 053737 005010 001124      BIS      UNIT,$GDDAT      ;INCLUDE UNIT NUMBER
12578
12579 036666 017737 145176 001126      MOV      @RHCS2,$BDDAT    ;READ RHCS2 FOR COMPARISON
12580 036674 023737 001124 001126      CMP      $GDDAT,$BDDAT    ;COMPARE EXPECTED
12581                                     ;DATA WITH DATA READ FROM
12582                                     ;RHCS2
12583 036702 001401      BEQ      63$                ;BRANCH IF GOOD
12584 036704 104126      ERROR    126
12585                                     ;AFTER SETTING "CLR" BIT #5
12586                                     ;IN RHCS2 TO INIT THE RH
12587                                     ;A ELEVEN WORD READ REVERSE FROM AN
12588                                     ;ODD WORD BOUNDARY WAS DONE
12589                                     ;THEN
12590                                     ;RHCS2 SHOULD HAVE IR
12591                                     ;=100
12592                                     ;TOGETHER WITH UNIT NUMBER
12593                                     ;BUT CONTAINED WHAT IS
12594                                     ;GIVEN IN BAD RHCS2
12595 036706                69$:
12596                                     ;CHECK THAT RHWC HAS 0
12597 036706 012737 000000 001124      MOV      #0,$GDDAT        ;GET GOOD = 0
12598
12599 036714 017737 145142 001126      MOV      @RHWC,$BDDAT    ;READ RHWC FOR COMPARISON
12600 036722 023737 001124 001126      CMP      $GDDAT,$BDDAT    ;COMPARE EXPECTED
12601                                     ;DATA WITH DATA READ FROM
12602                                     ;RHWC
12603 036730 001401      BEQ      71$                ;BRANCH IF GOOD
12604 036732 104131      ERROR    131
12605                                     ;AFTER SETTING "CLR" BIT #5
12606                                     ;IN RHCS2 TO INIT THE RH
12607                                     ;A ELEVEN WORD READ REVERSE FROM AN
12608                                     ;ODD WORD BOUNDARY WAS DONE
12609                                     ;THEN
12610                                     ;RHWC SHOULD HAVE 0
12611                                     ;BUT CONTAINED WHAT IS
12612                                     ;GIVEN IN BAD RHWC
12613 036734                71$:
12614
12615

```

:TEST 67 TEST DBL (RHCS3 BIT #10) TEST X

:* CLEAR THE SUBSYSTEM BY A CONTROLLER CLEAR (RHCS2 BIT #5)
:* SET UP FOR A TEN WORD READ REVERSE FROM AN ODD WORD BOUNDARY
:* WITH BAI IN RHCS2 BIT #3 SET
:* DO A TEN WORD READ REVERSE FROM AN ODD WORD BOUNDARY
:* THIS SHOULD NOT SET RHCS3 BIT #10 DBL
:* CHECK RHCS3
:* CHECK RHCS1, RHCS2, RHBA, RHBAE, RHW
:* IF MAG. TAPE NOT USED. THIS TEST IS NOT DONE

TST67: SCOPE

036734 000004
036736 012706 001000
036742 012707 000767 004656
036750 004737 340312
036754 022737 041710 004640
036762 001106

MOV #STACK, SP :RESET STACK
MOV #67, @TSTNM :SAVE TEST NUMBER
JSR PC, @CLDISK :GIVE RH INITIALIZE
:SETUP UNIT NUMBER
CMP #CMNDTM, CMND :CLEAR RHW AND FUNCTION BITS IN RHCS1
:IS TU THERE
BNE TST70 :BRANCH IF TU NOT THERE

036764 012701 000000
036770 012702 003002
036774 012722 052525
037000 005307
037002 001374
037004 012777 003002 145052

:SET UP FOR A TEN WORD READ REVERSE FROM AN ODD WORD BOUNDARY
MOV #3, R1 :COUNT OF THREE
MOV #BF00D, R2 :START THREE WORD BUFFER
:FROM AN ODD WORD BOUNDARY
IS: MOV #52525, (R2)+ :MOVE THREE 52525 INTO JUFFER
DEC R1 :COUNT
BNE IS :BRANCH IF THREE NOT DONE
MOV #BF00D, @RHBA :SET BUS ADDRESS TO START
:FROM AN ODD WORD BOUNDARY

037012 012777 177766 145042
037020 052777 000010 145042

MOV #-10, @RHW :WORD COUNT TEN
BIS #BAI, @RHCS2 :SET BAI IN RHCS2

037026 004077 145606
037032 004200

JSR @COMND :GO TO DO COMMAND
REVRED :REVERSE READ

037034 012737 000000 001124

:CHECK THAT RHCS3 HAS 0
MOV #0, \$GDDAT :GET GOOD = 0

037042 017737 145064 001126
037050 023737 001124 001126

MOV @RHCS3, \$BDDAT :READ RHCS3 FOR COMPARISON
CMP \$GDDAT, \$BDDAT :COMPARE EXPECTED
:DATA WITH DATA READ FROM
:RHCS3

037056 001401
037060 104124

BEG 655 :BRANCH IF GOOD
ERROR 124

:AFTER SETTING "CLR" BIT #5
:IN RHCS2 TO INIT THE RH
:A TEN WORD READ REVERSE FROM AN
:ODD WORD BOUNDARY WAS DONE
:WITH BAI IN RHCS2 SET
:THEN

MAINDEC-11-DERHAB-A
DERHAB.SRC T67

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TEST 03 (RHCS2 BIT #10) TEST X

:AFTER SETTING "CLR" BIT #5
:IN RHCS2 TO INIT THE RH
:A TEN WORD READ REVERSE FROM AN
:ODD WORD BOUNDARY WAS DONE
:WITH BAI IN RHCS2 SET
:THEN
:RHWC SHOULD HAVE 0
:BUT CONTAINED WHAT IS
:GIVEN IN BAD RHWC

037200

715:

*TEST 70 END OF ONE RH
* THIS IS THE END OF TEST FOR ONE RH
* IF THERE ARE MORE RH THEN THE PROGRAM
* JUMPS TO TEST 2 FOR NEXT RH TEST
* END PASS IS REACHED ONLY AFTER ALL RH ARE COMPLETE

037200 000004
037202 012737 000001 001212
037210 005037 177776
037214 104401 037222
037220 000414

037252
037252 013746 005510
037256 013732 005506
037262 162602
037264 006202
037266 016246 005436
037272 104401
037274 104401 037302
037280 000410

037322
037322 013746 004060
037326 104402
037330 104401 037336
037334 000421

037400
037400 013746 001112
037404 104405
037406 104401 037414
037412 000402

037420
037420 104401 037426
037424 000402

037432

TEST70: SCOPE
MOV #1,STIMES ;DO 1 ITERATION
CLR PS ;REINSTATE PS TO 0
TYPE 655 ;TYPE ASCIZ STRING
BR 648 ;GET OVER THE ASCIZ
655: .ASCIZ <15><12>/END OF TEST FOR RH NO/
648: MOV WORUNT,-(SP) ;GET WORKING RH NUMBER LEFT
MOV TSTUNT,R2 ;GET TOTAL NO OF RH FOUND
SUB (SP)+,R2 ;NO OF RH TESTED
ASL R2 ;INDEX FOR RH TESTED
MOV FRHNM(R2),-(SP) ;TYPE OF RH NO.
TYPDS
TYPE 675 ;TYPE ASCIZ STRING
BR 668 ;GET OVER THE ASCIZ
675: .ASCIZ / BASE /
668: MOV RHCS1,-(SP) ;TYPE BASE ADDRESS
TYPOC
TYPE 695 ;TYPE ASCIZ STRING
BR 695 ;GET OVER THE ASCIZ
695: .ASCIZ <15><12>/TOTAL NO OF ERRORS ON THIS PASS
688: MOV SERRTL,-(SP) ;TYPE TOTAL NO OF ERRORS
TYPDS
TYPE 715 ;TYPE ASCIZ STRING
BR 705 ;GET OVER THE ASCIZ
715: .ASCIZ <15><12> / /
705: TYPE 735 ;TYPE ASCIZ STRING
BR 725 ;GET OVER THE ASCIZ
735: .ASCIZ <15><12> / /
725:

MAINDEC-11-DERHAB-A
DERHAB.SRC T70

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END OF ONE RH

127784	037432	005037	001112		CLR	\$ERTTL	:CLEAR TOTAL NO OF ERRORS
127785	037436	005337	005510		DEC	WORUNT	:DECREASE NO. OF RH TESTED
127786	037442	001402			BEQ	IS	
127787	037444	000137	010622		JMP	TST2	
127788	037450	013737	005506	005510	MOV	*STUNT,WORUNT	:MAKE WORKING RH SAME
127789							:AS TOTAL RH

AT

128790
128791
128792
128793
128794
128795
128796
128797
128798
128799 037456
128800 037456 000004
128801 037460 005037 001102
128802 037464 005037 001212
128803 037470 005237 001100
128804 037474 042737 100000 001100
128805 037502 005327
128806 037504 000001
128807 037506 003022
128808 037510 012737
128809 037512 000001
128810 037514 037504
128811 037516 104401 037563
128812 037522 013746 001100
128813 037526 104405
128814 037530 104401 037560
128815 037534 013700 000042
128816 037540 001405
128817 037542 000005
128818 037544 004710
128819 037546 000240
128820 037550 000240
128821 037552 000240
128822 037554
128823 037554 000137
128824 037556 010622
128825 037560 377 377 000
128826 037563 015 042412 042116
128827 037570 050040 051501 020123
128828 037576 000043

.SBTTL END OF PASS ROUTINE

::*****
:*INCREMENT THE PASS NUMBER (\$PASS)
:*TYPE "END PASS #XXXXX" (WHERE XXXXX IS A DECIMAL NUMBER)
:*IF THERES A MONITOR GO TO IT
:*IF THERE ISN'T JUMP TO TST2

\$EOP:

SCOPE
CLR \$TSTNM ;;ZERO THE TEST NUMBER
CLR \$TIMES ;;ZERO THE NUMBER OF ITERATIONS
INC \$PASS ;;INCREMENT THE PASS NUMBER
BIC #100000,\$PASS ;;DON'T ALLOW A NEG. NUMBER
DEC (PC)+ ;;LOOP?
\$EOPCT: .WORD 1
BGT \$DOAGN ;;YES
MOV (PC)+,(PC)+ ;;RESTORE COUNTER
\$ENDCT: .WORD 1
\$EOPCT
TYPE \$ENDMG ;;TYPE "END PASS #"
MOV \$PASS,-(SP) ;;SAVE \$PASS FOR TYPEOUT
TYPDS ;;GO TYPE--DECIMAL ASCII WITH SIGN
TYPE \$ENULL ;;TYPE A NULL CHARACTER
\$GET42: MOV #42,R0 ;;GET MONITOR ADDRESS
BEQ \$DOAGN ;;BRANCH IF NO MONITOR
RESET ;;CLEAR THE WORLD
\$ENDAD: JSR PC,(R0) ;;GO TO MONITOR
NOP ;;SAVE ROOM
NOP ;;FOR
NOP ;;ACT11
\$DOAGN: JMP @PC+ ;;RETURN
\$RTNAD: .WORD TST2
\$ENULL: .BYTE -1,-1,0 ;;NULL CHARACTER STRING
\$ENDMG: .ASCIZ <15 <12> /END PASS #/

.SBTTL SUBROUTINES

::*****
* THIS ROUTINE HANDLES TIME OUT TRAPS THROUGH LOC. 4
::*****

TIEOUT:

TYPE 655 ;;TYPE ASCIZ STRING
BR 648 ;;GET OVER THE ASCIZ
::655: .ASCIZ <15 <12> /TIMEOUT OCCURED THROUGH LOCATION 4 FROM PROGRAM PC

128829 037600
128830 037600 10-401 037606
128831 037604 000423

```

12846 037674
12847 037674 162716 000002
12848 037700 104402
12849 037702 104401 037710
12850 037706 000410
12851
12852 037730
12853 037730 104402
12854 037732 000137 043120
12855
12856
12857
12858
12859
12860
12861
12862
12863
12864
12865
12866 037736
12867 037736 104401 037744
12868 037742 000417
12869
12870 040002
12871 040002 104401 040010
12872 040006 000413
12873
12874 040036
12875 040036 162716 000002
12876 040042 104402
12877 040044 104401 040052
12878 040050 000407
12879
12880 040070
12881 040070 104402
12882 040072 104401 040100
12883 040076 000421
12884
12885 040142
12886 040142 017746 145350
12887 040146 104402
12888 040150 104401 040156
12889 040154 000421
12890
12891 040220
12892 040220 017746 145270
12893 040224 104402
12894 040226 104401 040234
12895 040232 000422
12896
12897 040300
12898 040300 017746 145214
12899 040304 104402
12900 040306 000137 043120

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54$: SUB #2,(SP) ;TYPE PC
      TYPOC
      TYPE 67$ ;:TYPE ASCIZ STRING
      BR 66$ ;:GET OVER THE ASCIZ
::67$: .ASCIZ <15><12>/PSW WAS /
66$: TYPOC
      JMP $SCOPE ;RETURN TO TEST

*****
* THIS ROUTINE HANDLES PARITY ERRORS
*****
PARITY:
      TYPE 65$ ;:TYPE ASCIZ STRING
      BR 64$ ;:GET OVER THE ASCIZ
::65$: .ASCIZ <15><12>/PARITY TRAP THRU VECTOR 114/
64$: TYPE 67$ ;:TYPE ASCIZ STRING
      BR 66$ ;:GET OVER THE ASCIZ
::67$: .ASCIZ <15><12>/FROM PROGRAM PC /
66$: SUB #2,(SP) ;TYPE PC
      TYPOC
      TYPE 69$ ;:TYPE ASCIZ STRING
      BR 68$ ;:GET OVER THE ASCIZ
::69$: .ASCIZ <15><12>/PSW WAS /
68$: TYPOC
      TYPE 71$ ;:TYPE ASCIZ STRING
      BR 70$ ;:GET OVER THE ASCIZ
::71$: .ASCIZ <15><1>/HIGH ERROR ADDRESS REGISTER
70$: MOV @HERADD,-(SP) ;TYPE HIGH ERROR
      TYPOC
      TYPE 73$ ;:TYPE ASCIZ STRING
      BR 72$ ;:GET OVER THE ASCIZ
::73$: .ASCIZ <15><12>/LOW ERROR ADDRESS REGISTER
72$: MOV @LERADD,-(SP) ;TYPE LOW ERROR
      TYPOC
      TYPE 75$ ;:TYPE ASCIZ STRING
      BR 74$ ;:GET OVER THE ASCIZ
::75$: .ASCIZ <15><12>/MEMORY SYSTEM ERROR REGISTER
74$: MOV @MEMERR,-(SP)
      TYPOC
      JMP $SCOPE

```

129003									
129004									
129005									
129006									
129007									
129008	040312	012777	000040	143550	CLDISK: MOV	*CLR @RHCS2		:CLEAR ALL REG.	
129009	040320	013777	005010	143542	MOV	@UNIT,@RHCS2		;REINSTATE UNIT NO.	
129010	040326	042777	000077	143524	BIC	*77,@RHCS1		:CLEAR FUNCTION BITS	
129011	040334	005077	143522		CLR	@RHWC		:CLEAR RHWC	
129012	040340	000207			RTS	PC			


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040342 000000

040344 011637 040342
040350 162737 000004 040342
040356 011246
040360 052716 000100
040364 000406
040366 011637 040342
040372 162737 000004 040342
040400 011346
040402 011246
040404 042716 173577
040410 022726 0L+300
040414 001403
040416 011137 001122
040422 104062
040424 042716 102000
040430 022726 010700
040434 001403
040436 011337 001122
040442 104061
040444 000207

:THIS CHECKS DEVICE AVAILABLE (DVA) AND READY (RDY) IN RHCS1
:AND CHECKS MEDIUM ON LINE (MOL), DEVICE PRESENT (DPR), DEVICE READY (DRY) IN RHDS1
PCJSR: 0 ;PC OF JSR

CHECK: MOV (SP), 0#PCJSR ;SAVE PC OF JSR+4
SUB #4, 0#PCJSR ;GET PC OF JSR
MOV @R3, -(SP) ;GET RHDS1
BIS #VV, (SP) ;DONT CHECK VV BIT
BR CHECKC ;GOTO COMMON CHECK ROUTINE
CHECKT: MOV (SP), 0#PCJSR ;SAVE PC OF JSR+4
SUB #4, 0#PCJSR ;GET PC OF JSR
MOV @R3, -(SP) ;GET RHDS1 & DO VV CHECK AT 3\$
CHECKC: MOV @R1, -(SP) ;GET CSI
BIC #173577, (SP) ;CLEAR UNWANTED BITS
CMP #DVA!RDY, (SP)+ ;RHCS1 SHOULD HAVE DEVICE AVAILABLE
;AND BE READY
;BRANCH IF GOOD
;BAD DATA REGISTER (RHCS1)
;RHCS1 DID NOT HAVE DEVICE
;AVAILABLE RIGHT AT THE START
;ALL OTHER BITS SHOULD BE 0
3\$: BIC #ATA!LBT, (SP) ;CLEAR UNWANTED BITS
CMP #MOL!DPR!DRY!VV, (SP)+ ;RHDS1 SHOULD HAVE THESE SET
;BRANCH IF GOOD
;BAD DATA IN REGISTER (RHDS1)
;RHDS1 HAS SOME BITS OTHER
;THAN MOL, DRY, DPR, VV SET
;ALL OTHER BITS SHOULD BE 0
7\$: RTS PC ;RETJRN TO TEST NO.

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040446 177777
040450
040450 010046
040452 010346
040454 016600 000004
040460 010037 004666
040464 162737 000002 004666
040472 013037 004670
040476 012037 004672
040502 010066 000004
040506 005737 004646
040512 001025
040514 013703 040446
040520 033777 004672 144142 1\$:
040526 001046
040530 005303
040532 001372
040534 013703 040446
040540 033777 004672 144122 2\$:
040546 001036
040550 005303
040552 001372
040554 017737 144110 001126
040562 104135
040564 000427
040566 013703 040446
040572 033777 004672 144070 5\$:
040600 001421

: THIS IS A WAIT LOOP WHEN NO P-CLOCK IS AVAILABLE
: NO TIMING IS DONE
: CALL IS

WAT
A : POINTER TO REGISTER ADDRESS
B : BIT WAITED FOR
: R3 IS A TEMPORARY COUNTER
TIMCNT: 177777 ; COUNT FOR WAIT LOOP

WAIT.T:

MOV R0, -(SP) ;: PUSH R0 ON STACK
MOV R3, -(SP) ;: PUSH R3 ON STACK
MOV 4(SP), R0 ;: R0 HAS ADDRESS OF NEXT LOCATION
MOV R0, @#WAITPC ;: WAT PC +2 IS IN WAITPC
SUB #2, @#WAITPC ;: WAT PC IS IN WAITPC
MOV @#(R0)+, @#WAITRE ;: WAIT ON REGISTER ADDRESS
MOV @#(R0)+, @#WAITBT ;: WAIT ON BIT
MOV R0, 4(SP) ;: RESTORE RETURN ON STACK

: THIS HAS THE TWO COUNT DOWNS FROM 177777
: FOR WAITING FOR A BIT TO SET

TST WAT0 ;: WAITING FOR 1?
BNE 4\$;: BRANCH IF WAITING FOR 0
MOV @#TIMCNT, R3 ;: R3 HAS TEMPORARY COUNT
BIT @#WAITBT, @#WAITRE ;: IS REQUIRED BIT THERE
BNE 3\$;: BRANCH IF YES
DEC R3 ;: COUNT IF REQUIRED BIT NOT THERE
BNE 1\$

MOV @#TIMCNT, R3 ;: SECOND COUNT DOWN FROM 177777
BIT @#WAITBT, @#WAITRE ;: IS REQUIRED BIT THERE
BNE 3\$;: BRANCH IF YES
DEC R3 ;: COUNT IF REQUIRED BIT NOT THERE
BNE 2\$

MOV @#WAITRE, @#SBDAT ;: REGISTER CONTENTS FOR TYPEOUT
ERROR 135 ;: WAS WAITING FOR A BIT TO SET
;: BIT IN QUESTION IS IN "BIT WAITED"
;: REGISTER IN QUESTION IS IN "REG ADDR"
BR 3\$;: BRANCH OUT

: THIS HAS THE TWO COUNT DOWNS FROM 177777
: FOR WAITING FOR A BIT TO RESET

MOV @#TIMCNT, R3 ;: R3 HAS TEMPORARY COUNT
BIT @#WAITBT, @#WAITRE ;: IS REQUIRED BIT THERE
BEQ 3\$;: BRANCH IF NO

31
Jr

13006	040602	005303				DEC	R3	;COUNT IF REQUIRED BIT NOT THERE
13007	040604	001372				BNE	5\$	
13008	040606	013703	040446			MOV	@TIMCNT,R3	;SECOND COUNT DOWN FROM 177777
13009	040612	033777	004672	144050	6\$:	BIT	@WAITBT,@WAITRE	;IS REQUIRED BIT THERE
13010	040620	001411				BEQ	3\$;BRANCH IF NO
13011	040622	005303				DEC	R3	;COUNT IF REQUIRED BIT NOT THERE
13012	040624	001372				BNE	6\$	
13013	040626	017737	144036	001126		MOV	@WAITRE,@\$BDDAT	;REGISTER CONTENTS FOR TYPEOUT
13014	040634	104136				ERROR	136	;WAS WAITING FOR A BIT TO RESET
13015								;BIT IN QUESTION IS IN "BIT WAITED"
13016								;REGISTER IN QUESTION IS IN "REG ADDR"
13017	040636	017737	144026	001126		MOV	@WAITRE,@\$BDDAT	;REGISTER CONTENTS FOR TYPEOUT
13018	040644				3\$:			
13019	040644	012603				MOV	(SP)+,R3	::POP STACK INTO R3
13020	040646	012600				MOV	(SP)+,R0	::POP STACK INTO R0
13021	040650	000002				RTI		;RETLRN TO MAIN TEST

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040652 000000
040654
040654 005037 177776
040660 012737 177777 045100
040666 104401 040674
040672 000421
040736
040736 013746 004656
040742 104402
040744 104401 040752
040750 000414
041002
041002 013746 001110
041006 104402
041010 104401 001223
041014 104401 041022
041020 000430
041102
041102 104401 041110
041106 000430
041170
041170 104401 041176
041174 000422
041242
041242 104410
041244 062716 000002
041250 012637 001106
041254 104401 041262
041260 000417

:HERE IS A DETAILED EXPLANATION OF HOW THE LOOP ON ERROR WORKS.
:ON HITTING AN ERROR IF THE LOOP ON ERROR SWITCH IS SET, THE
:PROGRAM GOES BACK - USUALLY BACK TO THE BEGINNING OF THE TEST.

:WHEN THIS OPERATOR SELECTABLE SCOPE LOOP IS USED THEN THE POINT
:THE PROGRAM GOES BACK TO CAN BE CHANGED.
:THE RESTRICTIONS TO THE POINT WHERE THE PROGRAM CAN GO ARE: -
:1. IT MUST BE WITHIN THE TEST UNDER CONSIDERATION
:2. LOOP ON ERROR SWITCH MUST BE SET
:3. THE ERROR MUST OCCUR WITHIN THE TEST UNDER CONSIDERATION
:IF THE ERROR DOES NOT OCCUR WITHIN THE TEST UNDER CONSIDERATION
:THE PROGRAM WILL REVERT TO NORMAL OPERATION. HOWEVER, IF LOOP ON
:TEST SWITCH IS SET AND THIS OPERATOR SELECTABLE SCOPE LOOP IS USED
:THEN THE PROGRAM WILL LOOP BACK TO THE SELECTED POINT WHEN IT
:COMES TO THE END OF THE TEST UNDER CONSIDERATION.

:AFTER LOOPING FOR SOME TIME IF THE LOOP SWITCH IS PUT DOWN THEN
:NORMAL OPERATION WILL CONTINUE.

TESTAD: 0 ;FIRST ADDRESS OF TEST
OPERSEL:
CLR PS ;MAKE PROCESSOR STATUS ZERO
MOV #-1, @#PRITEM ;CLEAR PREVIOUS ITEM NUMBER
TYPE ,65\$;TYPE ASCIZ STRING
BR ,64\$;GET OVER THE ASCIZ
;:65\$: .ASCIZ <15><12>/THE PROGRAM WAS IN TEST NUMBER /
;64\$:
MOV @#TSTNM, -(SP) ;GET READY TO TYPE TEST
TYP0C ;NUMBER
TYPE ,67\$;TYPE ASCIZ STRING
BR ,66\$;GET OVER THE ASCIZ
;:67\$: .ASCIZ <15><12>/THE LOOP BACK PC WAS /
;66\$:
MOV @#\$LPERR, -(SP) ;GET READY TO TYPE LOOP BACK PC
TYP0C
TYPE ,69\$;TYPE ASCIZ STRING
BR ,68\$;GET OVER THE ASCIZ
;:69\$: .ASCIZ <15><12>/SET SWITCH FOR LOOP ON ERROR OR LOOP ON TEST/
;68\$:
TYPE ,71\$;TYPE ASCIZ STRING
BR ,70\$;GET OVER THE ASCIZ
;:71\$: .ASCIZ <15><12>/TYPE THE FIRST PC OF THE TEST TO BE LOOPED ON/
;70\$:
TYPE ,73\$;TYPE ASCIZ STRING
BR ,72\$;GET OVER THE ASCIZ
;:73\$: .ASCIZ <15><12>/ FOLLOWED BY A CARRIAGE RETURN ..15<12\
;72\$:
RDOCT
ADD #2, (SP) ;GET LPADR
MOV (SP)+, @#\$LPADR
TYPE ,75\$;TYPE ASCIZ STRING
BR ,74\$;GET OVER THE ASCIZ

```

13078
13079 041320
13080 041320 104401 041326
13081 041324 000440
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13083 041426
13084 041426 104410
13085 041430 012637 001110
13085 041434 013746 001106
13087 041440 000002
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13101 041442
13102 041442 010046
13103 041444 010146
13104 041446 010246
13105 041450 012700 004062
13106 041454 012701 000002
13107 041450 012702 000010
13108 041464 013021
13109 041466 005302
13110 041470 001375
13111 041472 012602
13112 041474 012601
13113 041476 012600
13114 041500 000207
  
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```

;;75$ : .ASCIZ <15><12>/TYPE THE PC WHERE YOU WANT/
74$ :
      TYPE 77$           ;;TYPE ASCIZ STRING
      BR 76$           ;;GET OVER THE ASCIZ
;;77$ : .ASCIZ <15><12>/ THE PROGRAM TO LOOP BACK TO FOLLOWED BY A CARRIAGE RETURN /<15
76$ :
      RDOCT
      MOV (SP)+, @#$LPERR ;GET LPERR
      MOV @#$LPADR, -(SP)
      RTI
  
```

```

; THIS SAVES THE CONTENTS OF ALL HARDWARE REGISTERS
; IN MEMORY LOCATIONS TAGED FROM "WC" TO "EC"
; THIS IS DONE SO THAT COMPARES ARE DONE WITH SAVED LOCATIONS
; AND NOT THE REGISTERS THEMSELVES. THIS WILL MAKE
; ERROR PRINTOUTS FOR GOOD AND BAD DATA ALWAYS DIFFRENT
  
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```

PUTREG:
      MOV R0, -(SP)           ;; PUSH R0 ON STACK
      MOV R1, -(SP)           ;; PUSH R1 ON STACK
      MOV R2, -(SP)           ;; PUSH R2 ON STACK
      MOV #RHWC, R0           ;; STARTING ADDRESS OF REG
      MOV #WC, R1             ;; STARTING ADDRESS OF WERE SAVED
      MOV #RHCC-RHWC+2/2, R2  ;; NUMBER OF REG. INTO R2
10$:  MOV @ (R0)+, (R1)+       ;; SAVE HARDWARE REG.
      DEC R2
      BNE 10$
      MOV (SP)+, R2           ;; POP STACK INTO R2
      MOV (SP)+, R1           ;; POP STACK INTO R1
      MOV (SP)+, R0           ;; POP STACK INTO R0
      RTS PC
  
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.SBTTL RS DATA TRANSFER SUBROUTINE

;* THIS SUBROUTINE WRITES OR READS OR DOES A
;* WRITE CHECK ON CYLINDER 0 TRACK 0 SECTOR 0
;* IT ASSUMES THE RH REGISTERS ARE APPROPRIATELY
;* FILLED
;* WHEN IT RETURNS FROM THIS SUBROUTINE TO THE
;* MAIN PROGRAM IT MEANS THE TRANSFER IS COMPLETE
;* THE CALL IS BY A JSR R0, @COMND COMAND

041502 012037 004642

CMNDRS: MOV (R0)+, COMAND ;GET COMMAND

;CHECK THAT RHDS1 HAS DRY!DPR!MOL
MOV #DRY!DPR!MOL, \$GDDAT ;GET GOOD = 10300

041506 012737 010600 001124

MOV @RHDS1, \$BDDAT ;READ RHDS1 FOR COMPARISON
CMP \$GDDAT, \$BDDAT ;COMPARE EXPECTED
;DATA WITH DATA READ FROM
;RHDS1
BEQ 65\$;BRANCH IF GOOD

041514 017737 142352 001126

041522 023737 001124 001126

041530 001401

041532 104133

ERROR 133 ;BEFORE ANY COMMAND IS GIVEN
;RHDS1 SHOULD HAVE DRY!DPR!MOL
;=10300
;BUT CONTAINED WHAT IS
;GIVEN IN BAD RHDS1

041534

65\$:

041534 005077 142326

CLR @RHDA ;TRANSFER ON CYLINDER 0
;TRANSFER ON SECTOR 0
;TRACK 0

041540 017777 143076 142312

041546 005737 004644

041552 001306

041554 052777 000001 142276

MOV @COMAND, @RHCS1 ;SET UP COMMAND
TST NOGO ;IS GO TO BE GIVEN
BNE 1\$;BRANCH IF NO
BIS #GO, @RHCS1 ;SET GO

;WAIT FOR RDY BIT IN RHCS1 REGISTER TO SET
WAT ;TRAP TO WAIT.T SUBROUTINE
RHCS1 ;AND WAIT FOR RDY BIT IN
RDY ;RHCS1 REGISTER
;IF ERROR OCCURS HERE
;IT MEANS "RDY" DID NOT
;SET FOR THE FULL COUNT
;DOWN OF THE WAIT.T SUBROUTINE
;THIS TIME IS APPROXIMATELY
;LARGER THAN 500 MILLISECONDS

041562 104411

041564 004060

041566 000200

1\$: RTS R0

.SBTTL RPO4 DATA TRANSFER SUBROUTINE

;* THIS SUBROUTINE WRITE/READ/WRITE CHECK ON THE RPO4 CYLINDER 0,
;* TRACK 0, SECTOR 0.
;* IT ASSUMES THE RH REGISTERS ARE APPROPRIATELY FILLED.
;* WHEN IT RETURNS FROM THIS SUBROUTINE TO THE MAIN

041672 012737 004642
041674 012746 004172
041676 012746 000001
041678 012746 142246
041682 012737 010700 001124
041684 012737 142246 001126
041686 012737 001124 001126
041688 001401
041690 104132
041640
041642 005377 142250
041644 005377 142216
041650 012777 014000 142234
041652 017777 142760 142174
041654 005737 004644
041656 001306
041658 000001 142160
041700 104411
041702 004060
041704 000200
041706 000200

```

:* PROGRAM IT MEANS THE COMMAND IS COMPLETE
:*
:* THE CALL IS BY A JSR RD, @COMND COMMAND.
CMNDRP: MOV      (RD)+, @COMND      ;GET COMMAND
MOV      @RACK, -(SP)             ;SET READY TO SET VV
BIS      @GO, (SP)                ;GET PACK ACKNOWLEDGE COMMAND
MOV      (SP)+, @RHCSI            ;SET VV
;CHECK THAT RHCSI HAS VV!DRY!DPR!MOL
MOV      @VV!DRY!DPR!MOL, @GODAT ;GET GOOD = 10700
MOV      @RHCSI, @BDDAT           ;READ RHCSI FOR COMPARISON
CMP      @GODAT, @BDDAT           ;COMPARE EXPECTED
;DATA WITH DATA READ FROM
;RHCSI
;BRANCH IF GOOD
BEG      655
ERROR   132
;BEFORE ANY COMMAND IS GIVEN
;RHCSI SHOULD HAVE VV!DRY!DPR!MOL
;=10700
;BUT CONTAINED WHAT IS
;GIVEN IN BAD RHCSI
655:
CLR      @RHCA                    ;TRANSFER ON CYLINDER 0
CLR      @RHCS1                   ;TRANSFER ON SECTOR 0
;TRACK 0
MOV      @ECI!FMT22, @RHCF        ;INHIBIT ECC
;FORMAT 16 BIT PER WORD
MOV      @COMAND, @RHCSI          ;SET UP COMMAND
TST      @NOGO                    ;IS GO TO BE GIVEN
BNE      @IS                       ;BRANCH IF NO
BIS      @GO, @RHCSI              ;SET GO
;WAIT FOR RDY BIT IN RHCSI REGISTER TO SET
WAT
RHCSI   ;TRAP TO WAIT.T SUBROUTINE
RDY     ;AND WAIT FOR RDY BIT IN
;RHCSI REGISTER
;IF ERROR OCCURS HERE
;IT MEANS "RDY" DID NOT
;SET FOR THE FULL COUNT
;DOWN OF THE WAIT.T SUBROUTINE
;THIS TIME IS APPROXIMATELY
;LARGER THAN 500 MILLISECOND
IS:     RTS      RD

```

```

.SBTL   TMO2 DATA TRANSFER SUBROUTINE
:* THIS SUBROUTINE WRITES/READS/WRITE CHECKS ON THE TMO2-TU16
:* ON THE FIRST BLOCK FROM BEGINNING OF TAPE (BOT)
:* IT ASSUMES THE RH REGISTERS ARE APPROPRIATELY FILLED.
:* WHEN IT RETURNS FROM THIS SUBROUTINE TO THE
:* MAIN PROGRAM IT MEANS THE COMMAND IS COMPLETE.
:*

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:* THE CALL IS
:* JSR RD,COMND

CMNDTM: MOV (R0)+,COMAND
MOV SLAVE,DRHTC ;GET SLAVE NUMBER
MOV #MOL!DPR!DRY,$GDDAT ;GET GOOD DATA
MOV DRHDS1,$BDDAT ;GET RHDS1
BIC #BOT!BIT05!BIT04,$BDDAT ;DISREGARD BOT
CMP $GDDAT,$BDDAT ;COMPARE EXPECTED DATA WITH
;DATA READ FROM RHDS1
BEQ 1$ ;BRANCH IF GOOD
ERROR 134 ;BEFORE ANY COMMAND
;IS GIVEN DRIVE STATUS REGISTER
;DID NOT CONTAIN WHAT IS IN
;GOOD
1$: BIT #BOT,DRHDS1 ;IS IT ALREADY AT BOT
BNE 2$ ;BRANCH IF YES
;IF NOT AT BOT GIVE A REWIND COMMAND
MOV REWIND,DRHCS1 ;LOAD RHCS1 WITH REWIND COMMAND
BIS #GO,DRHCS1
;WAIT FOR PIP BIT IN RHDS1 REGISTER TO SET
WAT ;TRAP TO WAIT.T SUBROUTINE
RHDS1 ;AND WAIT FOR PIP BIT IN
PIP ;RHDS1 REGISTER
;IF ERROR OCCURS HERE
;IT MEANS "PIP" DID NOT
;SET FOR THE FULL COUNT
;DOWN OF THE WAIT.T SUBROUTINE
;THIS TIME IS APPROXIMATELY
;LARGER THAN 500 MILLISECONDS
;WAIT FOR PIP BIT IN RHDS1 REGISTER TO SET
MOV #-1,WATC ;WAIT FOR PIP TO GO FROM 1 TO 0
WAT ;TRAP TO WAIT.T SUBROUTINE
RHDS1 ;AND WAIT FOR PIP BIT IN
PIP ;RHDS1 REGISTER
;IF ERROR OCCURS HERE
;IT MEANS "PIP" DID NOT
;SET FOR THE FULL COUNT
;DOWN OF THE WAIT.T SUBROUTINE
;THIS TIME IS APPROXIMATELY
;LARGER THAN 500 MILLISECONDS

042010 012737 004646 CLR WATC
;WAIT FOR BOT BIT IN RHDS1 REGISTER TO SET
042016 104411 WAT ;TRAP TO WAIT.T SUBROUTINE
042020 004072 RHDS1 ;AND WAIT FOR BOT BIT IN
042022 020000 PIP ;RHDS1 REGISTER
;IF ERROR OCCURS HERE
;IT MEANS "BOT" DID NOT
;SET FOR THE FULL COUNT
;DOWN OF THE WAIT.T SUBROUTINE
;THIS TIME IS APPROXIMATELY
;LARGER THAN 500 MILLISECONDS

042024 005037 004646 CLR WATC
;WAIT FOR BOT BIT IN RHDS1 REGISTER TO SET
042030 104411 WAT ;TRAP TO WAIT.T SUBROUTINE
042032 004072 RHDS1 ;AND WAIT FOR BOT BIT IN
042034 000002 BOT ;RHDS1 REGISTER
;IF ERROR OCCURS HERE
;IT MEANS "BOT" DID NOT
;SET FOR THE FULL COUNT
;DOWN OF THE WAIT.T SUBROUTINE
;THIS TIME IS APPROXIMATELY
;LARGER THAN 500 MILLISECONDS

;CLEAR ATTENTIONS
042036 013746 004142 MOV DCLEAR,-,SP)
042042 052718 000001 BIS #GO,(SP)

```


13339	042162	052777	001700	141722	BIS	#BPI8,NML,DRHTC	:800 BPI, NORMAL
13340	042170	042777	000010	141714	BIC	#EPAR,DRHTC	:ODD PARITY
13341	042176	017746	141660		MOV	DRHWC,-(SP)	:GET WORD COUNT
13342	042202	005416			NEG	(SP)	:GET POSITIVE NUMBER
13343	042204	061616			ADD	(SP),(SP)	:DOUBLE WORD COUNT
13344	042206	005416			NEG	(SP)	:GET NEGATIVE NUMBER FOR
13345							:FRAME COUNT REGISTER
13346	042210	012677	141652		MOV	(SP)+,DRHFC	:FILL FRAME COUNT
13347	042214	017777	142422	141636	MOV	DCOMAND,DRHCS1	:GET COMMAND
13348							
13349	042222	005737	004644		TST	NOGO	:IS GO TO BE GIVEN
13350	042226	001006			BNE	4\$:BRANCH IF NO
13351	042230	052777	000001	141622	BIS	#GO,DRHCS1	:GO
13352							:WAIT FOR DRY BIT IN RHDS1 REGISTER TO SET
13353	042236	104411			WAT		:TRAP TO WAIT.T SUBROUTINE
13354	042240	004072			RHDS1		:AND WAIT FOR DRY BIT IN
13355	042242	000200			DRY		:RHDS1 REGISTER
13356							:IF ERROR OCCURS HERE
13357							:IT MEANS "DRY" DID NOT
13358							:SET FOR THE FULL COUNT
13359							:DOWN OF THE WAIT.T SUBROUTINE
13360							:THIS TIME IS APPROXIMATELY
13361							:LARGER THAN 500 MILLISECONDS
13362	042244						
13363							
13364							
13365	042244	104411			WAT		:WAIT FOR RDY BIT IN RHCS1 REGISTER TO SET
13366	042246	004060			RHCS1		:TRAP TO WAIT.T SUBROUTINE
13367	042250	000200			RDY		:AND WAIT FOR RDY BIT IN
13368							:RHCS1 REGISTER
13369							:IF ERROR OCCURS HERE
13370							:IT MEANS "RDY" DID NOT
13371							:SET FOR THE FULL COUNT
13372							:DOWN OF THE WAIT.T SUBROUTINE
13373							:THIS TIME IS APPROXIMATELY
13374	042252	000200			RTS	RO	:LARGER THAN 500 MILLISECONDS

4\$:

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13376
13377
13378
13379 042254
13380 042254 104401 042262
13381 042260 000424
13382
13383 042332
13384 042332 013746 004060
13385 042336 104402
13386 042340 104401 042346
13387 042344 000425
13388
13389 042420
13390 042420 104410
13391 042422 012700 004102
13392 042426 012701 000024
13393 042432 042710 177700
13394 042436 051620
13395 042440 005301
13396 042442 001373
13397 042444 104401 042452
13398 042450 000417
13399
13400 042510
13401 042510 013746 002716
13402 042514 104402
13403 042516 104401 042524
13404 042522 000437
13405
13406 042622
13407 042622 104410
13408 042624 012637 002716
13409 042630 104401 042636
13410 042634 000421
13411
13412 042700
13413 042700 104401 042706
13414 042704 000414
13415
13416 042736
13417 042736 013746 004060
13418 042742 104402
13419 042744 104401 042752
13420 042750 000415
13421
13422 043004
13423 043004 013746 002716
13424 043010 104402
13425 043012 104401 043020
13426 043016 000416
13427
13428 043054
13429 043054 000000
13430
    
```

```

    ** THIS ROUTINE WILL ALLOW THE CHANGE OF THE BASE
    ** ADDRESS FROM 176700 TO ANY TYPED VALUE

BASECH:
    TYPE 65$           ;;TYPE ASCIZ STRING
    BR 64$           ;;GET OVER THE ASCIZ
    ;;65$: .ASCIZ <15><12>PRESENT BASE ADDRESS OF REGISTERS IS /
64$:
    MOV 3#RHCS1,-(SP) ;GET READY TO TYPE OLD BASE
    TYPOC
    TYPE 67$           ;;TYPE ASCIZ STRING
    BR 66$           ;;GET OVER THE ASCIZ
    ;;67$: .ASCIZ <15><12>/TYPE NEW BASE ADDRESS FOLLOWED BY 'CR'/
66$:
    RDOCT
    MOV #RHDB,RO      ;GET STARTING ADDRESS OF RGISTERS
    MOV #20,R1        ;NUMBER OF REGISTERS
    BIC #1C77,(RO)   ;CLEAR OLD BASE
    BIS (SP),(RO)+   ;SET NEW BASE
    DEC R1            ;COUNT
    BNE 1$           ;BRANCH IF 20 NOT DONE
    TYPE 69$           ;;TYPE ASCIZ STRING
    BR 68$           ;;GET OVER THE ASCIZ
    ;;69$: .ASCIZ <15><12>/PRESENT VECTOR ADDRESS IS /
68$:
    MOV 2#RPVEC,-(SP) ;GET READY TO TYPE OLD VECTOR ADDRESS
    TYPOC
    TYPE 71$           ;;TYPE ASCIZ STRING
    BR 70$           ;;GET OVER THE ASCIZ
    ;;71$: .ASCIZ <15><12>/TYPE NEW VECTOR ADDRESS OR RETYPE OLD ONE FOLLOWED BY "CR"/
70$:
    RDOCT
    MOV (SP)+,2#RPVEC ;SETUP VECTOR ADDRESS
    TYPE 73$           ;;TYPE ASCIZ STRING
    BR 72$           ;;GET OVER THE ASCIZ
    ;;73$: .ASCIZ <15><12>/RESTART PROGRAM FROM 200 OR 210/
72$:
    TYPE 75$           ;;TYPE ASCIZ STRING
    BR 74$           ;;GET OVER THE ASCIZ
    ;;75$: .ASCIZ <15><12>/NEW BASE WILL REMAIN/
74$:
    MOV 3#RHCS1,-(SP)
    TYPOC
    TYPE 77$           ;;TYPE ASCIZ STRING
    BR 76$           ;;GET OVER THE ASCIZ
    ;;77$: .ASCIZ <15><12>/NEW VECTOR WILL REMAIN /
76$:
    MOV 2#RPVEC,-(SP)
    TYPOC
    TYPE 79$           ;;TYPE ASCIZ STRING
    BR 78$           ;;GET OVER THE ASCIZ
    ;;79$: .ASCIZ <15><12>/UNTIL PROGRAM IS RELOADED/
78$:
    HALT
    
```

13431	043056			RFVECT:			
13432	043056	104401	043064		TYPE	.65\$::TYPE ASCIZ STRING
13433	043062	000411			BR	.64\$::GET OVER THE ASCIZ
13434				::65\$:	.ASCIZ	'TRAPED FROM PC	= /
13435	043106			.64\$:			
13436	043106	104402			TYPOC		:TYPE FROM PC
13437	043110	012777	043056		MOV	*RPVECT, @RPVEC	:RESTORE TRAP RPO4 VECTOR
13438	043116	000000			HALT		:CHANGE TO CONTINUE
13439							
13440							
13441							
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13443                                     ;*****
13444                                     .SBTTL SCOPE HANDLER ROUTINE
13445
13446                                     ::*****
13447                                     ::THIS ROUTINE CONTROLS THE LOOPING OF SUBTESTS. IT WILL INCREMENT
13448                                     ::AND LOAD THE TEST NUMBER($STNM) INTO THE DISPLAY REG.(DISPLAY<7:0>)
13449                                     ::AND LOAD THE ERROR FLAG ($ERFLG) INTO DISPLAY<15:08>
13450                                     ::THE SWITCH OPTIONS PROVIDED BY THIS ROUTINE ARE:
13451                                     ::SW14=1      LOOP ON TEST
13452                                     ::SW11=1      INHIBIT ITERATIONS
13453                                     ::SW09=1      LOOP ON ERROR
13454                                     ::SW08=1      LOOP ON TEST IN SWR<7:0>
13455                                     ::CALL
13456                                     ::*      SCOPE      ;;SCOPE=IOT
13457
13458 043120                                     $SCOPE:
13459 043120 005037 004646                       CLR      WATC      ;WAIT FOR BIT TO SET
13460                                     ;USED IN "WAT" WAIT ROUTINE
13461 043124 005037 004644                       CLR NOGO          ;DO GIVE GO IN COMMAND ROUTINE
13462 043130 032777 040000 136002 1$:      BIT      #BIT14,$SWR ;:LOOP ON PRESENT TEST?
13463 043136 001111                               BNE      $OVER    ;:YES IF SW14=1
13464                                     ;*****START OF CODE FOR THE XOR TESTER*****
13465 043140 000416      $XTSTR: BR      6$      ;:IF RUNNING ON THE "XOR" TESTER CHANGE
13466                                     ;THIS INSTRUCTION TO A "NOP" (NOP=24C)
13467 043142 013746 000004                       MOV      2#ERRVEC, -(SP) ;:SAVE THE CONTENTS OF THE ERROR VECTOR
13468 043146 012737 043166 000004                       MOV      #5$, 2#ERRVEC ;:SET FOR TIMEOUT
13469 043154 005737 177060                               TST      2#177060      ;:TIME OUT ON XOR?
13470 043160 012637 000004                       MOV      (SP)+, 2#ERRVEC ;:RESTORE THE ERROR VECTOR
13471 043164 000463                               BR      $SVLAD        ;:GO TO THE NEXT TEST
13472 043166 022626      5$:      CMP      (SP)+, (SP)+ ;:CLEAR THE STACK AFTER A TIME OUT
13473 043170 012637 000004                       MOV      (SP)+, 2#ERRVEC ;:RESTORE THE ERROR VECTOR
13474 043174 000423                               BR      7$           ;:LOOP ON THE PRESENT TEST
13475 043176                                     6$: ;*****END OF CODE FOR THE XOR TESTER*****
13476 043176 032777 000400 135734                       BIT      #BIT08,$SWR ;:LOOP ON SPEC. TEST?
13477 043204 001404                               BEQ      2$          ;:BR IF NO
13478 043206 127737 135726 001102                       CMPB     2$SWR,$STNM ;:ON THE RIGHT TEST? SWR<7:0>
13479 043214 001462                               BEQ      $OVER      ;:BR IF YES
13480 043216 105737 001103      2$:      TSTB     $ERFLG ;:HAS AN ERROR OCCURRED?
13481 043222 001421                               BEQ      3$          ;:BR IF NO
13482 043224 123737 001115 001103                       CMPB     $ERMAX,$ERFLG ;:MAX. ERRORS FOR THIS TEST OCCURRED?
13483 043232 101015                               BHI      3$          ;:BR IF NO
13484 043234 032777 001000 135676                       BIT      #BIT09,$SWR ;:LOOP ON ERROR?
13485 043242 001404                               BEQ      4$          ;:BR IF NO
13486 043244 013737 001110 001106      7$:      MOV      $LPERR,$LPADR ;:SET LOOP ADDRESS TO LAST SCOPE
13487 043252 000443                               BR      $OVER
13488 043254 105037 001103      4$:      CLRB     $ERFLG ;:ZERO THE ERROR FLAG
13489 043260 005037 001212                       CLR      $TIMES ;:CLEAR THE NUMBER OF ITERATIONS TO MAKE
13490 043264 000415                               BR      1$          ;:ESCAPE TO THE NEXT TEST
13491 043266 032777 004000 135644      3$:      BIT      #BIT11,$SWR ;:INHIBIT ITERATIONS?
13492 043274 001011                               BNE      1$          ;:BR IF YES
13493 043276 005737 001100                               TST      $PASS ;:IF FIRST PASS OF PROGRAM
13494 043302 001406                               BEQ      1$          ;:INHIBIT ITERATIONS
13495 043304 005237 001104                       INC      $ICNT ;:INCREMENT ITERATION COUNT
13496 043310 023737 001212 001104                       CMP      $TIMES,$ICNT ;:CHECK THE NUMBER OF ITERATIONS MADE
13497 043316 002021                               BGE      $OVER ;:BR IF MORE ITERATION REQUIRED
13498 043320 012737 000001 001104      1$:      MOV      #1,$ICNT ;:REINITIALIZE THE ITERATION COUNTER

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12499	043326	013737	043376	001212		MOV	\$MXCNT,\$TIMES	:: SET NUMBER OF ITERATIONS TO DO
12500	043334	105237	001102		\$SVLAD:	INCB	\$TSTNM	:: COUNT TEST NUMBERS
12501	043340	011637	001106			MOV	(SP), \$LPADR	:: SAVE SCOPE LOOP ADDRESS
12502	043344	011637	001110			MOV	(SP), \$LPERR	:: SAVE ERROR LOOP ADDRESS
12503	043350	005037	001214			CLR	\$ESCAPE	:: CLEAR THE ESCAPE FROM ERROR ADDRESS
12504	043354	112737	000001	001115		MOVB	#1,\$ERMAX	:: ONLY ALLOW ONE(1) ERROR ON NEXT TEST
12505	043362	013777	001102	135552	\$OVER:	MOV	\$TSTNM,\$DISPLAY	:: DISPLAY TEST NUMBER
12506	043370	013716	001106			MOV	\$LPADR,(SP)	:: FUDGE RETURN ADDRESS
12507	043374	000002				RTI		:: FIXES PS
12508	043376	000004			\$MXCNT:	4		:: MAX. NUMBER OF ITERATIONS

.SBTTL CONVERT BINARY TO DECIMAL AND TYPE ROUTINE

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13509
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13518
13519
13520
13521 043400
13522 043400 010046
13523 043402 010146
13524 043404 010246
13525 043406 010346
13526 043410 010546
13527 043412 012746 020200
13528 043416 016605 000020
13529 043422 100004
13530 043424 005405
13531 043426 112766 000055 000001
13532 043434 005000 1$:
13533 043436 012703 043614
13534 043442 112723 000040
13535 043446 005002 2$:
13536 043450 016001 043604
13537 043454 160105 3$:
13538 043456 002402
13539 043460 005202
13540 043462 000774
13541 043464 060105 4$:
13542 043466 005702
13543 043470 001002
13544 043472 105716
13545 043474 100407
13546 043476 106316 5$:
13547 043500 103003
13548 043502 116663 000001 177777
13549 043510 052702 030060 6$:
13550 043514 052702 000040 7$:
13551 043520 110223
13552 043522 005720
13553 043524 020027 000010
13554 043530 002746
13555 043532 003002
13556 043534 010502
13557 043536 000764
13558 043540 105726 8$:
13559 043542 100003
13560 043544 116663 177777 177776
13561 043552 105013 9$:
13562 043554 012605
13563 043556 012603
13564 043560 012602

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*****
*THIS ROUTINE IS USED TO CHANGE A 16-BIT BINARY NUMBER TO A 5-DIGIT
*SIGNED DECIMAL (ASCII) NUMBER AND TYPE IT. DEPENDING ON WHETHER THE
*NUMBER IS POSITIVE OR NEGATIVE A SPACE OR A MINUS SIGN WILL BE TYPED
*BEFORE THE FIRST DIGIT OF THE NUMBER. LEADING ZEROS WILL ALWAYS BE
*REPLACED WITH SPACES.
*CALL:
*   MOV   NUM,-(SP)   ;;PUT THE BINARY NUMBER ON THE STACK
*   TYPDS ;;GO TO THE ROUTINE

$TYPDS:
MOV   R0,-(SP)   ;;PUSH R0 ON STACK
MOV   R1,-(SP)   ;;PUSH R1 ON STACK
MOV   R2,-(SP)   ;;PUSH R2 ON STACK
MOV   R3,-(SP)   ;;PUSH R3 ON STACK
MOV   R5,-(SP)   ;;PUSH R5 ON STACK
MOV   #20200,-(SP) ;;SET BLANK SWITCH AND SIGN
MOV   20(SF),R5   ;;GET THE INPUT NUMBER
BPL   1$         ;;BR IF INPUT IS POS.
NEG   R5         ;;MAKE THE BINARY NUMBER POS.
MOVB  #'-,1(SP)  ;;MAKE THE ASCII NUMBER NEG.
1$:   CLR   R0    ;;ZERO THE CONSTANTS INDEX
MOV   #5DBLK,R3  ;;SETUP THE OUTPUT POINTER
MOVB  #'',(R3)+  ;;SET THE FIRST CHARACTER TO A BLANK
2$:   CLR   R2    ;;CLEAR THE BCD NUMBER
MOV   $DTBL(R0),R1 ;;GET THE CONSTANT
3$:   SUB   R1,R5  ;;FORM THIS BCD DIGIT
BLT   4$         ;;BR IF DONE
INC   R2         ;;INCREASE THE BCD DIGIT BY 1
4$:   ADD   R1,R5  ;;ADD BACK THE CONSTANT
TST   R2         ;;CHECK IF BCD DIGIT=0
BNE   5$         ;;FALL THROUGH IF 0
TSTB  (SP)       ;;STILL DOING LEADING 0'S?
BMI   7$         ;;BR IF YES
5$:   ASLB  (SP)   ;;MSD?
BCC   6$         ;;BR IF NO
MOVB  1(SP),-1(R3) ;;YES--SET THE SIGN
6$:   BIS   #'0,R2  ;;MAKE THE BCD DIGIT ASCII
7$:   BIS   #' ,R2  ;;MAKE IT A SPACE IF NOT ALREADY A DIGIT
MOVB  R2,(R3)+   ;;PUT THIS CHARACTER IN THE OUTPUT BUFFER
TST   (R0)+      ;;JUST INCREMENTING
CMP   R0,#10     ;;CHECK THE TABLE INDEX
BLT   2$         ;;GO DO THE NEXT DIGIT
BGT   8$         ;;GO TO EXIT
MOV   R5,R2      ;;GET THE LSD
BR    6$         ;;GO CHANGE TO ASCII
8$:   TSTB  (SP)+  ;;WAS THE LSD THE FIRST NON-ZERO?
BPL   9$         ;;BR IF NO
MOVB  -1(SP),-2(R3) ;;YES--SET THE SIGN FOR TYPING
9$:   CLRB  (R3)   ;;SET THE TERMINATOR
MOV   (SP)+,R5   ;;POP STACK INTO R5
MOV   (SP)+,R3   ;;POP STACK INTO R3
MOV   (SP)+,R2   ;;POP STACK INTO R2

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13565 043562 012601          MOV      (SP)+,R1          ;;POP STACK INTO R1
13566 043564 012600          MOV      (SP)+,R0          ;;POP STACK INTO R0
13567 043566 104401 043614        TYPE     $DBLK            ;;NOW TYPE THE NUMBER
13568 043572 016666 000002 000004  MOV      2(SP),4(SP)      ;;ADJUST THE STACK
13569 043600 012616          MOV      (SP)+,(SP)
13570 043602 000002          RTI                          ;;RETURN TO USER
13571 043604 023420          $DTBL: 10000.
13572 043606 001750          1000.
13573 043610 000144          100.
13574 043612 000012          10.
13575 043614 000004          $DBLK: .BLKW 4
13576                                     .SBTTL TYPE ROUTINE
13577
13578                                     ;;*****
13579                                     ;;ROUTINE TO TYPE ASCIZ MESSAGE. MESSAGE MUST TERMINATE WITH A 0 BYTE.
13580                                     ;;THE ROUTINE WILL INSERT A NUMBER OF NULL CHARACTERS AFTER A LINE FEED.
13581                                     ;;NOTE1: $NULL CONTAINS THE CHARACTER TO BE USED AS THE FILLER CHARACTER.
13582                                     ;;NOTE2: $FILLS CONTAINS THE NUMBER OF FILLER CHARACTERS REQUIRED.
13583                                     ;;NOTE3: $FILLC CONTAINS THE CHARACTER TO FILL AFTER.
13584                                     ;;
13585                                     ;;CALL:
13586                                     ;;1) USING A TRAP INSTRUCTION
13587                                     ;;      TYPE ,MESADR          ;;MESADR IS FIRST ADDRESS OF AN ASCIZ STRING
13588                                     ;;OR
13589                                     ;;      TYPE
13590                                     ;;      MESADR
13591                                     ;;
13592
13593 043624 105737 001157          $TYPE: TSTB  $TPFLG          ;; IS THERE A TERMINAL?
13594 043630 100002          BPL  1$          ;; BR IF YES
13595 043632 000000          HALT          ;; HALT HERE IF NO TERMINAL
13596 043634 000407          BR  3$          ;; LEAVE
13597 043636 010046          1$: MOV  RO,-(SP)          ;; SAVE RO
13598 043640 017600 000002          MOV  22(SP),RO          ;; GET ADDRESS OF ASCIZ STRING
13599 043644 112046          2$: MOVB (RO)+,-(SP)      ;; PUSH CHARACTER TO BE TYPED ONTO STACK
13600 043646 001005          BNE  4$          ;; BR IF IT ISN'T THE TERMINATOR
13601 043650 005726          TST  (SP)+          ;; IF TERMINATOR POP IT OFF THE STACK
13602 043652 012600          60$: MOV  (SP)+,RO          ;; RESTORE RO
13603 043654 062716 000002          3$: ADD  #2,(SP)          ;; ADJUST RETURN PC
13604 043660 000002          RTI                          ;; RETURN
13605 043662 122716 000011          4$: CMPB #HT,(SP)          ;; BRANCH IF <HT>
13606 043666 001430          BEQ  8$
13607 043670 122716 000200          CMPB #CRLF,(SP)          ;; BRANCH IF NOT <CRLF>
13608 043674 001006          BNE  5$
13609 043676 005726          TST  (SP)+          ;; POP <CR><LF> EQUIV
13610 043700 104401          TYPE          ;; TYPE A CR AND LF
13611 043702 001223          $CRLF
13612 043704 105037 044040          CLRB  $CHARCNT          ;; CLEAR CHARACTER COUNT
13613 043710 000755          BR  2$          ;; GET NEXT CHARACTER
13614 043712 004737 043774          5$: JSR  PC,$TYPEC          ;; GO TYPE THIS CHARACTER
13615 043716 123726 001156          6$: CMPB $FILLC,(SP)+      ;; IS IT TIME FOR FILLER CHARS.?
13616 043722 001350          BNE  2$          ;; IF NO GO GET NEXT CHAR.
13617 043724 013746 001154          MOV  $NULL,-(SP)          ;; GET # OF FILLER CHARS. NEEDED
13618                                     ;; AND THE NULL CHAR.
13619 043730 105366 000001          7$: DECB 1(SP)          ;; DOES A NULL NEED TO BE TYPED?
13620 043734 002770          BLT  6$          ;; BR IF NO--GO POP THE NULL OFF OF STACK

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13621 043736 004737 043774      JSR    PC,$TYPEC      ;;GO TYPE A NULL
13622 043742 105337 044040      DEC    $CHARCNT      ;;DO NOT COUNT AS A COUNT
13623 043746 000770                BR     7$             ;;LOOP
13624
13625                ;HORIZONTAL TAB PROCESSOR
13626
13627 043750 112716 000040      8$:   MOV    #' (SP)      ;;REPLACE TAB WITH SPACE
13628 043754 004737 043774      9$:   JSR    PC,$TYPEC      ;;TYPE A SPACE
13629 043760 132737 000007 044040      BIT    #7,$CHARCNT     ;;BRANCH IF NOT AT
13630 043766 001372                BNE    9$             ;;TAB STOP
13631 043770 005726                TST    (SP)+          ;;POP SPACE OFF STACK
13632 043772 000724                BR     2$             ;;GET NEXT CHARACTER
13633 043774 105777 135150      $TYPEC: TST    @STPS      ;;WAIT UNTIL PRINTER IS READY
13634 044000 100375                BPL    $TYPEC
13635 044002 116677 000002 135142      MOV    2(SP),@STPB     ;;LOAD CHAR TO BE TYPED INTO DATA REG.
13636 044010 122766 000015 000002      CMP    #CR,2(SP)      ;;IS CHARACTER A CARRIAGE RETURN?
13637 044016 001003                BNE    1$             ;;BRANCH IF NO
13638 044020 105037 044040      CLRB   $CHARCNT      ;;YES--CLEAR CHARACTER COUNT
13639 044024 000406                BR     $TYPEX        ;;EXIT
13640 044026 122766 000012 000002 1$:   CMP    #LF,2(SP)     ;;IS CHARACTER A LINE FEED?
13641 044034 001402                BEQ    $TYPEX        ;;BRANCH IF YES
13642 044036 105227                INCB   (PC)+          ;;COUNT THE CHARACTER
13643 044040 000000      $CHARCNT: .WORD    0      ;;CHARACTER COUNT STORAGE
13644 044042 000207      $TYPEX: RTS     PC
13645
13646                .SBTTL TTY INPUT ROUTINE
13647
13648                ;*****
13649                .ENABL  LSB
13650 044044 000000      $TKCNT: .WORD    0      ;;NUMBER OF ITEMS IN QUEUE
13651 044046 000000      $TKQIN: .WORD    0      ;;INPUT POINTER
13652 044050 000000      $TKQOUT: .WORD   0      ;;OUTPUT POINTER
13653 044052 000011      $TKQSR: .BLKB   9.     ;;TTY KEYBOARD QUEUE
13654                $TKQEND=.
13655                .EVEN
13656
13657                ;*TK INITIALIZE ROUTINE
13658                ;*THIS ROUTINE WILL INITIALIZE THE TTY KEYBOARD INPUT QUEUE
13659                ;*SETUP THE INTERRUPT VECTOR AND TURN ON THE KEYBOARD INTERRUPT
13660
13661                ;*CALL:
13662                ;*
13663                ;*   JSR    PC,$TKINT
13664                ;*   RETURN
13665
13665 044064 005037 044044      $TKINT: CLR    $TKCNT      ;;CLEAR COUNT OF ITEMS IN QUEUE
13666 044070 012737 044052 044046      MOV    #TKQSR,$TKQIN   ;;MOVE THE STARTING ADDRESS OF THE
13667 044076 013737 044046 044050      MOV    $TKQIN,$TKQOUT  ;;QUEUE INTO THE INPUT & OUTPUT POINTERS.
13668 044104 012737 044134 000060      MOV    #TKSRV,@TKVEC  ;;INITIALIZE THE KEYBOARD VECTOR
13669 044112 012737 000200 000062      MOV    #200,@TKVEC+2  ;;"BR" LEVEL 4
13670 044120 005777 135022      TST    @STKB          ;;CLEAR DONE FLAG
13671 044124 012777 000100 135012      MOV    #100,@STKS     ;;ENABLE TTY KEYBOARD INTERRUPT
13672 044132 000207                RTS     PC            ;;RETURN TO CALLER
13673
13674                ;*TK SERVICE ROUTINE
13675                ;*THIS ROUTINE WILL SERVICE THE TTY KEYBOARD INTERRUPT
13676                ;*BY READING THE CHARACTER FROM THE INPUT BUFFER AND PUTTING

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13677                                     : *IT IN THE QUEUE.
13678                                     : *IF THE CHARACTER IS A "CONTROL-C" (↑C) $TKINT IS CALLED AND
13679                                     : *UPON RETURN EXIT IS MADE TO THE "CONTROL-C" RESTART ADDRESS (OPERSEL)
13690
13681 044134 117746 135006 $TKSRV: MOVB @ $TKB, -(SP)      ;; PICKUP THE CHARACTER
13682 044140 042716 177600      BIC #↑C177, (SP)      ;; STRIP THE JUNK
13683 044144 021627 000003      CMP (SP), #3          ;; IS IT A CONTROL C?
13684 044150 001007              BNE 1$               ;; BRANCH IF NO
13685 044152 104401 044543      TYPE $CNTLC          ;; TYPE A CONTROL-C (↑C)
13686 044156 004737 044064      JSR PC, $TKINT       ;; INIT THE KEYBOARD
13687 044162 005726              TST (SP)+            ;; CLEAN UP STACK
13688 044164 000137 040654      JMP OPERSEL          ;; CONTROL C RESTART
13689
13690 044170                                     1$:
13691 044170 022737 000011 044044      CMP #9, $TKCNT       ;; IS THE QUEUE FULL?
13692 044176 001004              BNE 3$               ;; BRANCH IF NO
13693 044200 104401 001216      TYPE $BELL           ;; RING THE TTY BELL
13694 044204 005726              TST (SP)+            ;; CLEAN CHARACTER OFF OF STACK
13695 044206 000451              BR 5$                ;; EXIT
13696 044210 021627 000023      3$: CMP (SP), #23      ;; IS IT A CONTROL-S?
13697 044214 001021              BNE 32$              ;; BRANCH IF NO
13698 044216 005077 134722      CLR @ $TKS           ;; DISABLE TTY KEYBOARD INTERRUPTS
13699 044222 005726              TST (SP)+            ;; CLEAN CHAR OFF STACK
13700 044224 105777 134714      31$: TSTB @ $TKS         ;; WAIT FOR A CHAR
13701 044230 100375              BPL 31$              ;; LOOP UNTIL ITS THERE
13702 044232 117746 134710      MOVB @ $TKB, -(SP)   ;; GET THE CHARACTER
13703 044236 042716 177600      BIC #↑C177, (SP)    ;; MAKE IT 7-BIT ASCII
13704 044242 022627 000021      CMP (SP)+, #21      ;; IS IT A CONTROL-Q?
13705 044246 001366              BNE 31$              ;; BRANCH IF NO
13706 044250 012777 000100 134666      MOV #100, @ $TKS    ;; REENABLE TTY KEYBOARD INTERRUPTS
13707 044256 000002              RTI                 ;; RETURN
13708 044260 005237 044044      32$: INC $TKCNT       ;; COUNT THIS CHARACTER
13709 044264 021627 000140      CMP (SP), #140      ;; IS IT UPPER CASE?
13710 044270 002405              BLT 4$               ;; BRANCH IF YES
13711 044272 021627 000175      CMP (SP), #175      ;; IS IT A SPECIAL CHAR?
13712 044276 003002              BGT 4$               ;; BRANCH IF YES
13713 044300 042716 000040      BIC #40, (SP)       ;; MAKE IT UPPER CASE
13714 044304 112677 177536      4$: MOVB (SP)+, @ $TKQIN ;; AND PUT IT IN QUEUE
13715 044310 005237 044046      INC $TKQIN          ;; UPDATE THE POINTER
13716 044314 023727 044063      CMP $TKQIN, $STKQEND ;; GO OFF THE END?
13717 044322 001003              BNE 5$               ;; BRANCH IF NO
13718 044324 012737 044052 044046      MOV # $TKQSRT, $TKQIN ;; RESET THE POINTER
13719 044332 000002              RTI                 ;; RETURN
13720
13721                                     .DSABL LSB
13722
13723
13724                                     : *****
13725                                     : *THIS ROUTINE WILL INPUT A SINGLE CHARACTER FROM THE TTY
13726                                     : *CALL:
13727                                     : *      RDCHR          ;; GET A CHARACTER FROM THE QUEUE
13728                                     : *      RETURN HERE    ;; CHARACTER IS ON THE STACK
13729                                     : *                    ;; WITH PARITY BIT STRIPPED OFF
13730
13731
13732 044334 011646 $RDCHR: MOV (SP), -(SP)      ;; PUSH DOWN THE PC AND

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13733 044336 016666 000004 000002      MOV      4(SP),2(SP)      ;; THE PS
13734 044344 005066 000004      CLR      4(SP)           ;; GET READY FOR A CHARACTER
13735 044350 005046      CLR      -(SP)          ;; PUT NEW PS ON STACK
13736 044352 012746 044360      MOV      #64$,-(SP)     ;; PUT NEW PC ON STACK
13737 044356 000002      RTI                    ;; POP NEW PC AND PS
13739 044360      64$:
13739 044360 005737 044044      1$: TST      $TKCNT      ;; WAIT ON A CHARACTER
13740 044364 001775      BEQ      1$
13741 044366 005337 044044      DEC      $TKCNT        ;; DECREMENT THE COUNTER
13742 044372 117756 177452 000004      MOV      2($TKQOUT,4(SP) ;; GET ONE CHARACTER
13743 044400 005237 044050      INC      $TKQOUT        ;; UPDATE THE POINTER
13744 044404 023727 044050 044063      CMP      $TKQOUT,#$TKGEND ;; DID IT GO OFF OF THE END?
13745 044412 001003      BNE      2$            ;; BRANCH IF NO
13746 044414 012737 044052 044050      MOV      #$TKQSRT,$TKQOUT ;; RESET THE POINTER
13747 044422 000002      RTI                    ;; RETURN
13748      ;;*****
13749      ;;*THIS ROUTINE WILL INPUT A STRING FROM THE TTY
13750      ;;*CALL:
13751      ;;*
13751      RDLIN          ;; INPUT A STRING FROM THE TTY
13752      RETURN HERE   ;; ADDRESS OF FIRST CHARACTER WILL BE ON THE STACK
13753      ;;*
13753      ;;*TERMINATOR WILL BE A BYTE OF ALL 0'S
13754
13755 044424 010346      $RDLIN: MOV      R3,-(SP)   ;; SAVE R3
13756 044426 012703 044532      1$: MC        #$TTYIN,R3  ;; GET ADDRESS
13757 044432 022703 044543      2$: CMP      #$TTYIN+9.,R3 ;; BUFFER FULL?
13758 044436 101405      BLOS      4$           ;; BR IF YES
13759 044440 104406      RDCHR     ;; GO READ ONE CHARACTER FROM THE TTY
13760 044442 112613      MOV      (SP)+,(R3)    ;; GET CHARACTER
13761 044444 122713 000177      10$: CMP      #177,(R3)  ;; IS IT A RUBOUT
13762 044450 001003      BNE      3$           ;; SKIP IF NOT
13763 044452 104401 001222      4$: TYPE     $QUES      ;; TYPE A '?'
13764 044456 000763      BR        1$          ;; CLEAR THE BUFFER AND LOOP
13765 044460 111337 044530      3$: MOV      (R3),9$    ;; ECHO THE CHARACTER
13766 044464 104401 044530      TYPE     9$
13767 044470 122723 000015      CMP      #15,(R3)+    ;; CHECK FOR RETURN
13768 044474 001356      BNE      2$          ;; LOOP IF NOT RETURN
13769 044476 105063 177777      CLRB    -1(R3)       ;; CLEAR RETURN (THE 15)
13770 044502 104401 001224      TYPE     $LF         ;; TYPE A LINE FEED
13771 044506 012603      MOV      (SP)+,R3     ;; RESTORE R3
13772 044510 011646      MOV      (SP),-(SP)   ;; ADJUST THE STACK AND PUT ADDRESS OF THE
13773 044512 016666 000004 000002      MOV      4(SP),2(SP)  ;; FIRST ASCII CHARACTER ON IT
13774 044520 012766 044532 000004      MOV      #$TTYIN,4(SP)
13775 044526 000002      RTI
13776 044530      9$: .BYTE    0        ;; RETURN
13777 044531      .BYTE    0        ;; STORAGE FOR ASCII CHAR. TO TYPE
13778 044532 000011      $TTYIN: .BLKB    9.  ;; TERMINATOR
13779 044543      136 006503 000012      $CNTLC: .ASCIZ  /?C/<15><12> ;; RESERVE 9. BYTES FOR TTY INPUT
13780 044550 052536 005015 000      $CNTLU: .ASCIZ  /?U/<15><12> ;; CONTROL "C"
13781 044555      136 006507 000012      $CNTLG: .ASCIZ  /?G/<15><12> ;; CONTROL "U"
13782 044562 005015 053523 020122      $MSWR:  .ASCIZ  <15><12>/SWR = / ;; CONTROL "G"
13783 044570 020075      000
13784 044573      040 047040 053505      $MNEW:  .ASCIZ  / NEW = /
13785 044600 036440 000040
13786
13787
13788

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:FROM THE TTY

MACV 11 07 732)
ERROR HANDLER ROUTINE

.S3TTL ERROR HANDLER ROUTINE

*THIS ROUTINE WILL INCREMENT THE ERROR FLAG AND THE ERROR COUNT.
*SAVE THE ERROR ITEM NUMBER AND THE ADDRESS OF THE ERROR CALL
*AND GO TO SERRTYP ON ERROR
*THE SWITCH OPTIONS PROVIDED BY THIS ROUTINE ARE:
*SW15=1 HALT ON ERROR
*SW13=1 INHIBIT ERROR TYPEOUTS
*SW10=1 BELL ON ERROR
*SW09=1 LOOP ON ERROR
*CALL
* ERROR N ::ERROR=EMT AND N=ERROR ITEM NUMBER

SERROR:
\$: INCB SERRFLG ::SET THE ERROR FLAG
BEQ \$S ::DON'T LET THE FLAG GO TO ZERO
MOV \$STNM,\$DISPLAY ::DISPLAY TEST NUMBER AND ERROR FLAG
BIT #BIT10,\$SWR ::BELL ON ERROR?
BEQ \$S ::NO - SKIP
TYPE \$BELL ::RING BELL
1\$: INC \$SERRTL ::COUNT THE NUMBER OF ERRORS
MOV (\$P),SERRPC ::GET ADDRESS OF ERROR INSTRUCTION
SUB #2,SERRPC
MOVB \$SERRPC,\$ITEMB ::STRIP AND SAVE THE ERROR ITEM CODE
BIT #BIT13,\$SWR ::SKIP TYPEOUT IF SET
BNE \$S ::SKIP TYPEOUTS
JSR PC,SERRTYP ::GO TO USER ERROR ROUTINE
TYPE \$CRLF
2\$: TST \$SWR ::HALT ON ERROR
BPL \$S ::SKIP IF CONTINUE
HALT ::HALT ON ERROR!
3\$: BIT #BIT09,\$SWR ::LOOP ON ERROR SWITCH SET?
BEQ \$S ::BR IF NO
MOV \$LPERP,\$SP ::FUDGE RETURN FOR LOOPING
4\$: TST \$ESCAPE ::CHECK FOR AN ESCAPE ADDRESS
BEQ \$S ::BR IF NONE
MOV \$ESCAPE,(\$SP) ::FUDGE RETURN ADDRESS FOR ESCAPE
5\$: RTI ::RETURN

.S3TTL ERROR MESSAGE TYPEOUT ROUTINE
*THIS ROUTINE USES THE "ITEM CONTROL BYTE" (\$ITEMB) TO DETERMINE WHICH
*ERROR IS TO BE REPORTED. IT THEN OBTAINS, FROM THE "ERROR TABLE" (\$ERRTAB),
*AND REPORTS THE APPROPRIATE INFORMATION CONCERNING THE ERROR.
*IT IS A COPY OF THE SERRTYP SUBROUTINE FROM SYSMAC.
*WITH ONLY MINOR CHANGES
*FIRST IF SWITCH 6 IS SET AND SWITCH 8 RESET THEN
*ALL REGISTER CONTENTS WILL BE TYPED BEFORE REPORTING THE ERROR
*SECOND IF THE CURRENT ERROR HAS THE SAME ITEM NUMBER
*AS THE PREVIOUS ERROR THEN ONLY THE DATA WILL BE TYPED
*AND NOT THE ERROR MESSAGE AND HEADER.

PRITEM: 0 ::PREVIOUS ITEM NO. LOCATION
SERRTYP: MOV \$SWR,(\$SP) ::GET SWITCH SETTING
BIC #1C500,(\$SP) ::KEEP ONLY SWITCH 9 AND 6

138000 000002
138001 000001
138002 000000
138003 032777 001000 134062
138004 001402
138005 013716 001110
138006 005737 001214
138007 001402 001214
138008 000002
138009 045102 000000
138010 045102 013746 001140
138011 045106 042716 172777

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045112 022726 000100      CMP      #SW06,(SP)+      ;IS 6 SET AND 8 RESET
045116 001001      BNE      15             ;IF NOT BRANCH
045120 000402      BR       25             ;BRANCH IF SW 6 IS SET AND 3 RESET
045122 000137 046022      15:     JMP      20TYPERR ;JUMP IF SW 8 IS SET
                                ;OR IF SW 9 IS RESET AND SW 6 IS RESET
045126 104401 000200      25:     TYPE     .CRLF
045132 104401 045140      TYPE     65$           ;;TYPE ASCIZ STRING
045136 000405      BR       64$           ;;GET OVER THE ASCIZ
                                ;;65$: .ASCIZ / RHWC= /
045152 017746 136704      64$:     MOV      2RHWC,-(SP) ;GET READY TO TYPE RHWC CONTENTS
045156 104402      TYPOC
045160 104401 045166      TYPE     67$           ;;TYPE ASCIZ STRING
045164 000405      BR       66$           ;;GET OVER THE ASCIZ
                                ;;67$: .ASCIZ / RHBA= /
045200 017746 136660      66$:     MOV      2RHBA,-(SP) ;GET READY TO TYPE RHBA CONTENTS
045204 104402      TYPOC
045206 104401 045214      TYPE     69$           ;;TYPE ASCIZ STRING
045212 000406      BR       68$           ;;GET OVER THE ASCIZ
                                ;;69$: .ASCIZ / RHCS2= /
045230 017746 136634      68$:     MOV      2RHCS2,-(SP) ;GET READY TO TYPE RHCS2 CONTENTS
045234 104402      TYPOC
045236 104401 045244      TYPE     71$           ;;TYPE ASCIZ STRING
045242 000406      BR       70$           ;;GET OVER THE ASCIZ
                                ;;71$: .ASCIZ / RHCS1= /
045260 017746 136574      70$:     MOV      2RHCS1,-(SP) ;GET READY TO TYPE RHCS1 CONTENTS
045264 104402      TYPOC
045266 104401 045274      TYPE     73$           ;;TYPE ASCIZ STRING
045272 000406      BR       72$           ;;GET OVER THE ASCIZ
                                ;;73$: .ASCIZ / RHDS1= /
045310 017746 136556      72$:     MOV      2RHDS1,-(SP) ;GET READY TO TYPE RHDS1 CONTENTS
045314 104402      TYPOC
045316 104401 045324      TYPE     75$           ;;TYPE ASCIZ STRING
045322 000406      BR       74$           ;;GET OVER THE ASCIZ
                                ;;75$: .ASCIZ / RHER1= /
045340 017746 136530      74$:     MOV      2RHER1,-(SP) ;GET READY TO TYPE RHER1 CONTENTS
045344 104402      TYPOC
045346 104401 045354      TYPE     .77$         ;;TYPE ASCIZ STRING
  
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13955 045352 000406          BR      76$          ;;GET OVER THE ASCIZ
13956          ;;77$: .ASCIZ / RHER2= /
13957          76$:
13958 045370 017746 136524      MOV     @RHER2,-(SP) ;GET READY TO TYPE RHER2 CONTENTS
13959 045374 104402          TYPOC
13960
13961 045376 104401 045404          TYPE    79$          ;;TYPE ASCIZ STRING
13962          BR      78$          ;;GET OVER THE ASCIZ
13963 045402 000406          ;;79$: .ASCIZ / RHER3= /
13964          78$:
13965 045420 017746 136476      MOV     @RHER3,-(SP) ;GET READY TO TYPE RHER3 CONTENTS
13966 045420 104402          TYPOC
13967 045424
13968
13969 045426 104401 045434          TYPE    81$          ;;TYPE ASCIZ STRING
13970          BR      80$          ;;GET OVER THE ASCIZ
13971 045432 000406          ;;81$: .ASCIZ / RHDST= /
13972          80$:
13973 045450 017746 136412      MOV     @RHDST,-(SP) ;GET READY TO TYPE RHDST CONTENTS
13974 045450 104402          TYPOC
13975 045454
13976
13977 045456 104401 045464          TYPE    83$          ;;TYPE ASCIZ STRING
13978          BR      82$          ;;GET OVER THE ASCIZ
13979 045462 000405          ;;83$: .ASCIZ / RHCA= /
13980          82$:
13981 045476 017746 136412      MOV     @RHCA,-(SP) ;GET READY TO TYPE RHCA CONTENTS
13982 045476 104402          TYPOC
13983 045502
13984
13985 045504 104401 045512          TYPE    85$          ;;TYPE ASCIZ STRING
13986          BR      84$          ;;GET OVER THE ASCIZ
13987 045510 000405          ;;85$: .ASCIZ / RHAS= /
13988          84$:
13989 045524 017746 136346      MOV     @RHAS,-(SP) ;GET READY TO TYPE RHAS CONTENTS
13990 045524 104402          TYPOC
13991 045530
13992
13993 045532 104401 045540          TYPE    87$          ;;TYPE ASCIZ STRING
13994          BR      86$          ;;GET OVER THE ASCIZ
13995 045536 000405          ;;87$: .ASCIZ / RHOF= /
13996          86$:
13997 045552 017746 136334      MOV     @RHOF,-(SP) ;GET READY TO TYPE RHOF CONTENTS
13998 045552 104402          TYPOC
13999 045556
14000
14001 045560 104401 045566          TYPE    89$          ;;TYPE ASCIZ STRING
14002          BR      88$          ;;GET OVER THE ASCIZ
14003 045564 000405          ;;89$: .ASCIZ / RHMR= /
14004          88$:
14005 045600 017746 136300      MOV     @RHMR,-(SP) ;GET READY TO TYPE RHMR CONTENTS
14006 045600 104402          TYPOC
14007 045604
14008
14009 045606 104401 045614          TYPE    ,91$          ;;TYPE ASCIZ STRING

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14011 045612 000405          BR      90$          ;;GET OVER THE ASCIZ
14012          ;;91$: .ASCIZ / RHLA=
14013 045626          90$:
14014 045626 017746 136246      MOV      @RHLA,-(SP)  ;GET READY TO TYPE RHLA CONTENTS
14015 045632 104402
14016
14017
14018 045634 104401 045642      TYPE      93$          ;;TYPE ASCIZ STRING
14019 045640 000405          BR      92$          ;;GET OVER THE ASCIZ
14020          ;;93$: .ASCIZ / RHCC= /
14021 045654          92$:
14022 045654 017746 136220      MOV      @RHCC,-(SP) ;GET READY TO TYPE RHCC CONTENTS
14023 045650 104402
14024
14025
14026 045662 104401 045670      TYPE      95$          ;;TYPE ASCIZ STRING
14027 045666 000405          BR      94$          ;;GET OVER THE ASCIZ
14028          ;;95$: .ASCIZ / RHEC1= /
14029 045704          94$:
14030 045704 017746 136214      MOV      @RHEC1,-(SP);GET READY TO TYPE RHEC1 CONTENTS
14031 045710 104402
14032
14033
14034 045712 104401 045720      TYPE      97$          ;;TYPE ASCIZ STRING
14035 045716 000405          BR      96$          ;;GET OVER THE ASCIZ
14036          ;;97$: .ASCIZ / RHEC2= /
14037 045734          96$:
14038 045734 017746 136166      MOV      @RHEC2,-(SP);GET READY TO TYPE RHEC2 CONTENTS
14039 045740 104402
14040
14041
14042 045742 104401 045750      TYPE      99$          ;;TYPE ASCIZ STRING
14043 045746 000405          BR      98$          ;;GET OVER THE ASCIZ
14044          ;;99$: .ASCIZ / RHDT= /
14045 045762          98$:
14046 045762 017746 136120      MOV      @RHDT,-(SP) ;GET READY TO TYPE RHDT CONTENTS
14047 045766 104402
14048
14049
14050 045770 104401 045776      TYPE      101$         ;;TYPE ASCIZ STRING
14051 045774 000405          BR      100$         ;;GET OVER THE ASCIZ
14052          ;;101$: .ASCIZ / RHSN= /
14053 046010          100$:
14054 046010 017746 136074      MOV      @RHSN,-(SP) ;GET READY TO TYPE RHSN CONTENTS
14055 046014 104402
14056
14057 046016 005037 045100      CLR      @#PRITEM    ;CLEAR PREVIOUS ERROR ITEM
14058 046022          TYPERR:
14059 046022 104401 001223      TYPE      $CRLF      ;"CARRIAGE RETURN" & "LINE FEED"
14060 046026 010046          MOV      RO,-(SP)    ;SAVE RO
14061 046030 005000          CLR      RO          ;PICKUP THE ITEM INDEX
14062 046032 153700 001114      BISB     @#ITEMB,RO
14063 046036 001004          BNE     IS          ;IF ITEM NUMBER IS ZERO, JUST
14064          ;TYPE THE PC OF THE ERROR
14065 046040 013746 001116      MOV      $ERRPC,-(SP);SAVE $ERRPC FOR TYPEOUT
14066          ;ERROR ADDRESS
  
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:4067 046044 104402          TYP0C          ;GO TYPE--OCTAL ASCII(ALL DIGITS)
14068 046046 000454          BR          10$      ;GET OUT
14069 046050 005300          1$: DEC      RO      ;ADJUST THE INDEX SO THAT IT WILL
14070 046052 006300          ASL      RO      ; WORK FOR THE ERROR TABLE
14071 046054 006300          ASL      RO
14072 046056 006300          ASL      RO
14073 046060 062700 001226  ADD      #ERRTB,RO  ;FORM TABLE POINTER
14074 046064 020037 045100  CMP      RO,2*PRITEM ;WAS PREVIOUS ERROR SAME
14075 046070 001002          BNE      13$      ;BRANCH IF NOT
14076 046072 022020          CMP      (RO)+,(RO)+ ;POP RO OVER EM AND DH
14077 046074 000420          BR          5$
14078 046076 010037 045100  13$: MOV     RO,2*PRITEM ;SAVE NEW ERROR ITEM
14079 046102 012037 046112  MOV     (RO)+,2$    ;PICKUP "ERROR MESSAGE" POINTER
14080 046106 001404          BEQ      3$        ;SKIP TYPEOUT IF NO POINTER
14081 046110 104401          TYPE     ;TYPE THE "ERROR MESSAGE"
14082 046112 000000          2$: .WORD 0        ;"ERROR MESSAGE" POINTER GOES HERE
14083 046114 104401 001223  TYPE     ,.CRLF    ;"CARRIAGE RETURN" & "LINE FEED"
14084 046120 012037 046130  3$: MOV     (RO)+,4$    ;PICKUP "DATA HEADER" POINTER
14085 046122 001404          BEQ      5$        ;SKIP TYPEOUT IF 0
14086 046126 104401          TYPE     ;TYPE THE "DATA HEADER"
14087 046130 000000          4$: .WORD 0        ;"DATA HEADER" POINTER GOES HERE
14088 046132 104401 001223  TYPE     ,.CRLF    ;"CARRIAGE RETURN" & "LINE FEED"
14089 046136 010146          5$: MOV     R1,-(SP)  ;SAVE R1
14090 046140 012001          MOV     (RO)+,R1   ;PICKUP "DATA TABLE" POINTER
14091 046142 001415          BEQ      9$        ;BR IF NO DATA TO BE TYPED
14092 046144 012000          MOV     (RO)+,RO   ;PICKUP "DATA FORMAT" POINTER
14093 046146 105720          6$: TSTB   (RO)+    ;"OCTAL" OR "DECIMAL"
14094 046150 001003          BNE      7$        ;BR IF DECIMAL
14095 046152 013146          MOV     2(R1)+,-(SP) ;SAVE 2(R1)+ FOR TYPEOUT
14096 046154 104402          TYP0C          ;GO TYPE--OCTAL ASCII(ALL DIGITS)
14097 046156 000402          BR          8$
14098 046160          7$:
14099 046160 013146          MOV     2(R1)+,-(SP) ;SAVE 2(R1)+ FOR TYPEOUT
14100 046162 104405          TYPDS          ;GO TYPE--DECIMAL ASCII WITH SIGN
14101 046164 005711          8$: TST    (R1)    ;IS THERE ANOTHER NUMBER?
14102 046166 001403          BEQ      9$        ;BR IF NO
14103 046170 104401 046204  TYPE     ,11$     ;TYPE TWO(2) SPACES
14104 046174 000764          BR          6$     ;LOOP
14105
14106 046176 012601          9$: MOV     (SP)+,R1  ;RESTORE R1
14107 046200 012600          10$: MOV    (SP)+,RO ;"CARRIAGE RETURN" & "LINE FEED"
14108 046202 000207          RTS     PC        ;RETURN
14109 046204 020040 000          11$: .ASCIZ  / /    ;TWO(2) SPACES
14110 .EVEN
14111 ;*****
14112 ;*****
14113 .SBTTL BINARY TO OCTAL (ASCII) AND TYPE
14114
14115 ;*****
14116 ;*THIS ROUTINE IS USED TO CHANGE A 16-BIT BINARY NUMBER TO A 6-DIGIT
14117 ;*OCTAL (ASCII) NUMBER AND TYPE IT.
14118 ;*$TYPOS---ENTER HERE TO SETUP SUPPRESS ZEROS AND NUMBER OF DIGITS TO TYPE
14119 ;*CALL:
14120 ;*      MOV     NUM,-(SP) ;:NUMBER TO BE TYPED
14121 ;*      TYPOS          ;:CALL FOR TYPEOUT
14122 ;*      .BYTE  N        ;:N=1 TO 6 FOR NUMBER OF DIGITS TO TYPE

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H05

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 DERHAB.SRC BINARY TO OCTAL (ASCII) AND TYPE

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14123      *      .BYTE      M      ;:M=1 OR 0
14124      *      ;:1=TYPE LEADING ZEROS
14125      *      ;:0=SUPPRESS LEADING ZEROS
14126      *
14127      *$TYPON----ENTER HERE TO TYPE OUT WITH THE SAME PARAMETERS AS THE LAST
14128      *$TYPOS OR $TYPOC
14129      *CALL:
14130      *      MOV      NUM,-(SP)      ;:NUMBER TO BE TYPED
14131      *      TYPON      ;:CALL FOR TYPEOUT
14132      *
14133      *$TYPOC---ENTER HERE FOR TYPEOUT OF A 16 BIT NUMBER
14134      *CALL:
14135      *      MOV      NUM,-(SP)      ;:NUMBER TO BE TYPED
14136      *      TYPOC      ;:CALL FOR TYPEOUT
14137
14138      046210      017646      000000      $TYPOS:      MOV      0(SP),-(SP)      ;:PICKUP THE MODE
14139      046214      116637      000001      046433      MOVVB      1(SP),SOFILL      ;:LOAD ZERO FILL SWITCH
14140      046222      112637      046435      MOVVB      (SP)+,SOMODE+1      ;:NUMBER OF DIGITS TO TYPE
14141      046226      062716      000002      ADD      #2,(SP)      ;:ADJUST RETURN ADDRESS
14142      046232      000406      BR      $TYPON
14143      046234      112737      000001      046433      $TYPOC:      MOVVB      #1,SOFILL      ;:SET THE ZERO FILL SWITCH
14144      046242      112737      000006      046435      MOVVB      #6,SOMODE+1      ;:SET FOR SIX(6) DIGITS
14145      046250      112737      000005      046432      $TYPON:      MOVVB      #5,$OCNT      ;:SET THE ITERATION COUNT
14146      046256      010346      MOV      R3,-(SP)      ;:SAVE R3
14147      046260      010446      MOV      R4,-(SP)      ;:SAVE R4
14148      046262      010546      MOV      R5,-(SP)      ;:SAVE R5
14149      046264      113704      C46435      MOVVB      SOMODE+1,R4      ;:GET THE NUMBER OF DIGITS TO TYPE
14150      046270      005404      NEG      R4
14151      046272      062704      000006      ADD      #6,R4      ;:SUBTRACT IT FOR MAX. ALLOWED
14152      046276      110437      046434      MOVVB      R4,SOMODE      ;:SAVE IT FOR USE
14153      046302      113704      046433      MOVVB      SOFILL,R4      ;:GET THE ZERO FILL SWITCH
14154      046306      015605      000012      MOV      12(SP),R5      ;:PICKUP THE INPUT NUMBER
14155      046312      005003      CLR      R3      ;:CLEAR THE OUTPUT WORD
14156      046314      006105      1$:      ROL      R5      ;:ROTATE MSB INTO "C"
14157      046316      000404      BR      3$      ;:GO DO MSB
14158      046320      006105      2$:      ROL      R5      ;:FORM THIS DIGIT
14159      046322      006105      ROL      R5
14160      046324      006105      ROL      R5
14161      046326      010503      MOV      R5,R3
14162      046330      006103      3$:      ROL      R3      ;:GET LSB OF THIS DIGIT
14163      046332      105337      046434      DECB      $OMODE      ;:TYPE THIS DIGIT?
14164      046336      100016      BPL      7$      ;:BR IF NO
14165      046340      042703      177770      BIC      #177770,R3      ;:GET RID OF JUNK
14166      046344      001002      BNE      4$      ;:TEST FOR 0
14167      046346      005704      TST      R4      ;:SUPPRESS THIS 0?
14168      046350      001403      BEQ      5$      ;:BR IF YES
14169      046352      005204      4$:      INC      R4      ;:DON'T SUPPRESS ANYMORE 0'S
14170      046354      052703      000060      BIS      #'0,R3      ;:MAKE THIS DIGIT ASCII
14171      046360      052703      000040      5$:      BIS      #' ,R3      ;:MAKE ASCII IF NOT ALREADY
14172      046364      110337      046430      MOVVB      R3,#$      ;:SAVE FOR TYPING
14173      046370      104401      046430      TYPE      #8$      ;:GO TYPE THIS DIGIT
14174      046374      105237      046432      7$:      DECB      $OCNT      ;:COUNT BY 1
14175      046400      003347      BGT      2$      ;:BR IF MORE TO DO
14176      046402      002402      BLT      6$      ;:BR IF DONE
14177      046404      005204      INC      R4      ;:INSURE LAST DIGIT ISN'T A BLANK
14178      046406      000744      BR      2$      ;:GO DO THE LAST DIGIT

```

MA:NDCC-11-DERHAB-A MACY11 27(732) 22-SEP-76 15:11 PAGE 269
DERHAB.SRC BINARY TO OCTAL (ASCII) AND TYPE

14179	046410	012605		6S:	MOV	(SP)+,R5	::RESTORE R5
14180	046412	012604			MOV	(SP)+,R4	::RESTORE R4
14181	046414	012603			MOV	(SP)+,R3	::RESTORE R3
14182	046416	016656	000002 000004		MOV	2(SP),4(SP)	::SET THE STACK FOR RETURNING
14183	046424	012616			MOV	(SP)+,(SP)	
14184	046426	000002			RTI		::RETURN
14185	046430	000		8S:	.BYTE	0	::STORAGE FOR ASCII DIGIT
14186	046431	000			.BYTE	000	::TERMINATOR FOR TYPE ROUTINE
14187	046432	000		\$JCNT:	.BYTE	000	::OCTAL DIGIT COUNTER
14188	046433	000		\$CFILL:	.BYTE	000	::ZERO FILL SWITCH
14189	046434	000000		\$CMODE:	.WORD	0	::NUMBER OF DIGITS TO TYPE

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.SBTTL TRAP DECODER

```

:*****
:*THIS ROUTINE WILL PICKUP THE LOWER BYTE OF THE "TRAP" INSTRUCTION
:*AND USE IT TO INDEX THROUGH THE TRAP TABLE FOR THE STARTING ADDRESS
:*OF THE DESIRED ROUTINE. THEN USING THE ADDRESS OBTAINED IT WILL
:*GO TO THAT ROUTINE.
  
```

```

$TRAP:  MOV    RO, -(SP)      ;;SAVE RO
        MOV    2(SP),RO      ;;GET TRAP ADDRESS
        TST    -(RO)         ;;BACKUP BY 2
        MOVB   (RO),RO       ;;GET RIGHT BYTE OF TRAP
        ASL    RO            ;;POSITION FOR INDEXING
        MOV    $TRPAD(RO),RO  ;;INDEX TO TABLE
        RTS    RO            ;;GO TO ROUTINE
  
```

;;THIS IS USE TO HANDLE THE "GETPRI" MACRO

```

$TRAP2: MOV    (SP), -(SP)    ;;MOVE THE PC DOWN
        MOV    4(SP),2(SP)   ;;MOVE THE PSW DOWN
        RTI                      ;;RESTORE THE PSW
  
```

.SBTTL TRAP TABLE

```

:*THIS TABLE CONTAINS THE STARTING ADDRESSES OF THE ROUTINES CALLED
:*BY THE "TRAP" INSTRUCTION.
  
```

```

:      ROUTINE
:      -----
$TRPAD: .WORD  $TRAP2
        $TYPE  ;;CALL=TYPE      TRAP+1(104401)  TTY TYPEOUT ROUTINE
        $TYPOC ;;CALL=TYPOC    TRAP+2(104402)  TYPE OCTAL NUMBER (WITH LEADING ZEROS)
        $TYPOS ;;CALL=TYPOS    TRAP+3(104403)  TYPE OCTAL NUMBER (NO LEADING ZEROS)
        $TYPON ;;CALL=TYPON    TRAP+4(104404)  TYPE OCTAL NUMBER (AS PER LAST CALL)
        $TYPDS ;;CALL=TYPDS    TRAP+5(104405)  TYPE DECIMAL NUMBER (WITH SIGN)

        $RDCHR ;;CALL=RDCHR    TRAP+6(104406)  TTY TYPEIN CHARACTER ROUTINE
        $RDLIN ;;CALL=RDLIN    TRAP+7(104407)  TTY TYPEIN STRING ROUTINE
        $RDOCT ;;CALL=RDOCT    TRAP+10(104410) READ AN OCTAL NUMBER FROM TTY
        WAIT.T ;;CALL=WAT      TRAP+11(104411) DONT ADD ABOVE THIS TRAP
  
```

```

14235          .SBTTL  POWER DOWN AND UP ROUTINES
14236
14237          ;:*****
14238          ;:POWER DOWN ROUTINE
14239 046516 012737 046662 000024 $PWRDN: MOV    $SILLUP,@#PWRVEC ;:SET FOR FAST UP
14240 046524 012737 000340 000026      MOV    #340,@#PWRVEC+2 ;:PRIO:7
14241 046532 010046      MOV    R0,-(SP) ;:PUSH R0 ON STACK
14242 046534 010146      MOV    R1,-(SP) ;:PUSH R1 ON STACK
14243 046536 010246      MOV    R2,-(SP) ;:PUSH R2 ON STACK
14244 046540 010346      MOV    R3,-(SP) ;:PUSH R3 ON STACK
14245 046542 010446      MOV    R4,-(SP) ;:PUSH R4 ON STACK
14246 046544 010546      MOV    R5,-(SP) ;:PUSH R5 ON STACK
14247 046546 017746 132366      MOV    @SWR,-(SP) ;:PUSH @SWR ON STACK
14248 046552 010637 046666      MOV    SP,$SAVR6 ;:SAVE SP
14249 046556 012737 046570 000024      MOV    $PWRUP,@#PWRVEC ;:SET UP VECTOR
14250 046564 000000      HALT
14251 046566 000776      BR      -2          ;:HANG UP
14252
14253          ;:*****
14254          ;:POWER UP ROUTINE
14255 046570 012737 046662 000024 $PWRUP: MOV    $SILLUP,@#PWRVEC ;:SET FOR FAST DOWN
14256 046576 013706 046666      MOV    $SAVR6,SP ;:GET SP
14257 046602 005037 046666      CLR    $SAVR6 ;:WAIT LOOP FOR THE TTY
14258 046606 005237 046666      1$: INC    $SAVR6 ;:WAIT FOR THE INC
14259 046612 001375      BNE    1$ ;:OF WORD
14260 046614 012677 132320      MOV    (SP)+,@SWR ;:POP STACK INTO @SWR
14261 046620 012605      MOV    (SP)+,R5 ;:POP STACK INTO R5
14262 046622 012604      MOV    (SP)+,R4 ;:POP STACK INTO R4
14263 046624 012603      MOV    (SP)+,R3 ;:POP STACK INTO R3
14264 046626 012602      MOV    (SP)+,R2 ;:POP STACK INTO R2
14265 046630 012601      MOV    (SP)+,R1 ;:POP STACK INTO R1
14266 046632 012600      MOV    (SP)+,R0 ;:POP STACK INTO R0
14267 046634 012737 046516 000024      MOV    $PWRDN,@#PWRVEC ;:SET UP THE POWER DOWN VECTOR
14268 046642 012737 000340 000026      MOV    #340,@#PWRVEC+2 ;:PRIO:7
14269 046650 104401      TYPE ;:REPORT THE POWER FAILURE
14270 046652 046670      $PWRMG: .WORD $POWER ;:POWER FAIL MESSAGE POINTER
14271 046654 012716      MOV    (PC)+,(SP) ;:RESTART AT BEGIN
14272 046656 005522      $PWRAD: .WORD BEGIN ;:RESTART ADDRESS
14273 046660 000002      RTI
14274 046662 000000      $SILLUP: HALT ;:THE POWER UP SEQUENCE WAS STARTED
14275 046664 000776      BR      -2          ;:BEFORE THE POWER DOWN WAS COMPLETE
14276 046666 000000      $SAVR6: 0 ;:PUT THE SP HERE
14277 046670 005015 047520 042527 $POWER: .ASCIZ <15><12>"POWER"
14278 046676 000122
14279          .EVEN

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14286 046700 044124 020105 044122
14287 046706 041040 051501 020105
14288 046714 042101 051104 051505
14289 046722 020123 040527 020123
14290 046730 033461 030062 030064
14291 046736 044440 042116 041511
14292 046744 052101 047111 020107
14293 046752 047101 051040 020123
14294 046760 052502 020124 044124
14295 046766 020105 051104 053111
14296 046774 020105 054524 042520
14297 047002 005015
14298 047004 044504 020104 047516
14299 047012 020124 047503 052116
14300 047020 044501 020116 020060
14301 047026 051117 030440 047440
14302 047034 020122 020062 051117
14303 047042 031440 047440 020122
14304 047050 000064
14305
14306 047052 044124 020105 044122
14307 047060 041040 051501 020105
14308 047066 042101 051104 051505
14309 047074 020123 040527 020123
14310 047102 033461 033466 030060
14311 047110 044440 042116 041511
14312 047116 052101 047111 020107
14313 047124 047101 051040 030120
14314 047132 026064 026065 020066
14315 047140 052502 020124 044124
14316 047146 020105 051104 053111
14317 047154 020105 054524 042520
14318 047162 005015
14319 047164 044504 020104 047516
14320 047172 020124 047503 052116
14321 047200 044501 020116 030062
14322 047206 031060 026060 032062
14323 047214 031060 026060 030062
14324 047222 031060 026061 032062
14325 047230 031060 026061 030062
14326 047236 031060 026062 047440
14327 047244 020122 032062 031060
14328 047252 000062
14329
14330 047254 044124 020105 044122
14331 047262 041040 051501 020105
14332 047270 042101 051104 051505
14333 047276 020123 040527 020123
14334 047304 033461 032062 030064
14335 047312 044440 042116 041511

```

```

:*****
:
:ERROR AND MESSAGE TABLE CONDIMITS
:
:*****

```

EM1: .ASCII /THE RH BASE ADDRESS WAS 172040 INDICATING AN RS BUT THE DRIVE TYPE/<15>

.ASCIZ /DID NOT CONTAIN 0 OR 1 OR 2 OR 3 OR 4/

EM2: .ASCII /THE RH BASE ADDRESS WAS 176700 INDICATING AN RPO4,5,6 BUT THE DRIVE TYP

.ASCIZ /DID NOT CONTAIN 20020,24020,20021,24021,20022, OR 24022/

EM3: .ASCII /THE RH BASE ADDRESS WAS 172440 INDICATING A TMO2 BUT THE DRIVE TYPE/<15

14336	047320	052101	047111	020107
14337	047326	020101	046524	031060
14338	047334	041040	052125	052040
14339	047342	042510	042040	044522
14340	047350	042526	052040	050131
14341	047356	006505	012	
14342	047361	104	042111	047040
14343	047366	052117	041440	047117
14344	047374	040524	047111	030440
14345	047402	031064	030460	000060
14346				
14347	047410	044124	020105	044122
14348	047416	041040	051501	020105
14349	047424	042101	051104	051505
14350	047432	020123	040527	020123
14351	047440	033461	031466	030060
14352	047446	044440	042116	041511
14353	047454	052101	047111	020107
14354	047462	047515	042522	052040
14355	047470	040510	020116	047117
14356	047476	020105	044122	042040
14357	047504	053105	041511	020105
14358	047512	052502	006524	012
14359	047517	124	042510	042040
14360	047524	044522	042526	052040
14361	047532	050131	020105	044504
14362	047540	020104	047516	020124
14363	047546	047503	052116	044501
14364	047554	020116	026060	026061
14365	047562	026062	026063	026064
14366	047570	030062	031060	026060
14367	047576	032062	031060	026060
14368	047604	005015		
14369	047606	030062	031060	026061
14370	047614	032062	031060	026061
14371	047622	030062	031060	026062
14372	047630	032062	031060	026062
14373	047636	032061	030062	030061
14374	047644	005015	000	
14375	047647	111	042116	041511
14376	047654	052101	047111	020107
14377	047662	047516	051040	020110
14378	047670	042504	044526	042503
14379	047676	050040	042522	042523
14380	047704	052116	000	
14381				
14382	047707	124	042510	052440
14383	047714	044516	052502	020123
14384	047722	044524	042515	020104
14385	047730	052517	020124	047506
14386	047736	020122	044505	044103
14387	047744	047440	020106	044124
14388	047752	020105	047506	046114
14389	047760	033517	047111	020107
14390	047766	042101	051104	051505
14391	047774	042523	026123	044440

.ASCIZ /DID NOT CONTAIN 142010/

EM4: .ASCII /THE RH BASE ADDRESS WAS 176300 INDICATING MORE THAN ONE RH DEVICE BUT/

.ASCII /THE DRIVE TYPE DID NOT CONTAIN 0,1,2,3,4,20020,24020, /<15><12>

.ASCIZ /20021,24021,20022,24022.142010/<15><12>

.ASCIZ /INDICATING NO RH DEVICE PRESENT/

EM5: .ASCII /THE UNIBUS TIMED OUT FOR EACH OF THE FOLLOWING ADDRESSES. INDICATING/<1

14392	050002	042116	041511	052101	
14393	050010	047111	006507	012	
14394	050015	116	020117	044122	.ASCIZ /NO RH ON THE SYSTEM SO ABORT PROGRAM/
14395	050022	047440	020116	044124	
14396	050030	020105	054523	052123	
14397	050036	046505	051440	020117	
14398	050044	041101	051117	020124	
14399	050052	051120	043517	040522	
14400	050060	000115			
14401					
14402	050062	043101	042524	020122	EM6: .ASCII /AFTER AN RH CLEAR (RHCS2-BIT #5) RHCS2 DOES NOT HAVE ONLY IR/<15><12>
14403	050070	047101	051040	020110	
14404	050076	046103	040505	020122	
14405	050104	051050	041510	031123	
14406	050112	041055	052111	021440	
14407	050120	024465	051040	041510	
14408	050126	031123	042040	042517	
14409	050134	020123	047516	020124	
14410	050142	040510	042526	047440	
14411	050150	046116	020131	051111	
14412	050156	005015			
14413	050160	047101	020104	047125	.ASCIZ /AND UNIT NUMBER/
14414	050166	052111	047040	046525	
14415	050174	042502	000122		
14416					
14417	050200	043101	042524	020122	EM7: .ASCII /AFTER CLEARING THE RH AND WRITING ONE WORD INTO RHDB AND/<15><12>
14418	050206	046103	040505	044522	
14419	050214	043516	052040	042510	
14420	050222	051040	020110	047101	
14421	050230	020104	051127	052111	
14422	050236	047111	020107	047117	
14423	050244	020105	047527	042122	
14424	050252	044440	052116	020117	
14425	050260	044122	041104	040440	
14426	050266	042116	005015		
14427	050272	042522	042101	047111	.ASCIZ /READING IT BACK, CAUSED RHDB TO HAVE WRONG VALUE GIVEN IN "BAD"/
14428	050300	020107	052111	041040	
14429	050306	041501	026113	041440	
14430	050314	052501	042523	020104	
14431	050322	044122	041104	052040	
14432	050330	020117	040510	042526	
14433	050336	053440	047522	043516	
14434	050344	053040	046101	042525	
14435	050352	043440	053111	047105	
14436	050360	044440	020116	041042	
14437	050366	042101	000042		
14438					
14439	050372	043101	042524	020122	EM10: .ASCII /AFTER CLEARING THE RH AND WRITING ONE WORD INTO RHDB AND/<15><12>
14440	050400	046103	040505	044522	
14441	050406	043516	052040	042510	
14442	050414	051040	020110	047101	
14443	050422	020104	051127	052111	
14444	050430	047111	020107	047117	
14445	050436	020105	047527	042122	
14446	050444	044440	052116	020117	
14447	050452	044122	041104	040440	


```

00000000 041440 042514 051101
00000000 047111 020107 044124
00000000 020105 044122 040440
00000000 042116 053440 044522
00000000 044524 043516 052040
00000000 047527 053440 051117
00000000 020104 047111 047524
00000000 051040 042110 020102
00000000 047101 006504 012
00000000 043505 044504
00000000 043516 047440 042516
00000000 041040 041501 026113
00000000 041440 052501 042523
00000000 020104 044122 051503
00000000 020106 047524 044040
00000000 052105 020105 051124
00000000 047111 020107 040526
00000000 052514 020105 044507
00000000 042526 020116 047111
00000000 021040 040502 021104
00000000 000

```

.ASCIZ /READING ONE BACK, CAUSED RHCS2 TO HAVE WRONG VALUE GIVEN IN "BAD"/

```

052106 051105
041440 042514 051101
047111 020107 044124
020105 044122 040440
042116 053440 044522
044524 043516 052040
047527 053440 051117
020104 047111 047524
051040 042110 020102
047101 006504 012
043505 044504
043516 044440 020124
040502 045503 052040
044527 042503 020054
040503 051525 042105
051040 042110 020102
047524 044040 053101
020105 051127 047117
020107 040526 052514
020105 044507 042526
020116 047111 021040
040502 021104 000

```

EM21: .ASCII /AFTER CLEARING THE RH AND WRITING TWO WORD INTO RHDB AND/<15><12>

.ASCIZ /READING IT BACK TWICE, CAUSED RHDB TO HAVE WRONG VALUE GIVEN IN "BAD"/

```

052713 101 052106 051105
052720 041440 042514 051101
052726 047111 020107 044124
052734 020105 044122 040440
052742 042116 053440 044522
052750 044524 043516 052040
052756 047527 053440 051117
052764 020104 047111 047524
052772 051040 042110 020102
053000 047101 006504 012
053005 122 040505 044504

```

EM22: .ASCII /AFTER CLEARING THE RH AND WRITING TWO WORD INTO RHDB AND/<15><12>

.ASCIZ /READING IT BACK TWICE, CAUSED RHCS2 TO HAVE WRONG VALUE GIVEN IN "BAD"

14673	0533012	043516	044440	020124
14674	0533020	040503	045503	052040
14675	0533026	044527	042503	020054
14676	0533034	040503	051525	042105
14677	0533042	051040	041510	031123
14678	0533050	052040	020117	040510
14679	0533056	042526	053440	047522
14680	0533064	043516	053040	046101
14681	0533072	042525	043440	053111
14682	0533100	047105	044440	020116
14683	0533106	041042	042101	000042
14684	0533114	043101	042524	020122
14685	0533122	046103	040505	044522
14686	0533130	043516	052040	042510
14687	0533136	051040	020110	047101
14688	0533144	020104	051127	052111
14689	0533152	047111	020107	044505
14690	0533160	044107	020124	047527
14691	0533166	042122	044440	052116
14692	0533174	020117	044122	041104
14693	0533202	040440	042116	005015
14694	0533210	042522	042101	047111
14695	0533216	020107	052111	041040
14696	0533224	041501	026113	041440
14697	0533232	052501	042523	020104
14698	0533240	044122	051503	020062
14699	0533246	047524	044040	053101
14700	0533254	020105	051127	047117
14701	0533262	020107	040526	052514
14702	0533270	020105	044507	042526
14703	0533276	020116	047111	021040
14704	0533304	040502	021104	000
14705				
14706	0533311	124	042510	051040
14707	0533316	020110	040527	020123
14708	0533324	046103	040505	042522
14709	0533332	020104	047101	020104
14710	0533340	020101	040520	052124
14711	0533346	051105	020116	043117
14712	0533354	034040	053440	051117
14713	0533362	051504	053440	051105
14714	0533370	020105	051127	052111
14715	0533376	042524	020116	047111
14716	0533404	047524	051040	042110
14717	0533412	006502	012	
14718	0533415	122	040505	044504
14719	0533422	043516	051040	042110
14720	0533430	020102	047506	020122
14721	0533436	044124	020105	047042
14722	0533444	020042	044124	020056
14723	0533452	044524	042515	043440
14724	0533460	053101	020105	051127
14725	0533466	047117	020107	040526
14726	0533474	052514	020105	047111
14727	0533502	051040	042110	006502

EM23: .ASCII /AFTER CLEARING THE RH AND WRITING EIGHT WORD INTO RHDB AND/<15><12>

.ASCIIZ /READING IT BACK, CAUSED RHCS2 TO HAVE WRONG VALUE GIVEN IN "BAD"/

EM24: .ASCII /THE RH WAS CLEARED AND A PATTERN OF 9 WORDS WERE WRITTEN INTO RHDB/<15>

.ASCII /READING RHDB FOR THE "N" TH. TIME GAVE WRONG VALUE IN RHDB/<15><12>

14728	053510	012			
14729	053511	042	021116	044440	.ASCIZ /"N" IS GIVEN IN WORD NO/
14730	053516	020123	044507	042526	
14731	053524	020116	047111	053440	
14732	053532	051117	020104	047516	
14733	053540	000			
14734					
14735	053541	124	042510	051040	EM25: .ASCII /THE RH WAS CLEARED AND A PATTERN OF 9 WORDS WERE WRITTEN INTO RHDB/<15>
14736	053546	020110	040527	020123	
14737	053554	046103	040505	042522	
14738	053562	020104	047101	020104	
14739	053570	020101	040520	052124	
14740	053576	051105	020116	043117	
14741	053604	034040	053440	051117	
14742	053612	051504	053440	051105	
14743	053620	020105	051127	052111	
14744	053626	042524	020116	047111	
14745	053634	047524	051040	042110	
14746	053642	006502	012		
14747	053645	101	052106	051105	.ASCIZ /AFTER READING ALL 8 WORDS, FOLLOWING REGISTER CONTAINED WRONG VALUE GIV
14748	053652	051040	040505	044504	
14749	053660	043516	040440	046114	
14750	053666	034040	053440	051117	
14751	053674	051504	020054	047506	
14752	053702	046114	053517	047111	
14753	053710	020107	042522	044507	
14754	053716	052123	051105	041440	
14755	053724	047117	040524	047111	
14756	053732	042105	053440	047522	
14757	053740	043516	053040	046101	
14758	053746	042525	043440	053111	
14759	053754	047105	044440	020116	
14760	053762	041042	042101	000042	
14761					
14762	053770	042523	052124	047111	EM33: .ASCIZ /SETTING RH CLEAR (RHCS2-BIT #5) CAUSED ERROR REGISTER 1 TO HAVE WRONG V
14763	053776	020107	044122	041440	
14764	054004	042514	051101	024040	
14765	054012	044122	051503	026462	
14766	054020	044502	020124	032443	
14767	054026	020051	040503	051525	
14768	054034	042105	042440	051122	
14769	054042	051117	051040	043505	
14770	054050	051511	042524	020122	
14771	054056	020061	047524	044040	
14772	054064	053101	020105	051127	
14773	054072	047117	020107	040526	
14774	054100	052514	000105		
14775					
14776	054104	047101	051040	020110	EM34: .ASCII /AN RH CLEAR WAS GIVEN RHER1 WAS CHECKED TO HAVE ZERO. PAT (RHCS2-BIT #4
14777	054112	046103	040505	020122	
14778	054120	040527	020123	044507	
14779	054126	042526	020116	044122	
14780	054134	051105	020061	040527	
14781	054142	020123	044103	041505	
14782	054150	042513	020104	047524	
14783	054156	044040	053101	020105	

14794	054164	042532	047522	020056
14795	054172	040520	020124	051050
14796	054200	041510	031123	041055
14797	054206	052111	021440	024454
14798	054214	053440	051501	051440
14799	054222	052105	005015	
14800	054226	047524	044440	053116
14801	054234	051105	020124	040520
14802	054242	044522	054524	041440
14803	054250	042510	045503	047111
14804	054256	027107	051040	042510
14805	054264	030522	053440	051501
14806	054272	051040	040505	020124
14807	054300	052502	020124	044504
14808	054306	020104	047516	020124
14809	054314	047503	052116	044501
14810	054322	020116	044127	052101
14811	054330	044440	020123	047111
14812	054336	021040	047507	042117
14813	054344	000042		
14814				
14815	054346	044122	041440	042514
14816	054354	051101	053440	051501
14817	054362	043440	053111	047105
14818	054370	020056	044122	051105
14819	054376	020061	040527	020123
14820	054404	044103	041505	042513
14821	054412	027104	050040	052101
14822	054420	024040	044122	051503
14823	054426	026462	044502	020124
14824	054434	032043	020051	040527
14825	054442	020123	042523	006524
14826	054450	012		
14827	054451	101	042116	051040
14828	054456	042510	030522	053440
14829	054464	051501	051040	040505
14830	054472	027104	051040	041510
14831	054500	030523	051440	047510
14832	054506	046125	020104	040510
14833	054514	042526	051040	054504
14834	054522	020054	041523	040440
14835	054530	042116	046440	050103
14836	054536	020105	042523	000124
14837				
14838	054544	044122	041440	042514
14839	054552	051101	053440	051501
14840	054560	043440	053111	047105
14841	054566	020056	044122	051105
14842	054574	020061	040527	020123
14843	054602	044103	041505	042513
14844	054610	027104	050040	052101
14845	054616	024040	044122	051503
14846	054624	026462	044502	020124
14847	054632	032043	020051	040527
14848	054640	020123	042523	006524
14849	054646	012		

.ASCIZ /TO INVERT PARITY CHECKING. RHER1 WAS READ BUT DID NOT CONTAIN WHAT IS :

EM35: .ASCII /RH CLEAR WAS GIVEN. RHER1 WAS CHECKED. PAT (RHCS2-BIT #4) WAS SET/<15><

.ASCIZ /AND RHER1 WAS READ. RHCS1 SHOULD HAVE RDY, SC AND MCFE SET/

EM36: .ASCII /RH CLEAR WAS GIVEN. RHER1 WAS CHECKED. PAT (RHCS2-BIT #4) WAS SET/<15><

14850	054647	122	042510	030522
14851	054647	054647	051501	051040
14852	054647	054647	051501	051040
14853	054647	054647	053440	051501
14854	054647	054647	044522	052124
14855	054647	054647	044440	052116
14856	054647	054647	052042	042522
14857	054647	054647	047117	051040
14858	054647	054647	030522	050515
14859	054647	054647	041440	042510
14860	054647	054647	047117	020107
14861	054647	054647	045503	047111
14862	054647	054647	051503	020061
14863	054647	054647	042040	042111
14864	054647	054647	052117	041440
14865	054647	054647	040524	047111
14866	055000	053440	040510	020124
14867	055006	051511	044440	020116
14868	055014	047507	042117	000
14869	055021	122	020110	040527
14870	055026	020123	046103	040505
14871	055034	042522	027104	051040
14872	055042	042510	030522	053440
14873	055050	051501	041440	042510
14874	055056	045503	042105	020056
14875	055064	040520	020124	051050
14876	055072	041510	031123	041055
14877	055100	052111	021440	024464
14878	055106	053440	051501	051440
14879	055114	052105	005015	
14880	055120	044122	051105	020061
14881	055126	040527	020123	042522
14882	055134	042101	020056	030442
14883	055142	020042	040527	020123
14884	055150	051127	052111	042524
14885	055156	020116	047111	021040
14886	055164	051124	021105	044440
14887	055172	020116	044122	051503
14888	055200	006461	012	
14889	055203	101	020116	044122
14890	055210	041440	042514	051101
14891	055216	053440	051501	043440
14892	055224	053111	047105	041040
14893	055232	052125	043040	046117
14894	055240	047514	044527	043516
14895	055246	051040	043505	051511
14896	055254	042524	020122	044504
14897	055262	020104	047516	006524
14898	055270	012		
14899	055271	103	047117	040524
14900	055276	047111	053440	040510
14901	055304	020124	051511	044440
14902	055312	020116	043442	047517
14903	055320	021104	000	
14904				
14905	055323	101	052106	051105

.ASCII /RHER1 WAS READ. "1" WAS WRITTEN INTO "TRE" IN RHCS1/<15><12>

.ASCIZ /ON CHECKING RHCS1 IT DID NOT CONTAIN WHAT IS IN GOOD/

EM37: .ASCII /RH WAS CLEARED. RHER1 WAS CHECKED. PAT (RHCS2-BIT #4) WAS SET/<15><12>

.ASCII /RHER1 WAS READ. "1" WAS WRITTEN IN "TRE" IN RHCS1/<15><12>

.ASCII /AN RH CLEAR WAS GIVEN BUT FOLLO WING REGISTER DID NOT/<15><12>

.ASCIZ /CONTAIN WHAT IS IN "GOOD"/

EM45: .ASCII /AFTER AN RH CLEAR "1" WAS WRITTEN IN THE DISK (TAPE) ADDRESS/<15><12>

14896	055330	040440	020116	044122
14897	055336	041440	042514	051101
14898	055344	021040	021061	053440
14899	055352	051501	053440	044522
14900	055360	052124	047105	044440
14901	055366	020116	044124	020105
14902	055374	044504	045523	024040
14903	055402	040524	042520	020051
14904	055410	042101	051104	051505
14905	055416	006523	012	
14906	055421	122	043505	051511
14907	055426	042524	027122	050040
14908	055434	052101	044440	020116
14909	055442	044122	051503	020061
14910	055450	040527	020123	042523
14911	055456	027124	047440	020116
14912	055464	042522	042101	047111
14913	055472	020107	044504	045523
14914	055500	024040	040524	042520
14915	055506	020051	042101	051104
14916	055514	051505	006523	012
14917	055521	122	043505	051511
14918	055526	042524	020122	052111
14919	055534	042040	042111	047040
14920	055542	052117	041440	047117
14921	055550	040524	047111	021040
14922	055556	021061	000	
14923				
14924	055561	101	052106	051105
14925	055566	040440	020116	044122
14926	055574	041440	042514	051101
14927	055502	021040	021061	053440
14928	055610	051501	053440	044522
14929	055616	052124	047105	044440
14930	055624	020116	044124	020105
14931	055632	044504	045523	024040
14932	055640	040524	042520	020051
14933	055646	042101	051104	051505
14934	055654	020123	042522	044507
14935	055662	052123	051105	005015
14936	055670	040520	020124	047111
14937	055676	051040	041510	030523
14938	055704	053440	051501	051440
14939	055712	052105	040440	052106
14940	055720	051105	051040	040505
14941	055726	044504	043516	042040
14942	055734	051511	020113	052050
14943	055742	050101	024505	040440
14944	055750	042104	042522	051523
14945	055756	051040	043505	051511
14946	055764	042524	020122	044124
14947	055772	020105	047506	046114
14948	056000	053517	047111	006507
14949	056006	012		
14950	056007	122	043505	051511
14951	056014	042524	020122	044504

.ASCII /REGISTER. PAT IN RHCSI WAS SET. ON READING DISK (TAPE) ADDRESS/<15><12>

.ASCIIZ /REGISTER IT DID NOT CONTAIN "1"/

EM46: .ASCII /AFTER AN RH CLEAR "1" WAS WRITTEN IN THE DISK (TAPE) ADDRESS REGISTER/<

.ASCII /PAT IN RHCSI WAS SET AFTER READING DISK (TAPE) ADDRESS REGISTER THE FOL

.ASCIIZ /REGISTER DID NOT CONTAIN WHAT IS IN "GOOD"/

14952	056022	020104	047516	020124
14953	056030	047503	052116	044501
14954	056036	020116	044127	052101
14955	056044	044440	020123	047111
14956	056052	021040	047507	042117
14957	056060	000042		
14958				
14959	056062	047101	051040	020110
14960	056070	046103	040505	020122
14961	056078	051050	041510	031123
14962	056104	041040	052111	021440
14963	056112	024465	053440	051501
14964	056120	043440	053111	047105
14965	056126	020056	030501	020066
14966	056134	051050	041510	030523
14967	056142	041040	052111	021440
14968	056150	024470	053440	051501
14969	056156	051440	052105	005015
14970	056164	047117	051040	040505
14971	056172	044504	043516	052040
14972	056200	042510	043040	046117
14973	056206	047514	044527	043516
14974	056214	051040	043505	051511
14975	056222	042524	020122	052111
14976	056230	042040	042111	047040
14977	056236	052117	041440	047117
14978	056244	040524	047111	053440
14979	056252	040510	020124	051511
14980	056260	044440	020116	043442
14981	056266	047517	021104	000
14982				
14983	056273	101	020116	044122
14984	056300	041440	042514	051101
14985	056306	024040	044122	051503
14986	056314	020062	044502	020124
14987	056322	032443	020051	040527
14988	056330	020123	044507	042526
14989	056336	027115	040440	033061
14990	056344	024040	044122	051503
14991	056352	020061	044502	020124
14992	056360	034043	020051	040527
14993	056366	020123	042523	006524
14994	056374	012		
14995	056375	101	046114	055040
14996	056402	051105	051517	053440
14997	056410	051105	020105	051127
14998	056416	052111	042524	020116
14999	056424	047111	051040	041110
15000	056432	042501	020056	044124
15001	056440	047105	052040	042510
15002	056446	043040	046117	047514
15003	056454	044527	043516	051040
15004	056462	043111	051511	042524
15005	056470	020111	044504	020104
15006	056476	047111	020124	040510
15007	056504	042526	053440	040510

EMS4: .ASCII /AN RH CLEAR (RHCS2 BIT #5) WAS GIVEN. A16 (RHCS1 BIT #8) WAS SET/<15><1

.ASCIZ /ON READING THE FOLLOWING REGISTER IT DID NOT CONTAIN WHAT IS IN "GOOD"/

EMS6: .ASCII /AN RH CLEAR (RHCS2 BIT #5) WAS GIVEN. A16 (RHCS1 BIT #8) WAS SET/<15><1

.ASCIZ /ALL ZEROS WERE WRITTEN IN RHBAE. THEN THE FOLLOWING REGISTER DID NOT HA

15008	056512	020124	051511	044440
15009	056520	020116	043442	047517
15010	056526	021104	000	
15011				
15012	056531	101	020116	044122
15013	056536	041440	042514	051101
15014	056544	024040	044122	051503
15015	056552	020062	044502	020124
15016	056560	032443	020051	040527
15017	056566	020123	044507	042526
15018	056574	027116	040440	033461
15019	056602	024040	044122	051503
15020	056610	020061	044502	020124
15021	056616	034443	020051	040527
15022	056624	020123	042523	006524
15023	056632	012		
15024	056633	117	020116	042522
15025	056640	042101	047111	020107
15026	056646	044124	020105	047506
15027	056654	046114	053517	047111
15028	056662	020107	042522	044507
15029	056670	052123	051105	044440
15030	056676	020124	044504	020104
15031	056704	047516	020124	047503
15032	056712	052116	044501	020116
15033	056720	044127	052101	044440
15034	056726	020123	047111	021040
15035	056734	047507	042117	000042
15036				
15037	056742	047101	051040	020110
15038	056750	046103	040505	020122
15039	056756	051050	041510	031123
15040	056764	041040	052111	021440
15041	056772	024470	053440	051501
15042	057000	043440	053111	047105
15043	057006	020056	030501	020067
15044	057014	051050	041510	030523
15045	057022	041040	052111	021440
15046	057030	024471	053440	051501
15047	057036	051440	052105	005015
15048	057044	046101	020114	042532
15049	057052	047522	020123	042527
15050	057060	042522	053440	044522
15051	057066	052124	047105	044440
15052	057074	020116	044122	040502
15053	057102	020105	044124	047105
15054	057110	052040	042510	043040
15055	057116	046117	047514	044527
15056	057124	043516	051040	043505
15057	057132	051511	042524	020122
15058	057140	044504	020104	047516
15059	057146	020124	040510	042526
15060	057154	053440	040510	020124
15061	057162	051511	044440	020116
15062	057170	047507	042117	005015
15063	057176	000		

EM60: .ASCII /AN RH CLEAR (RHCS2 BIT #5) WAS GIVEN. A17 (RHCS1 BIT #9) WAS SET/<15><1

.ASCIZ /ON READING THE FOLLOWING REGISTER IT DID NOT CONTAIN WHAT IS IN "GOOD"/

EM62: .ASCII /AN RH CLEAR (RHCS2 BIT #8) WAS GIVEN. A17 (RHCS1 BIT #9) WAS SET/<15><1

.ASCIZ /ALL ZEROS WERE WRITTEN IN RHBAE THEN THE FOLLOWING REGISTER DID NOT HAV

15064					
15065	057177	101	020116	044122	EM64: .ASCII /AN RH CLEAR (RHCS2-BIT #5) WAS GIVEN. IE (RHCS1 BIT #6) WAS SET/<15><12
15066	057204	041440	042514	051101	
15067	057212	024040	044122	051503	
15068	057220	026462	044502	020124	
15069	057226	032443	020051	040527	
15070	057234	020123	044507	042526	
15071	057242	027116	044440	020105	
15072	057250	051050	041510	030523	
15073	057256	041040	052111	021440	
15074	057264	024466	053440	051501	
15075	057272	051440	052105	005015	
15076	057300	047117	051040	040505	.ASCIZ /ON READING THE FOLLOWING REGISTER IT DID NOT CONTAIN WHAT IS IN "GOOD"/
15077	057306	044504	043516	052040	
15078	057314	042510	043040	046117	
15079	057322	047514	044527	043516	
15080	057330	051040	043505	051511	
15081	057336	042524	020122	052111	
15082	057344	042040	042111	047040	
15083	057352	052117	041440	047117	
15084	057360	040524	047111	053440	
15085	057366	040510	020124	051511	
15086	057374	044440	020116	043442	
15087	057402	047517	021104	000	
15088					
15089	057407	101	020116	044122	EM66: .ASCII /AN RH CLEAR (RHCS2-BIT #5) WAS GIVEN. IE (RHCS1 BIT #6) WAS SET/<15><12
15090	057414	041440	042514	051101	
15091	057422	024040	044122	051503	
15092	057430	026462	044502	020124	
15093	057436	032443	020051	040527	
15094	057444	020123	044507	042526	
15095	057452	027116	044440	020105	
15096	057460	051050	041510	030523	
15097	057466	041040	052111	021440	
15098	057474	024466	053440	051501	
15099	057502	051440	052105	005015	
15100	057510	046101	020114	042532	.ASCIZ /ALL ZEROS WERE WRITTEN IN RHCS3 THEN THE FOLLOWING REGISTER DID NOT CON
15101	057516	047522	020123	042527	
15102	057524	042522	053440	044522	
15103	057532	052124	047105	044440	
15104	057540	020116	044122	051503	
15105	057546	020063	044124	047105	
15106	057554	052040	042510	043040	
15107	057562	046117	047514	044527	
15108	057570	043516	051040	043505	
15109	057576	051511	042524	020122	
15110	057604	044504	020104	047516	
15111	057612	020124	047503	052116	
15112	057620	044501	020116	044127	
15113	057626	052101	044440	020123	
15114	057634	047111	021040	047507	
15115	057642	042117	000042		
15116					
15117	057646	044122	041440	042514	EM70: .ASCII /RH CLEAR WAS GIVEN. TWO SUCCESSIVE "GO" (RHCS1-BIT #0) WAS GIVEN/<15><1
15118	057654	051101	053440	051501	
15119	057662	043440	053111	047105	

15120	057670	020056	053524	020117
15121	057676	052523	041503	051505
15122	057704	044523	042526	021040
15123	057712	047507	020042	051050
15124	057720	041510	030523	041055
15125	057726	052111	021440	024460
15126	057734	053440	051501	043440
15127	057742	053111	047105	005015
15128	057750	044527	044124	052517
15129	057756	020124	040527	052111
15130	057764	047111	020107	047506
15131	057772	020122	044124	020105
15132	060000	044506	051522	020124
15133	060006	043442	021117	052040
15134	060014	020117	047503	050115
15135	060022	042514	042524	020056
15136	060030	044124	020105	047506
15137	060036	046114	053517	047111
15138	060044	020107	042522	044507
15139	060052	052123	051105	005015
15140	060060	044504	020104	047516
15141	060066	020124	047503	052116
15142	060074	044501	020116	044127
15143	060102	052101	044440	020123
15144	060110	047111	021040	047507
15145	060116	042117	000042	
15146				
15147	060122	044122	041440	042514
15148	060130	051101	053440	051501
15149	060136	043440	053111	047105
15150	060144	040440	031040	053440
15151	060152	051117	020104	051127
15152	060160	052111	020105	040527
15153	060166	020123	047504	042516
15154	060174	043040	047522	020115
15155	060202	020101	047514	040503
15156	060210	044524	047117	005015
15157	060216	040524	042507	020104
15158	060224	051127	051106	046517
15159	060232	040440	042116	053440
15160	060240	052111	020110	040502
15161	060246	020111	051050	041510
15162	060254	031123	041040	052111
15163	060262	021440	024463	051440
15164	060270	052105	040440	020124
15165	060276	044124	020105	047105
15166	060304	006504	012	
15167	060307	117	020106	044124
15168	060314	020105	051127	052111
15169	060322	020105	044124	020105
15170	060330	047506	046114	053517
15171	060336	047111	020107	042522
15172	060344	044507	052123	051105
15173	060352	042040	042111	047040
15174	060360	052117	041440	047117
15175	060366	040524	047111	053440

.ASCII /WITHOUT WAITING FOR THE FIRST "GO" TO COMPLETE. THE FOLLOWING REGISTER/

.ASCIZ /DID NOT CONTAIN WHAT IS IN "GOOD"/

EM76: .ASCII /RH CLEAR WAS GIVEN A 2 WORD WRITE WAS DONE FROM A LOCATION/<15><12>

.ASCII /TAGED WRFROM AND WITH BAI (RHCS2 BIT #3) SET AT THE END/<15><12>

.ASCIZ /OF THE WRITE THE FOLLOWING REGISTER DID NOT CONTAIN WHAT IS IN "GOOD"/

061523 117 020116 020101
061524 04 047514 052040
061525 051503 020124 047524
061526 051505 020124 047524
061527 051503 020062 044502
061528 020124 020442 020060
061529 047514 020105 047506
061530 053517 047111
061531 012
061532 043505 051511
061533 020122 044504
061534 020104 047516 020124
061535 047503 052116 044501
061536 020116 044127 052101
061537 044440 020123 047111
061538 021040 047507 042117
061539 000042
061540 042502 047506 042522
061541 042040 052101 020101
061542 051124 047101 043123
061543 051105 041440 046517
061544 040515 042116 053440
061545 051501 052040 006517
061546 012
061547 020105 044507
061548 042526 020116 044124
061549 020105 050122 042040
061550 044522 042525 051440
061551 040524 052524 020123
061552 042522 044507 052123
061553 051105 042040 042111
061554 005015
061555 047516 020124 047503
061556 052116 044501 020116
061557 044127 052101 044440
061558 020123 047111 021040
061559 047507 042117 000042
061560 042502 047506 042522
061561 042040 052101 020101
061562 051124 047101 043123
061563 051105 041440 046517
061564 040515 042116 053440
061565 051501 052040 006517
061566 012
061567 020105 044507
061568 042526 020116 044124
061569 020105 051522 042040
061570 044522 042526 051440
061571 040524 052524 020123
061572 042522 044507 052123
061573 051105 042040 042111
061574 005015
061575 047516 020124 047503
061576 052116 044501 020116
061577 044127 052101 044440

EM124: .ASCII /ON A SILO TEST TO TEST DBL RHCS3 BIT #10 THE FOLLOWING/15/12/

.ASCIZ /REGISTER DID NOT CONTAIN WHAT IS IN "GOOD"/

EM132: .ASCII /BEFORE DATA TRANSFER COMMAND WAS TO/15/12/

.ASCII /BE GIVEN THE RP DRIVE STATUS REGISTER DID/15/12/

.ASCIZ /NOT CONTAIN WHAT IS IN "GOOD"/

EM133: .ASCII /BEFORE DATA TRANSFER COMMAND WAS TO/15/12/

.ASCII /BE GIVEN THE RS DRIVE STATUS REGISTER DID/15/12/

.ASCIZ /NOT CONTAIN WHAT IS IN "GOOD"/


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15374 062206 020123 047111 021040
15375 062207 047507 042117 000042
15376 062208 042502 047506 042522
15377 062209 042040 052101 020101
15378 062210 051124 047101 043123
15379 062211 051105 041440 046517
15380 062212 040515 042116 053440
15381 062213 051501 052040 020117
15382 062214 042502 043440 053111
15383 062215 047105 005015
15384 062216 044124 020105 040515
15385 062217 020107 040524 042520
15386 062218 042040 044522 042526
15387 062219 051440 040524 052524
15388 062220 020123 042522 044507
15389 062221 052123 051105 042040
15390 062222 042111 047040 052117
15391 062223 005015
15392 062224 047503 052116 044501
15393 062225 020116 044127 052101
15394 062226 044440 020123 047111
15395 062227 051040 042110 030523
15396 062228 043455 047517 000104
15397 062229 040527 020123 040527
15398 062230 052111 047111 020107
15399 062231 047506 020122 020101
15400 062232 044502 020124 047524
15401 062233 051440 052105 044440
15402 062234 020116 020101 042522
15403 062235 044507 052123 051105
15404 062236 005015
15405 062237 044502 020124 047111
15406 062238 050440 042525 052123
15407 062239 047511 020116 051511
15408 062240 044440 020116 041042
15409 062241 052111 053440 044501
15410 062242 042524 021104 005015
15411 062243 000
15412 062244 122 043505 051511
15413 062245 042524 020122 042101
15414 062246 051104 051505 020123
15415 062247 047111 050440 042525
15416 062248 052123 047511 020116
15417 062249 051511 044440 020116
15418 062250 051042 043505 020056
15419 062251 042101 051104 000042
15420 062252 040527 020123 040527
15421 062253 052111 047111 020107
15422 062254 047506 020122 020101
15423 062255 044502 020124 047524
15424 062256 051040 051505 052105
15425 062257 044440 020116 020101
15426 062258 042522 044507 052123
15427 062259 051105 005015
15428 062260 044502 020124 047111
15429 062261 050440 042525 052123

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EM134: .ASCII BEFORE DATA TRANSFER COMMAND WAS TO BE GIVEN<<15><12>

.ASCII /THE MAG TAPE DRIVE STATUS REGISTER DID NOT/<<15><12>

.ASCIZ /CONTAIN WHAT IS IN RHDS1-GOOD/

EM135: .ASCII /WAS WAITING FOR A BIT TO SET IN A REGISTER/<<15><12>

.ASCIZ /BIT IN QUESTION IS IN "BIT WAITED"/<<15><12>

.ASCIZ /REGISTER ADDRESS IN QUESTION IS IN "REG. ADDR"

EM136: .ASCII /WAS WAITING FOR A BIT TO RESET IN A REGISTER/<<15><12>

.ASCIZ /BIT IN QUESTION IS IN "BIT WAITED"/<<15><12>

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15400 062204 047511 020116 051511
15401 062212 044440 020116 041042
15402 062220 052111 053440 044501
15403 062226 042524 021104 005015
15404 062234 000 000
15405 062235 122 043505 051511
15406 062242 042524 020122 042101
15407 062250 051104 051505 020123
15408 062256 047111 050440 042525
15409 062264 052122 047511 020116
15410 062272 051511 044440 020116
15411 062300 051042 043505 020056
15412 062306 042101 051104 000042
15413 062314 047117 053440 044522
15414 062322 044524 043516 040440
15415 062330 042116 051040 040505
15416 062336 044504 043516 052040
15417 062344 042510 051040 043505
15418 062352 051511 042524 020122
15419 062360 047111 021040 042522
15420 062366 027107 040440 042104
15421 062374 021122 005015
15422 062380 052111 042040 042111
15423 062386 047040 052117 041440
15424 062394 047117 040524 047111
15425 062402 042440 050130 041505
15426 062410 042524 020104 040526
15427 062418 052514 000105
15428 062426 044122 041440 042514
15429 062434 051101 053440 051501
15430 062442 043440 053111 047105
15431 062450 040440 020116 050111
15432 062458 045503 041040 052111
15433 062466 051440 047510 047127
15434 062474 044440 020116 044442
15435 062482 041520 021113 053440
15436 062490 051501 051440 052105
15437 062498 005015
15438 062506 042532 047522 020123
15439 062514 042527 042522 046440
15440 062522 053117 042105 044440
15441 062530 052116 020117 044122
15442 062538 041104 020056 047117
15443 062546 051040 040505 044504
15444 062554 043516 020040 044124
15445 062562 020105 047506 046114
15446 062570 053517 047111 006507
15447 062578 012
15448 062586 122 043505 051511
15449 062594 042524 020122 044504
15450 062602 020104 047516 020124
15451 062610 047503 052116 044501
15452 062618 020116 044127 052101
15453 062626 044440 020123 047111
15454 062634 021040 047507 042117
15455 062642 000042

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.ASCIZ REGISTER ADDRESS IN QUESTION IS IN "REG. ADDR"/

EM137: .ASCII /ON WRITING AND READING THE REGISTER IN "REG. ADDR"/<15><12>

.ASCIZ /IT DID NOT CONTAIN EXPECTED VALUE/

EM140: .ASCII /RH CLEAR WAS GIVEN AN IPCK BIT SHOWN IN "IPCK" WAS SET/<15><12>

.ASCII /ZEROS WERE MOVED INTO RHOB. ON READING THE FOLLOWING/<15><12>

.ASCIZ /REGISTER DID NOT CONTAIN WHAT IS IN "GOOD"/

15456	063374	043101	042524	020122	EM141: .ASCII /AFTER CLEARING THE RH AND WRITING TWO WORD INTO RHDB AND/<15><12>
15457	063402	046103	040505	044522	
15458	063410	043516	052040	042510	
15459	063416	051040	020110	047101	
15460	063424	020104	051127	052111	
15461	063432	047111	020107	053524	
15462	063440	020117	047527	042122	
15463	063446	044440	052116	020117	
15464	063454	044122	041104	040440	
15465	063462	042116	005015		
15466	063466	042522	042101	047111	.ASCIZ /READING IT BACK TWICE, CAUSED RHCS1 TO HAVE WRONG VALUE GIVEN IN "BAD"/
15467	063474	020107	052111	041040	
15468	063502	041501	020113	053524	
15469	063510	041511	026105	041440	
15470	063516	052501	042523	020104	
15471	063524	044122	051503	020061	
15472	063532	047524	044040	053101	
15473	063540	020105	051127	047117	
15474	063546	020107	040526	052514	
15475	063554	020105	044507	042526	
15476	063562	020116	047111	021040	
15477	063570	040502	021104	000	
15478	063575	101	052106	051105	EM142: .ASCII /AFTER CLEARING THE RH AND WRITING TWO WORD INTO RHDB AND/<15><12>
15479	063602	041440	042514	051101	
15480	063610	047111	020107	044124	
15481	063616	020105	044122	040440	
15482	063624	042116	053440	044522	
15483	063632	044524	043516	052040	
15484	063640	047527	053440	051117	
15485	063646	020104	047111	047524	
15486	063654	051040	042110	020102	
15487	063662	047101	006504	012	
15488	063667	122	040505	044504	.ASCIZ /READING IT BACK TWICE, CAUSED RHCS3 TO HAVE WRONG VALUE GIVEN IN "BAD"/
15489	063674	043516	044440	020124	
15490	063702	040502	045503	052040	
15491	063710	044527	042503	020054	
15492	063716	040503	051525	042105	
15493	063724	051040	041510	031523	
15494	063732	052040	020117	040510	
15495	063740	042526	053440	047522	
15496	063746	043516	053040	046101	
15497	063754	042525	043440	053111	
15498	063762	047105	044440	020116	
15499	063770	041042	042101	000042	
15500	063776	043101	042524	020122	EM143: .ASCII /AFTER CLEARING THE RH AND WRITING TWO WORD INTO RHDB AND/<15><12>
15501	064004	046103	040505	044522	
15502	064012	043516	052040	042510	
15503	064020	051040	020110	047101	
15504	064026	020104	051127	052111	
15505	064034	047111	020107	053524	
15506	064042	020117	047527	042122	
15507	064050	044440	052116	020117	
15508	064056	044122	041104	040440	
15509	064064	042116	005015		
15510	064070	042522	042101	047111	.ASCIZ /READING IT BACK TWICE, CAUSED RHBA TO HAVE WRONG VALUE GIVEN IN "BAD"/
15511	064076	020107	052111	041040	

15512	064104	041501	020113	053524
15513	064112	041511	026105	041440
15514	064120	052501	042523	020104
15515	064126	044122	040502	052040
15516	064134	020117	040510	042526
15517	064142	053440	047522	043516
15518	064150	053040	046101	042525
15519	064156	043440	053111	047105
15520	064164	044440	020116	041042
15521	064172	042101	000042	
15522	064176	043101	042524	020122
15523	064204	046103	040505	044522
15524	064212	043516	052040	042510
15525	064220	051040	020110	047101
15526	064226	020104	051127	052111
15527	064234	047111	020107	053524
15528	064242	020117	047527	042122
15529	064250	044440	052116	020117
15530	064256	044122	041104	040440
15531	064264	042116	005015	
15532	064270	042522	042101	047111
15533	064276	020107	052111	041040
15534	064304	041501	020113	053524
15535	064312	041511	026105	041440
15536	064320	052501	042523	020104
15537	064326	044122	040502	020105
15538	064334	047524	044040	053101
15539	064342	020105	051127	047117
15540	064350	020107	040526	052514
15541	064356	020105	044507	042526
15542	064364	020116	047111	021040
15543	064372	040502	021104	000
15544	064377	101	052106	051105
15545	064404	041440	042514	051101
15546	064412	047111	020107	044124
15547	064420	020105	044122	040440
15548	064426	042116	053440	044522
15549	064434	044524	043516	052040
15550	064442	047527	053440	051117
15551	064450	020104	047111	047524
15552	064456	051040	042110	020102
15553	064464	047101	006504	012
15554	064471	122	040505	044504
15555	064476	043516	044440	020124
15556	064504	040502	045503	052040
15557	064512	044527	042503	020054
15558	064520	040503	051525	042105
15559	064526	051040	053510	020103
15560	064534	047524	044040	053101
15561	064542	020105	051127	047117
15562	064550	020107	040526	052514
15563	064556	020105	044507	042526
15564	064564	020116	047111	021040
15565	064572	040502	021104	000
15566				
15567	064577	101	042040	053105

EM144: .ASCII /AFTER CLEARING THE RH AND WRITING TWO WORD INTO RHDB AND/<15><12>

.ASCIZ /READING IT BACK TWICE, CAUSED RHBAE TO HAVE WRONG VALUE GIVEN IN "BAC"/

EM145: .ASCII /AFTER CLEARING THE RH AND WRITING TWO WORD INTO RHDB AND/<15><12>

.ASCIZ /READING IT BACK TWICE, CAUSED RHWC TO HAVE WRONG VALUE GIVEN IN "BAC"

EM146: .ASCII /A DEVICE BASE ADDRESS DID NOT TIME OUT BUT/<15><12>

15568	064604	041511	020105	040502
15569	064612	042523	040440	042104
15570	064620	042522	051523	042040
15571	064626	042111	047040	052117
15572	064634	052040	046511	020105
15573	064642	052517	020124	052502
15574	064650	006524	012	
15575	064653	124	042510	041440
15576	064660	051117	042522	050123
15577	064666	047117	044504	043516
15578	064674	053040	041505	047524
15579	064702	020122	044504	020104
15580	064710	044524	042515	047440
15581	064716	052125	006456	012
15582	064723	110	052111	041440
15583	064730	047117	044524	052516
15584	064736	020105	047524	051040
15585	064744	050105	040505	020124
15586	064752	042526	052103	051117
15587	064760	052040	046511	047505
15588	064766	052125	000	
15589	064771	101	042040	053105
15590	064776	041511	020105	040502
15591	065004	23	040440	042104
15592	065012	22	051523	042040
15593	065020	042111	047040	052117
15594	065026	052040	046511	020105
15595	065034	052517	006524	012
15596	065041	102	052125	047040
15597	065046	020117	047125	052111
15598	065054	047040	046525	042502
15599	065062	051522	044040	042101
15600	065070	040440	050120	047522
15601	065076	051120	040511	042524
15602	065104	042040	044522	042526
15603	065112	052040	050131	051505
15604	065120	000		
15605				
15606				
15607	065121	120	004503	044122
15608	065126	052104		
15609				
15610	065130	041520	052411	044516
15611	065136	052502	020123	042101
15612	065144	051104	051505	020123
15613	065152	047117	053440	044510
15614	065160	044103	052040	046511
15615	065166	020105	052517	020124
15616	065174	041517	052503	051122
15617	065202	042105		
15618				
15619	065204	041520	052011	051505
15620	065212	020124	047516	051040
15621	065220	041510	031123	051011
15622	065226	041510	031123	005015
15623	065234	004411	047507	042117

.ASCII /THE CORRESPONDING VECTOR DID TIME OUT./<15><12>

.ASCIZ /HIT CONTINUE TO REPEAT VECTOR TIMEOUT/

EM147: .ASCII /A DEVICE BASE ADDRESS DID NOT TIME OUT/<15><12>

.ASCIZ /BUT NO UNIT NUMBERS HAD APPROPRIATE DRIVE TYPES/

DH1: .ASCII /PC RHDT/

DH5: .ASCII /PC UNIBUS ADDRESS ON WHICH TIME OUT OCCURRED/

DH6: .ASCII /PC TEST NO RHCS2 RHCS2/<15><12>

.ASCIZ / GOOD BAD/

15624	065242	041011	042101	000							
15625											
15626	065247	120	004503	042524	DH7:	.ASCII	/PC	TEST	RHDB	RHDB/<15><12>	
15627	065254	052123	051011	042110							
15628	065262	004502	044122	041104							
15629	065270	005015									
15630	065272	047011	004517	047507		.ASCIZ	/	NO	GOOD	BAD/	
15631	065300	042117	041011	042101							
15632	065306	000									
15633											
15634	065307	120	004503	042524	DH11:	.ASCII	/PC	TEST	RHCS1	RHCS1/<15><12>	
15635	065314	052123	051011	041510							
15636	065322	030523	051011	041510							
15637	065330	030523	005015								
15638	065334	047011	004517	047507		.ASCIZ	/	NO	GOOD	BAD/	
15639	065342	042117	041011	042101							
15640	065350	000									
15641											
15642	065351	120	004503	042524	DH12:	.ASCII	/PC	TEST	RHCS3	RHCS3/<15><12>	
15643	065356	052123	051011	041510							
15644	065364	031523	051011	041510							
15645	065372	031523	005015								
15646	065376	047011	004517	047507		.ASCIZ	/	NO	GOOD	BAD/	
15647	065404	042117	041011	042101							
15648	065412	000									
15649											
15650	065413	120	004503	042524	DH13:	.ASCII	/PC	TEST	RHBA	RHBA/<15><12>	
15651	065420	052123	051011	041110							
15652	065426	004501	044122	040502							
15653	065434	005015									
15654	065436	047011	004517	047507		.ASCIZ	/	NO	GOOD	BAD/	
15655	065444	042117	041011	042101							
15656	065452	000									
15657											
15658	065453	120	004503	042524	DH14:	.ASCII	/PC	TEST	RHBAE	RHBAE/<15><12>	
15659	065460	052123	051011	041110							
15660	065466	042501	051011	041110							
15661	065474	042501	005015								
15662	065500	047011	004517	047507		.ASCIZ	/	NO	GOOD	BAD/	
15663	065506	042117	041011	042101							
15664	065514	000									
15665											
15666	065515	120	004503	042524	DH15:	.ASCII	/PC	TEST	RHWC	RHWC/<15><12>	
15667	065522	052123	051011	053510							
15668	065530	004503	044122	041527							
15669	065536	005015									
15670	065540	047011	004517	047507		.ASCIZ	/	NO	GOOD	BAD/	
15671	065546	042117	041011	042101							
15672	065554	000									
15673											
15674	065555	120	004503	042524	DH24:	.ASCII	/PC	TEST	WORD	RHDB	RHDB/<15><12>
15675	065562	052123	053411	051117							
15676	065570	004504	044122	041104							
15677	065576	051011	042110	006502							
15678	065604	012									
15679	065605	011	047516	047011		.ASCIZ	/	NO	NO	GOOD	BAD/

15680	065612	004517	047507	042117						
15691	065620	041011	042101	000						
15692										
15693	065625	:20	004503	042524	DH33:	.ASCII	/PC	TEST	RHER1	RHER1/<15><12>
15694	065632	052:23	051011	042510						
15685	065640	030522	051011	042510						
15686	065646	030522	005015							
15687	065652	047011	004517	047507		.ASCIZ	/	NO	GOOD	BAD/
15689	065660	042117	041011	042101						
15699	065666	000								
15690										
15691	065667	120	004503	042524	DH45:	.ASCII	/PC	TEST	RHDA	RHDA/<15><12>
15692	065674	052123	051011	042110						
15693	065702	004501	044122	040504						
15694	065710	005015								
15695	065712	047011	004517	047507		.ASCIZ	/	NO	GOOD	BAD/
15696	065720	042117	041011	042101						
15697	065726	000								
15698										
15699	065727	120	004503	042524	DH104:	.ASCII	/PC	TEST	IPCK	RHCS3 RHCS3/<15><12>
15700	065734	052123	044411	041520						
15701	065742	004513	044122	051503						
15702	065750	004463	044122	051503						
15703	065756	006463	012							
15704	065761	011	047516	004411		.ASCIZ	/	NO	GOOD	BAD/
15705	065766	047507	042117	041011						
15706	065774	042101	000							
15707										
15708	065777	120	004503	042524	DH105:	.ASCII	/PC	TEST	IPCK	RHCS2 RHCS2/<15><12>
15709	066004	052123	044411	041520						
15710	066012	004513	044122	051503						
15711	066020	004462	044122	051503						
15712	066026	006462	012							
15713	066031	011	047516	004411		.ASCIZ	/	NO	GOOD	BAD/
15714	066036	047507	042117	041011						
15715	066044	042101	000							
15716										
15717	066047	120	004503	042524	DH106:	.ASCII	/PC	TEST	IPCK	RHCS1 RHCS1/<15><12>
15718	066054	052123	044411	041520						
15719	066062	004513	044122	051503						
15720	066070	004461	044122	051503						
15721	066076	006461	012							
15722	066101	011	047516	004411		.ASCIZ	/	NO	GOOD	BAD/
15723	066106	047507	042117	041011						
15724	066114	042101	000							
15725										
15726	066117	120	004503	042524	DH111:	.ASCII	/PC	TEST	IPCK	RHBA RHBA/<15><12>
15727	066124	052123	044411	041520						
15728	066132	004513	044122	040502						
15729	066140	051011	041110	006501						
15730	066146	012								
15731	066147	011	047516	004411		.ASCIZ	/	NO	GOOD	BAD/
15732	066154	047507	042117	041011						
15733	066162	042101	000							
15734										
15735	066165	120	004503	042524	DH112:	.ASCII	/PC	TEST	IPCK	RHBAE RHBAE/<15><12>

DERHAB.SRC POWER DOWN AND UP ROUTINES

15736	066172	052123	044411	041520						
15737	066200	004513	044122	040502						
15738	066206	004505	044122	040502						
15739	066214	006505	012							
15740	066217	011	047516	004411	.ASCIZ	/	NO	GOOD	BAD/	
15741	066224	047507	042117	041011						
15742	066232	042101	000							
15743										
15744	066235	120	004503	042524	DH113:	.ASCII	/PC	TEST	IPCK	RHWC RHWC/<15><12>
15745	066242	052123	044411	041520						
15746	066250	004513	044122	041527						
15747	066256	051011	053510	006503						
15748	066254	012								
15749	066265	011	047516	004411	.ASCIZ	/	NO	GOOD	BAD/	
15750	066272	047507	042117	041011						
15751	066300	042101	000							
15752	066303	120	004505	042524	DH132:	.ASCII	/PE	TEST	RHDS1	RHDS1/<15><12>
15753	066310	052123	051011	042110						
15754	066316	030523	051011	042110						
15755	066324	030523	005015							
15756	066330	047011	004517	047507	.ASCIZ	/	NO	GOOD	BAD/	
15757	066336	042117	041040	042101						
15758	066344	000								
15759	066345	120	004503	042524	DH135:	.ASCII	/PC	TEST	PC OF	BIT WAIT REG/<15><12>
15760	066352	052123	050011	020103						
15761	066360	043117	041011	052111						
15762	066366	053440	044501	020124						
15763	066374	051040	043505	005015						
15764	066402	047011	004517	040527	.ASCIZ	/	NO	WAT	FOR	ADDR/
15765	066410	004524	047506	004522						
15766	066416	020040	042101	051104						
15767	066424	000								
15768	066425	120	004503	042524	DH137:	.ASCII	/PC	TEST	REG.	GOOD BAD/<15><12>
15769	066432	052123	051011	043505						
15770	066440	004456	047507	042117						
15771	066446	041011	042101	005015						
15772	066454	047011	004517	042101	.ASCIZ	/	NO	ADDR	DATA	DATA/
15773	066462	051104	042011	052101						
15774	066470	004501	040504	040524						
15775	066476	000								
15776										
15777	066477	120	020103	020040	DH146:	.ASCIZ	/PC	BASE	VECTOR/	
15778	066504	020040	041040	051501						
15779	066512	020105	020040	053040						
15780	066520	041505	047524	000122						
15781	066526	041520	020040	020040	DH147:	.ASCIZ	/PC	BASE	ADDRESS/	
15782	066534	020040	040502	042523						
15783	066542	040440	042104	042522						
15784	066550	051523	000							
15785										
15786										
15787	066554				.EVEN					
15788										
15789	066554	001116	001126	000000	DT1:	.WORD	\$ERRPC,\$BDDAT,0			
15790	066562	001116	004046	004050	DT5:	.WORD	\$ERRPC,\$RCSI,\$PCSI,\$TMCSI,\$MIXCSI,0			
15791	066570	004052	004054	000000						

15792	066576	001116	004656	001124	DT6:	.WORD	\$ERRPC, TSTNM, \$GDDAT, \$BDDAT, 0
15793	066604	001126	000000				
15794	066610	001116	004656	004664	DT24:	.WORD	\$ERRPC, TSTNM, SILONM, \$GDDAT, \$BDDAT, 0
15795	066616	001124	001126	000000			
15796	066624	001116	004656	004662	DT104:	.WORD	\$ERRPC, TSTNM, IP, \$GDDAT, \$BDDAT, 0
15797	066632	001124	001126	000000			
15798	066640	001116	004656	004662	DT105:	.WORD	\$ERRPC, TSTNM, IP, \$GDDAT, \$BDDAT, 0
15799	066646	001124	001126	000000			
15800	066654	001116	004656	004662	DT106:	.WORD	\$ERRPC, TSTNM, IP, \$GDDAT, \$BDDAT, 0
15801	066662	001124	001126	000000			
15802	066670	001116	004656	004672	DT135:	.WORD	\$ERRPC, TSTNM, WAITBT, WAITPC, WAITRE, 0
15803	066676	004666	004670	000000			
15804	066704	001116	004656	001122	DT137:	.WORD	\$ERRPC, TSTNM, \$BDADR, \$GDDAT, \$BDDAT, 0
15805	066712	001124	001126	000000			
15806	066720	001116	005324	005326	DT146:	.WORD	\$ERRPC, TESTDV, TESTVC, 0
15807	066726	000000					
15808	066730	001116	005324	000000	DT147:	.WORD	\$ERRPC, TESTDV, 0
15809							
15810							
15811							
15812	066736	000	000		DF1:	.BYTE	0,0
15813	066740	000	000	000	DF5:	.BYTE	0,0,0,0,0
15814	066743	000	000				
15815	066745	000	000	000	DF6:	.BYTE	0,0,0,0
15816	066750	000					
15817	066751	000	000	000	DF24:	.BYTE	0,0,0,0,0
15818	066754	000	000				
15819	066756	000	000	000	DF104:	.BYTE	0,0,0,0,0
15820	066761	000	000				
15821	066763	000	000	000	DF105:	.BYTE	0,0,0,0,0
15822	066766	000	000				
15823	066770	000	000	000	DF106:	.BYTE	0,0,0,0,0
15824	066773	000	000				
15825	066775	000	000	000	DF135:	.BYTE	0,0,0,0,0
15826	067000	000	000				
15827	067002	000	000	000	DF137:	.BYTE	0,0,0,0,0
15828	067005	000	000				
15829	067007	000	000	000	DF146:	.BYTE	0,0,0
15830	067012	000	000		DF147:	.BYTE	0,0
15831						.EVEN	
15832		000001				.END	

JOB

RHEC2	004126	2981*	14038															
RHER1	004074	2856*	6921	6943	13950													
RHER2	004120	2878*	13958															
RHER3	004122	2879*	13966															
RHFC	004066	2861*	13302*	13346*														
RHLA	004100	2869*	14014															
RHMR	004104	2871*	14006															
RHOF	004112	2875*	13200*	13999														
RHSN	004110	2873*	14054															
RHTC	004112	2874*	3300*	13231*	13299*	13300*	13339*	13340*										
RHWC	004062	2859*	3958*	3959	3963	3983*	3984	3988	4337	4509	4781	5171	5588	6011				
		6432	6869	7144	7323	7726*	7831*	7935	8167	8403	8705	9007	9056*	9064*				
		9241	9289*	9297*	9474	9522*	9530*	9707	9755*	9763*	9940	9991*	10058	10104*				
		10172	10218*	10285	10331*	10398	10445*	10516	10563*	10631	10678*	10745	10791*	10859				
		10905*	10972	11018*	11085	11131*	11199	11246*	11317	11365*	11432	11478*	11545	11591*				
		11659	11705*	11773	11819*	11886	11933*	12004	12057*	12125	12176*	12243	12294*	12362				
		12413*	12481	12532*	12599	12651*	12722	12911*	13105	13107	13341	13910						
		2637*																
RMR	= 000004	2834*	15790															
RPCS:	004050	2542*	3143	13401	13408*	13423	13437*											
RPVFC	002716	3144	13431*	13437														
RPVFC	043056	3144	13431*	13437														
RSCS:	004046	2833*	15790															
RJ	= 000000	1294*	3143*	3144*	3145*	3211*	3214	3231*	3245*	3254*	3255	3261*	3266	7731*				
		7834*	9059*	9068*	9292*	9301*	9525*	9534*	9759*	9767*	9992*	10105*	10219*	10332*				
		10447*	10564*	10679*	10792*	10906*	11019*	11132*	11249*	11366*	11479*	11592*	11706*	11820*				
		11935*	12059*	12178*	12296*	12415*	12534*	12654*	12814*	12817	12970	12972*	12973	12975				
		12976	12977	13020*	13102	13105*	13108	13113*	13127	13163*	13175	13217*	13230	13374*				
		13391*	13393*	13394*	13522	13532*	13536	13552	13553	13566*	13597	13598*	13599	13602*				
		13806	13810*	13811	13814	13834*	13837*	14060	14061*	14062*	14069*	14070*	14071*	14072*				
		14073*	14074	14076	14078	14079	14084	14090	14092*	14093	14107*	14198	14199*	14200				
		14201*	14202*	14203*	14204*	14241	14266*											
R1	= 000001	1295*	3212*	3215*	3262*	3267	4846*	4847	4848*	5241*	5243	5244*	5662*	5665				
		5666*	6084*	6086	6088*	6506*	6509	6511*	9983*	9987*	10096*	10100*	10210*	10214*				
		10323*	10327*	10437*	10441*	10555*	10559*	10670*	10674*	10783*	10787*	10897*	10901*	11010*				
		11014*	11123*	11127*	11238*	11242*	11357*	11361*	11470*	11474*	11583*	11587*	11697*	11701*				
		11811*	11815*	11925*	11929*	12049*	12053*	12168*	12172*	12286*	12290*	12405*	12409*	12524*				
		12528*	12643*	12647*	12932	12937	13103	13106*	13108*	13112*	13392*	13395*	13523	13536*				
		13537	13541	13565*	13807	13812*	13820*	13822*	13824*	13827*	13830	13833*	14089	14090*				
		14095	14099	14101	14106*	14242	14265*											
R2	= 000002	1296*	3213*	3214*	3271*	3276*	3287*	3288*	3289*	3290	3311*	3312*	3325*	3326*				
		3328*	3329*	3330*	3331*	3332*	3333*	3334*	3335*	3336	3338*	3341*	3343*	3346				
		3349	3350	3352	3354	3356	3358*	3361*	3363*	3366	3368	3370	3372	3374				
		3376*	3379*	3381*	3392	3398	3404	3410	3419	3426	3433	3435*	3441	3443*				
		3449	3451*	3457	3459*	3465	3467*	3473	3475*	3481	3483*	3489	3491*	3497				
		3499*	3511	3517	3583*	3584*	3585*	3594	3605	3607	3613	3616	3623	3630				
		3637	3651	3657	3661	3667	3668	3669	3670	3671	3672	4845*	4849*	5242*				
		5245*	5664*	5667*	6085*	6089*	6509*	6512*	9984*	9986*	10097*	10099*	10211*	10213*				
		10324*	10326*	10438*	10440*	10556*	10559*	10671*	10673*	10784*	10786*	10898*	10900*	11011*				
		11013*	11124*	11126*	11239*	11241*	11358*	11360*	11471*	11473*	11584*	11596*	11698*	11700*				
		11812*	11814*	11926*	11928*	12050*	12052*	12169*	12171*	12287*	12289*	12406*	12408*	12525*				
		12527*	12644*	12646*	12759*	12760*	12761*	12762	13104	13107*	13109*	13111*	13524	13535*				
		13539*	13542	13549*	13550*	13551	13556*	13564*	13808	13813*	13821*	13823*	13825*	13831				
		13832*	14243	14264*														
R3	= 000003	1297*	3272*	3274*	3659*	3664*	12926	12931	12944	12971	12983*	12986*	12988*	12991*				
		13003*	13006*	13008*	13011*	13019*	13525	13533*	13534*	13548*	13551*	13560*	13561*	13563*				
		13755	13756*	13757	13760*	13761	13765	13767	13769*	13771*	14146	14155*	14161*	14162*				

TST20	020552	6472#												
TST21	021466	6909#												
TST22	022100	7181#												
TST23	022374	7356#												
TST24	022556	7476#												
TST25	022740	7596#												
TST26	023122	7717#												
TST27	023316	7821#												
TST3	011420	3700#												
TST30	023600	7966#												
TST31	024202	8202#												
TST32	024604	8439#												
TST33	025332	8741#												
TST34	026060	9046#												
TST35	026456	9279#												
TST36	027054	9512#												
TST37	027452	9745#												
TST4	011722	3780#												
TST40	030050	9974#												
TST41	030276	10087#												
TST42	030524	10201#												
TST43	030752	10314#												
TST44	031200	10428#												
TST45	031434	10546#												
TST46	031662	10661#												
TST47	032110	10774#												
TST5	012202	3858#												
TST50	032336	10988#												
TST51	032564	11001#												
TST52	033012	11114#												
TST53	033240	11229#												
TST54	033474	11348#												
TST55	033722	11461#												
TST56	034150	11574#												
TST57	034376	11688#												
TST6	012504	3938#												
TST60	034624	11802#												
TST61	035052	11916#												
TST62	035306	12036#												
TST63	035544	12045	12155#											
TST64	036002	12164	12273#											
TST65	036240	12282	12392#											
TST66	036476	12401	12511#											
TST67	036734	12520	12630#											
TST7	012764	4016#												
TST70	037200	12639	12751#											
TSUNIT	005054	2993#												
TSVEC	005120	3003#												
TUF	= 000100	2704#												
TYPDS	= 104405	3239	3251	3393	3411	3512	3595	3614	3652	12763	12775	12812	14100	14235#
TYPE	= 104401	3154	3219	3223	3234	3240	3246	3252	3388	3394	3400	3406	3421	3428
		3436	3444	3452	3460	3468	3476	3484	3492	3500	3507	3513	3519	3523
		3586	3590	3597	3601	3609	3618	3625	3632	3639	3647	3653	12754	12764
		12770	12776	12780	12810	12813	12843	12849	12867	12871	12877	12882	12888	12894
		13048	13054	13060	13061	13065	13069	13076	13080	13380	13386	13397	13403	13409
		13413	13419	13425	13432	13567	13610	13685	13693	13763	13766	13770	13838	13840

	5609*	5611	6031*	6033	6452*	6454	6890*	6892	7167*	7169	7341*	7343	7461*	
	7463	7581*	7593	7701*	7703	7797*	7799	7953*	7955	8189*	8191	9425*	9427	
	9727*	9729	9030*	9032	9263*	9265	9496*	9498	9729*	9731	9963*	9965	10076*	
	10078	10190*	10192	10303*	10305	10416*	10418	10535*	10537	10650*	10652	10763*	10765	
	10877*	10879	10990*	10992	11103*	11105	11217*	11219	11337*	11339	11450*	11452	11563*	
	11565	11677*	11679	11791*	11793	11904*	11906	12024*	12026	12.43*	12145	12261*	12253	
	12380*	12382	12499*	12501	12617*	12619	12744*	12746						
\$CNT	046432	14145*	14174*	14187*										
\$CMODE	046434	14140*	14144*	14149	14152*	14163*	14189*							
\$COVER	043362	13463	13479	13487	13497	13505*								
\$PASS	001100	1454*	12802*	12803*	12811	12824	13493	13509						
\$PCIER	046670	14270	14277*											
\$PWAD	046656	14272*												
\$PWADN	046516	3116	14239*	14267										
\$PWARM	046652	14270*												
\$PWFLP	046570	14249	14255*											
\$QLES	001222	1500*	13646	13763	13779	13840	13843	13884						
\$RDCHR	044334	13732*	14228											
\$RDLEC=	***** U	14231												
\$RDLYN	044424	13755*	14229											
\$RDOCT	044604	13804*	14230											
\$RDSZ =	000011	13748*												
\$REGAD	001160	1483*												
\$REGO	001162	1485*												
\$REG1	001164	1486*												
\$REG2	001166	1487*												
\$REG3	001170	1488*												
\$REG4	001172	1489*												
\$REG5	001174	1490*												
\$RTNAD	037536	12823*												
\$RZA =	***** U	14231												
\$SAVRE =	**:*:* U	14231												
\$SAVRE	046666	14248*	14256	14257*	14258*	14276*								
\$SCOPE	043120	3110	12854	12900	13459*									
\$SETUP =	000017	3102*	3109	3110	3112	3114	3116	3118	3119	3121	12800	13459	13699	13690
		13721	13786	13858	13876	13883								
\$SS1 =	000000	3139*												
\$STUP =	177777	3102*												
\$SVLAD	043334	13471	13500*											
\$SWR =	167700	1251*	1260	1265	1266	1267	1268	1269	1270	1271	1497	1498	1499	3118
		3119	3121	3122	3208	3578	3701	3781	3859	3939	4017	4098	4183	4372
		4548	4816	5210	5630	6052	6473	6910	7182	7357	7477	7597	7719	7823
		7967	8203	8440	8742	9047	9280	9513	9746	9975	10088	10202	10315	10429
		10547	10662	10775	10889	11002	11115	11230	11349	11462	11575	11689	11803	11917
		12037	12156	12274	12393	12512	12631	12752	12795	12901	12816	12822	12824	13450
		13451	13452	13453	13454	13462	13474	13476	13477	13480	13481	13492	13499	13490
		13491	13502	13505	13508	13849	13850	13851	13852	13853	13861	13868	13873	13876
		13884	14273											
\$SLRMK=	000000	1271	1272	13454	13455	13478								
\$TIMES	001212	1497*	3118*	3208*	3578*	12752*	12801*	13489*	13496	13499*	13508			
\$TKB	001146	1476*	13649	13670	13681	13702								
\$TKCNT	044044	13650*	13665*	13691	13708*	13739	13741*							
\$TKINT	044064	3147	13665*	13686										
\$TKGEN=	044063	13654*	13716	13744										
\$TKGIN	044046	13651*	13666*	13667	13714*	13715*	13716	13718*						
\$TKCCU	044050	13652*	13667*	13742	13743*	13744	13746*							

.STYPO	1*	1251*	14113
.S40CA	1*		
.1170	1*		

REFERENCE TABLE

REF ID: A66000

13429	13438	13595	13875	14250	14274	14169	14177	14258
13495	13539	13702	13715	13743	13264			
33306	34112	34225	3432	3440	3534	3540	3545	3546
4101	4196	4375	451	481	505	533	505	5476
7731	7825	7934	795	804	825	834	844	883
9283	9299	9301	935	936	936	934	935	9743
10091	10205	10219	10318	10332	10447	10447	10500	10564
10306	11019	11118	11132	11133	11243	11252	11256	11455
11820	11920	11935	12040	12059	12178	12178	12277	12296
12817	13614	13621	13628	13655				
13313	13444	13445	13446	13447				
13448	13449	13450	13451	13452				
13453	13454	13455	13456	13457				

	6999	6908	6909	6910	6920	6942	6953	6968	7019	7045	7071	7095	7119	7143	7168
	10010	10010	10111	10131	10148	10034	10037	10055	10057	10077	10078	10086	10097	10098	10094
	10210	10210	10244	10261	10264	10292	10169	10171	10191	10192	10200	10201	10202	10208	10221
	10310	10310	10374	10377	10395	10397	10284	10304	10305	10313	10314	10315	10321	10334	10335
	10410	10410	10494	10513	10515	10536	10417	10418	10427	10428	10429	10435	10449	10450	10453
	10610	10610	10628	10630	10651	10652	10537	10545	10546	10547	10553	10566	10567	10570	10590
	10710	10710	10742	10744	10764	10765	10660	10661	10662	10668	10679	10691	10682	10685	10704
	10810	10810	10838	10858	10876	10879	10887	10888	10889	10895	10906	10909	10909	10912	10931
	10910	10910	10951	10971	10991	10992	11000	11001	11002	11008	11019	11021	11022	11025	11044
	11010	11010	11064	11082	11104	11105	11113	11114	11115	11121	11132	11134	11135	11138	11158
	11110	11110	11195	11198	11218	11219	11228	11229	11230	11236	11248	11250	11251	11254	11274
	11210	11210	11244	11316	11338	11339	11347	11348	11349	11355	11366	11368	11369	11372	11389
	11310	11310	11411	11429	11431	11451	11452	11460	11461	11462	11468	11479	11481	11492	11495
	11410	11410	11521	11524	11542	11544	11564	11565	11573	11574	11575	11581	11592	11594	11595
	11510	11510	11616	11635	11639	11656	11658	11678	11679	11687	11689	11689	11695	11706	11708
	11610	11610	11730	11732	11749	11752	11770	11772	11792	11793	11801	11802	11803	11809	11827
	11710	11710	11826	11843	11845	11862	11865	11882	11885	11905	11906	11915	11916	11917	11923
	11810	11810	11938	11941	11959	11961	11979	11992	12001	12003	12025	12026	12035	12036	12037
	11910	11910	12058	12064	12084	12101	12104	12122	12124	12144	12145	12154	12155	12156	12166
	12010	12010	12183	12202	12219	12222	12240	12242	12252	12263	12272	12273	12274	12284	12295
	12110	12110	12301	12338	12341	12359	12361	12381	12382	12391	12392	12393	12403	12414	12417
	12210	12210	12440	12457	12478	12480	12500	12501	12510	12511	12512	12522	12533	12536	12539
	12310	12310	12575	12596	12598	12618	12619	12629	12630	12631	12641	12653	12656	12659	12679
	12410	12410	12700	12721	12745	12746	12750	12751	12752	12753	12757	12767	12773	12779	12783
	12510	12510	12793	12797	12800	12806	12809	12810	12814	12816	12822	12824	12825	12828	12839
	12610	12610	12841	12864	12866	12870	12874	12880	12885	12891	12897	13051	13057	13064	13068
	12710	12710	13072	13079	13183	13383	13389	13400	13406	13412	13416	13422	13428	13435	13447
	12810	12810	13450	13455	13462	13464	13475	13478	13482	13484	13491	13495	13500	13501	13505
	12910	12910	13509	13512	13579	13599	13649	13680	13689	13720	13721	13725	13736	13749	13755
	13010	13010	13761	13763	13779	13780	13786	13793	13799	13843	13846	13849	13858	13870	13871
	13110	13110	13872	13883	13884	13909	13917	13925	13933	13941	13949	13957	13965	13973	13989
	13210	13210	13997	14005	14013	14021	14029	14037	14045	14053	14116	14199	14202	14221	14223
	13310	13310	14224	14225	14226	14227	14228	14229	14230	14231	14238	14247	14248	14254	14271
	13410	13410	14273	14280	1287	1302	1303	1332	1333	1334	1335	1336	1337	1338	1341
.EQUIV	1278	1279	1360	1361	1362	1363	1364	1365	1366	1367	1368	1369	1370	1371	1372
.EVEN	3157	3222	3463	3471	3479	3487	3495	3503	3510	3516	3522	3526	3539	3543	3555
	3621	3628	3635	3642	3650	3656	12757	12767	12773	12779	12783	12846	12852	12870	12874
	12880	12885	12891	12897	13051	13057	13064	13068	13072	13079	13083	13093	13099	13400	13406
	13416	13416	13422	13428	13435	13655	13909	13917	13925	13933	13941	13949	13957	13965	13973
.IF	13981	13989	13997	14005	14013	14021	14029	14037	14045	14053	14116	14199	14202	14221	14223
	1252	1258	1269	1270	1271	1272	1276	1342	1370	1394	1421	1432	1443	1444	1448

.15F	13845	13848	13858	13861	13868	13870	13871	13873	13876	13883	13884	13308	13916	13924	13932
	13845	13948	13956	13964	13972	13980	13988	13996	14004	14012	14020	14028	14036	14044	14052
	14198	14198	14198	14202	14213	14222	14223	14224	14225	14226	14227	14229	14229	14230	14231
	14237	14247	14248	14253	14260	14261	14269	14271	14273	14277					
	12700	12700	12700	12702	12708	1445	1449	1452	1454	1483	1504	2538	2544	2596	2598
	3701	3701	3701	3704	3707	3208	3209	3573	3574	3577	3578	3579	3585	3686	3700
	4016	4016	4082	4083	4097	4098	4167	4168	4182	4183	4356	4357	4371	4372	4527
	4528	4547	4548	4799	4800	4815	4816	5191	5192	5209	5210	5510	5611	5629	5530
	6033	6033	6051	6052	6453	6454	6472	6473	6891	6892	6909	6910	7168	7169	7181
	7182	7343	7343	7355	7357	7462	7463	7476	7477	7592	7583	7596	7597	7702	7703
	7717	7718	7798	7799	7821	7822	7954	7955	7966	7967	8190	8191	8202	8203	8426
	8427	8439	8440	8728	8729	8741	8742	9031	9032	9046	9047	9264	9265	9279	9280
	9497	9498	9512	9513	9730	9731	9745	9746	9964	9965	9974	9975	10077	10078	10087
	10088	10191	10192	10201	10202	10304	10305	10314	10315	10417	10418	10428	10429	10536	10537
	10546	10547	10651	10652	10661	10662	10764	10765	10774	10775	10878	10879	10888	10889	10991
	10992	11001	11002	11104	11105	11114	11115	11218	11219	11229	11230	11338	11339	11348	11349
	11451	11452	11461	11462	11564	11565	11574	11575	11678	11679	11688	11689	11792	11793	11802
	11803	11905	11906	11916	11917	12025	12026	12036	12037	12144	12145	12155	12156	12262	12263
	12273	12274	12381	12382	12392	12393	12500	12501	12511	12512	12618	12619	12630	12631	12745
	12746	12751	12752	12753	12793	12796	12800	12806	12809	12824	12839	12841	12864	12866	13447
	13475	13478	13479	13482	13508	13512	13579	13649	13721	13725	13728	13736	13748	13749	13757
	13762	13778	13793	13846	13848	13861	13883	13884	14116	14193	14199	14238	14254	14271	
.15F	3157	3222	3226	3237	3243	3249	3391	3397	3403	3409	3424	3431	3439	3447	3455
	3463	3471	3479	3487	3495	3503	3510	3516	3522	3526	3589	3593	3600	3604	3612
	3621	3628	3635	3642	3650	3656	12757	12767	12773	12779	12783	12846	12852	12870	12874
	12880	12885	12891	12897	13051	13057	13064	13068	13072	13079	13083	13383	13389	13400	13406
	13412	13416	13422	13428	13435	13488	13721	13725	13728	13812	13820	13842	13870	13909	13917
	13923	13941	13949	13957	13965	13973	13981	13989	13997	14005	14013	14021	14029	14037	14045
.15TF	3157	3222	3226	3237	3243	3249	3391	3397	3403	3409	3424	3431	3439	3447	3455
	3463	3471	3479	3487	3495	3503	3510	3516	3522	3526	3589	3593	3600	3604	3612
	3621	3628	3635	3642	3650	3656	12757	12767	12773	12779	12783	12846	12852	12870	12874
	12880	12885	12891	12897	13051	13057	13064	13068	13072	13079	13083	13383	13389	13400	13406
	13412	13416	13422	13428	13435	13488	13721	13725	13728	13812	13820	13842	13870	13909	13917
	13923	13941	13949	13957	13965	13973	13981	13989	13997	14005	14013	14021	14029	14037	14045
.15F	1251	1256	1260	1265	1266	1267	1268	1271	1272	1273	1274	1391	1503	3109	3112
	3118	3119	3121	3122	3710	3723	3750	3790	3802	3828	3868	3881	3908	3948	3950
	4026	4039	4066	4107	4120	4147	4192	4204	4205	4214	4227	4239	4240	4245	4245
	4259	4265	4277	4278	4283	4295	4300	4312	4313	4318	4330	4331	4336	4348	4348
	4382	4394	4395	4404	4417	4429	4434	4447	4448	4454	4466	4467	4472	4484	4484
	4485	4490	4502	4503	4508	4520	4521	4557	4570	4571	4580	4593	4606	4607	4616
	4628	4640	4645	4658	4659	4668	4681	4686	4700	4701	4707	4720	4721	4726	4739
	4744	4757	4762	4775	4780	4793	4825	4837	4838	4853	4865	4879	4880	4890	4905
	4912	4927	4934	4949	4956	4971	4978	4993	5000	5015	5022	5037	5044	5059	5066
	5082	5083	5089	5104	5105	5110	5125	5130	5145	5150	5165	5170	5185	5220	5223
	5234	5248	5261	5276	5277	5287	5303	5311	5327	5335	5351	5359	5375	5393	5399
	5407	5423	5431	5447	5455	5471	5478	5495	5496	5502	5518	5519	5524	5540	5545
	5561	5566	5582	5587	5603	5640	5653	5654	5670	5683	5699	5700	5710	5726	5724
	5750	5758	5774	5782	5798	5806	5822	5830	5846	5854	5870	5878	5894	5901	5918
	5919	5925	5941	5942	5947	5963	5968	5984	5989	6005	6010	6026	6062	6075	6076
	6099	6105	6120	6121	6131	6147	6155	6171	6179	6195	6203	6219	6222	6243	6251
	6267	6275	6291	6299	6315	6322	6339	6340	6346	6362	6363	6368	6384	6389	6405
	6410	6426	6431	6447	6483	6496	6497	6515	6528	6544	6545	6555	6572	6590	6597
	6600	6622	6630	6647	6655	6672	6680	6697	6705	6722	6730	6747	6754	6772	6783

67997	67997	67997	68002	6819	6824	6841	6846	6863	6869	6885	6920	6931	6942	6954
7006	7006	7006	7007	7006	7007	7019	7038	7039	7044	7064	7065	7071	7090	7095
7143	7143	7143	7202	7162	7202	7216	7217	7223	7237	7238	7243	7258	7259	7255
7303	7303	7303	7322	7317	7322	7336	7371	7383	7384	7391	7404	7405	7415	7431
7491	7491	7491	7504	7503	7504	7511	7524	7525	7535	7551	7558	7575	7576	7611
7644	7644	7644	7655	7645	7655	7671	7678	7695	7696	7739	7752	7753	7758	7772
7840	7840	7840	7854	7853	7854	7859	7873	7874	7880	7893	7898	7911	7916	7929
7958	7958	7958	7978	7961	7978	7979	7981	7992	7993	7994	8002	8016	8017	8021
8069	8069	8069	8070	8069	8070	8075	8093	8094	8100	8117	8122	8139	8144	8161
8197	8197	8197	8214	8197	8214	8215	8217	8228	8229	8230	8238	8252	8253	8257
8305	8305	8305	8306	8305	8306	8311	8329	8330	8336	8353	8358	8375	8380	8397
8431	8431	8431	8433	8431	8433	8451	8453	8455	8466	8468	8469	8472	8477	8491
8514	8514	8514	8518	8514	8518	8522	8536	8537	8542	8557	8558	8564	8578	8579
8621	8621	8621	8632	8621	8632	8638	8655	8660	8677	8682	8699	8704	8721	8731
8757	8757	8757	8757	8757	8757	8768	8770	8771	8774	8779	8793	8794	8798	8800
8844	8844	8844	8849	8844	8849	8844	8859	8860	8866	8880	8881	8892	8909	8910
8979	8979	8979	8982	8979	8982	8979	8984	9001	9006	9023	9074	9076	9091	9092
9144	9144	9144	9143	9144	9143	9144	9149	9167	9168	9174	9191	9196	9213	9218
9325	9325	9325	9324	9325	9324	9325	9329	9332	9346	9347	9359	9376	9377	9382
9451	9451	9451	9446	9451	9446	9451	9469	9473	9490	9540	9542	9557	9558	9565
9633	9633	9633	9615	9633	9615	9633	9634	9640	9657	9662	9679	9684	9701	9705
9812	9812	9812	9798	9812	9798	9812	9813	9825	9842	9843	9848	9866	9867	9873
9956	9956	9956	9939	9956	9939	9956	9969	9972	9991	9992	9996	9998	10009	10011
10048	10048	10048	10050	10048	10050	10048	10050	10051	10055	10057	10068	10070	10081	10085
10125	10125	10125	10128	10125	10128	10125	10128	10131	10142	10144	10145	10148	10150	10162
10195	10195	10195	10199	10195	10199	10195	10199	10218	10219	10223	10225	10236	10238	10244
10277	10277	10277	10278	10277	10278	10277	10278	10282	10284	10295	10297	10308	10312	10331
10368	10368	10368	10370	10368	10370	10368	10370	10371	10374	10376	10388	10390	10391	10395
10446	10446	10446	10451	10446	10451	10446	10451	10453	10464	10467	10473	10484	10487	10489
10513	10513	10513	10526	10513	10526	10513	10526	10529	10540	10544	10563	10564	10568	10570
10601	10601	10601	10604	10601	10604	10601	10604	10607	10609	10621	10623	10624	10628	10630
10679	10679	10679	10685	10679	10685	10679	10685	10696	10698	10704	10715	10717	10718	10721
10744	10744	10744	10757	10744	10757	10744	10757	10768	10772	10791	10792	10796	10798	10809
10831	10831	10831	10835	10831	10835	10831	10835	10837	10849	10851	10852	10856	10858	10869
10910	10910	10910	10923	10910	10923	10910	10923	10925	10931	10942	10944	10945	10948	10950
10982	10982	10982	10995	10982	10995	10982	10995	10999	11018	11019	11023	11025	11036	11038
11075	11075	11075	11077	11075	11077	11075	11077	11078	11082	11084	11095	11097	11108	11112
11151	11151	11151	11155	11151	11155	11151	11155	11158	11169	11171	11172	11175	11177	11189
11211	11211	11211	11227	11211	11227	11211	11227	11246	11247	11252	11254	11265	11268	11274
11306	11306	11306	11310	11306	11310	11306	11310	11314	11316	11327	11330	11342	11346	11365
11391	11391	11391	11404	11391	11404	11391	11404	11405	11408	11410	11422	11424	11425	11431
11479	11479	11479	11483	11479	11483	11479	11483	11496	11498	11504	11515	11517	11519	11521
11555	11555	11555	11557	11555	11557	11555	11557	11572	11591	11592	11596	11598	11609	11611
11631	11631	11631	11635	11631	11635	11631	11635	11649	11651	11652	11658	11669	11671	11682
11723	11723	11723	11725	11723	11725	11723	11725	11729	11732	11743	11745	11746	11749	11751
11783	11783	11783	11785	11783	11785	11783	11785	11800	11819	11820	11824	11826	11837	11845
11876	11876	11876	11878	11876	11878	11876	11878	11895	11896	11898	11909	11914	11933	11934
11975	11975	11975	11975	11975	11975	11975	11975	11979	11981	11993	11996	11997	12003	12014
12062	12062	12062	12064	12062	12064	12062	12064	12077	12078	12081	12084	12095	12097	12098
12122	12122	12122	12124	12122	12124	12122	12124	12137	12148	12152	12176	12177	12181	12188
12216	12216	12216	12219	12216	12219	12216	12219	12221	12235	12236	12240	12242	12253	12258
12301	12301	12301	12301	12301	12301	12301	12301	12315	12318	12321	12332	12334	12335	12338
12361	12361	12361	12372	12361	12372	12361	12372	12385	12389	12413	12414	12418	12420	12421
12451	12451	12451	12454	12451	12454	12451	12454	12459	12471	12473	12474	12478	12480	12491
12533	12533	12533	12537	12533	12537	12533	12537	12552	12558	12569	12571	12572	12575	12577
12609	12609	12609	12611	12609	12611	12609	12611	12627	12651	12652	12657	12659	12670	12673

	12679	12690	12693	12694	12697	12699	12711	12714	12715	12719	12721	12732	12735	12794	12800
	12801	12812	12824	12828	13131	13140	13141	13154	13183	13192	13193	13208	13248	13258	13270
	13286	13307	13317	13329	13354	13365	13450	13451	13452	13453	13454	13455	13459	13489	13490
	13505	13508	13509	13646	13649	13655	13690	13771	13779	13786	13843	13849	13850	13851	13852
.IRP	13853	13858	13876	13883	13884	14221	14222	14223	14224	14225	14228	14229	14230	14231	
	3102	3182	3572	3684	3765	3842	3923	4000	4081	4166	4355	4526	4798	5190	5609
	6031	6452	6890	7167	7341	7461	7581	7701	7797	7953	8189	8425	8727	9030	9253
	9496	9729	9963	10076	10190	10303	10416	10535	10650	10763	10877	10990	11103	11217	11337
	11450	11563	11677	11791	11904	12024	12143	12261	12380	12499	12617	12744	12970	13019	13102
	13111	13459	13522	13562	13806	13832	14241	14247	14260	14261					
.LIST	1	1251	1261	1271	1384	1391	1483	1485	1486	1487	1488	1489	1490	1491	1492
	1493	1494	1495	1496	1497	3102	3123	3139	3157	3182	3208	3222	3226	3237	3243
	3249	3391	3397	3403	3409	3424	3431	3439	3447	3455	3463	3471	3479	3487	3495
	3503	3510	3516	3522	3526	3572	3578	3589	3593	3600	3604	3612	3621	3628	3635
	3642	3650	3656	3684	3701	3765	3781	3842	3859	3923	3939	4000	4017	4081	4098
	4166	4183	4355	4372	4526	4548	4798	4816	5190	5210	5609	5630	6031	6052	6452
	6473	6890	6910	7167	7182	7341	7357	7461	7477	7581	7597	7701	7718	7797	7822
	7953	7967	8189	8203	8425	8440	8727	8742	9030	9047	9263	9280	9496	9513	9729
	9746	9963	9975	10076	10088	10190	10202	10303	10315	10416	10429	10535	10547	10650	10662
	10763	10775	10877	10889	10990	11002	11103	11115	11217	11230	11337	11349	11450	11462	11563
	11575	11677	11689	11791	11803	11904	11917	12024	12037	12143	12156	12261	12274	12380	12393
	12499	12512	12617	12631	12744	12752	12757	12767	12773	12779	12783	12800	12816	12846	12852
	12870	12874	12880	12885	12891	12897	13051	13057	13064	13068	13072	13079	13093	13083	13099
	13400	13406	13412	13416	13422	13428	13435	13454	13748	13883	13909	13917	13925	13933	13941
	13949	13957	13965	13973	13981	13989	13997	14005	14013	14021	14029	14037	14045	14053	14213
	14221	14222	14223	14224	14225	14226	14228	14229	14230	14231	14232				
.MACRO	1	1261	1272	1446	3181	3572	3684	3765	3842	3923	4000	4081	4165	4190	4224
	4354	4380	4414	4525	4555	4591	4626	4664	4797	4823	4963	4985	4886	4909	4931
	4953	4975	4997	5019	5041	5063	5189	5217	5258	5282	5283	5307	5331	5355	5379
	5403	5427	5451	5475	5608	5637	5680	5705	5706	5730	5754	5778	5802	5826	5850
	5874	5898	6030	6059	6102	6126	6127	6151	6175	6199	6223	6247	6271	6295	6319
	6451	6480	6525	6550	6551	6576	6601	6626	6651	6676	6701	6726	6751	6889	6917
	6939	6984	6985	7016	7166	7199	7340	7367	7368	7388	7412	7435	7460	7487	7488
	7508	7532	7555	7580	7607	7608	7628	7652	7675	7700	7736	7796	7837	7952	7978
	7979	7999	8000	8049	8188	8214	8215	8235	8236	8285	8424	8452	8453	8474	8475
	8587	8725	8754	8755	8776	8777	8889	9029	9071	9072	9123	9262	9304	9305	9356
	9495	9537	9538	9589	9728	9770	9771	9822	9962	9995	10075	10108	10189	10222	10302
	10335	10415	10450	10534	10567	10649	10692	10762	10795	10876	10909	10989	11022	11102	11135
	11216	11251	11336	11369	11449	11482	11562	11595	11676	11709	11790	11823	11903	11938	12023
	12061	12142	12180	12260	12298	12379	12417	12498	12536	12616	12656	12743	13128	13176	14213
.MCALL	1251	1384	3123												
.NLIST	1	1251	1261	1271	1384	1391	1483	1485	1486	1487	1488	1489	1490	1491	1492
	1493	1494	1495	1496	1497	3102	3123	3139	3157	3182	3208	3222	3226	3237	3243
	3249	3391	3397	3403	3409	3424	3431	3439	3447	3455	3463	3471	3479	3487	3495
	3503	3510	3516	3522	3526	3572	3578	3589	3593	3600	3604	3612	3621	3628	3635
	3642	3650	3656	3684	3701	3765	3781	3842	3859	3923	3939	4000	4017	4081	4098
	4166	4183	4355	4372	4526	4548	4798	4816	5190	5210	5609	5630	6031	6052	6452
	6473	6890	6910	7167	7182	7341	7357	7461	7477	7581	7597	7701	7718	7797	7822
	7953	7967	8189	8203	8425	8440	8727	8742	9030	9047	9263	9280	9496	9513	9729
	9746	9963	9975	10076	10088	10190	10202	10303	10315	10416	10429	10535	10547	10650	10662
	10763	10775	10877	10889	10990	11002	11103	11115	11217	11230	11337	11349	11450	11462	11563
	11575	11677	11689	11791	11803	11904	11917	12024	12037	12143	12156	12261	12274	12380	12393
	12499	12512	12617	12631	12744	12752	12757	12767	12773	12779	12783	12800	12816	12846	12852
	12870	12874	12880	12885	12891	12897	13051	13057	13064	13068	13072	13079	13093	13083	13099
	13400	13406	13412	13416	13422	13428	13435	13454	13748	13883	13909	13917	13925	13933	13941
	13949	13957	13965	13973	13981	13989	13997	14005	14013	14021	14029	14037	14045	14053	14213

.PAGE	14221 1446 13943	14222 1504 14190	14223 2536 14235	14224 2799 14280	14225 2842	14226 3096	14228 12790	14229 12913	14230 12950	14231 13022	14232 13115	13375	13431	13443	13509
.REM	1														
.REPT	1391	1485	1491												
.SBTTL	1261 3923 7581 10416 12143 13790	1274 4000 7701 10535 12261 13843	1385 4091 7797 10650 12380 13895	1394 4166 7953 10763 12499 14113	1409 4355 8189 10977 12617 14190	1446 4526 8425 10990 12744 14213	1504 4798 8727 11103 12790 14235	2841 5190 9030 11217 12831	3096 5609 9263 11337 13116	3102 6031 9496 11450 13165	3182 6452 9729 11563 13219	3572 6890 9963 11677 13444	3684 7167 10076 11791 13509	3765 7341 10190 11904 13576	3842 7461 10303 12024 13646
.TITLE	1251														
.WORD	1391 1472 1496 14092 15806	1392 1473 2914 14087 15808	1393 1474 3031 14189	1454 1483 3032 14220	1457 1485 3033 14270	1458 1486 3064 14272	1459 1487 12805 15789	1460 1488 12808 15790	1463 1489 12823 15792	1464 1490 13643 15794	1465 1491 13650 15796	1466 1492 13651 15798	1467 1493 13652 15800	1468 1494 13839 15802	1469 1495 13842 15804

ERRORS DETECTED: 0
DEFAULT GLOBALS GENERATED: 0

*.DERHAB.SEQ/SOL/CRF/PAGNUM/NL:TOC=DERHAB.SML,DERHAB.SRC
RUN-TIME: 106 158 18 SECONDS
RUN-TIME RATIO: 1222/283=4.3
CORE USED: 38K (75 PAGES)

