

# KE11-F

EXERCISER  
MD-11-DBKEB-A

EP-DBKEB-A-DL-A  
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FICHE 1 OF 1

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**digital**  
MADE IN USA

The microfiche card contains a grid of 60 frames of data, arranged in 10 rows and 6 columns. Each frame displays a table of information, possibly a schedule or a list of items. The data is organized in columns and rows, with some frames showing headers and others showing data entries. The overall appearance is that of a structured data set presented in a compact, grid-like format.



B01

IDENTIFICATION

PRODUCT CODE: MAINDEC-11-DSKEB-A-D  
PRODUCT NAME: KE11F (PDP-11 FIS) EXERCISER  
DATE CREATED: 1-AUG-72  
MAINTAINER: DIAGNOSTIC GROUP  
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CONTENTS

1. ABSTRACT
2. REQUIREMENTS
  - 2.1 Equipment
  - 2.2 Storage
  - 2.3 Preliminary programs
3. LOADING PROCEDURE
4. STARTING PROCEDURE
  - 4.1 Control switch settings
  - 4.2 Starting address
  - 4.3 Program and/or operator action
5. OPERATING PROCEDURE
  - 5.1 Operational switch settings
  - 5.2 Subroutine abstracts
6. ERRORS
  - 6.1 Error printout
  - 6.2 Error recovery
  - 6.3 Error counter
7. RESTRICTIONS
8. MISCELLANEOUS
  - 8.1 Execution time
  - 8.2 Stack pointer
  - 8.3 Pass counter
  - 8.4 Power fail
9. PROGRAM DESCRIPTION

1. ABSTRACT

This program exercises the KE11F floating point instructions (FADD, FSUB, FMUL, FDIV) with random number patterns. The answers are checked against results obtained using the corresponding FORTRAN software routines. About 200 passes should be run to establish credibility.

2. REQUIREMENTS

2.1 Equipment

PDP-11 (KD11A) standard computer with KE11F option

2.2 Storage

The routines use memory locations 0 - 17500. The map at the end of the listings shows the absolute locations of the FORTRAN math routines which were assembled separately and linked to the main program via LNKX11 on a DECsystem-10.

2.3 Preliminary programs

MAINDEC-11-DBKEA-A KE11F Instruction Tests.

3. LOADING PROCEDURE

Use standard procedure for ABS tapes.

4. STARTING PROCEDURE

4.1 Control switch settings

See 5.1.1 (all down for worst case testing)

4.2 Starting address

The program should always be started at 200.

4.3 Program and/or operator action

- 1) Load program into memory using ABS loader.
- 2) Load address 200.
- 3) Set switches (see 5.1.1) All down for worst case.
- 4) Press start.

5) The program will loop and bell will ring once every pass.

## 5. OPERATING PROCEDURE

### 5.1 Operational switch settings

SW<15> = 1 ..... HALT ON ERROR  
SW<14> = 1 ..... SCOPE LOOP  
SW<13> = 1 ..... INHIBIT PRINTOUT  
SW<12> = 1 ..... INHIBIT TRACE TRAPPING  
SW<11> = 1 ..... INHIBIT ITERATIONS OF SUBTEST  
SW<10> = 1 ..... BELL ON ERROR  
          0 ..... BELL ON PASS COMPLETE  
SW<09> = 1 ..... LOOP ON ERROR  
SW<08> = 1 ..... LOOP ON TEST IN SW<6:0>  
SW<07> = 1 ..... INPUT DATA FROM THE TELETYPE

Caution: SW<8:0> are also used for ROM word match with KM11 maintenance card.

### 5.2 Subroutine Abstracts

#### 5.2.1 TYPIN

If SW<7> is on a 0, the program calculates a pseudo-random number to be used as input data. If SW<7> is on a 1, the program will ask for input data from the teletype at the beginning of each pass. The same data is used with all instructions (FADD, FSUB, FMUL, FDIV) for the entire pass. If SW<7> is put down after entering the data entry routine, that data is used as the starting numbers for the random number generator.

The input format is:

Type input data:  
A1: NNNNN  
A2: NNNNN  
B1: NNNNN  
B2: NNNNN

Where:

A1 = left word of first argument  
A2 = right word of first argument  
B1 = left word of second argument  
B2 = right word of second argument

i.e. A1,A2(+,-,\*,/)B1,B2 = answer

NNNNN = data typed by the operator

A1, A2, B1, and B2 must be 16 bit left justified octal numbers.

E.G.

42 = 000042  
200000 = not accepted (17 bits)  
4812 = not accepted (8 is not octal)

They are assumed to be in floating point format. I.E. bit 15 of A1 and B1 are the sign bits, bits 7-14 of A1 and B1 are the exponents (excess 128 format) and the rest (bits 0-6 of A1 and B1 and all of A2 and B2) form the mantissa (normalized) less the hidden bit. For more information read the maintenance manual. A1, A2, B1, and B2 are put into RAND.RAND.B, RAND.C, and RAND.D respectively.

### 5.2.2 FORTAN

This routine make use of "polish mode" to link the FORTRAN MATH PACKAGE ROUTINES TO CALCULATE THE EXPECTED RESULT.

LOCATIONS SADD1, SADD2 contain addition answer.  
Locations SSUB1, SSUB2 contain subtract answer.  
Locations SMUL1, SMUL2 contain multiply answer.  
Locations SDIV1, SDIV2 contain divide answer.

If a floating error occurs (overflow, underflow, or divide by zero), these answers are meaningless. The locations SADDPS, SSUBPS, SMULPS, or SDIVPS contains 340 and SADDR, SSUBER, SMULER, or SDIVER, contain the conditions codes of the error.

### 5.2.3 SCOPE

This subroutine call is placed between each subtest in the test section. It records the starting address of each subtest as it is being entered in location "LADS". If a scope loop is requested, the current subtest will be looped upon. SW<11> on a 1 inhibits iteration of subtests. The contents of LADS may be used to determine the last subtest successfully completed.

### 5.2.4 HLT

This routine prints out an error message (See 6.1). To inhibit typeouts, put SW<13> on a 1.

5.2.5 TRTRAP

If SW<12> is on a 0, the T-bit will be set on alternate passes. When the T-bit is set, the processor traps after each instruction. The first instruction executed upon trapping is an "RTT" which returns to the interrupted sequence of instructions. This sequence is continued until the end of the program is reached.

5.2.6 TRAPCATCHER

A ".+2" - "HALT" sequence is repeated from 0 - 776 to catch any unexpected traps. Thus any unexpected traps or interrupts will HALT at the vector + 2.

5.2.7 FLOATING POINT TRAP (to 244)

All tests set the floating point trap vector (244) to point to the instruction following the floating point instruction. Thus, whether or not a trap occurs is only detected if the data or the stack pointer(s) are wrong.

6. ERRORS

6.1 Error printout

There are two formats for error typeout; one for normal numbers and one for floating errors (overflow, underflow and divide by zero).

6.1.1 The normal format (when no floating point error is indicated) is as follows:

```

AAAAAA  NNNNNN,NNNNNN S  NNNNNN,NNNNNN
          PSW   SP   ANSWER
EXPECT:  NNN  NNN  NNNNNN,NNNNNN
GOT:     NNN  NNN  NNNNNN,NNNNNN

```

Where:  
 AAAAAA ==> PC of HLT instruction  
 NNNNNN ==> input data (RAND.A, RAND.B, RAND.C, RAND.D)  
 S ==> type of operation being tested (+, -, \*, or /)  
 NNNNNN ==> results  
     PSW = processor status word  
     SP = stack pointer (not necessarily R6)  
     ANSWER = resulting answer off the stack

6.1.2 When a floating point error is indicated (overflow,

underflow, or divide by zero) the format is as follows:

```
AAAAAA MMMMM,MMMMM S MMMMM,MMMMM  
      PSW  SP  ANS1  ANS2  ANS3  ANS4  ANS5  ANS6  
EXPECT: NNN  NNN  NNNNNN NNNNNN NNNNNN NNNNNN NNNNNN NNNNNN  
GOT:    NNN  NNN  NNNNNN NNNNNN NNNNNN NNNNNN NNNNNN NNNNNN
```

Where:

```
AAAAAA ==> PC of HLT instruction  
MMMMM  ==> input data (RAND.A, RAND.B, RAND.C, RAND.D)  
S      ==> type of operation being tested (+, -, *, or /)  
NNNNN  ==> results  
      PSW = processor status word  
      SP = stack pointer (not necessarily R6)  
      ANS1 = PC of interrupted instruction (should be  
            FIS)  
      ANS2 = PSW at interrupt time  
      ANS3 = input data (RAND.C)  
      ANS4 = " " (RAND.D)  
      ANS5 = " " (RAND.A)  
      ANS6 = " " (RAND.B)
```

To find the failing test, look at the listing above the address typed.

6.2 Error recovery  
Restart at 200

6.3 Error count  
An error count is kept in "ERRORS" (LOC 1002). It is cleared by restarting at 200.

7. RESTRICTIONS  
None

8. MISCELLANEOUS

8.1 Execution time  
A bell will ring within 5 seconds with all switches down. More than 200 passes should be run to insure a wide variety of number patterns.

8.2 Stack Pointer



MAINDEC-11-DBKEB-A-D, KE11F (PDP-11 FIS) EXERCISER  
Description

Stack is initially set to 604

8.3 Pass counter

A 32 bit (2 words) pass count is kept in "PCNT" (LOC 1004,1006). It is cleared by restarting at 200.

8.4 Power Fail

Each test can be power failed with no errors. To use, start the test as usual and power down then up at any time. The program should type "POWER" and continue to run from where power fail interrupts

9. PROGRAM DESCRIPTION

This program tests all the FIS instructions on the KE11F using all registers except 7 for the "stack pointer". The program has many subtests (the code between 2 SCOPE statements) which are run 256 times before continuing to the next. SW<11> on a 1 causes each subtest to be run only once. The address ICNT (LOC 1000) contains the iteration count in the left byte and the test number in the right byte. All the subtests should be run sequentially by starting at 200 not by starting at the beginning of the subtest. To loop on a particular subtest, put the test number (see listing) in SW<6:0> of the switch register and SW<8> on a 1. This test will be looped upon until SW<8> is put on a 0 or the right byte is changed. If the test is non-existent, the program will be run as usual.

The FORTRAN math routines, which are used to calculate the correct answers, were to PDP-11 FORTRAN package and assembled as separate modules. They were linked to the main programs via LNK11 on a DECsystem-10 which produces a binary tape in the normal absolute format. Thus, the program loads and runs just like any other diagnostic program.

458  
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468  
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472  
473  
474  
475

000000

.TITLE MAINDEC-11-DBKEB-A KE11F (PDP-11 FIS) EXERCISER.  
.ASECT  
.GLOBL SADR,SSBR,SMLR,SDVR,SERR,SERRA

;COPYRIGHT 1972, DIGITAL EQUIPMENT CORP., MAYNARD, MASS  
;PROGRAM BY KEN CHAPMAN  
.REM!

SWITCH	USE
7	TTY DATA INPUT
8	LOOP ON TEST IN SW<6:0>
9	LOOP ON ERROR
10	0-BELL ON PASS COMPLETED
	1-BELL ON ERROR
11	INHIBIT ITERATIONS
12	INHIBIT TRACE TRAP
13	INHIBIT ERROR TYPEOUTS

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497

14  
15

LOI  
LOCATION TEST  
HALT ON ERROR

ERROR MESSAGE FORMATS:

1. WHEN NO FLOATING POINT ERROR IS INDICATED

AAAAAA MMMMM,MMMMM S MMMMM,MMMMM

          PSW  SP      ANSWER

EXPECT:  NNN  NNN  NNNNNN,NNNNNN

GOT:      NNN  NNN  NNNNNN,NNNNNN

WHERE:

AAAAAA ==> PC OF HLT INSTRUCTION

MMMMMM ==> INPUT DATA (RAND.A, RAND.B, RAND.C, RAND.D)

S       ==> TYPE OF OPERATION BEING TESTED (+, -, \*, OR /)

NNN     ==> RESULTS

      PSW = PROCESSOR STATUS WORD

      SP = STACK POINTER (NOT NECESSARILY R6)

      ANSWER = RESULTING ANSWER OFF THE STACK

2. WHEN A FLOATING POINT ERROR IS INDICATED (OVERFLOW, UNDERFLOW,  
OR DIVIDE BY ZERO):

498  
499  
500  
501  
502  
503  
504  
505  
506  
507  
508  
509  
510

AAAAAA	MMMMMM	MMMMMM	S	MMMMMM	MMMMMM					
	PSW	SP	ANS1	ANS2	ANS3	ANS4	ANS5	ANS6		
EXPECT:	NNN	NNN	NNNNNN	NNNNNN	NNNNNN	NNNNNN	NNNNNN	NNNNNN	NNNNNN	NNNNNN
GOT:	NNN	NNN	NNNNNN	NNNNNN	NNNNNN	NNNNNN	NNNNNN	NNNNNN	NNNNNN	NNNNNN

WHERE:  
 AAAAAA, MMMMMM, S, NNN, PSW, AND SP ARE THE SAME AS ABOVE.  
 ANS1 = PC OF INTERRUPTED INSTRUCTION (SHOULD BE FIS)  
 ANS2 = PSW AT INTERRUPT TIME  
 ANS3 = INPUT DATA (RAND.C)  
 ANS4 = " " (RAND.D)  
 ANS5 = " " (RAND.A)  
 ANS6 = " " (RAND.B)!

511	104400	SCOPE=	TRAP
512	104000	HLT=	EMT
513	000004	TYPE=	IOT
514	177776	PS=	177776
515	177570	SWR=	177570
516	177570	DISPLAY=	SWR
517	000007	BELL=	7
518	000000	R0=	%0
519	000001	R1=	%1
520	000002	R2=	%2
521	000003	R3=	%3
522	000004	R4=	%4
523	000005	R5=	%5
524	000005	TTY=	%5
525	000006	SP=	%6
526	000007	PC=	%7
527	100000	SW15=	100000
528	040000	SW14=	40000
529	020000	SW13=	20000
530	010000	SW12=	10000
531	004000	SW11=	4000
532	002000	SW10=	2000
533	001000	SW09=	1000
534	000400	SW08=	400
535	000001	BIT0 =	000001
536	000002	BIT1 =	000002
537	000004	BIT2 =	000004
538	000010	BIT3 =	000010
539	000020	BIT4 =	000020
540	000040	BIT5 =	000040
541	000100	BIT6 =	000100
542	000200	BIT7 =	000200
543	000400	BIT8 =	000400
544	001000	BIT9 =	001000
545	002000	BIT10 =	002000
546	004000	BIT11 =	004000
547	010000	BIT12 =	010000
548	020000	BIT13 =	020000
549	040000	BIT14 =	040000
550	100000	BIT15 =	100000
551	000000	LEVEL0 =	000
552	000040	LEVEL1 =	040
553	000100	LEVEL2 =	100
554	000140	LEVEL3 =	140
555	000200	LEVEL4 =	200
556	000240	LEVEL5 =	240
557	000300	LEVEL6 =	300
558	000340	LEVEL7 =	340

```

559
560      000000      .=      0      ;TRAP CATCHER FROM 0 - 776
561
562      000200      .=      200
563
564 000200 000167 000604      JMP      BEGIN      ;JUMP TO STARTING ADDRESS OF PROGRAM
565
566      000204      .=      204
567 000204 000167 000736      JMP      START      ;RESTART ADDRESS
568
569      000600      .=      600
570

```

```

;THE FOLLOWING LOCATIONS ARE USED FOR THE STACKS. R6 IS INITIALLY SET
;TO 604 (STACK0), AS ARE THE OTHER REGISTERS (R0 THRU R5) WHEN
;THEY ARE TO BE USED AS THE FLOATING POINT STACK POINTER.
;THE DATA IS PUT DIRECTLY ONTO THE STACK, NOT BY PUSHES.
;IF NO ERROR OCCURES THE STACK POINTER (ANY REGISTER) IS POINTING
;TO 610 (ANS1). IF AN ERROR OCCURES, R6 IS POINTING TO 604,
;SO THE TRAP PUTS THE RETURN ADDRESS AND PS IN 600 (STK1)
;AND 602 (STK2) RESPECTIVELY.

```

```

580 000600 000000      STK1:  0
581 000602 000000      STK2:  0
582 000604 000000      STK3:  STACK0: 0
583 000606 000000      STK4:  STACK2: 0
584 000610 000000      STK5:  STACK4: ANS1:  0
585 000612 000000      STK6:  STACK6: ANS2:  0
586 000614 000000      $PSW:  0
587 000616 000000      $SP:   0
588
589 000620 000000      RAND.A: 0
590 000622 000000      RAND.B: 0
591 000624 000000      RAND.C: 0
592 000626 000000      RAND.D: 0
593
594 000630 000000      $ADDPS: 0
595 000632 000000      $ADD1:  0
596 000634 000000      $ADD2:  0
597 000636 000000      $ADDER: 0
598
599 000640 000000      $SUBPS: 0
600 000642 000000      $SUB1:  0
601 000644 000000      $SUB2:  0
602 000646 000000      $SUBER: 0
603
604 000650 000000      $MULPS: 0
605 000652 000000      $MUL1:  0
606 000654 000000      $MUL2:  0
607 000656 000000      $MULER: 0
608
609 000660 000000      $DIVPS: 0
610 000662 000000      $DIV1:  0
611 000664 000000      $DIV2:  0
612 000666 000000      $DIVER: 0
613
614 000670 000000      $AVSTK: 0

```

```

615 000672 000000          RNDFLG: 0          ;FOR FLAGS TO KEEP TRACK OF ROUNDING
616
617
618 000674 105367 177726    RAND4$: DECB      RAND.D          ;INSURE ALL ZEROES WORKS
619 000700 066767 177716    ADD             RAND.B, RAND.A
620 000706 005567 177714    ADC            RAND.D
621 000712 066767 177706    ADD             RAND.C, RAND.B
622 000720 005567 177700    ADC            RAND.C
623 000724 066767 177676    ADD             RAND.D, RAND.C
624 000732 005567 177664    ADC            RAND.B
625 000736 066767 177656    ADD             RAND.A, RAND.D
626 000744 005567 177650    ADC            RAND.A
627 000750 000207          RTS            PC
628
629
630 000752 000006          YESRT: RTT      ;TRACE TRAP SERVICE ROUTINE
631
632 000754 104000          FISTRP: HLT     ;ERRONIOUS FIS TRAP
633 000756 000002          RTI
634

```

```

635
636          001000          . =      1000
637
638 001000 000000          ICNT:   0          ; ITERATION COUNT (HI BYTE); TEST # (LO BYTE)
639 001002 000000          ERRORS:  0          ; ERROR COUNT LOCATION
640 001004 000000 000000  PCNT:   0,0        ; PASS COUNT LOCATION
641
642 001010 012706 000604          BEGIN:  MOV    #STACKD,SP          ; SET UP STACK
643 001014 012737 000752 000014  MOV    #YESRT,2#14        ; SET UP TRACE TRAP
644 001022 012700 000020          MOV    #20,R0
645 001026 012720 015256          MOV    #.IOT,(R0)+        ; SET UP IOT VECTOR
646 001032 012720 000340          MOV    #340,(R0)+
647 001036 012720 015536          MOV    #PDOWNS,(R0)+      ; SET UP POWER FAIL VECTOR
648 001042 012720 000340          MOV    #340,(R0)+
649 001046 012720 014020          MOV    #HLTS,(R0)+        ; SET EMT VECTOR
650 001052 012720 000340          MOV    #340,(R0)+
651 001056 012720 013644          MOV    #SCOPES,(R0)+      ; SET TRAP VECTOR
652 001062 012720 000340          MOV    #340,(R0)+
653 001066 012737 000754 000244  MOV    #FISTRP,2#244      ; SET UP FIS VECTOR
654 001074 012737 000340 000246  MOV    #340,2#246
655 001102 012767 123456 177510  MOV    #123456,RAND.A      ; PRIME THE RANDOM NUMBER GENERATOR
656 001110 012767 107654 177504  MOV    #107654,RAND.B
657 001116 012767 070707 177500  MOV    #070707,RAND.C
658 001124 012767 125252 177474  MOV    #125252,RAND.D
659 001132 005067 177644          CLR    ERRORS              ; CLEAR ERROR COUNTER
660 001136 005067 177642          CLR    PCNT                ; CLEAR PASS COUNTER
661 001142 005067 177640          CLR    PCNT+2
662 001146 012706 000604          START: MOV    #STACKD,SP      ; SET UP STACK
663 001152 012737 000140 177776  MOV    #140,2#PS          ; SET UP PROCESSOR STATUS
664 001160 005067 177614          CLR    ICNT
665 001164 005067 012622          CLR    LADS
666 001170 005067 177476          CLR    RNDFLG              ; CLEAR THE ROUNDING FLAGS
667 001174 105737 177570          TSTB   2#SWR               ; CHECK FOR TTY INPUT
668 001200 100403
669 001202 004767 177466          BMI    TYPIN
670 001206 000464          JSR    PC,RAND4S
671          BR    FORTAN          ; BRANCH TO ROUTINE TO CALCULATE ANSWERS
672
673          ; THE FOLLOWING ROUTINE ACCEPTS DATA FROM THE TELETYPE.
674          ; THE FORMAT IS FIXED:  A1,A2 (+,-,*,/) B1,B2.
675          ; THE PROGRAM ASKES FOR ONE ARGUMENT AT A TIME, AND RE-ASKES
676          ; WHEN INVALID DATA IS ENTERED.
677 001210 000004 001214          TYPIN: TYPE    +2
678 001214 005015 054524 042520  .ASCIZ  <15><12>"TYPE INPUT DATA:"<15><12>
679 001222 044440 050116 052125
680 001230 042040 052101 035101
681 001236 005015          000
682          001242
683 001242 000004 001246          15:   .EVEN
684 001246 030501 020072 000040  .TYPE  +2
685 001254 004567 011502          .ASCIZ  "A1:  "
686 001260 000620          JSR    R5, READIN        ; ACCEPT FIRST ARGUMENT FROM THE TTY
687 001262 103752          RAND.A
688 001264 000004 001270          BCS   TYPIN
689 001270 031101 020072 000040  25:   .TYPE  +2
690 001276 004567 011460          .ASCIZ  "A2:  "
          JSR    R5, READIN        ; ACCEPT SECOND ARGUMENT FROM THE TTY

```

691	001302	000622				RAND.B			
692	001304	103767				BCS	2\$		
693	001306	001340				BNE	TYPIN		
694	001310	000004	001314		3\$:	TYPE	+2		
695	001314	030502	020072	000040		.ASCIZ	*B1: "		
696	001322	004567	011434			JSR	R5,	READIN	;ACCEPT THIRD ARGUMENT FROM THE TTY
697	001326	000624				RAND.C			
698	001330	103767				BCS	3\$		
699	001332	001326				BNE	TYPIN		
700	001334	000004	001340		4\$:	TYPE	+2		
701	001340	031102	020072	000040		.ASCIZ	*B2: "		
702	001346	004567	011410			JSR	R5,	READIN	;ACCEPT FOURTH ARGUMENT FROM THE TTY
703	001352	000626				RAND.D			
704	001354	103767				BCS	4\$		
705	001356	001314				BNE	TYPIN		
706									
707	001360	005067	177244		FORTAN:	CLR	\$ADDPS		;CLEAR ALL THE PS SAVE LOCATIONS
708	001364	005067	177250			CLR	\$SUBPS		
709	001370	005067	177254			CLR	\$MULPS		
710	001374	005067	177260			CLR	\$DIVPS		
711									
712	001400	004467	011460			JSR	%4,	SPOLSH	;ENTER POLISH MODE
713	001404	013044				SPUSH			;PUSH THE DATA ONTO THE STACK
714	001406	000000G				SADR			;FORTAN ADD ROUTINE
715	001410	013066				SPOPAD			;SAVE THE ADD ANSWERS
716	001412	013044				SPUSH			;PUSH THE DATA ONTO THE STACK
717	001414	000000G				\$SBR			;FORTRAN SUBTRACT ROUTINE
718	001416	013144				SPOPSB			;SAVE THE SUBTRACT ANSWERS
719	001420	013044				SPUSH			;PUSH THE DATA ONTO THE STACK
720	001422	000000G				\$MLR			;FORTRAN MULTIPLY ROUTINE
721	001424	013222				SPOPML			;SAVE THE MULTIPLY ANSWERS
722	001426	013044				SPUSH			;PUSH THE DATA ONTO THE STACK
723	001430	000000G				\$DVR			;FORTRAN DIVIDE ROUTINE
724	001432	013300				SPOPDV			;SAVE THE DIVIDE ANSWERS
725	001434	013412				SEXIT			;EXIT POLISH MODE
726									
727	001436	104400				SCOPE			



```

728
729
730
731
732
733
734
735 001440 012700 000604 TST1: MOV #STACK0,RO ;SET UP THE STACK POINTER
736 001444 004767 012130 JSR PC, PUSHR ;PUT THE DATA ON THE STACK
737
738 001450 000240 NOP
739 001452 075000 FADD+ RO ;FLOATING ADD ON THE RO STACK
740
741 001454 013767 177776 177132 1S: MOV #PS, SPSW ;SAVE PROCESSOR STATUS
742 001462 010067 177130 MOV RO, SSP ;SAVE THE STACK POINTER
743 001466 026767 177136 177120 6S: CMP $ADDP, SPSW ;CHECK THE PROCESSOR STATUS
744 001474 001023 BNE 4S ;GO CHECK FOR ROUNDING ERROR
745
746 001476 105767 177112 TSTB $PSW ;CHECK FOR ERROR
747 001502 100464 BMI 2S ;BRANCH IF ERROR
748
749 001504 012767 000610 177156 MOV #STACK4, SAVSTK ;SAVE PROPER STACK ADDRESS FOR TYPING
750 001512 026767 177152 177076 CMP SAVSTK, SSP ;CHECK THE STACK POINTER
751 001520 001401 BEQ .+4 ;BRANCH IF OK
752 001522 104000 HLT ;STACK POINTER NOT EQUAL TO #STACK4
753
754 001524 026767 177102 177056 CMP $ADD1, ANS1 ;CHECK THE ANSWER
755 001532 001004 BNE 4S
756 001534 026767 177074 177050 CMP $ADD2, ANS2 ;CHECK THE ANSWER
757 001542 001515 BEQ 3S
758 001544 032767 000002 177120 4S: BIT #BIT1, RNDFLG ;CHECK THE ROUNDING FLAG
759 001552 001022 BNE 5S
760 001554 052767 000002 177110 BIS #BIT1, RNDFLG ;SET ROUNDING FLAG
761 001562 062767 000001 177044 ADD #1, $ADD2 ;INCREMENT FORTRAN ANSWER
762 001570 005567 177036 ADC $ADD1 ;ADD CARRY
763 001574 102334 BVC 6S ;BRANCH IF NO OVERFLOW
764 001576 000257 CCC ;CLEAR ALL CONDITION CODES
765 001600 000262 SEV ;SET V-BIT
766 001602 013767 177776 177026 MOV #PS, $ADDER ;SET UP PSM FOR OVERFLOW
767 001610 012767 000340 177012 MOV #340, $ADDP ;SET UP TRAP PSM
768 001616 000723 BR 6S ;TRY IT AGAIN
769
770 001620 132767 000002 177045 5S: BITB #BIT1, RNDFLG+1 ;CHECK "DEROUNDING" FLAG
771 001626 001010 BNE 7S ;BRANCH IF SET
772 001630 152767 000002 177035 BISB #BIT1, RNDFLG+1 ;SET "DEROUNDING" FLAG
773 001636 162767 000001 176770 SUB #1, $ADD2 ;RESTORE ORIGINAL ANSWER
774 001644 005667 176762 SBC $ADD1 ;SUBTRACT CARRY
775 001650 104000 7S: HLT ;WRONG PSM OR ANSWER
776
777 001652 000451 BR 3S
778
779 001654 012767 000604 177006 2S: MOV #STACK0, SAVSTK ;SAVE STACK ADDRESS FOR TYPING
780 001662 026767 177002 176726 CMP SAVSTK, SSP ;CHECK THE STACK POINTER
781 001670 001401 BEQ .+4 ;BRANCH IF OK
782 001672 104000 HLT ;STACK POINTER FOULED UP
783
    
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784	001674	022767	001454	176676	CMP	#15,	STK1	;CHECK THE RTI ADDRESS ON THE STACK
785	001702	001401			BEQ	.+4		;BRANCH IF OK
786	001704	104000			HLT			;RTI ADDRESS NOT EQUAL TO #15
787								
788	001706	026767	176724	176666	CMP	\$ADDER,	STK2	;CHECK THE PSW ON THE STACK
789	001714	001401			BEQ	.+4		;BRANCH IF OK
790	001716	104000			HLT			;RTI PSW NOT EQUAL TO 200
791								
792	001720	026767	176700	176656	CMP	RAND.C,	STK3	;CHECK THE DATA ON THE STACK
793	001726	001401			BEQ	.+4		;BRANCH IF OK
794	001730	104000			HLT			;STK3 NOT EQUAL TO RAND.C
795								
796	001732	026767	176670	176646	CMP	RAND.D,	STK4	;CHECK THE DATA ON THE STACK
797	001740	001401			BEQ	.+4		;BRANCH IF OK
798	001742	104000			HLT			;STK4 NOT EQUAL TO RAND.D
799								
800	001744	026767	176650	176636	CMP	RAND.A,	STK5	;CHECK THE DATA ON THE STACK
801	001752	001401			BEQ	.+4		;BRANCH IF OK
802	001754	104000			HLT			;STK5 NOT EQUAL TO RAND.A
803								
804	001756	026767	176640	176626	CMP	RAND.B,	STK6	;CHECK THE DATA ON THE STACK
805	001764	001401			BEQ	.+4		;BRANCH IF OK
806	001766	104000			HLT			;STK6 NOT EQUAL TO RAND.B
807								
808	001770	012716	001776		MOV	#3\$,	(SP)	;RESET THE STACK
809	001774	000002			RTI			;RESTORE THE STATUS (T-BIT)
810								
811	001776	104400						
812								

35: SCOPE

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*****
TEST 2: EXERCISE FSUB (PDP-11 FLOATING SUBTRACT INSTRUCTION)
RAND.A,RAND.B - RAND.C,RAND.D = ANS1,ANS2
STACK POINTER = R1
*****
```

002000	012701	000604		TST2:	MOV	#STACK0,R1		;SET UP THE STACK POINTER
002004	004767	01157G			JSR	PC,	PUSHR	;PUT THE DATA ON THE STACK
002010	000240				NOP			
002012	075011				FSUB+	R1		;FLOATING SUBTRACT ON THE R1 STACK
002014	013767	177776	176572	1S:	MOV	#PS,	SPSW	;SAVE PROCESSOR STATUS
002022	010167	176570			MOV	R1,	SSP	;SAVE THE STACK POINTER
002026	026767	176606	176560	6S:	CMP	SSUBPS,	SPSW	;CHECK THE PROCESSOR STATUS
002034	001023				BNE	4S		;GO CHECK FOR ROUNDING ERROR
002036	105767	176552			TSTB	SPSW		;CHECK FOR ERROR
002042	100464				BMI	2S		;BRANCH IF ERROR
002044	012767	000610	176616		MOV	#STACK4,SAVSTK		;SAVE PROPER STACK ADDRESS FOR TYPING
002052	026767	176612	176536		CMP	SAVSTK,	SSP	;CHECK THE STACK POINTER
002060	001401				BEQ	.+4		;BRANCH IF OK
002062	104000				HLT			;STACK POINTER NOT EQUAL TO #STACK4
002064	026767	176552	176516		CMP	SSUB1,	ANS1	;CHECK THE ANSWER
002072	001004				BNE	4S		
002074	026767	176544	176510		CMP	SSUB2,	ANS2	;CHECK THE ANSWER
002102	001515				BEQ	3S		
002104	032767	000004	176560	4S:	BIT	#BIT2,	RNDFLG	;CHECK THE ROUNDING FLAG
002112	001022				BNE	5S		
002114	052767	000004	176550		BIS	#BIT2,	RNDFLG	;SET ROUNDING FLAG
002122	062767	000001	176514		ADD	#1,	SSUB2	;INCREMENT FORTRAN ANSWER
002130	005567	176506			ADC	SSUB1		;ADD CARRY
002134	102334				BVC	6S		;BRANCH IF NO OVERFLOW
002136	000257				CCC			;CLEAR ALL CONDITION CODES
002140	000262				SEV			;SET V-BIT
002142	013767	177776	176476		MOV	#PS,	SSUBER	;SET UP PSM FOR OVERFLOW
002150	012767	000340	176462		MOV	#340,	SSUBPS	;SET UP TRAP PSM
002156	000723				BR	6S		;TRY IT AGAIN
002160	132767	000004	176505	5S:	BITB	#BIT2,	RNDFLG+1	;CHECK "DEROUNDING" FLAG
002166	001010				BNE	7S		;BRANCH IF SET
002170	152767	000004	176475		BISB	#BIT2,	RNDFLG+1	;SET "DEROUNDING" FLAG
002176	162767	000001	176440		SUB	#1,	SSUB2	;RESTORE ORIGINAL ANSWER
002204	005667	176432			SBC	SSUB1		;SUBTRACT CARRY
002210	104000			7S:	HLT			;WRONG PSM OR ANSWER
002212	000451				BR	3S		
002214	012767	000604	176446	2S:	MOV	#STACK0,SAVSTK		;SAVE STACK ADDRESS FOR TYPING
002222	026767	176442	176366		CMP	SAVSTK,	SSP	;CHECK THE STACK POINTER
002230	001401				BEQ	.+4		;BRANCH IF OK
002232	104000				HLT			;STACK POINTER FOULED UP

869	002234	022767	002014	176336	CMP	#1\$,	STK1	;CHECK THE RTI ADDRESS ON THE STACK
870	002242	001401			BEQ	+.4		;BRANCH IF OK
871	002244	104000			HLT			;RTI ADDRESS NOT EQUAL TO #1\$
872								
873	002246	026767	176374	176326	CMP	\$\$SUBER,	STK2	;CHECK THE PSW ON THE STACK
874	002254	001401			BEQ	+.4		;BRANCH IF OK
875	002256	104000			HLT			;RTI PSW NOT EQUAL TO 200
876								
877	002260	026767	176340	176316	CMP	RAND.C,	STK3	;CHECK THE DATA ON THE STACK
878	002266	001401			BEQ	+.4		;BRANCH IF OK
879	002270	104000			HLT			;STK3 NOT EQUAL TO RAND.C
880								
881	002272	026767	176330	176306	CMP	RAND.D,	STK4	;CHECK THE DATA ON THE STACK
882	002300	001401			BEQ	+.4		;BRANCH IF OK
883	002302	104000			HLT			;STK4 NOT EQUAL TO RAND.D
884								
885	002304	026767	176310	176276	CMP	RAND.A,	STK5	;CHECK THE DATA ON THE STACK
886	002312	001401			BEQ	+.4		;BRANCH IF OK
887	002314	104000			HLT			;STK5 NOT EQUAL TO RAND.A
888								
889	002316	026767	176300	176266	CMP	RAND.B,	STK6	;CHECK THE DATA ON THE STACK
890	002324	001401			BEQ	+.4		;BRANCH IF OK
891	002326	104000			HLT			;STK6 NOT EQUAL TO RAND.B
892								
893	002330	012716	002336		MOV	#3\$,	(SP)	;RESET THE STACK
894	002334	000002			RTI			;RESTORE THE STATUS (T-BIT)
895								
896	002336	104400						
897								

3\$: SCOPE

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*****
:TEST 3: EXERCISE FMUL (PDP-11 FLOATING MULTIPLY INSTRUCTION)
: RAND.A,RAND.B * RAND.C,RAND.D = ANS1,ANS2
: STACK POINTER = R2
*****
```

```
TST3:  MOV    #STACK0,R2    ;SET UP THE STACK POINTER
      JSR    PC,    PUSHR   ;PUT THE DATA ON THE STACK

      NOP
      FMUL+  R2           ;FLOATING MULTIPLY ON THE R2 STACK

1$:   MOV    2#PS,  $PSW    ;SAVE PROCESSOR STATUS
      MOV    R2,   $SSP    ;SAVE THE STACK POINTER
6$:   CMP    $MULPS,$PSW   ;CHECK THE PROCESSOR STATUS
      BNE   4$           ;GO CHECK FOR ROUNDING ERROR

      TSTB  $PSW         ;CHECK FOR ERROR
      BMI  2$           ;BRANCH IF ERROR

      MOV    #STACK4,SAVSTK ;SAVE PROPER STACK ADDRESS FOR TYPING
      CMP    SAVSTK,$SSP   ;CHECK THE STACK POINTER
      BEQ   .+4          ;BRANCH IF OK
      HLT                    ;STACK POINTER NOT EQUAL TO #STACK4

      CMP    $MUL1,  ANS1  ;CHECK THE ANSWER
      BNE   4$
      CMP    $MUL2,  ANS2  ;CHECK THE ANSWER
      BEQ   3$
8$:   BIT    #BIT3,  RNDFLG ;CHECK THE ROUNDING FLAG
      BNE   5$
      BIS   #BIT3,  RNDFLG ;SET ROUNDING FLAG
      ADD   #1,    $MUL2   ;INCREMENT FORTRAN ANSWER
      ADC   $MUL1
      BVC   6$           ;BRANCH IF NO OVERFLOW
      CCC                    ;CLEAR ALL CONDITION CODES
      SEV                    ;SET V-BIT
      MOV   2#PS,  $MULR   ;SET UP PSW FOR OVERFLOW
      MOV   #340,  $MULPS  ;SET UP TRAP PSW
      BR   6$           ;TRY IT AGAIN

5$:   BITB  #BIT3,  RNDFLG+1 ;CHECK "DEROUNDING" FLAG
      BNE   7$           ;BRANCH IF SET
      BISB #BIT3,  RNDFLG+1 ;SET "DEROUNDING" FLAG
      SUB   #1,    $MUL2   ;RESTORE ORIGINAL ANSWER
      SBC   $MUL1
7$:   HLT                    ;SUBTRACT CARRY
      ;WRONG PSW OR ANSWER

      BR   3$

2$:   MOV    #STACK0,SAVSTK ;SAVE STACK ADDRESS FOR TYPING
      CMP    SAVSTK,$SSP   ;CHECK THE STACK POINTER
      BEQ   .+4          ;BRANCH IF OK
      HLT                    ;STACK POINTER FOULED UP
```

954	002574	022767	002354	175776	CMP	#15,	STK1	;CHECK THE RTI ADDRESS ON THE STACK
955	002602	001401			BEQ	.+4		;BRANCH IF OK
956	002604	104000			HLT			;RTI ADDRESS NOT EQUAL TO #15
957								
958	002606	026767	176044	175766	CMP	\$MULER,	STK2	;CHECK THE PSM ON THE STACK
959	002614	001401			BEQ	.+4		;BRANCH IF OK
960	002616	104000			HLT			;RTI PSM NOT EQUAL TO 200
961								
962	002620	026767	176000	175756	CMP	RAND.C,	STK3	;CHECK THE DATA ON THE STACK
963	002626	001401			BEQ	.+4		;BRANCH IF OK
964	002630	104000			HLT			;STK3 NOT EQUAL TO RAND.C
965								
966	002632	026767	175770	175746	CMP	RAND.D,	STK4	;CHECK THE DATA ON THE STACK
967	002640	001401			BEQ	.+4		;BRANCH IF OK
968	002642	104000			HLT			;STK4 NOT EQUAL TO RAND.D
969								
970	002644	026767	175750	175736	CMP	RAND.A,	STK5	;CHECK THE DATA ON THE STACK
971	002652	001401			BEQ	.+4		;BRANCH IF OK
972	002654	104000			HLT			;STK5 NOT EQUAL TO RAND.A
973								
974	002656	026767	175740	175726	CMP	RAND.B,	STK6	;CHECK THE DATA ON THE STACK
975	002664	001401			BEQ	.+4		;BRANCH IF OK
976	002666	104000			HLT			;STK6 NOT EQUAL TO RAND.B
977								
978	002670	012716	002676		MOV	#3\$,	(SP)	;RESET THE STACK
979	002674	000002			RTI			;RESTORE THE STATUS (T-BIT)
980								
981	002676	104400						
982								

3\$: SCOPE

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990 002700 012703 000604 TST4: MOV #STACK0,R3 ;SET UP THE STACK POINTER
991 002704 004767 010670 JSR PC, PUSHR ;PUT THE DATA ON THE STACK
992
993 002710 000240 NOP
994 002712 075033 FDIV+ R3 ;FLOATING DIVIDE ON THE R3 STACK
995
996 002714 013767 177776 175672 1S: MOV #PS, $PSW ;SAVE PROCESSOR STATUS
997 002722 010367 175670 MOV R3, $SP ;SAVE THE STACK POINTER
998 002726 026767 175726 175660 6S: CMP $DIVPS, $PSW ;CHECK THE PROCESSOR STATUS
999 002734 001023 BNE 4S ;GO CHECK FOR ROUNDING ERROR
1000
1001 002736 105767 175652 TSTB $PSW ;CHECK FOR ERROR
1002 002742 100464 BMI 2S ;BRANCH IF ERROR
1003
1004 002744 012767 000610 175716 MOV #STACK4, SAVSTK ;SAVE PROPER STACK ADDRESS FOR TYPING
1005 002752 026767 175712 175636 CMP SAVSTK, $SP ;CHECK THE STACK POINTER
1006 002760 001401 BEQ .+4 ;BRANCH IF OK
1007 002762 104000 HLT ;STACK POINTER NOT EQUAL TO #STACK4
1008
1009 002764 026767 175672 175616 CMP $DIV1, ANS1 ;CHECK THE ANSWER
1010 002772 001004 BNE 4S
1011 002774 026767 175664 175610 CMP $DIV2, ANS2 ;CHECK THE ANSWER
1012 003002 001515 BEQ 3S
1013 003004 032767 000020 175660 4S: BIT #BIT4, RNDFLG ;CHECK THE ROUNDING FLAG
1014 003012 001022 BNE 5S
1015 003014 052767 000020 175650 BIS #BIT4, RNDFLG ;SET ROUNDING FLAG
1016 003022 062767 000001 175634 ADD #1, $DIV2 ;INCREMENT FORTRAN ANSWER
1017 003030 005567 175626 ADC $DIV1 ;ADD CARRY
1018 003034 102334 BVC 6S ;BRANCH IF NO OVERFLOW
1019 003036 000257 CCC ;CLEAR ALL CONDITION CODES
1020 003040 000262 SEV ;SET V-BIT
1021 003042 013767 177776 175616 MOV #PS, $DIVER ;SET UP PSW FOR OVERFLOW
1022 003050 012767 000340 175602 MOV #340, $DIVPS ;SET UP TRAP PSW
1023 003056 000723 BR 6S ;TRY IT AGAIN
1024
1025 003060 132767 000020 175605 5S: BITB #BIT4, RNDFLG+1 ;CHECK "DEROUNDING" FLAG
1026 003066 001010 BNE 7S ;BRANCH IF SET
1027 003070 152767 000020 175575 BISB #BIT4, RNDFLG+1 ;SET "DEROUNDING" FLAG
1028 003076 162767 000001 175560 SUB #1, $DIV2 ;RESTORE ORIGINAL ANSWER
1029 003104 005667 175552 SBC $DIV1 ;SUBTRACT CARRY
1030 003110 104000 HLT ;WRONG PSW OR ANSWER
1031
1032 003112 000451 BR 3S
1033
1034 003114 012767 000604 175546 2S: MOV #STACK0, SAVSTK ;SAVE STACK ADDRESS FOR TYPING
1035 003122 026767 175542 175466 CMP SAVSTK, $SP ;CHECK THE STACK POINTER
1036 003130 001401 BEQ .+4 ;BRANCH IF OK
1037 003132 104000 HLT ;STACK POINTER FOULED UP
1038

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1039	003134	022767	002714	175436	CMP	#1\$,	STK1	;CHECK THE RTI ADDRESS ON THE STACK
1040	003142	001401			BEQ	+.4		;BRANCH IF OK
1041	003144	104000			HLT			;RTI ADDRESS NOT EQUAL TO #1\$
1042								
1043	003146	026767	175514	175426	CMP	\$DIVER,	STK2	;CHECK THE PSW ON THE STACK
1044	003154	001401			BEQ	+.4		;BRANCH IF OK
1045	003156	104000			HLT			;RTI PSW NOT EQUAL TO 200
1046								
1047	003160	026767	175440	175416	CMP	RAND.C,	STK3	;CHECK THE DATA ON THE STACK
1048	003166	001401			BEQ	+.4		;BRANCH IF OK
1049	003170	104000			HLT			;STK3 NOT EQUAL TO RAND.C
1050								
1051	003172	026767	175430	175406	CMP	RAND.D,	STK4	;CHECK THE DATA ON THE STACK
1052	003200	001401			BEQ	+.4		;BRANCH IF OK
1053	003202	104000			HLT			;STK4 NOT EQUAL TO RAND.D
1054								
1055	003204	026767	175410	175376	CMP	RAND.A,	STK5	;CHECK THE DATA ON THE STACK
1056	003212	001401			BEQ	+.4		;BRANCH IF OK
1057	003214	104000			HLT			;STK5 NOT EQUAL TO RAND.A
1058								
1059	003216	026767	175400	175366	CMP	RAND.B,	STK6	;CHECK THE DATA ON THE STACK
1060	003224	001401			BEQ	+.4		;BRANCH IF OK
1061	003226	104000			HLT			;STK6 NOT EQUAL TO RAND.B
1062								
1063	003230	012716	003236		MOV	#3\$,	(SP)	;RESET THE STACK
1064	003234	000002			RTI			;RESTORE THE STATUS (T-BIT)
1065								
1066	003236	104400						
1067								

3\$: SCOPE



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1075 003240 012704 000604  
1076 003244 004767 010330  
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1078 003250 000240  
1079 003252 075004  
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1081 003254 013767 177776 175332 1S:  
1082 003262 010467 175330  
1083 003266 026767 175336 175320  
1084 003274 001401  
1085 003276 104000  
1086  
1087 003300 105767 175310  
1088 003304 100423  
1089  
1090 003306 012767 000610 175354  
1091 003314 026767 175350 175274  
1092 003322 001401  
1093 003324 104000  
1094  
1095 003326 026767 175300 175254  
1096 003334 001401  
1097 003336 104000  
1098  
1099 003340 026767 175270 175244  
1100 003346 001401  
1101 003350 104000  
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1103 003352 000451  
1104  
1105 003354 012767 000604 175306 2S:  
1106 003362 026767 175302 175226  
1107 003370 001401  
1108 003372 104000  
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1110 003374 022767 003254 175176  
1111 003402 001401  
1112 003404 104000  
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1114 003406 026767 175224 175166  
1115 003414 001401  
1116 003416 104000  
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1118 003420 026767 175200 175156  
1119 003426 001401  
1120 003430 104000  
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1122 003432 026767 175170 175146  
1123 003440 001401

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*****
TEST 5: EXERCISE FADD (PDP-11 FLOATING ADD INSTRUCTION)
RAND.A,RAND.B + RAND.C,RAND.D = ANS1,ANS2
STACK POINTER = R4
*****

TST5: MOV #STACK0,R4 ;SET UP THE STACK POINTER
      JSR PC, PUSHR ;PUT THE DATA ON THE STACK

      NOP
      FADD+ R4 ;FLOATING ADD ON THE R4 STACK

1S: MOV #PS, $PSW ;SAVE PROCESSOR STATUS
     MOV R4, $SP ;SAVE THE STACK POINTER
     CMP $ADDP, $PSW ;CHECK THE PROCESSOR STATUS
     BEQ .+4 ;BRANCH IF OK
     HLT ;PSW NOT EQUAL TO $ADDP

     TSTB $PSW ;CHECK FOR ERROR
     BMI 2S ;BRANCH IF ERROR

     MOV #STACK4,SAVSTK ;SAVE PROPER STACK ADDRESS FOR TYPING
     CMP SAVSTK,$SP ;CHECK THE STACK POINTER
     BEQ .+4 ;BRANCH IF OK
     HLT ;STACK POINTER NOT EQUAL TO #STACK4

     CMP $ADD1, ANS1 ;CHECK THE ANSWER
     BEQ .+4 ;BRANCH IF OK
     HLT ;LEFT HALF OF ANSWER WRONG

     CMP $ADD2, ANS2 ;CHECK THE ANSWER
     BEQ .+4 ;BRANCH IF OK
     HLT ;RIGHT HALF OF ANSWER WRONG

     BR 3S

2S: MOV #STACK0,SAVSTK ;SAVE STACK ADDRESS FOR TYPING
     CMP SAVSTK,$SP ;CHECK THE STACK POINTER
     BEQ .+4 ;BRANCH IF OK
     HLT ;STACK POINTER FOULED UP

     CMP #1S, STK1 ;CHECK THE RTI ADDRESS ON THE STACK
     BEQ .+4 ;BRANCH IF OK
     HLT ;RTI ADDRESS NOT EQUAL TO #1S

     CMP $ADDR, STK2 ;CHECK THE PSW ON THE STACK
     BEQ .+4 ;BRANCH IF OK
     HLT ;RTI PSW NOT EQUAL TO 200

     CMP RAND.C, STK3 ;CHECK THE DATA ON THE STACK
     BEQ .+4 ;BRANCH IF OK
     HLT ;STK3 NOT EQUAL TO RAND.C

     CMP RAND.D, STK4 ;CHECK THE DATA ON THE STACK
     BEQ .+4 ;BRANCH IF OK
```

```

1124 003442 104000          HLT          ;STK4 NOT EQUAL TO RAND.D
1125
1126 003444 026767 175150 175136  CMP      RAND.A, STK5  ;CHECK THE DATA ON THE STACK
1127 003452 001401          BEQ      .+4         ;BRANCH IF OK
1128 003454 104000          HLT          ;STK5 NOT EQUAL TO RAND.A
1129
1130 003456 026767 175140 175126  CMP      RAND.B, STK6  ;CHECK THE DATA ON THE STACK
1131 003464 001401          BEQ      .+4         ;BRANCH IF OK
1132 003466 104000          HLT          ;STK6 NOT EQUAL TO RAND.B
1133
1134 003470 012716 003476      MOV      #3$, (SP)    ;RESET THE STACK
1135 003474 000002          RTI         ;RESTORE THE STATUS (T-BIT)
1136

```

```

1137 003476 104400          3$:      SCOPE
1138
1139

```

```

:*****
:TEST 6:      EXERCISE FSUB (PDP-11 FLOATING SUBTRACT INSTRUCTION)
:      RAND.A,RAND.B - RAND.C,RAND.D = ANS1,ANS2
:      STACK POINTER = R5
:*****

```

```

1146 003500 012705 000604      TST6:   MOV      #STACK0,R5 ;SET UP THE STACK POINTER
1147 003504 004767 010070          JSR      PC,    PUSHR  ;PUT THE DATA ON THE STACK
1148
1149 003510 000240          NOP
1150 003512 075015          FSUB+   R5         ;FLOATING SUBTRACT ON THE R5 STACK
1151
1152 003514 013767 177776 175072 1$:      MOV      @#PS,  $PSW   ;SAVE PROCESSOR STATUS
1153 003522 010567 175070          MOV      R5,    $SP   ;SAVE THE STACK POINTER
1154 003526 026767 175106 175060  CMP      $SUBPS,$PSW  ;CHECK THE PROCESSOR STATUS
1155 003534 001401          BEQ      .+4         ;BRANCH IF OK
1156 003536 104000          HLT          ;PSW NOT EQUAL TO $SUBPS
1157
1158 003540 105767 175050          TSTB   $PSW        ;CHECK FOR ERROR
1159 003544 100423          BMI    2$         ;BRANCH IF ERROR
1160
1161 003546 012767 000610 175114  MOV      #STACK4,SAVSTK ;SAVE PROPER STACK ADDRESS FOR TYPING
1162 003554 026767 175110 175034  CMP      SAVSTK,$SP   ;CHECK THE STACK POINTER
1163 003562 001401          BEQ      .+4         ;BRANCH IF OK
1164 003564 104000          HLT          ;STACK POINTER NOT EQUAL TO #STACK4
1165
1166 003566 026767 175050 175014  CMP      $SUB1,  ANS1  ;CHECK THE ANSWER
1167 003574 001401          BEQ      .+4         ;BRANCH IF OK
1168 003576 104000          HLT          ;LEFT HALF OF ANSWER WRONG
1169
1170 003600 026767 175040 175004  CMP      $SUB2,  ANS2  ;CHECK THE ANSWER
1171 003606 001401          BEQ      .+4         ;BRANCH IF OK
1172 003610 104000          HLT          ;RIGHT HALF OF ANSWER WRONG
1173
1174 003612 000451          BR      3$
1175
1176 003614 012767 000604 175046 2$:      MOV      #STACK0,SAVSTK ;SAVE STACK ADDRESS FOR TYPING
1177 003622 026767 175042 174766  CMP      SAVSTK,$SP   ;CHECK THE STACK POINTER
1178 003630 001401          BEQ      .+4         ;BRANCH IF OK
1179 003632 104000          HLT          ;STACK POINTER FOULED UP

```





```

1282
1283
1284
1285
1286
1287
1288
1289 004202 012700 000604
1290 004206 004767 007366
1291
1292 004212 000240
1293 004214 075030
1294
1295 004216 013767 177776 174370 1S:
1296 004224 010067 174366
1297 004230 026767 174424 174356
1298 004236 001401
1299 004240 104000
1300
1301 004242 105767 174346
1302 004246 100423
1303
1304 004250 012767 000610 174412
1305 004256 026767 174406 174332
1306 004264 001401
1307 004266 104000
1308
1309 004270 026767 174366 174312
1310 004276 001401
1311 004300 104000
1312
1313 004302 026767 174356 174302
1314 004310 001401
1315 004312 104000
1316
1317 004314 000451
1318
1319 004316 012767 000604 174344 2S:
1320 004324 026767 174340 174264
1321 004332 001401
1322 004334 104000
1323
1324 004336 022767 004216 174234
1325 004344 001401
1326 004346 104000
1327
1328 004350 026767 174312 174224
1329 004356 001401
1330 004360 104000
1331
1332 004362 026767 174236 174214
1333 004370 001401
1334 004372 104000
1335
1336 004374 026767 174226 174204
1337 004402 001401

```

```

*****
:TEST 10: EXERCISE FDIV (PDP-11 FLOATING DIVIDE INSTRUCTION)
: RAND.A,RAND.B / RAND.C,RAND.D = ANS1,ANS2
: STACK POINTER = RO
*****
TST10: MOV #STACK0,RO ;SET UP THE STACK POINTER
JSR PC, PUSHR ;PUT THE DATA ON THE STACK
NOP
FDIV+ RO ;FLOATING DIVIDE ON THE RO STACK
1S: MOV #PS, SPSW ;SAVE PROCESSOR STATUS
MOV RO, SSP ;SAVE THE STACK POINTER
CMP $DIVPS, SPSW ;CHECK THE PROCESSOR STATUS
BEQ .+4 ;BRANCH IF OK
HLT ;PSW NOT EQUAL TO $DIVPS
TSTB SPSW ;CHECK FOR ERROR
BNI 2S ;BRANCH IF ERROR
MOV #STACK4,SAVSTK ;SAVE PROPER STACK ADDRESS FOR TYPING
CMP SAVSTK, SSP ;CHECK THE STACK POINTER
BEQ .+4 ;BRANCH IF OK
HLT ;STACK POINTER NOT EQUAL TO #STACK4
CMP $DIV1, ANS1 ;CHECK THE ANSWER
BEQ .+4 ;BRANCH IF OK
HLT ;LEFT HALF OF ANSWER WRONG
CMP $DIV2, ANS2 ;CHECK THE ANSWER
BEQ .+4 ;BRANCH IF OK
HLT ;RIGHT HALF OF ANSWER WRONG
BR 3S
2S: MOV #STACK0,SAVSTK ;SAVE STACK ADDRESS FOR TYPING
CMP SAVSTK, SSP ;CHECK THE STACK POINTER
BEQ .+4 ;BRANCH IF OK
HLT ;STACK POINTER FOULED UP
CMP #1S, STK1 ;CHECK THE RTI ADDRESS ON THE STACK
BEQ .+4 ;BRANCH IF OK
HLT ;RTI ADDRESS NOT EQUAL TO #1S
CMP $DIVER, STK2 ;CHECK THE PSW ON THE STACK
BEQ .+4 ;BRANCH IF OK
HLT ;RTI PSW NOT EQUAL TO 200
CMP RAND.C, STK3 ;CHECK THE DATA ON THE STACK
BEQ .+4 ;BRANCH IF OK
HLT ;STK3 NOT EQUAL TO RAND.C
CMP RAND.D, STK4 ;CHECK THE DATA ON THE STACK
BEQ .+4 ;BRANCH IF OK

```

```

1338 004404 104000 HLT ;STK4 NOT EQUAL TO RAND.D
1339
1340 004406 026767 174206 174174 CMP RAND.A, STK5 ;CHECK THE DATA ON THE STACK
1341 004414 001401 BEQ .+4 ;BRANCH IF OK
1342 004416 104000 HLT ;STK5 NOT EQUAL TO RAND.A
1343
1344 004420 026767 174176 174164 CMP RAND.B, STK6 ;CHECK THE DATA ON THE STACK
1345 004426 001401 BEQ .+4 ;BRANCH IF OK
1346 004430 104000 HLT ;STK6 NOT EQUAL TO RAND.B
1347
1348 004432 012716 004440 MOV #3$, (SP) ;RESET THE STACK
1349 004436 000002 RTI ;RESTORE THE STATUS (T-BIT)
1350

```

```

1351 004440 104400 3$: SCOPE
1352
1353
1354
1355
1356
1357
1358
1359

```

```

:*****
:TEST 11: EXERCISE FADD (PDP-11 FLOATING ADD INSTRUCTION)
: RAND.A,RAND.B + RAND.C,RAND.D = ANS1,ANS2
: STACK POINTER = R1
:*****

```

```

1360 004442 012701 000604 TST11: MOV #STACK0,R1 ;SET UP THE STACK POINTER
1361 004446 004767 007126 JSR PC, PUSHR ;PUT THE DATA ON THE STACK
1362
1363 004452 000240 NOP
1364 004454 075001 FADD+ R1 ;FLOATING ADD ON THE R1 STACK
1365
1366 004456 013767 177776 174130 1$: MOV #PS, SPSW ;SAVE PROCESSOR STATUS
1367 004464 010167 174126 MOV R1, SSP ;SAVE THE STACK POINTER
1368 004470 026767 174134 174116 CMP $ADDPS, SPSW ;CHECK THE PROCESSOR STATUS
1369 004476 001401 BEQ .+4 ;BRANCH IF OK
1370 004500 104000 HLT ;PSW NOT EQUAL TO $ADDPS
1371
1372 004502 105767 174106 TSTB SPSW ;CHECK FOR ERROR
1373 004506 100423 BMI 2$ ;BRANCH IF ERROR
1374
1375 004510 012767 000610 174152 MOV #STACK4, SAVSTK ;SAVE PROPER STACK ADDRESS FOR TYPING
1376 004516 026767 174146 174072 CMP SAVSTK, SSP ;CHECK THE STACK POINTER
1377 004524 001401 BEQ .+4 ;BRANCH IF OK
1378 004526 104000 HLT ;STACK POINTER NOT EQUAL TO #STACK4
1379
1380 004530 026767 174076 174052 CMP $ADD1, ANS1 ;CHECK THE ANSWER
1381 004536 001401 BEQ .+4 ;BRANCH IF OK
1382 004540 104000 HLT ;LEFT HALF OF ANSWER WRONG
1383
1384 004542 026767 174066 174042 CMP $ADD2, ANS2 ;CHECK THE ANSWER
1385 004550 001401 BEQ .+4 ;BRANCH IF OK
1386 004552 104000 HLT ;RIGHT HALF OF ANSWER WRONG
1387
1388 004554 000451 BR 3$
1389
1390 004556 012767 000604 174104 2$: MOV #STACK0, SAVSTK ;SAVE STACK ADDRESS FOR TYPING
1391 004564 026767 174100 174024 CMP SAVSTK, SSP ;CHECK THE STACK POINTER
1392 004572 001401 BEQ .+4 ;BRANCH IF OK
1393 004574 104000 HLT ;STACK POINTER FOULED UP

```







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1495
1496
1497
1498
1499
1500
1501
1502 005142 012703 000604
1503 005146 004767 006426
1504
1505 005152 000240
1506 005154 075023
1507
1508 005156 013767 177776 173430 1S:
1509 005164 010367 173426
1510 005170 026767 173454 173416
1511 005176 001401
1512 005200 104000
1513
1514 005202 105767 173406
1515 005206 100423
1516
1517 005210 012767 000610 173452
1518 005216 026767 173446 173372
1519 005224 001401
1520 005226 104000
1521
1522 005230 026767 173416 173352
1523 005236 001401
1524 005240 104000
1525
1526 005242 026767 173406 173342
1527 005250 001401
1528 005252 104000
1529
1530 005254 000451
1531
1532 005256 012767 000604 173404 2S:
1533 005264 026767 173400 173324
1534 005272 001401
1535 005274 104000
1536
1537 005276 022767 005156 173274
1538 005304 001401
1539 005306 104000
1540
1541 005310 026767 173342 173264
1542 005316 001401
1543 005320 104000
1544
1545 005322 026767 173276 173254
1546 005330 001401
1547 005332 104000
1548
1549 005334 026767 173266 173244
1550 005342 001401

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```

*****
:TEST 13: EXERCISE FMUL (PDP-11 FLOATING MULTIPLY INSTRUCTION)
: RAND.A,RAND.B * RAND.C,RAND.D = ANSI,ANS2
: STACK POINTER = R3
*****
TST13: MOV #STACK0,R3 ;SET UP THE STACK POINTER
JSR PC, PUSHR ;PUT THE DATA ON THE STACK
NOP
FMUL+ R3 ;FLOATING MULTIPLY ON THE R3 STACK
1S: MOV 2#PS, $PSW ;SAVE PROCESSOR STATUS
MOV R3, $SP ;SAVE THE STACK POINTER
CMP $MULPS, $PSW ;CHECK THE PROCESSOR STATUS
BEQ .+4 ;BRANCH IF OK
HLT ;PSW NOT EQUAL TO $MULPS
TSTB $PSW ;CHECK FOR ERROR
BMI 2$ ;BRANCH IF ERROR
MOV #STACK4,SAVSTK ;SAVE PROPER STACK ADDRESS FOR TYPING
CMP SAVSTK, $SP ;CHECK THE STACK POINTER
BEQ .+4 ;BRANCH IF OK
HLT ;STACK POINTER NOT EQUAL TO #STACK4
CMP $MUL1, ANS1 ;CHECK THE ANSWER
BEQ .+4 ;BRANCH IF OK
HLT ;LEFT HALF OF ANSWER WRONG
CMP $MUL2, ANS2 ;CHECK THE ANSWER
BEQ .+4 ;BRANCH IF OK
HLT ;RIGHT HALF OF ANSWER WRONG
BR 3$
MOV #STACK0,SAVSTK ;SAVE STACK ADDRESS FOR TYPING
CMP SAVSTK, $SP ;CHECK THE STACK POINTER
BEQ .+4 ;BRANCH IF OK
HLT ;STACK POINTER FOULED UP
CMP #1$, STK1 ;CHECK THE RTI ADDRESS ON THE STACK
BEQ .+4 ;BRANCH IF OK
HLT ;RTI ADDRESS NOT EQUAL TO #1$
CMP $MULER, STK2 ;CHECK THE PSW ON THE STACK
BEQ .+4 ;BRANCH IF OK
HLT ;RTI PSW NOT EQUAL TO 200
CMP RAND.C, STK3 ;CHECK THE DATA ON THE STACK
BEQ .+4 ;BRANCH IF OK
HLT ;STK3 NOT EQUAL TO RAND.C
CMP RAND.D, STK4 ;CHECK THE DATA ON THE STACK
BEQ .+4 ;BRANCH IF OK

```

```

1551 005344 104000 HLT ;STK4 NOT EQUAL TO RAND.D
1552
1553 005346 026767 173246 173234 CMP RAND.A, STK5 ;CHECK THE DATA ON THE STACK
1554 005354 001401 BEQ .+4 ;BRANCH IF OK
1555 005356 104000 HLT ;STK5 NOT EQUAL TO RAND.A
1556
1557 005360 026767 173236 173224 CMP RAND.B, STK6 ;CHECK THE DATA ON THE STACK
1558 005366 001401 BEQ .+4 ;BRANCH IF OK
1559 005370 104000 HLT ;STK6 NOT EQUAL TO RAND.B
1560
1561 005372 012716 005400 MOV #3$, (SP) ;RESET THE STACK
1562 005376 000002 RTI ;RESTORE THE STATUS (T-BIT)
1563

```

1564 005400 104400 3\$: SCOPE

```

1565
1566
1567 ;*****
1568 ;TEST 14: EXERCISE FDIV (PDP-11 FLOATING DIVIDE INSTRUCTION)
1569 ;RAND.A,RAND.B / RAND.C,RAND.D = ANS1,ANS2
1570 ;STACK POINTER = R4
1571 ;*****
1572

```

```

1573 005402 012704 000604 TST14: MOV #STACK0,R4 ;SET UP THE STACK POINTER
1574 005406 004767 006166 JSR PC, PUSHR ;PUT THE DATA ON THE STACK
1575
1576 005412 000240 NOP
1577 005414 075034 FDIV+ R4 ;FLOATING DIVIDE ON THE R4 STACK
1578
1579 005416 013767 177776 173170 1$: MOV #PS, SPSW ;SAVE PROCESSOR STATUS
1580 005424 010467 173166 MOV R4, SSP ;SAVE THE STACK POINTER
1581 005430 026767 173224 173156 CMP $DIVPS, SPSW ;CHECK THE PROCESSOR STATUS
1582 005436 001401 BEQ .+4 ;BRANCH IF OK
1583 005440 104000 HLT ;PSW NOT EQUAL TO $DIVPS
1584

```

```

1585 005442 105767 173146 TSTB SPSW ;CHECK FOR ERROR
1586 005446 100423 BMI 2$ ;BRANCH IF ERROR
1587
1588 005450 012767 000610 173212 MOV #STACK4, SAVSTK ;SAVE PROPER STACK ADDRESS FOR TYPING
1589 005456 026767 173206 173132 CMP SAVSTK, SSP ;CHECK THE STACK POINTER
1590 005464 001401 BEQ .+4 ;BRANCH IF OK
1591 005466 104000 HLT ;STACK POINTER NOT EQUAL TO #STACK4
1592

```

```

1593 005470 026767 173166 173112 CMP $DIV1, ANS1 ;CHECK THE ANSWER
1594 005476 001401 BEQ .+4 ;BRANCH IF OK
1595 005500 104000 HLT ;LEFT HALF OF ANSWER WRONG
1596
1597 005502 026767 173156 173102 CMP $DIV2, ANS2 ;CHECK THE ANSWER
1598 005510 001401 BEQ .+4 ;BRANCH IF OK
1599 005512 104000 HLT ;RIGHT HALF OF ANSWER WRONG
1600

```

```

1601 005514 000451 BR 3$
1602
1603 005516 012767 000604 173144 2$: MOV #STACK0, SAVSTK ;SAVE STACK ADDRESS FOR TYPING
1604 005524 026767 173140 173064 CMP SAVSTK, SSP ;CHECK THE STACK POINTER
1605 005532 001401 BEQ .+4 ;BRANCH IF OK
1606 005534 104000 HLT ;STACK POINTER FOULED UP

```





```

1708
1709
1710
1711
1712
1713
1714
1715 006102 012706 000604
1716 006106 004767 005466
1717
1718 006112 000240
1719 006114 075016
1720
1721 006116 013767 177776 172470 1S:
1722 006124 010667 172466
1723 006130 026767 172504 172456
1724 006136 001401
1725 006140 104000
1726
1727 006142 105767 172446
1728 006146 100424
1729
1730 006150 012767 000610 172512
1731 006156 026767 172506 172432
1732 006164 001401
1733 006166 104000
1734
1735 006170 026767 172446 172412
1736 006176 001401
1737 006200 104000
1738
1739 006202 026767 172436 172402
1740 006210 001401
1741 006212 104000
1742
1743 006214 024646
1744 006216 000451
1745
1746 006220 012767 000600 172442 2S:
1747 006226 026767 172436 172362
1748 006234 001401
1749 006236 104000
1750
1751 006240 022767 006116 172332
1752 006246 001401
1753 006250 104000
1754
1755 006252 026767 172370 172322
1756 006260 001401
1757 006262 104000
1758
1759 006264 026767 172334 172312
1760 006272 001401
1761 006274 104000
1762
1763 006276 026767 172324 172302
    
```

\*\*\*\*\*  
 TEST 16: EXERCISE FSUB (PDP-11 FLOATING SUBTRACT INSTRUCTION)  
 RAND.A,RAND.B - RAND.C,RAND.D = ANS1,ANS2  
 STACK POINTER = SP  
 \*\*\*\*\*

TST16: MOV #STACK0,SP ;SET UP THE STACK POINTER  
 JSR PC, PUSHR ;PUT THE DATA ON THE STACK  
 NOP  
 FSUB+ SP ;FLOATING SUBTRACT ON THE SP STACK  
 1S: MOV #PS, \$PSW ;SAVE PROCESSOR STATUS  
 MOV SP, \$SP ;SAVE THE STACK POINTER  
 CMP \$SUBPS, \$PSW ;CHECK THE PROCESSOR STATUS  
 BEQ .+4 ;BRANCH IF OK  
 HLT ;PSW NOT EQUAL TO \$SUBPS  
 TSTB \$PSW ;CHECK FOR ERROR  
 BMI 2S ;BRANCH IF ERROR  
 MOV #STACK4, SAVSTK ;SAVE PROPER STACK ADDRESS FOR TYPING  
 CMP SAVSTK, \$SP ;CHECK THE STACK POINTER  
 BEQ .+4 ;BRANCH IF OK  
 HLT ;STACK POINTER NOT EQUAL TO #STACK4  
 CMP \$SUB1, ANS1 ;CHECK THE ANSWER  
 BEQ .+4 ;BRANCH IF OK  
 HLT ;LEFT HALF OF ANSWER WRONG  
 CMP \$SUB2, ANS2 ;CHECK THE ANSWER  
 BEQ .+4 ;BRANCH IF OK  
 HLT ;RIGHT HALF OF ANSWER WRONG  
 CMP -(SP), -(SP) ;RESTORE THE STACK  
 BR 3S  
 2S: MOV #STK1, SAVSTK ;SAVE PROPER STACK ADDRESS FOR TYPING  
 CMP SAVSTK, \$SP ;CHECK THE STACK POINTER  
 BEQ .+4 ;BRANCH IF OK  
 HLT ;STACK POINTER FOULED UP  
 CMP #1S, STK1 ;CHECK THE RTI ADDRESS ON THE STACK  
 BEQ .+4 ;BRANCH IF OK  
 HLT ;RTI ADDRESS NOT EQUAL TO #1S  
 CMP \$SUBER, STK2 ;CHECK THE PSW ON THE STACK  
 BEQ .+4 ;BRANCH IF OK  
 HLT ;RTI PSW NOT EQUAL TO 200  
 CMP RAND.C, STK3 ;CHECK THE DATA ON THE STACK  
 BEQ .+4 ;BRANCH IF OK  
 HLT ;STK3 NOT EQUAL TO RAND.C  
 CMP RAND.D, STK4 ;CHECK THE DATA ON THE STACK

```

1764 006304 001401      BEQ      .+4      ;BRANCH IF OK
1765 006306 104000      HLT
1766
1767 006310 026767 172304 172272      CMP      RAND.A, STK5 ;CHECK THE DATA ON THE STACK
1768 006316 001401      BEQ      .+4      ;BRANCH IF OK
1769 006320 104000      HLT      ;STK5 NOT EQUAL TO RAND.A
1770
1771 006322 026767 172274 172262      CMP      RAND.B, STK6 ;CHECK THE DATA ON THE STACK
1772 006330 001401      BEQ      .+4      ;BRANCH IF OK
1773 006332 104000      HLT      ;STK6 NOT EQUAL TO RAND.B
1774
1775 006334 012716 006342      MOV      #3$, (SP) ;RESET THE STACK
1776 006340 000002      RTI      ;RESTORE THE STATUS (T-BIT)
1777
1778 006342 104400      3$:     SCOPE
1779
1780
1781
1782
1783
1784
1785
1786
1787 006344 012700 000604      TST17:  MOV     #STACK0,RO ;SET UP THE STACK POINTER
1788 006350 004767 005224      JSR     PC,    PUSHR ;PUT THE DATA ON THE STACK
1789
1790 006354 000240      NOP
1791 006356 075020      FMUL+  RO      ;FLOATING MULTIPLY ON THE RO STACK
1792
1793 006360 013767 177776 172226 1$:     MOV     @#PS,  $PSW ;SAVE PROCESSOR STATUS
1794 006366 010067 172224      MOV     RO,    $SP  ;SAVE THE STACK POINTER
1795 006372 026767 172252 172214      CMP     $MULPS,$PSW ;CHECK THE PROCESSOR STATUS
1796 006400 001401      BEQ     .+4      ;BRANCH IF OK
1797 006402 104000      HLT     ;PSW NOT EQUAL TO $MULPS
1798
1799 006404 105767 172204      TSTB   $PSW ;CHECK FOR ERROR
1800 006410 100423      BMI    2$      ;BRANCH IF ERROR
1801
1802 006412 012767 000610 172250      MOV     #STACK4,SAVSTK ;SAVE PROPER STACK ADDRESS FOR TYPING
1803 006420 026767 172244 172170      CMP     SAVSTK,$SP ;CHECK THE STACK POINTER
1804 006426 001401      BEQ     .+4      ;BRANCH IF OK
1805 006430 104000      HLT     ;STACK POINTER NOT EQUAL TO #STACK4
1806
1807 006432 026767 172214 172150      CMP     $MUL1, ANS1 ;CHECK THE ANSWER
1808 006440 001401      BEQ     .+4      ;BRANCH IF OK
1809 006442 104000      HLT     ;LEFT HALF OF ANSWER WRONG
1810
1811 006444 026767 172204 172140      CMP     $MUL2, ANS2 ;CHECK THE ANSWER
1812 006452 001401      BEQ     .+4      ;BRANCH IF OK
1813 006454 104000      HLT     ;RIGHT HALF OF ANSWER WRONG
1814
1815 006456 000451      BR     3$
1816
1817 006460 012767 000604 172202 2$:     MOV     #STACK0,SAVSTK ;SAVE STACK ADDRESS FOR TYPING
1818 006466 026767 172176 172122      CMP     SAVSTK,$SP ;CHECK THE STACK POINTER
1819 006474 001401      BEQ     .+4      ;BRANCH IF OK
    
```

```

;*****
;TEST 17: EXERCISE FMUL (PDP-11 FLOATING MULTIPLY INSTRUCTION)
;RAND.A,RAND.B * RAND.C,RAND.D = ANS1,ANS2
;STACK POINTER = RO
;*****
    
```

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1820 006476 104000 HLT ;STACK POINTER FOULED UP
1821
1822 006500 022767 006360 172072 CMP #1$, STK1 ;CHECK THE RTI ADDRESS ON THE STACK
1823 006506 001401 BEQ .+4 ;BRANCH IF OK
1824 006510 104000 HLT ;RTI ADDRESS NOT EQUAL TO #1$
1825
1826 006512 026767 172140 172062 CMP $MULER, STK2 ;CHECK THE PSW ON THE STACK
1827 006520 001401 BEQ .+4 ;BRANCH IF OK
1828 006522 104000 HLT ;RTI PSW NOT EQUAL TO 200
1829
1830 006524 026767 172074 172052 CMP RAND.C, STK3 ;CHECK THE DATA ON THE STACK
1831 006532 001401 BEQ .+4 ;BRANCH IF OK
1832 006534 104000 HLT ;STK3 NOT EQUAL TO RAND.C
1833
1834 006536 026767 172064 172042 CMP RAND.D, STK4 ;CHECK THE DATA ON THE STACK
1835 006544 001401 BEQ .+4 ;BRANCH IF OK
1836 006546 104000 HLT ;STK4 NOT EQUAL TO RAND.D
1837
1838 006550 026767 172044 172032 CMP RAND.A, STK5 ;CHECK THE DATA ON THE STACK
1839 006556 001401 BEQ .+4 ;BRANCH IF OK
1840 006560 104000 HLT ;STK5 NOT EQUAL TO RAND.A
1841
1842 006562 026767 172034 172022 CMP RAND.B, STK6 ;CHECK THE DATA ON THE STACK
1843 006570 001401 BEQ .+4 ;BRANCH IF OK
1844 006572 104000 HLT ;STK6 NOT EQUAL TO RAND.B
1845
1846 006574 012716 006602 MOV #3$, (SP) ;RESET THE STACK
1847 006600 000002 RTI ;RESTORE THE STATUS (T-BIT)
1848

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1849 006602 104400 3\$: SCOPE

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1852 ;*****
1853 ;TEST 20: EXERCISE FDIV (PDP-11 FLOATING DIVIDE INSTRUCTION)
1854 ; RAND.A,RAND.B / RAND.C,RAND.D = ANS1,ANS2
1855 ; STACK POINTER = R1
1856 ;*****
1857

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1858 006604 012701 000604 TST20: MOV #STACK0,R1 ;SET UP THE STACK POINTER
1859 006610 004767 004764 JSR PC, PUSHR ;PUT THE DATA ON THE STACK
1860
1861 006614 000240 NOP
1862 006616 075031 FDIV+ R1 ;FLOATING DIVIDE ON THE R1 STACK
1863
1864 006620 013767 177776 171766 1$: MOV @#PS, $PSW ;SAVE PROCESSOR STATUS
1865 006626 010167 171764 MOV R1, $SP ;SAVE THE STACK POINTER
1866 006632 026767 172022 171754 CMP $DIVPS, $PSW ;CHECK THE PROCESSOR STATUS
1867 006640 001401 BEQ .+4 ;BRANCH IF OK
1868 006642 104000 HLT ;PSW NOT EQUAL TO $DIVPS
1869
1870 006644 105767 171744 TSTB $PSW ;CHECK FOR ERROR
1871 006650 100423 BMI 2$ ;BRANCH IF ERROR
1872
1873 006652 012767 000610 172010 MOV #STACK4, SAVSTK ;SAVE PROPER STACK ADDRESS FOR TYPING
1874 006660 026767 172004 171730 CMP SAVSTK, $SP ;CHECK THE STACK POINTER
1875 006666 001401 BEQ .+4 ;BRANCH IF OK

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007044 012702 000604  
007050 004767 004524  
007054 000240  
007056 075002  
007060 013767 177776 171526  
007066 010267 171524  
007072 026767 171532 171514  
007100 001401  
007102 104000  
007104 105767 171504  
007110 100423  
007112 012767 000610 171550  
007120 026767 171544 171470  
007126 001401  
007130 104000  
007132 026767 171474 171450  
007140 001401  
007142 104000  
007144 026767 171464 171440  
007152 001401  
007154 104000  
007156 000451  
007160 012767 000604 171502  
007166 026767 171476 171422  
007174 001401  
007176 104000  
007200 022767 007060 171372  
007206 001401  
007210 104000  
007212 026767 171420 171362  
007220 001401  
007222 104000  
007224 026767 171374 171352  
007232 001401  
007234 104000  
007236 026767 171364 171342  
007244 001401

\*\*\*\*\*  
:TEST 21: EXERCISE FADD (PDP-11 FLOATING ADD INSTRUCTION)  
: RAND.A,RAND.B + RAND.C,RAND.D = ANS1,ANS2  
: STACK POINTER = R2  
:\*\*\*\*\*

TST21: MOV #STACK0,R2 ;SET UP THE STACK POINTER  
JSR PC, PUSHR ;PUT THE DATA ON THE STACK  
NOP  
FADD+ R2 ;FLOATING ADD ON THE R2 STACK  
15: MOV #PS, \$PSW ;SAVE PROCESSOR STATUS  
MOV R2, \$SP ;SAVE THE STACK POINTER  
CMP \$ADDP, \$PSW ;CHECK THE PROCESSOR STATUS  
BEQ .+4 ;BRANCH IF OK  
HLT ;PSW NOT EQUAL TO \$ADDP  
TSTB \$PSW ;CHECK FOR ERROR  
BMI 2\$ ;BRANCH IF ERROR  
MOV #STACK4, SAVSTK ;SAVE PROPER STACK ADDRESS FOR TYPING  
CMP SAVSTK, \$SP ;CHECK THE STACK POINTER  
BEQ .+4 ;BRANCH IF OK  
HLT ;STACK POINTER NOT EQUAL TO #STACK4  
CMP \$ADD1, ANS1 ;CHECK THE ANSWER  
BEQ .+4 ;BRANCH IF OK  
HLT ;LEFT HALF OF ANSWER WRONG  
CMP \$ADD2, ANS2 ;CHECK THE ANSWER  
BEQ .+4 ;BRANCH IF OK  
HLT ;RIGHT HALF OF ANSWER WRONG  
BR 3\$  
2\$: MOV #STACK0, SAVSTK ;SAVE STACK ADDRESS FOR TYPING  
CMP SAVSTK, \$SP ;CHECK THE STACK POINTER  
BEQ .+4 ;BRANCH IF OK  
HLT ;STACK POINTER FOULED UP  
CMP #15, STK1 ;CHECK THE RTI ADDRESS ON THE STACK  
BEQ .+4 ;BRANCH IF OK  
HLT ;RTI ADDRESS NOT EQUAL TO #15  
CMP \$ADDER, STK2 ;CHECK THE PSW ON THE STACK  
BEQ .+4 ;BRANCH IF OK  
HLT ;RTI PSW NOT EQUAL TO 200  
CMP RAND.C, STK3 ;CHECK THE DATA ON THE STACK  
BEQ .+4 ;BRANCH IF OK  
HLT ;STK3 NOT EQUAL TO RAND.C  
CMP RAND.D, STK4 ;CHECK THE DATA ON THE STACK  
BEQ .+4 ;BRANCH IF OK

```

1978 007246 104000 HLT ;STK4 NOT EQUAL TO RAND.D
1979
1980 007250 026767 171344 171332 CMP RAND.A, STK5 ;CHECK THE DATA ON THE STACK
1981 007256 001401 BEQ .+4 ;BRANCH IF OK
1982 007260 104000 HLT ;STK5 NOT EQUAL TO RAND.A
1983
1984 007262 026767 171334 171322 CMP RAND.B, STK6 ;CHECK THE DATA ON THE STACK
1985 007270 001401 BEQ .+4 ;BRANCH IF OK
1986 007272 104000 HLT ;STK6 NOT EQUAL TO RAND.B
1987
1988 007274 012716 007302 MOV #3$, (SP) ;RESET THE STACK
1989 007300 000002 RTI ;RESTORE THE STATUS (T-BIT)
1990
1991 007302 104400 3$: SCOPE
1992
1993
1994
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1999

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:TEST 22: EXERCISE FSUB (PDP-11 FLOATING SUBTRACT INSTRUCTION)
: RAND.A,RAND.B - RAND.C,RAND.D = ANS1,ANS2
: STACK POINTER = R3
*****

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2000 007304 012703 000604 TST22: MOV #STACK0,R3 ;SET UP THE STACK POINTER
2001 007310 004767 004264 JSR PC, PUSHR ;PUT THE DATA ON THE STACK
2002
2003 007314 000240 NOP
2004 007316 075013 FSUB+ R3 ;FLOATING SUBTRACT ON THE R3 STACK
2005
2006 007320 013767 177776 171266 1$: MOV #PS, SPSW ;SAVE PROCESSOR STATUS
2007 007326 010367 171264 MOV R3, SSP ;SAVE THE STACK POINTER
2008 007332 026767 171302 171254 CMP $$SUBPS, SPSW ;CHECK THE PROCESSOR STATUS
2009 007340 001401 BEQ .+4 ;BRANCH IF OK
2010 007342 104000 HLT ;PSW NOT EQUAL TO $$SUBPS
2011
2012 007344 105767 171244 TSTB SPSW ;CHECK FOR ERROR
2013 007350 100423 BMI 2$ ;BRANCH IF ERROR
2014
2015 007352 012767 000610 171310 MOV #STACK4, SAVSTK ;SAVE PROPER STACK ADDRESS FOR TYPING
2016 007360 026767 171304 171230 CMP SAVSTK, SSP ;CHECK THE STACK POINTER
2017 007366 001401 BEQ .+4 ;BRANCH IF OK
2018 007370 104000 HLT ;STACK POINTER NOT EQUAL TO #STACK4
2019
2020 007372 026767 171244 171210 CMP $$SUB1, ANS1 ;CHECK THE ANSWER
2021 007400 001401 BEQ .+4 ;BRANCH IF OK
2022 007402 104000 HLT ;LEFT HALF OF ANSWER WRONG
2023
2024 007404 026767 171234 171200 CMP $$SUB2, ANS2 ;CHECK THE ANSWER
2025 007412 001401 BEQ .+4 ;BRANCH IF OK
2026 007414 104000 HLT ;RIGHT HALF OF ANSWER WRONG
2027
2028 007416 000451 BR 3$
2029
2030 007420 012767 000604 171242 2$: MOV #STACK0, SAVSTK ;SAVE STACK ADDRESS FOR TYPING
2031 007426 026767 171236 171162 CMP SAVSTK, SSP ;CHECK THE STACK POINTER
2032 007434 001401 BEQ .+4 ;BRANCH IF OK
2033 007436 104000 HLT ;STACK POINTER FOULED UP

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2090
2091 007632 026767 171014 170750      CMP      $MUL1, ANS1      ;CHECK THE ANSWER
2092 007640 001401                      BEQ      .+4             ;BRANCH IF OK
2093 007642 104000                      HLT                               ;LEFT HALF OF ANSWER WRONG
2094
2095 007644 026767 171004 170740      CMP      $MUL2, ANS2      ;CHECK THE ANSWER
2096 007652 001401                      BEQ      .+4             ;BRANCH IF OK
2097 007654 104000                      HLT                               ;RIGHT HALF OF ANSWER WRONG
2098
2099 007656 000451                      BR       35
2100
2101 007660 012767 000604 171002 25:    MOV      $STACK0, SAVSTK ;SAVE STACK ADDRESS FOR TYPING
2102 007666 026767 170776 170722      CMP      SAVSTK, $SP      ;CHECK THE STACK POINTER
2103 007674 001401                      BEQ      .+4             ;BRANCH IF OK
2104 007676 104000                      HLT                               ;STACK POINTER FOULED UP
2105
2106 007700 022767 007560 170672      CMP      #15, STK1        ;CHECK THE RTI ADDRESS ON THE STACK
2107 007706 001401                      BEQ      .+4             ;BRANCH IF OK
2108 007710 104000                      HLT                               ;RTI ADDRESS NOT EQUAL TO #15
2109
2110 007712 026767 170740 170662      CMP      $MULR, STK2      ;CHECK THE PSW ON THE STACK
2111 007720 001401                      BEQ      .+4             ;BRANCH IF OK
2112 007722 104000                      HLT                               ;RTI PSW NOT EQUAL TO 200
2113
2114 007724 026767 170674 170652      CMP      RAND.C, STK3     ;CHECK THE DATA ON THE STACK
2115 007732 001401                      BEQ      .+4             ;BRANCH IF OK
2116 007734 104000                      HLT                               ;STK3 NOT EQUAL TO RAND.C
2117
2118 007736 026767 170664 170642      CMP      RAND.D, STK4     ;CHECK THE DATA ON THE STACK
2119 007744 001401                      BEQ      .+4             ;BRANCH IF OK
2120 007746 104000                      HLT                               ;STK4 NOT EQUAL TO RAND.D
2121
2122 007750 026767 170644 170632      CMP      RAND.A, STK5     ;CHECK THE DATA ON THE STACK
2123 007756 001401                      BEQ      .+4             ;BRANCH IF OK
2124 007760 104000                      HLT                               ;STK5 NOT EQUAL TO RAND.A
2125
2126 007762 026767 170634 170622      CMP      RAND.B, STK6     ;CHECK THE DATA ON THE STACK
2127 007770 001401                      BEQ      .+4             ;BRANCH IF OK
2128 007772 104000                      HLT                               ;STK6 NOT EQUAL TO RAND.B
2129
2130 007774 012716 010002                      MOV      #35, (SP)       ;RESET THE STACK
2131 010000 000002                      RTI                               ;RESTORE THE STATUS (T-BIT)
2132
2133 010002 104400                      35:   SCOPE
2134

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2135
2136
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2141
2142 010004 012705 000604 TST24: MOV #STACK0,R5 ;SET UP THE STACK POINTER
2143 010010 004767 003564 JSR PC, PUSHR ;PUT THE DATA ON THE STACK
2144
2145 010014 000240 NOP
2146 010016 075035 FDIV+ R5 ;FLOATING DIVIDE ON THE R5 STACK
2147
2148 010020 013767 177776 170566 1S: MOV #PS, $PSW ;SAVE PROCESSOR STATUS
2149 010026 010567 170564 MOV R5, $SP ;SAVE THE STACK POINTER
2150 010032 026767 170622 170554 CMP $DIVPS, $PSW ;CHECK THE PROCESSOR STATUS
2151 010040 001401 BEQ .+4 ;BRANCH IF OK
2152 010042 104000 HLT ;PSW NOT EQUAL TO $DIVPS
2153
2154 010044 105767 170544 TSTB $PSW ;CHECK FOR ERROR
2155 010050 100423 BMI ZS ;BRANCH IF ERROR
2156
2157 010052 012767 000610 170610 MOV #STACK4,SAVSTK ;SAVE PROPER STACK ADDRESS FOR TYPING
2158 010060 026767 170604 170530 CMP SAVSTK, $SP ;CHECK THE STACK POINTER
2159 010066 001401 BEQ .+4 ;BRANCH IF OK
2160 010070 104000 HLT ;STACK POINTER NOT EQUAL TO #STACK4
2161
2162 010072 026767 170564 170510 CMP $DIV1, ANS1 ;CHECK THE ANSWER
2163 010100 001401 BEQ .+4 ;BRANCH IF OK
2164 010102 104000 HLT ;LEFT HALF OF ANSWER WRONG
2165
2166 010104 026767 170554 170500 CMP $DIV2, ANS2 ;CHECK THE ANSWER
2167 010112 001401 BEQ .+4 ;BRANCH IF OK
2168 010114 104000 HLT ;RIGHT HALF OF ANSWER WRONG
2169
2170 010116 000451 BR ZS
2171
2172 010120 012767 000604 170542 2S: MOV #STACK0,SAVSTK ;SAVE STACK ADDRESS FOR TYPING
2173 010126 026767 170536 170462 CMP SAVSTK, $SP ;CHECK THE STACK POINTER
2174 010134 001401 BEQ .+4 ;BRANCH IF OK
2175 010136 104000 HLT ;STACK POINTER FOULED UP
2176
2177 010140 022767 010020 170432 CMP #1S, STK1 ;CHECK THE RTI ADDRESS ON THE STACK
2178 010146 001401 BEQ .+4 ;BRANCH IF OK
2179 010150 104000 HLT ;RTI ADDRESS NOT EQUAL TO #1S
2180
2181 010152 026767 170510 170422 CMP $DIVER, STK2 ;CHECK THE PSW ON THE STACK
2182 010160 001401 BEQ .+4 ;BRANCH IF OK
2183 010162 104000 HLT ;RTI PSW NOT EQUAL TO 200
2184
2185 010164 026767 170434 170412 CMP RAND.C, STK3 ;CHECK THE DATA ON THE STACK
2186 010172 001401 BEQ .+4 ;BRANCH IF OK
2187 010174 104000 HLT ;STK3 NOT EQUAL TO RAND.C
2188
2189 010176 026767 170424 170402 CMP RAND.D, STK4 ;CHECK THE DATA ON THE STACK
2190 010204 001401 BEQ .+4 ;BRANCH IF OK
    
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2191 010206 104000 HLT ;STK4 NOT EQUAL TO RAND.D
2192
2193 010210 026767 170404 170372 CMP RAND.A, STK5 ;CHECK THE DATA ON THE STACK
2194 010216 001401 BEQ .+4 ;BRANCH IF OK
2195 010220 104000 HLT ;STK5 NOT EQUAL TO RAND.A
2196
2197 010222 026767 170374 170362 CMP RAND.B, STK6 ;CHECK THE DATA ON THE STACK
2198 010230 001401 BEQ .+4 ;BRANCH IF OK
2199 010232 104000 HLT ;STK6 NOT EQUAL TO RAND.B
2200
2201 010234 012716 010242 MOV #3$, (SP) ;RESET THE STACK
2202 010240 000002 RTI ;RESTORE THE STATUS (T-BIT)
2203

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2204 010242 104400 3$: SCOPE
2205
2206

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:*****
:TEST 25: EXERCISE FADD (PDP-11 FLOATING ADD INSTRUCTION)
: RAND.A,RAND.B + RAND.C,RAND.D = ANS1,ANS2
: STACK POINTER = SP
:*****

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2212
2213 010244 012706 000604 TST25: MOV #STACK0,SP ;SET UP THE STACK POINTER
2214 010250 004767 003324 JSR PC, PUSHR ;PUT THE DATA ON THE STACK
2215
2216 010254 000240 NOP
2217 010256 075006 FADD+ SP ;FLOATING ADD ON THE SP STACK
2218
2219 010260 013767 177776 170326 1$: MOV #PS, $PSW ;SAVE PROCESSOR STATUS
2220 010266 010667 170324 MOV SP, $SP ;SAVE THE STACK POINTER
2221 010272 026767 170332 170314 CMP $ADDP, $PSW ;CHECK THE PROCESSOR STATUS
2222 010300 001401 BEQ .+4 ;BRANCH IF OK
2223 010302 104000 HLT ;PSW NOT EQUAL TO $ADDP
2224
2225 010304 105767 170304 TSTB $PSW ;CHECK FOR ERROR
2226 010310 100424 BMI 2$ ;BRANCH IF ERROR
2227
2228 010312 012767 000610 170350 MOV #STACK4, SAVSTK ;SAVE PROPER STACK ADDRESS FOR TYPING
2229 010320 026767 170344 170270 CMP SAVSTK, $SP ;CHECK THE STACK POINTER
2230 010326 001401 BEQ .+4 ;BRANCH IF OK
2231 010330 104000 HLT ;STACK POINTER NOT EQUAL TO #STACK4
2232
2233 010332 026767 170274 170250 CMP $ADD1, ANS1 ;CHECK THE ANSWER
2234 010340 001401 BEQ .+4 ;BRANCH IF OK
2235 010342 104000 HLT ;LEFT HALF OF ANSWER WRONG
2236
2237 010344 026767 170264 170240 CMP $ADD2, ANS2 ;CHECK THE ANSWER
2238 010352 001401 BEQ .+4 ;BRANCH IF OK
2239 010354 104000 HLT ;RIGHT HALF OF ANSWER WRONG
2240
2241 010356 024646 CMP -(SP), -(SP) ;RESTORE THE STACK
2242 010360 000451 BR 3$
2243
2244 010362 012767 000600 170300 2$: MOV #STK1, SAVSTK ;SAVE PROPER STACK ADDRESS FOR TYPING
2245 010370 026767 170274 170220 CMP SAVSTK, $SP ;CHECK THE STACK POINTER
2246 010376 001401 BEQ .+4 ;BRANCH IF OK

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2247 010400 104000 HLT ;STACK POINTER FOULED UP
2248
2249 010402 022767 010260 170170 CMP #15, STK1 ;CHECK THE RTI ADDRESS ON THE STACK
2250 010410 001401 BEQ .+4 ;BRANCH IF OK
2251 010412 104000 HLT ;RTI ADDRESS NOT EQUAL TO #15
2252
2253 010414 026767 170216 170160 CMP $ADDER, STK2 ;CHECK THE PSW ON THE STACK
2254 010422 001401 BEQ .+4 ;BRANCH IF OK
2255 010424 104000 HLT ;RTI PSW NOT EQUAL TO 200
2256
2257 010426 026767 170172 170150 CMP RAND.C, STK3 ;CHECK THE DATA ON THE STACK
2258 010434 001401 BEQ .+4 ;BRANCH IF OK
2259 010436 104000 HLT ;STK3 NOT EQUAL TO RAND.C
2260
2261 010440 026767 170162 170140 CMP RAND.D, STK4 ;CHECK THE DATA ON THE STACK
2262 010446 001401 BEQ .+4 ;BRANCH IF OK
2263 010450 104000 HLT ;STK4 NOT EQUAL TO RAND.D
2264
2265 010452 026767 170142 170130 CMP RAND.A, STK5 ;CHECK THE DATA ON THE STACK
2266 010460 001401 BEQ .+4 ;BRANCH IF OK
2267 010462 104000 HLT ;STK5 NOT EQUAL TO RAND.A
2268
2269 010464 026767 170132 170120 CMP RAND.B, STK6 ;CHECK THE DATA ON THE STACK
2270 010472 001401 BEQ .+4 ;BRANCH IF OK
2271 010474 104000 HLT ;STK6 NOT EQUAL TO RAND.B
2272
2273 010476 012716 010504 MOV #35, (SP) ;RESET THE STACK
2274 010502 000002 RTI ;RESTORE THE STATUS (T-BIT)
2275
2276 010504 104400 35: SCOPE
2277
2278
2279 ;*****
2280 ;TEST 26: EXERCISE FSUB (PDP-11 FLOATING SUBTRACT INSTRUCTION)
2281 ; RAND.A,RAND.B - RAND.C,RAND.D = ANS1,ANS2
2282 ; STACK POINTER = RO
2283 ;*****
2284
2285 010506 012700 000604 TST26: MOV #STACK0,RO ;SET UP THE STACK POINTER
2286 010512 004767 003062 JSR PC, PUSHR ;PUT THE DATA ON THE STACK
2287
2288 010516 000240 NOP
2289 010520 075010 FSUB+ RO ;FLOATING SUBTRACT ON THE RO STACK
2290
2291 010522 013767 177776 170064 15: MOV @#PS, SPSW ;SAVE PROCESSOR STATUS
2292 010530 010067 170062 MOV RO, SSP ;SAVE THE STACK POINTER
2293 010534 026767 170100 170052 CMP $SUBPS, SPSW ;CHECK THE PROCESSOR STATUS
2294 010542 001401 BEQ .+4 ;BRANCH IF OK
2295 010544 104000 HLT ;PSW NOT EQUAL TO $SUBPS
2296
2297 010546 105767 170042 TSTB SPSW ;CHECK FOR ERROR
2298 010552 100423 BMI 25 ;BRANCH IF ERROR
2299
2300 010554 012767 000610 170106 MOV #STACK4,SAVSTK ;SAVE PROPER STACK ADDRESS FOR TYPING
2301 010562 026767 170102 170026 CMP SAVSTK, $SP ;CHECK THE STACK POINTER
2302 010570 001401 BEQ .+4 ;BRANCH IF OK

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2356 010746 012701 000604
2357 010752 004767 002622
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2359 010756 000240
2360 010760 075021
2361
2362 010762 013767 177776 167624 15:
2363 010770 010167 167622
2364 010774 026767 167650 167612
2365 011002 001401
2366 011004 104000
2367
2368 011006 105767 167602
2369 011012 100423
2370
2371 011014 012767 000610 167646
2372 011022 026767 167642 167566
2373 011030 001401
2374 011032 104000
2375
2376 011034 026767 167612 167546
2377 011042 001401
2378 011044 104000
2379
2380 011046 026767 167602 167536
2381 011054 001401
2382 011056 104000
2383
2384 011060 000451
2385
2386 011062 012767 000604 167600 25:
2387 011070 026767 167574 167520
2388 011076 001401
2389 011100 104000
2390
2391 011102 022767 010762 167470
2392 011110 001401
2393 011112 104000
2394
2395 011114 026767 167536 167460
2396 011122 001401
2397 011124 104000
2398
2399 011126 026767 167472 167450
2400 011134 001401
2401 011136 104000
2402
2403 011140 026767 167462 167440
2404 011146 001401

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\*\*\*\*\*  
 TEST 27: EXERCISE FMUL (PDP-11 FLOATING MULTIPLY INSTRUCTION)  
 RAND.A,RAND.B \* RAND.C,RAND.D = ANS1,ANS2  
 STACK POINTER = R1  
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TST27: MOV #STACK0,R1 ;SET UP THE STACK POINTER
        JSR PC, PUSHR ;PUT THE DATA ON THE STACK
        NOP
        FMUL+ R1 ;FLOATING MULTIPLY ON THE R1 STACK
15: MOV @#PS, $PSW ;SAVE PROCESSOR STATUS
     MOV R1, $SP ;SAVE THE STACK POINTER
     CMP $MULPS, $PSW ;CHECK THE PROCESSOR STATUS
     BEQ .+4 ;BRANCH IF OK
     HLT ;PSW NOT EQUAL TO $MULPS
        TSTB $PSW ;CHECK FOR ERROR
        BMI 25 ;BRANCH IF ERROR
        MOV #STACK4,SAVSTK ;SAVE PROPER STACK ADDRESS FOR TYPING
        CMP SAVSTK,$SP ;CHECK THE STACK POINTER
        BEQ .+4 ;BRANCH IF OK
        HLT ;STACK POINTER NOT EQUAL TO #STACK4
        CMP $MUL1, ANS1 ;CHECK THE ANSWER
        BEQ .+4 ;BRANCH IF OK
        HLT ;LEFT HALF OF ANSWER WRONG
        CMP $MUL2, ANS2 ;CHECK THE ANSWER
        BEQ .+4 ;BRANCH IF OK
        HLT ;RIGHT HALF OF ANSWER WRONG
        BR 35
25: MOV #STACK0,SAVSTK ;SAVE STACK ADDRESS FOR TYPING
     CMP SAVSTK,$SP ;CHECK THE STACK POINTER
     BEQ .+4 ;BRANCH IF OK
     HLT ;STACK POINTER FOULED UP
        CMP #15, STK1 ;CHECK THE RTI ADDRESS ON THE STACK
        BEQ .+4 ;BRANCH IF OK
        HLT ;RTI ADDRESS NOT EQUAL TO #15
        CMP $MULR, STK2 ;CHECK THE PSW ON THE STACK
        BEQ .+4 ;BRANCH IF OK
        HLT ;RTI PSW NOT EQUAL TO 200
        CMP RAND.C, STK3 ;CHECK THE DATA ON THE STACK
        BEQ .+4 ;BRANCH IF OK
        HLT ;STK3 NOT EQUAL TO RAND.C
        CMP RAND.D, STK4 ;CHECK THE DATA ON THE STACK
        BEQ .+4 ;BRANCH IF OK

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2405 011150 104000 HLT ;STK4 NOT EQUAL TO RAND.D
2406
2407 011152 026767 167442 167430 CMP RAND.A, STK5 ;CHECK THE DATA ON THE STACK
2408 011160 001401 BEQ .+4 ;BRANCH IF OK
2409 011162 104000 HLT ;STK5 NOT EQUAL TO RAND.A
2410
2411 011164 026767 167432 167420 CMP RAND.B, STK6 ;CHECK THE DATA ON THE STACK
2412 011172 001401 BEQ .+4 ;BRANCH IF OK
2413 011174 104000 HLT ;STK6 NOT EQUAL TO RAND.B
2414
2415 011176 012716 011204 MOV #3$, (SP) ;RESET THE STACK
2416 011202 000002 RTI ;RESTORE THE STATUS (T-BIT)
2417
2418 011204 104400 3$: SCOPE
2419
2420
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:*****
:TEST 30: EXERCISE FDIV (PDP-11 FLOATING DIVIDE INSTRUCTION)
: RAND.A,RAND.B / RAND.C,RAND.D = ANS1,ANS2
: STACK POINTER = R2
:*****

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2427 011206 012702 000604 TST30: MOV #STACK0,R2 ;SET UP THE STACK POINTER
2428 011212 004767 002362 JSR PC, PUSHR ;PUT THE DATA ON THE STACK
2429
2430 011216 000240 NOP
2431 011220 075032 FDIV+ R2 ;FLOATING DIVIDE ON THE R2 STACK
2432
2433 011222 013767 177776 167364 1$: MOV #PS, $PSW ;SAVE PROCESSOR STATUS
2434 011230 010267 167362 MOV R2, $SP ;SAVE THE STACK POINTER
2435 011234 026767 167420 167352 CMP $DIVPS, $PSW ;CHECK THE PROCESSOR STATUS
2436 011242 001401 BEQ .+4 ;BRANCH IF OK
2437 011244 104000 HLT ;PSW NOT EQUAL TO $DIVPS
2438
2439 011246 105767 167342 TSTB $PSW ;CHECK FOR ERROR
2440 011252 100423 BMI 2$ ;BRANCH IF ERROR
2441
2442 011254 012767 000610 167406 MOV #STACK4,SAVSTK ;SAVE PROPER STACK ADDRESS FOR TYPING
2443 011262 026767 167402 167326 CMP SAVSTK, $SP ;CHECK THE STACK POINTER
2444 011270 001401 BEQ .+4 ;BRANCH IF OK
2445 011272 104000 HLT ;STACK POINTER NOT EQUAL TO #STACK4
2446
2447 011274 026767 167362 167306 CMP $DIV1, ANS1 ;CHECK THE ANSWER
2448 011302 001401 BEQ .+4 ;BRANCH IF OK
2449 011304 104000 HLT ;LEFT HALF OF ANSWER WRONG
2450
2451 011306 026767 167352 167276 CMP $DIV2, ANS2 ;CHECK THE ANSWER
2452 011314 001401 BEQ .+4 ;BRANCH IF OK
2453 011316 104000 HLT ;RIGHT HALF OF ANSWER WRONG
2454
2455 011320 000451 BR 3$
2456
2457 011322 012767 000604 167340 2$: MOV #STACK0,SAVSTK ;SAVE STACK ADDRESS FOR TYPING
2458 011330 026767 167334 167260 CMP SAVSTK, $SP ;CHECK THE STACK POINTER
2459 011336 001401 BEQ .+4 ;BRANCH IF OK
2460 011340 104000 HLT ;STACK POINTER FOULED UP

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2563
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2567
2568
2569 011706 012704 000604
2570 011712 004767 001662
2571
2572 011716 000240
2573 011720 075014
2574
2575 011722 013767 177776 166664 1S:
2576 011730 010467 166662
2577 011734 026767 166700 166652
2578 011742 001401
2579 011744 104000
2580
2581 011746 105767 166642
2582 011752 100423
2583
2584 011754 012767 000610 166706
2585 011762 026767 166702 166626
2586 011770 001401
2587 011772 104000
2588
2589 011774 026767 166642 166606
2590 012002 001401
2591 012004 104000
2592
2593 012006 026767 166632 166576
2594 012014 001401
2595 012016 104000
2596
2597 012020 000451
2598
2599 012022 012767 000604 166640 2S:
2600 012030 026767 166634 166560
2601 012036 001401
2602 012040 104000
2603
2604 012042 022767 011722 166530
2605 012050 001401
2606 012052 104000
2607
2608 012054 026767 166566 166520
2609 012062 001401
2610 012064 104000
2611
2612 012066 026767 166532 166510
2613 012074 001401
2614 012076 104000
2615
2616 012100 026767 166522 166500
2617 012106 001401

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TEST 32: EXERCISE FSUB (PDP-11 FLOATING SUBTRACT INSTRUCTION)  
RAND.A,RAND.B - RAND.C,RAND.D = ANS1,ANS2  
STACK POINTER = R4  
\*\*\*\*\*

```

TST32: MOV #STACK0,R4 ;SET UP THE STACK POINTER
JSR PC, PUSHR ;PUT THE DATA ON THE STACK

NOP
FSUB+ R4 ;FLOATING SUBTRACT ON THE R4 STACK

1S: MOV #PS, $PSW ;SAVE PROCESSOR STATUS
MOV R4, $SP ;SAVE THE STACK POINTER
CMP $SUBPS, $PSW ;CHECK THE PROCESSOR STATUS
BEQ .+4 ;BRANCH IF OK
HLT ;PSW NOT EQUAL TO $SUBPS

TSTB $PSW ;CHECK FOR ERROR
BMI 2S ;BRANCH IF ERROR

MOV #STACK4,SAVSTK ;SAVE PROPER STACK ADDRESS FOR TYPING
CMP SAVSTK, $SP ;CHECK THE STACK POINTER
BEQ .+4 ;BRANCH IF OK
HLT ;STACK POINTER NOT EQUAL TO #STACK4

CMP $SUB1, ANS1 ;CHECK THE ANSWER
BEQ .+4 ;BRANCH IF OK
HLT ;LEFT HALF OF ANSWER WRONG

CMP $SUB2, ANS2 ;CHECK THE ANSWER
BEQ .+4 ;BRANCH IF OK
HLT ;RIGHT HALF OF ANSWER WRONG

BR 3S

2S: MOV #STACK0,SAVSTK ;SAVE STACK ADDRESS FOR TYPING
CMP SAVSTK, $SP ;CHECK THE STACK POINTER
BEQ .+4 ;BRANCH IF OK
HLT ;STACK POINTER FOULED UP

CMP #1S, STK1 ;CHECK THE RTI ADDRESS ON THE STACK
BEQ .+4 ;BRANCH IF OK
HLT ;RTI ADDRESS NOT EQUAL TO #1S

CMP $SUBER, STK2 ;CHECK THE PSW ON THE STACK
BEQ .+4 ;BRANCH IF OK
HLT ;RTI PSW NOT EQUAL TO 200

CMP RAND.C, STK3 ;CHECK THE DATA ON THE STACK
BEQ .+4 ;BRANCH IF OK
HLT ;STK3 NOT EQUAL TO RAND.C

CMP RAND.D, STK4 ;CHECK THE DATA ON THE STACK
BEQ .+4 ;BRANCH IF OK

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2618 012110 104000 HLT ;STK4 NOT EQUAL TO RAND.D
2619
2620 012112 026767 166502 166470 CMP RAND.A, STK5 ;CHECK THE DATA ON THE STACK
2621 012120 001401 BEQ .+4 ;BRANCH IF OK
2622 012122 104000 HLT ;STK5 NOT EQUAL TO RAND.A
2623
2624 012124 026767 166472 166460 CMP RAND.B, STK6 ;CHECK THE DATA ON THE STACK
2625 012132 001401 BEQ .+4 ;BRANCH IF OK
2626 012134 104000 HLT ;STK6 NOT EQUAL TO RAND.B
2627
2628 012136 012716 012144 MOV #3$, (SP) ;RESET THE STACK
2629 012142 000002 RTI ;RESTORE THE STATUS (T-BIT)
2630
2631 012144 104400 3$: SCOPE
    
```

```

:*****
:TEST 33: EXERCISE FMUL (PDP-11 FLOATING MULTIPLY INSTRUCTION)
: RAND.A,RAND.B * RAND.C,RAND.D = ANS1,ANS2
: STACK POINTER = R5
:*****
    
```

```

2640 012146 012705 000604 TST33: MOV #STACK0,R5 ;SET UP THE STACK POINTER
2641 012152 004767 001422 JSR PC, PUSHR ;PUT THE DATA ON THE STACK
2642
2643 012156 000240 NOP
2644 012160 075025 FMUL+ R5 ;FLOATING MULTIPLY ON THE R5 STACK
2645
2646 012162 013767 177776 166424 1$: MOV #PS, SPSW ;SAVE PROCESSOR STATUS
2647 012170 010567 166422 MOV R5, SSP ;SAVE THE STACK POINTER
2648 012174 026767 166450 166412 CMP #MULPS, SPSW ;CHECK THE PROCESSOR STATUS
2649 012202 001401 BEQ .+4 ;BRANCH IF OK
2650 012204 104000 HLT ;PSW NOT EQUAL TO #MULPS
2651
2652 012206 105767 166402 TSTB SPSW ;CHECK FOR ERROR
2653 012212 100423 BMI 2$ ;BRANCH IF ERROR
2654
2655 012214 012767 000610 166446 MOV #STACK4, SAVSTK ;SAVE PROPER STACK ADDRESS FOR TYPING
2656 012222 026767 166442 166366 CMP SAVSTK, SSP ;CHECK THE STACK POINTER
2657 012230 001401 BEQ .+4 ;BRANCH IF OK
2658 012232 104000 HLT ;STACK POINTER NOT EQUAL TO #STACK4
2659
2660 012234 026767 166412 166346 CMP #MUL1, ANS1 ;CHECK THE ANSWER
2661 012242 001401 BEQ .+4 ;BRANCH IF OK
2662 012244 104000 HLT ;LEFT HALF OF ANSWER WRONG
2663
2664 012246 026767 166402 166336 CMP #MUL2, ANS2 ;CHECK THE ANSWER
2665 012254 001401 BEQ .+4 ;BRANCH IF OK
2666 012256 104000 HLT ;RIGHT HALF OF ANSWER WRONG
2667
2668 012260 000451 BR 3$
2669
2670 012262 012767 000604 166400 2$: MOV #STACK0, SAVSTK ;SAVE STACK ADDRESS FOR TYPING
2671 012270 026767 166374 166320 CMP SAVSTK, SSP ;CHECK THE STACK POINTER
2672 012276 001401 BEQ .+4 ;BRANCH IF OK
2673 012300 104000 HLT ;STACK POINTER FOULED UP
    
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2730
2731 012474 026767 166162 166106      CMP    $DIV1, ANS1      ;CHECK THE ANSWER
2732 012502 001401                      BEQ    .+4              ;BRANCH IF OK
2733 012504 104000                      HLT                                ;LEFT HALF OF ANSWER WRONG
2734
2735 012506 026767 166152 166076      CMP    $DIV2, ANS2      ;CHECK THE ANSWER
2736 012514 001401                      BEQ    .+4              ;BRANCH IF OK
2737 012516 104000                      HLT                                ;RIGHT HALF OF ANSWER WRONG
2738
2739 012520 024646                      CMP    -(SP), -(SP)     ;RESTORE THE STACK
2740 012522 000451                      BR
2741
2742 012524 012767 000600 166136 25:    MOV    #STK1, SAVSTK    ;SAVE PROPER STACK ADDRESS FOR TYPING
2743 012532 026767 166132 166056      CMP    SAVSTK, $SP      ;CHECK THE STACK POINTER
2744 012540 001401                      BEQ    .+4              ;BRANCH IF OK
2745 012542 104000                      HLT                                ;STACK POINTER FOULED UP
2746
2747 012544 022767 012422 166026      CMP    #15, STK1        ;CHECK THE RTI ADDRESS ON THE STACK
2748 012552 001401                      BEQ    .+4              ;BRANCH IF OK
2749 012554 104000                      HLT                                ;RTI ADDRESS NOT EQUAL TO #15
2750
2751 012556 026767 166104 166016      CMP    $DIVER, STK2     ;CHECK THE PSM ON THE STACK
2752 012564 001401                      BEQ    .+4              ;BRANCH IF OK
2753 012566 104000                      HLT                                ;RTI PSM NOT EQUAL TO 200
2754
2755 012570 026767 166030 166006      CMP    RAND.C, STK3     ;CHECK THE DATA ON THE STACK
2756 012576 001401                      BEQ    .+4              ;BRANCH IF OK
2757 012600 104000                      HLT                                ;STK3 NOT EQUAL TO RAND.C
2758
2759 012602 026767 166020 165776      CMP    RAND.D, STK4     ;CHECK THE DATA ON THE STACK
2760 012610 001401                      BEQ    .+4              ;BRANCH IF OK
2761 012612 104000                      HLT                                ;STK4 NOT EQUAL TO RAND.D
2762
2763 012614 026767 166000 165766      CMP    RAND.A, STK5     ;CHECK THE DATA ON THE STACK
2764 012622 001401                      BEQ    .+4              ;BRANCH IF OK
2765 012624 104000                      HLT                                ;STK5 NOT EQUAL TO RAND.A
2766
2767 012626 026767 165770 165756      CMP    RAND.B, STK6     ;CHECK THE DATA ON THE STACK
2768 012634 001401                      BEQ    .+4              ;BRANCH IF OK
2769 012636 104000                      HLT                                ;STK6 NOT EQUAL TO RAND.B
2770
2771 012640 012716 012646                      MOV    #35, (SP)        ;RESET THE STACK
2772 012644 000002                      RTI                          ;RESTORE THE STATUS (T-BIT)
2773
2774 012646 104400                      35:  SCOPE
2775
    
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2776
2777 012650 062767 000001 166130      ADD    #1, PCNT+2 ;COUNT PASSES
2778 012656 005567 166122      ADC    PCNT
2779
2780 012662      001          DONE:
2781 012662 032737 002000 177570      BIT    #SW10,#SWR ;RING THE BELL?
2782 012670 001002          BNE    1$ ;NO!
2783 012672 000004 000007          TYPE  ,BELL
2784 012676 005046          CLR    -(6) ;CLEAR TRACE TRAP
2785 012700 032737 010000 177570      BIT    #SW12,#SWR ;RUN WITH TRT?
2786 012706 001010          BNE    2$
2787 012710 005167 000044          COM    .TBIT
2788 012714 100005          BPL    2$
2789 012716 052716 000020          BIS    #20,(6) ;SET TRACE TRAP
2790 012722 012746 012754          MOV    #3$,-(6) ;JUMP TO START OF TEST
2791 012726 000002          RTI
2792 012730 012746 012736      2$: MOV    #4$,-(6) ;JUMP TO START OF TEST
2793 012734 000002          RTI ;RETURN
2794 012736 013700 000042      4$: MOV    #42,R0 ;GET MONITOR ADDRESS
2795 012742 001404          BEQ    3$ ;IF NONE
2796 012744 004710          JSR    7,(0) ;GO TO MONITOR
2797 012746 000240          NOP
2798 012750 000240          NOP
2799 012752 000240          NOP
2800 012754 000137 001146      3$: JMP    #START ;RETURN
2801
2802 012760 000000          .TBIT: 0
2803
2804
2805 ;SUBROUTINE TO READ TTY INPUT AND SAVE OCTAL NUMBER
2806
2807 012762 004767 002124      READIN: JSR    PC_READ$
2808 012766 012702 015212          MOV    #INPUT,R2
2809 012772 012501          MOV    (R5)+,R1
2810 012774 005011          CLR    (R1)
2811 012776 112203      1$: MOVB  (R2)+,R3 ;STORE DATA
2812 013000 001420          BEQ    4$ ;BRANCH IF DONE
2813 013002 162703 000060          SUB    #60,R3
2814 013006 000241          CLC
2815 013010 032703 177770          BIT    #177770,R3
2816 013014 001010          BNE    2$
2817 013016 006311          ASL    (R1)
2818 013020 103407          BCS    3$
2819 013022 006311          ASL    (R1)
2820 013024 103405          BCS    3$
2821 013026 006311          ASL    (R1)
2822 013030 103403          BCS    3$
2823 013032 050311          BIS    R3,(R1)
2824 013034 000760          BR    1$
2825 013036 000261      2$: SEC ;SET C-BIT IF NOT
2826 013040 000244      3$: CLZ
2827 013042 000205      4$: RTS    R5

```

2828									
2829	013044	016746	165552		SPUSH:	MOV	RAND.B, -(SP)		
2830	013050	016746	165544			MOV	RAND.A, -(SP)		
2831	013054	016746	165546			MOV	RAND.D, -(SP)		
2832	013060	016746	165540			MOV	RAND.C, -(SP)		
2833	013064	000134			SPOLSH:	JMP	2(R4)+		
2834									
2835	013066	005767	165536		SPOPAD:	TST	\$ADDPS		;CHECK FOR ERROR
2836	013072	001145				BNE	\$SKIP		;BRANCH IF PS SET
2837	013074	032716	077600			BIT	#77600, (SP)		;CHECK FOR ZERO
2838	013100	001010				BNE	1\$		;BRANCH IF NOT
2839	013102	013767	177776	165520		MOV	2#PS, \$ADDPS		;Z-BIT IN PSW
2840	013110	005067	165516			CLR	\$ADD1		;ZERO ANSWER
2841	013114	005067	165514			CLR	\$ADD2		
2842	013120	000532				BR	\$SKIP		
2843									
2844	013122	005716			1\$:	TST	(SP)		;GET N-BIT, CLEAR C-BIT, V-BIT
2845	013124	013767	177776	165476		MOV	2#PS, \$ADDPS		;SET THE PSW SAVE
2846	013132	012667	165474			MOV	(SP)+, \$ADD1		
2847	013136	012667	165472			MOV	(SP)+, \$ADD2		
2848	013142	000134				JMP	2(R4)+		
2849									
2850	013144	005767	165470		SPOPSB:	TST	\$SUBPS		;CHECK FOR ERROR
2851	013150	001116				BNE	\$SKIP		;BRANCH IF PS SET
2852	013152	032716	077600			BIT	#77600, (SP)		;CHECK FOR ZERO
2853	013156	001010				BNE	1\$		;BRANCH IF NOT
2854	013160	013767	177776	165452		MOV	2#PS, \$SUBPS		;Z-BIT IN PSW
2855	013166	005067	165450			CLR	\$SUB1		;ZERO ANSWER
2856	013172	005067	165446			CLR	\$SUB2		
2857	013176	000503				BR	\$SKIP		
2858									
2859	013200	005716			1\$:	TST	(SP)		;GET N-BIT, CLEAR C-BIT, V-BIT
2860	013202	013767	177776	165430		MOV	2#PS, \$SUBPS		;SET THE PSW SAVE
2861	013210	012667	165426			MOV	(SP)+, \$SUB1		
2862	013214	012667	165424			MOV	(SP)+, \$SUB2		
2863	013220	000134				JMP	2(R4)+		
2864									
2865	013222	005767	165422		SPOPML:	TST	\$MULPS		;CHECK FOR ERROR
2866	013226	001067				BNE	\$SKIP		;BRANCH IF PS SET
2867	013230	032716	077600			BIT	#77600, (SP)		;CHECK FOR ZERO
2868	013234	001010				BNE	1\$		;BRANCH IF NOT
2869	013236	013767	177776	165404		MOV	2#PS, \$MULPS		;Z-BIT IN PSW
2870	013244	005067	165402			CLR	\$MUL1		;ZERO ANSWER
2871	013250	005067	165400			CLR	\$MUL2		
2872	013254	000454				BR	\$SKIP		
2873									
2874	013256	005716			1\$:	TST	(SP)		;GET N-BIT, CLEAR C-BIT, V-BIT
2875	013260	013767	177776	165362		MOV	2#PS, \$MULPS		;SET THE PSW SAVE
2876	013266	012667	165360			MOV	(SP)+, \$MUL1		
2877	013272	012667	165356			MOV	(SP)+, \$MUL2		
2878	013276	000134				JMP	2(R4)+		

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2879
2880 013300 032767 077600 165316 $POPDV: BIT #77600,RAND.C ;CHECK FOR DIVIDED BY ZERO
2881 013306 001010 BNE 1$
2882 013310 000277 SCC ;SET ALL CONDITION CODES
2883 013312 000244 CLZ ;CLEAR THE Z-BIT
2884 013314 013767 177776 165344 MOV @#PS, $SDIVER ;SET UP DIVIDE BY ZERO CC'S
2885 013322 012767 000340 165330 MOV #340, $SDIVPS ;SET UP PSW
2886 013330 005767 165324 1$: TST $SDIVPS ;CHECK FOR ERROR
2887 013334 001024 BNE $SKIP ;BRANCH IF PS SET
2888 013336 032716 077600 BIT #77600, (SP) ;CHECK FOR ZERO
2889 013342 001010 BNE 2$ ;BRANCH IF NOT
2890 013344 013767 177776 165306 MOV @#PS, $SDIVPS ;Z-BIT IN PSW
2891 013352 005067 165304 CLR $DIV1 ;ZERO ANSWER
2892 013356 005067 165302 CLR $DIV2
2893 013362 000411 BR $SKIP
2894
2895 013364 005716 2$: TST (SP) ;GET N-BIT, CLEAR C-BIT, V-BIT
2896 013366 013767 177776 165264 MOV @#PS, $SDIVPS ;SET THE PSW SAVE
2897 013374 012667 165262 MOV (SP)+, $DIV1
2898 013400 012667 165260 MOV (SP)+, $DIV2
2899 013404 000134 JMP @R4+
2900
2901 013406 022626 $SKIP: CMP (SP)+, (SP)+ ;POP GARBAGE OFF THE STACK
2902 013410 000134 JMP @R4+
2903
2904 013412 000204 $EXIT: RTS R4 ;EXIT POLISH MODE
2905
2906 013414 016500 000002 $ERR: MOV 2(5), RO ;PUT CODE INTO RO
2907 013420 022700 004003 $ERRA: CMP #4003, RO ;CHECK FOR DIVIDE BY ZERO
2908 013424 001464 BEQ 8$ ;SKIP OUT
2909
2910 013426 122700 000003 CMPB #3, RO ;CHECK FOR OVERFLOW
2911 013432 001006 BNE 2$ ;BRANCH IF NOT
2912 013434 000257 CCC ;CLEAR ALL CONDITION CODES
2913 013436 000262 SEV ;SET THE V-BIT
2914 013440 013767 177776 165146 MOV @#PS, $PSW ;SET UP PSW FOR OVERFLOW
2915 013446 000405 BR 3$
2916
2917 013450 000257 2$: CCC ;CLEAR ALL CONDITION CODES
2918 013452 000272 SMV ;SET N-BIT AND V-BIT
2919 013454 013767 177776 165132 MOV @#PS, $PSW ;SET UP PSW FOR UNDERFLOW
2920 013462 105000 3$: CLRB RO ;CLEAR LOW BYTE
2921 013464 000300 SWAB RO ;HIGH BYTE INTO LOW
2922 013466 162700 000002 SUB #2, RO ;CHECK FOR ADD/SUB
2923 013472 001021 BNE 5$ ;BRANCH IF NOT
2924 013474 005767 165130 TST $ADDP5 ;CHECK FOR ADD
2925 013500 001007 BNE 4$ ;BRANCH IF NOT
2926 013502 016767 165106 165126 MOV $PSW, $ADDER ;SET UP ADD ERROR PSW
2927 013510 012767 000340 165112 MOV #340, $ADDP5 ;SET UP ADD PSW
2928 013516 000427 BR 8$
2929
2930 013520 016767 165070 165120 4$: MOV $PSW, $SUBER ;SET UP SUBTRACT ERROR PSW
2931 013526 012767 000340 165104 MOV #340, $SUBPS ;SET UP SUBTRACT PSW
2932 013534 000420 BR 8$
2933
2934 013536 162700 000004 5$: SUB #4, RO ;CHECK FOR MUL

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2935 013542 003407          BLE      6$          ;BRANCH IF NOT
2936 013544 016767 165044 165104      MOV     $PSW, $MULR ;SET UP MULTIPLY ERROR PSW
2937 013552 012767 000340 165070      MOV     #340, $MULPS ;SET UP MULTIPLY PSW
2938 013560 000406          BR      8$
2939
2940 013562 016767 165026 165076 6$:     MOV     $PSW, $SDIVER ;SET UP DIVIDE ERROR PSW
2941 013570 012767 000340 165062 7$:     MOV     #340, $SDIVPS ;SET UP DIVIDE PSW
2942 013576 000205          8$:     RTS      R5          ;RETURN TO FORTRAN
2943
2944          ;SUBROUTINE TO PUSH DATA ONTO STACK
2945
2946 013600 016767 165016 165004 PUSHR:  MOV     RAND.B, STACK6 ;PUT DATA ON THE STACK
2947 013606 016767 165006 164774          MOV     RAND.A, STACK4
2948 013614 016767 165006 164764          MOV     RAND.D, STACK2
2949 013622 016767 164776 164754          MOV     RAND.C, STACK0
2950 013630 011637 000244          MOV     (SP), @#244 ;SET UP TRAP VECTOR
2951 013634 062737 000004 000244          ADD     #4, @#244
2952 013642 000207          RTS      PC
2953
2954 013644 032737 000400 177570 SCOPES: BIT     #SW08, @#SWR ;KILL LDUB OR LOOP ON SPEC. TEST
2955 013652 001412          BEQ     1$
2956 013654 013767 177570 000134          MOV     @#SWR, SCOTMP ;SAVE SWR
2957 013662 042767 177600 000126          BIC     #177600, SCOTMP ;CLR ALL BUT TEST NO.
2958 013670 126767 000122 165102          CMPB   SCOTMP, ICNT ;ON RIGHT TEST? *SW6-0*
2959 013676 001434          BEQ     OVERS
2960 013700 032737 040000 177570 1$:     BIT     #SW14, @#SWR ;LOOP ON TEST
2961 013706 001026          BNE    KITS
2962 013710 032737 004000 177570          BIT     #SW11, @#SWR ;KILL ITERATIONS
2963 013716 001012          BNE    SVLADS
2964 013720 105767 165055          TSTB   ICNT+1
2965 013724 001404          BEQ     2$          ;BRANCH IF FIRST
2966 013726 126767 000062 165045          CMPB   TIMES, ICNT+1 ;DONE?
2967 013734 001013          BNE    KITS          ;BRANCH IF NOT
2968 013736 112767 000001 165035 2$:     MOVB   #1, ICNT+1 ;FIRST ITERATION
2969 013744 105267 165030          SVLADS: INCB   ICNT ;COUNT TEST NUMBERS
2970 013750 011667 000036          MOV     (6), LADS ;SAVE LOOP ADDRESS
2971 013754 016737 165020 177570          MOV     ICNT, @#DISPLAY ;DISPLAY TEST NO. AND ITERATION COUNT
2972 013762 000002          RTI
2973
2974 013764 105267 165011          KITS:  INCB   ICNT+1
2975 013770 016737 165004 177570 OVERS:  MOV     ICNT, @#DISPLAY ;SET UP DISPLAY
2976 013776 005767 000010          TST    LADS ;FIRST ONE?
2977 014002 001760          BEQ     SVLADS
2978 014004 016716 000002          MOV     LADS, (6) ;FUDGE RETURN ADDRESS
2979 014010 000002          RTI ;FIXES PS
2980
2981 014012 000000          LADS:  0 ;LOOP ADDRESS
2982 014014 000377          TIMES: 377 ;RUN 377 TIMES
2983 014016 000000          SCOTMP: 0

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2984 014020 032737 002000 177570 HLTS: BIT      #SW10,2#SWR      ;BELL ON ERROR?
2985 014026 001402          BEQ      1$          ;NO - SKIP
2986 014030 000004 000007          TYPE     BELL        ;RING BELL
2987 014034 005267 164742          INC      ERRORS     ;COUNT THE NUMBER OF ERRORS
2988 014040 032737 020000 177570 1$: BIT      #SW13,2#SWR      ;SKIP TYPEOUT IF SET
2989 014046 001017          BNE     2$          ;SKIP TYPEOUTS
2990 014050 000004 015362          TYPE     RETURN
2991 014054 011667 000060          MOV     (6),HLTADS ;PUT ADDRESS OF INSTRUCTION ON STACK
2992 014060 162767 000002 000052          SUB     #2,HLTADS
2993 014066 016705 000046          MOV     HLTADS,TTY ;TYPE HLTADS IN OCTAL
2994 014072 004767 001300          JSR     %7,PRINTR  ;TYPE LEADING ZERO'S
2995 014076 000004 015370          TYPE     SPACE+3
2996 014102 004767 000034          JSR     PC, ERRORS ;GO TO USER ERROR ROUTINE
2997 014106 005737 177570          2$: TST     2#SWR      ;HALT ON ERROR
2998 014112 100001          BPL     .+4         ;SKIP IF CONTINUE
2999 014114 000000          HALT
3000 014116 032737 001000 177570          BIT      #SW09,2#SWR ;CHECK FOR INHIBIT LOOP ON ERROR
3001 014124 001001          BNE     .+4         ;SKIP IF LOOP ON ERROR
3002 014126 000002          RTI
3003 014130 105067 164645          CLRB   ICNT+1
3004 014134 000167 177624          JMP     KITS       ;LOOP ON TEST UNTIL NO ERRORS
3005
3006 014140 000000          HLTADS: 0
3007
3008 014142 010046          ERRORS: MOV     R0, -(SP) ;SAVE R0
3009 014144 010146          MOV     R1, -(SP) ;SAVE R1
3010 014146 000004 015370          TYPE     SPACE+3
3011 014152 016705 164442          MOV     RAND.A,TTY ;TYPE RAND.A IN OCTAL
3012 014156 004767 001214          JSR     %7,PRINTR  ;TYPE LEADING ZERO'S
3013 014162 000004 014702          TYPE     COMMA
3014 014166 016705 164430          MOV     RAND.B,TTY ;TYPE RAND.B IN OCTAL
3015 014172 004767 001200          JSR     %7,PRINTR  ;TYPE LEADING ZERO'S
3016 014176 013700 000244          MOV     2#244, R0  ;GET PC+2 OF INSTRUCTION
3017 014202 014001          MOV     -(R0), R1  ;GET THE INSTRUCTION
3018 014204 042701 177747          BIC     #177747,R1 ;MASK ALL BUT TYPE (+,-,*,/)
3019 014210 006201          ASR     R1         ;DIV BY 2
3020 014212 012767 014662 000006          MOV     #SIGNS, 1$ ;SET TO TOP OF SIGN TABLE
3021 014220 060167 000002          ADD     R1, 1$    ;ADD OFFSET
3022 014224 000004          TYPE
3023 014226 014662          1$: SIGNS ;TYPE THE RIGHT SIGN
3024 014230 016705 164370          MOV     RAND.C,TTY ;TYPE RAND.C IN OCTAL
3025 014234 004767 001136          JSR     %7,PRINTR  ;TYPE LEADING ZERO'S
3026 014240 000004 014702          TYPE     COMMA
3027 014244 016705 164356          MOV     RAND.D,TTY ;TYPE RAND.D IN OCTAL
3028 014250 004767 001122          JSR     %7,PRINTR  ;TYPE LEADING ZERO'S
3029 014254 006301          ASL     R1         ;RESET TABLE POINTER
3030 014256 062701 000630          ADD     #SADDP5,R1
3031 014262 105767 164326          TSTB   SPSW      ;CHECK FOR ERROR CONDITIONS
3032 014266 100460          BMI     3$         ;BRANCH IF ERROR
3033 014270 000004 014704          TYPE     HEAD1
3034 014274 000004 015062          TYPE     EXPECT
3035 014300 012105          MOV     (R1)+,TTY ;TYPE (R1)+ IN OCTAL
3036 014302 004767 001070          JSR     %7,PRINTR  ;TYPE LEADING ZERO'S
3037 014306 000004 015370          TYPE     SPACE+3
3038 014312 012705 000610          MOV     #STACK4,TTY ;TYPE #STACK4 IN OCTAL
3039 014316 004767 001054          JSR     %7,PRINTR  ;TYPE LEADING ZERO'S

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3040	014327	000004	015370		TYPE,	SPACE+3	
3041	014326	012105			MOV	(R1)+, TTY	;TYPE (R1)+ IN OCTAL
3042	014330	004767	001042		JSR	%7, PRINTR	;TYPE LEADING ZERO'S
3043	014334	000004	014702		TYPE,	COMMA	
3044	014340	011105			MOV	(R1), TTY	;TYPE (R1) IN OCTAL
3045	014342	004767	001030		JSR	%7, PRINTR	;TYPE LEADING ZERO'S
3046	014346	000004	015076		TYPE,	GO↑	
3047	014352	016705	164236		MOV	\$PSW, TTY	;TYPE \$PSW IN OCTAL
3048	014356	004767	001014		JSR	%7, PRINTR	;TYPE LEADING ZERO'S
3049	014362	000004	015370		TYPE,	SPACE+3	
3050	014366	016705	164224		MOV	\$SP, TTY	;TYPE \$SP IN OCTAL
3051	014372	004767	001000		JSR	%7, PRINTR	;TYPE LEADING ZERO'S
3052	014376	000004	015370		TYPE,	SPACE+3	
3053	014402	016705	164202		MOV	ANS1, TTY	;TYPE ANS1 IN OCTAL
3054	014406	004767	000764		JSR	%7, PRINTR	;TYPE LEADING ZERO'S
3055	014412	000004	014702		TYPE,	COMMA	
3056	014416	016705	164170		MOV	ANS2, TTY	;TYPE ANS2 IN OCTAL
3057	014422	004767	000750		JSR	%7, PRINTR	;TYPE LEADING ZERO'S
3058	014426	000510			BR	7\$	
3059							
3060	014430	000004	014751	3\$:	TYPE,	HEAD2	
3061	014434	000004	015062		TYPE,	EXPECT	
3062	014440	012105			MOV	(R1)+, TTY	;TYPE (R1)+ IN OCTAL
3063	014442	004767	000730		JSR	%7, PRINTR	;TYPE LEADING ZERO'S
3064	014446	000004	015370		TYPE,	SPACE+3	
3065	014452	016705	164212		MOV	SAVSTK, TTY	;TYPE SAVSTK IN OCTAL
3066	014456	004767	000714		JSR	%7, PRINTR	;TYPE LEADING ZERO'S
3067	014462	000004	015370		TYPE,	SPACE+3	
3068	014466	005720			TST	(R0)+	;UPDATE R0 TO RIGHT ADDRESS
3069	014470	010005			MOV	R0, TTY	;TYPE R0 IN OCTAL
3070	014472	004767	000700		JSR	%7, PRINTR	;TYPE LEADING ZERO'S
3071	014476	000004	015370		TYPE,	SPACE+3	
3072	014502	022121			CMP	(R1)+, (R1)+	;ADD 4 TO R1
3073	014504	011105			MOV	(R1), TTY	;TYPE (R1) IN OCTAL
3074	014506	004767	000664		JSR	%7, PRINTR	;TYPE LEADING ZERO'S
3075	014512	000004	015370		TYPE,	SPACE+3	
3076	014516	016705	164102		MOV	RAND.C, TTY	;TYPE RAND.C IN OCTAL
3077	014522	004767	000650		JSR	%7, PRINTR	;TYPE LEADING ZERO'S
3078	014526	000004	015370		TYPE,	SPACE+3	
3079	014532	016705	164070		MOV	RAND.D, TTY	;TYPE RAND.D IN OCTAL
3080	014536	004767	000634		JSR	%7, PRINTR	;TYPE LEADING ZERO'S
3081	014542	000004	015370		TYPE,	SPACE+3	
3082	014546	016705	164046		MOV	RAND.A, TTY	;TYPE RAND.A IN OCTAL
3083	014552	004767	000620		JSR	%7, PRINTR	;TYPE LEADING ZERO'S
3084	014556	000004	015370		TYPE,	SPACE+3	
3085	014562	016705	164034		MOV	RAND.B, TTY	;TYPE RAND.B IN OCTAL
3086	014566	004767	000604		JSR	%7, PRINTR	;TYPE LEADING ZERO'S
3087	014572	000004	015076		TYPE,	GO↑	
3088	014576	016705	164012		MOV	\$PSW, TTY	;TYPE \$PSW IN OCTAL
3089	014602	004767	000570		JSR	%7, PRINTR	;TYPE LEADING ZERO'S
3090	014606	000004	015370		TYPE,	SPACE+3	
3091	014612	016705	164000		MOV	\$SP, TTY	;TYPE \$SP IN OCTAL
3092	014616	004767	000554		JSR	%7, PRINTR	;TYPE LEADING ZERO'S
3093	014622	012701	000600		MOV	#STK1, R1	;SET UP TABLE POINTER
3094	014626	012700	000006		MOV	#6, R0	
3095	014632	000004	015370	6\$:	TYPE,	SPACE+3	

3096	014636	012105			MOV	(R1)+, TTY		:TYPE (R1)+ IN OCTAL
3097	014640	004767	000532		JSR	%7, PRINTR		:TYPE LEADING ZERO'S
3098	014644	005300			DEC	RO		:COUNT DOWN
3099	014646	001371			BNE	6\$		:KEEP TYPING
3100	014650	000004	015362	7\$:	TYPE,	RETURN		
3101	014654	012601			MOV	(SP)+, R1		:RESTORE R1
3102	014656	012600			MOV	(SP)+, RO		:RESTORE RO
3103	014660	000207			RTS	PC		
3104								
3105	014662	025440	000040	SIGNS:	.ASCIZ	" + "		
3106	014666	026440	000040		.ASCIZ	" - "		
3107	014672	025040	000040		.ASCIZ	" * "		
3108	014676	027440	000040		.ASCIZ	" / "		
3109								
3110	014702	000054		COMMA:	.ASCIZ	","		
3111								
3112	014704	005015	020040	020040	HEAD1:	.ASCIZ <15><12>"	PSW	SP ANSWER"
3113	014712	020040	020040	020040				
3114	014720	050040	053523	020040				
3115	014726	020040	051440	020120				
3116	014734	020040	020040	020040				
3117	014742	047101	053523	051105				
3118	014750	000						
3119	014751	015	020012	020040	HEAD2:	.ASCIZ <15><12>"	PSW	SP STK1 STK2 STK3 STK4 STK5
3120	014756	020040	020040	020040				
3121	014764	020040	051520	020127				
3122	014772	020040	020040	050123				
3123	015000	020040	020040	051440				
3124	015006	045524	020061	020040				
3125	015014	051440	045524	020062				
3126	015022	020040	051440	045524				
3127	015030	020063	020040	051440				
3128	015036	045524	020064	020040				
3129	015044	051440	045524	020065				
3130	015052	020040	051440	045524				
3131	015060	000066						
3132	015062	005015	054105	042520	EXPECT:	.ASCIZ <15><12>"EXPECT: "		
3133	015070	052103	020072	000040				
3134	015076	005015	047507	035124	GOT:	.ASCIZ <15><12>"GOT: "		
3135	015104	020040	020040	000040				
3136					.EVEN			

3137											
3138	015112	010346			READS:	MOV	R3, -(6)			:	SAVE R3
3139	015114	012703	015212		1\$:	MOV	#INPUT, R3			:	GET ADDRESS
3140	015120	022703	015252		2\$:	CMP	#.QUES, R3			:	CHECK FOR BUFFER OVERFLOW
3141	015124	001412				BEQ	4\$			:	ABORT
3142	015126	105737	177560			TSTB	@#177560			:	WAIT FOR
3143	015132	100375				BPL	-4			:	A CHARACTER
3144	015134	113713	177562			MOVB	@#177562, (3)			:	GET CHARACTER
3145	015140	142713	000200			BICB	#200, (3)			:	GET RID OF JUNK
3146	015144	122713	000177			CMPB	#177, (3)			:	IS IT A RUBOUT
3147	015150	001003				BNE	3\$			:	SKIP IF NOT
3148	015152	000004	015252		4\$:	TYPE	1\$ .QUES			:	TYPE A '?'
3149	015156	000756				BR	1\$			:	ZAP THE BUFFER AND LOOP
3150	015160	111367	000210		3\$:	MOVB	(3), .TYPE			:	SET UP FOR TYPING
3151	015164	000004	015374			TYPE	.TYPE			:	ECHO IT
3152	015170	122723	000015			CMPB	#15, (3)+			:	CHECK FOR RETURN
3153	015174	001351				BNE	2\$			:	LOOP IF NOT RETURN
3154	015176	105063	177777			CLRB	-1(3)			:	ZAP RETURN (THE 15)
3155	015202	000004	000012			TYPE	12			:	TYPE A LINE FEED
3156	015206	012603				MOV	(6)+, R3			:	RESTORE R3
3157	015210	000207				RTS	PC			:	RETURN
3158											
3159	015212	000020			INPUT:	.BLKW	20				
3160	015252	006477	000012		.QUES:	.ASCIZ	"?"<15><12>				
3161											
3162	015256	010546			.IOT:	MOV	TTY, -(6)			:	SAVE TTY
3163	015260	017605	000002			MOV	@2(6), TTY			:	GET ADDRESS TO BE TYPED
3164	015264	032705	177400			BIT	#177400, TTY			:	IS IT A TYPEN?
3165	015270	001004				BNE	1\$			:	NO
3166	015272	010567	000076			MOV	TTY, .TYPE			:	GET THE CHARACTER
3167	015276	012705	015374			MOV	#.TYPE, TTY			:	FUDGE THE ADDRESS
3168	015302	105715			1\$:	TSTB	(TTY)			:	TERMINATOR?
3169	015304	001406				BEQ	2\$			:	GET OUT IF SO
3170	015306	112537	177566			MOVB	(TTY)+, @#177566			:	LOAD AND TYPE THE CHARACTER
3171	015312	105737	177564			TSTB	@#177564			:	IS THE PRINTER READY
3172	015316	100375				BPL	-4			:	WAIT UNTIL IT IS
3173	015320	000770				BR	1\$			:	GET THE NEXT CHARACTER
3174	015322	017646	000002		2\$:	MOV	@2(6), -(6)			:	GET ADDRESS TO BE TYPED
3175	015326	062766	000002	000004		ADD	#2, 4(6)			:	ADD 2 TO THE ADDRESS
3176	015334	022666	000002			CMP	(6)+, 2(6)			:	IS IT .+2?
3177	015340	001006				BNE	3\$			:	NO
3178	015342	062705	000002			ADD	#2, TTY			:	ADD 2 TO THE ADDRESS
3179	015346	042705	000001			BIC	#1, TTY			:	BACK UP TO AN EVEN BYTE
3180	015352	010566	000002			MOV	TTY, 2(6)			:	RESTORE ADDRESS
3181	015356	012605			3\$:	MOV	(6)+, TTY			:	RESTORE TTY
3182	015360	000002				RTI				:	RETURN
3183											
3184	015362	005015	000		RETURN:	.ASCIZ	<15><12>			:	RETURN AND LINEFEED
3185	015365	015	020012	020040	SPACE:	.ASCIZ	<15><12>" "			:	RETURN AND 3 SPACES
3186	015372	000									
3187		015374			.EVEN						
3188	015374	000000			.TYPE:	0				:	CHARACTER TYPE LOCATION



3189											
3190	015376	112767	000001	000130	PRINTR:	MOVB	#1,.PR				;SET ZERO FILL SWITCH
3191	015404	000402				BR	+.6				;SKIP
3192	015406	005067	000122		PRINTS:	CLR	.PR				;SUPRESS LEADING ZERO'S
3193	015412	112767	177772	000115		MOVB	#-6,.PR+1				;SET COUNT
3194	015420	010446				MOV	R4,-(6)				;SAVE R4
3195	015422	012704	015524			MOV	#.PRBUF,R4				;SET POINTER TO FIRST ASCII CHAR.
3196	015426	105014				CLRB	(4)				;CLEAR FIRST BYTE
3197	015430	000405				BR	.PRF				;ROTATE FIRST BIT
3198	015432	105014			.PRL:	CLRB	(4)				;CLEAR BYTE OF CHARACTER
3199	015434	006105				ROL	TTY				;ROTATE BIT INTO C
3200	015436	106114				ROLB	(4)				;PACK IT
3201	015440	006105				ROL	TTY				;ROTATE BIT INTO C
3202	015442	106114				ROLB	(4)				;PACK IT
3203	015444	006105			.PRF:	ROL	TTY				;ROTATE BIT INTO C
3204	015446	106114				ROLB	(4)				;PACK IT
3205	015450	105714				TSTB	(4)				;IS IT ZERO?
3206	015452	001402				BEQ	+.6				;SKIP INC
3207	015454	105267	000054			INCB	.PR				;SET FILL SWITCH
3208	015460	105767	000050			TSTB	.PR				;CHECK FILL SWITCH
3209	015464	001402				BEQ	+.6				;SKIP BITSET
3210	015466	152724	000060			BISB	#'0,(4)+				;MAKE INTO ASCII CHAR
3211	015472	105267	000037			INCB	.PR+1				;INC COUNT
3212	015476	001355				BNE	.PRL				;REPEAT
3213	015500	022704	015524			CMP	#.PRBUF,R4				;EMPTY BUFFER?
3214	015504	001002				BNE	+.6				;SKIP IF NOT
3215	015506	112724	000060			MOVB	#'0,(4)+				;LOAD I ZERO
3216	015512	105014				CLRB	(4)				;NULL TERMINATOR
3217	015514	000004	015524			TYPE	.PRBUF				;TYPE IT
3218	015520	012604				MOV	(6)+,R4				;RESTORE R4
3219	015522	000207				RTS	PC				;RETURN
3220											
3221	015524	000004			.PRBUF:	.BLKW	4				;OUTPUT BUFFER
3222	015534	000000			.PR:	0					;COUNT AND SWITCH

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3223
3224 015536 012777 015652 000120 PDOWN$: MOV #ILLUP, @PUVECS ;SET FOR FAST UP
3225 015544 012777 000340 000114 MOV #340, @PUVECS+2 ;PRIO:7
3226 015552 010046 MOV R0, -(6) ;PUSH R0 ON STACK
3227 015554 010146 MOV R1, -(6) ;PUSH R1 ON STACK
3228 015556 010246 MOV R2, -(6) ;PUSH R2 ON STACK
3229 015560 010346 MOV R3, -(6) ;PUSH R3 ON STACK
3230 015562 010446 MOV R4, -(6) ;PUSH R4 ON STACK
3231 015564 010546 MOV R5, -(6) ;PUSH R5 ON STACK
3232 015566 010667 000064 MOV SP, SAVR6 ;SAVE SP
3233 015572 012777 015602 000064 MOV #PUPS, @PUVECS ;SET UP VECTOR
3234 015600 000000 HALT
3235
3236 015602 016706 000050 PUPS: MOV .SAVR6, SP ;GET SP
3237 015606 005001 CLR R1 ;WAIT LOOP FOR THE TTY
3238 015610 005201 1$: INC R1 ;WAIT FOR THE INC
3239 015612 001376 BNE 1$ ;OF WORD
3240 015614 012605 MOV (6)+, R5 ;POP STACK INTO R5
3241 015616 012604 MOV (6)+, R4 ;POP STACK INTO R4
3242 015620 012603 MOV (6)+, R3 ;POP STACK INTO R3
3243 015622 012602 MOV (6)+, R2 ;POP STACK INTO R2
3244 015624 012601 MOV (6)+, R1 ;POP STACK INTO R1
3245 015626 012600 MOV (6)+, R0 ;POP STACK INTO R0
3246 015630 012777 015536 000022 MOV #PDOWN$, @PDVECS ;SET UP THE POWER DOWN VECTOR
3247 015636 012777 000340 000016 MOV #340, @PDVECS+2 ;PRIO:7
3248 015644 000004 015670 TYPE ,POWERS
3249 015650 000002 RTI
3250
3251 015652 000000 ILLUP: HALT ;THE POWER UP SEQUENCE WAS STARTED
3252 015654 000776 BR .-2 ; BEFORE THE POWER DOWN WAS COMPLETE
3253
3254 015656 000000 .SAVR6: 0 ;PUT THE SP HERE
3255 015660 000024 000026 PDVECS: 24, 26 ;POWER DOWN VECTOR
3256 015664 000024 000026 PUVECS: 24, 26 ;POWER UP VECTOR
3257 015670 005015 047520 042527 POWERS: .ASCIZ <15><12>"POWER"
3258 015676 000122 .EVEN
3259
3260
3261 000001 .END

```

ANS1	000610	584#	754	839	924	1009	1095	1166	1237	1309	1380	1451	1522	1593
		1664	1735	1807	1878	1949	2020	2091	2162	2233	2305	2376	2447	2518
ANS2	000612	2589	2660	2731	3053									
		585#	756	841	926	1011	1099	1170	1241	1313	1384	1455	1526	1597
		1668	1739	1811	1882	1953	2024	2095	2166	2237	2309	2380	2451	2522
		2593	2664	2735	3056									
BEGIN	001010	564	642#											
BELL	= 000007	517#	2783	2986										
BIT0	= 000001	535#												
BIT1	= 000002	536#	758	760	770	772								
BIT10	= 002000	545#												
BIT11	= 004000	546#												
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BIT14	= 040000	549#												
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BIT2	= 000004	537#	843	845	855	857								
BIT3	= 000010	538#	928	930	940	942								
BIT4	= 000020	539#	1013	1015	1025	1027								
BIT5	= 000040	540#												
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BIT7	= 000200	542#												
BIT8	= 000400	543#												
BIT9	= 001000	544#												
CCC	= 000257	511#												
COMMA	014702	3013	3026	3043	3055	3110#								
DISPLA=	177570	516#	2971*	2975*										
DONE	012662	2780#												
ERRORS	001002	639#	659*	2987*										
ERRORS	014142	2996	3008#											
EXPECT	015062	3034	3061	3132#										
FADD	= 075000	511#												
FDIV	= 075030	511#												
FISTRP	000754	632#	653											
FMUL	= 075020	511#												
FORTAN	001360	670	707#											
FSUB	= 075010	511#												
GOT	015076	3046	3087	3134#										
HEAD1	014704	3033	3112#											
HEAD2	014751	3060	3119#											
HLT	= 104000	512#	632	752	775	782	786	790	794	798	802	806	837	860
		867	871	875	879	883	887	891	922	945	952	956	960	964
		968	972	976	1007	1030	1037	1041	1045	1049	1053	1057	1061	1085
		1093	1097	1101	1108	1112	1116	1120	1124	1128	1132	1156	1164	1168
		1172	1179	1183	1187	1191	1195	1199	1203	1227	1235	1239	1243	1251
		1255	1259	1263	1267	1271	1275	1299	1307	1311	1315	1322	1326	1330
		1334	1338	1342	1346	1370	1378	1382	1386	1393	1397	1401	1405	1409
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		1512	1520	1524	1528	1535	1539	1543	1547	1551	1555	1559	1583	1591
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		1677	1681	1685	1689	1693	1697	1701	1725	1733	1737	1741	1749	1753
		1757	1761	1765	1769	1773	1797	1805	1809	1813	1820	1824	1828	1832
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		1915	1939	1947	1951	1955	1962	1966	1970	1974	1978	1982	1986	2010
		2018	2022	2026	2033	2037	2041	2045	2049	2053	2057	2081	2089	2093
		2097	2104	2108	2112	2116	2120	2124	2128	2152	2160	2164	2168	2175

		2179	2183	2187	2191	2195	2199	2223	2231	2235	2239	2247	2251	2255
		2259	2263	2267	2271	2295	2303	2307	2311	2318	2322	2326	2330	2334
		2338	2342	2366	2374	2378	2382	2389	2393	2397	2401	2405	2409	2413
		2437	2445	2449	2453	2460	2464	2468	2472	2476	2480	2484	2508	2516
		2520	2524	2531	2535	2539	2543	2547	2551	2555	2579	2587	2591	2595
		2602	2606	2610	2614	2618	2622	2626	2650	2658	2652	2666	2673	2677
		2681	2685	2689	2693	2697	2721	2729	2733	2737	2745	2749	2753	2757
		2761	2765	2769										
HLTADS	014140	2991*	2992*	2993	3006#									
HLTS	014020	649	2984#											
ICNT	001000	638#	664*	2958	2964	2966	2968*	2969*	2971	2974*	2975	3003*		
ILLUP	015652	3224	3251#											
INPUT	015212	2808	3139	3159#										
KITS	013764	2961	2967	2974#	3004									
LADS	014012	665*	2970*	2976	2978	2981#								
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LEVEL1=	000040	552#												
LEVEL2=	000100	553#												
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LEVEL4=	000200	555#												
LEVEL5=	000240	556#												
LEVEL6=	000300	557#												
LEVEL7=	000340	558#												
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		1637#	1708#	1780#	1851#	1922#	1993#	2064#	2135#	2206#	2278#	2349#	2420#	2491#
		2562#	2633#	2704#	2776#									
OVERS	013770	2959	2975#											
PC	=%000007	526#	627*	669*	736*	821*	906*	991*	1076*	1147*	1218*	1290*	1361*	1432*
		1503*	1574*	1645*	1716*	1788*	1859*	1930*	2001*	2072*	2143*	2214*	2286*	2357*
		2428#	2499*	2570*	2641*	2712*	2807*	2952*	2996*	3103*	3157*	3219*		
		640#	660*	661*	2777*	2778*								
PCNT	001004	647	3224#	3246										
PDOWNB	015536	3246#	3247*	3255#										
PDVECS	015660	3248	3257#											
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PRINTR	015376	3063	3066	3070	3074	3077	3080	3083	3086	3089	3092	3097	3190#	
PRINTS	015406	3192#												
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		1295	1366	1437	1508	1579	1650	1721	1793	1864	1935	2006	2077	2148
		2219	2291	2362	2433	2504	2575	2646	2717	2839	2845	2854	2860	2869
		2875	2884	2890	2896	2914	2919							
PUPS	015602	3233	3236#											
PUSHR	013600	736	821	906	991	1076	1147	1218	1290	1361	1432	1503	1574	1645
		1716	1788	1859	1930	2001	2072	2143	2214	2286	2357	2428	2499	2570
		2641	2712	2946#										
PVECS	015664	3224*	3225*	3233*	3256#									
RAND.A	000620	589#	619*	625	626*	655*	686	800	885	970	1055	1126	1197	1269
		1340	1411	1482	1553	1624	1695	1767	1838	1909	1980	2051	2122	2193
		2265	2336	2407	2478	2549	2620	2691	2763	2830	2947	3011	3082	
RAND.B	000622	590#	619	621*	624*	656*	691	804	889	974	1059	1130	1201	1273
		1344	1415	1486	1557	1628	1699	1771	1842	1913	1984	2055	2126	2197
		2269	2340	2411	2482	2553	2624	2695	2767	2829	2946	3014	3085	
RAND.C	000624	591#	621	622*	623*	657*	697	792	877	962	1047	1118	1189	1261
		1332	1403	1474	1545	1616	1687	1759	1830	1901	1972	2043	2114	2185
		2257	2328	2399	2470	2541	2612	2683	2755	2832	2880	2949	3024	3076
RAND.D	000626	592#	618*	620*	623	625*	658*	703	796	881	966	1051	1122	1193







	1675	1722*	1731	1747	1794*	1803	1818	1865*	1874	1889	1936*	1945	1960
	2007*	2016	2031	2078*	2087	2102	2149*	2158	2173	2220*	2229	2245	2292*
	2301	2316	2363*	2372	2387	2434*	2443	2458	2505*	2514	2529	2576*	2585
	2600	2647*	2656	2671	2718*	2727	2743	3050	3091				
\$SUBER 000646	602#	851*	873	1185	1470	1755	2039	2324	2608	2930*			
\$SUBPS 000640	599#	708*	828	852*	1154	1439	1723	2008	2293	2577	2850	2854*	2860*
	2931*												
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\$SUB2 000644	601#	841	846*	858*	1170	1455	1739	2024	2309	2593	2856*	2862*	
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	886	890	921	951	955	959	963	967	971	975	1006	1036	1040
	1044	1048	1052	1056	1060	1084	1092	1096	1100	1107	1111	1115	1119
	1123	1127	1131	1155	1163	1167	1171	1178	1182	1186	1190	1194	1198
	1202	1226	1234	1238	1242	1250	1254	1258	1262	1266	1270	1274	1298
	1306	1310	1314	1321	1325	1329	1333	1337	1341	1345	1369	1377	1381
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	1467	1471	1475	1479	1483	1487	1511	1519	1523	1527	1534	1538	1542
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	1625	1629	1653	1661	1665	1669	1676	1680	1684	1688	1692	1696	1700
	1724	1732	1736	1740	1748	1752	1756	1760	1764	1768	1772	1796	1804
	1808	1812	1819	1823	1827	1831	1835	1839	1843	1867	1875	1879	1883
	1890	1894	1898	1902	1906	1910	1914	1938	1946	1950	1954	1961	1965
	1969	1973	1977	1981	1985	2009	2017	2021	2025	2032	2036	2040	2044
	2048	2052	2056	2080	2088	2092	2096	2103	2107	2111	2115	2119	2123
	2127	2151	2159	2163	2167	2174	2178	2182	2186	2190	2194	2198	2222
	2230	2234	2238	2246	2250	2254	2258	2262	2266	2270	2294	2302	2306
	2310	2317	2321	2325	2329	2333	2337	2341	2365	2373	2377	2381	2388
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	2550	2554	2578	2586	2590	2594	2601	2605	2609	2613	2617	2621	2625
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.IOT 015256	645	3162#											
.PR 015534	3190*	3192*	3193*	3207*	3208	3211*	3222#						
.PRBUF 015524	3195	3213	3217	3221#									
.PRF 015444	3197	3203#											
.PRL 015432	3198#	3212											
.QUES 015252	3140	3148	3160#										
.SAVR6 015656	3232*	3236	3254#										
.TBIT 012760	2787*	2802#											
.TYPE 015374	3150*	3151	3166*	3167	3188#								





ADC	620	622	624	626	762	847	932	1017	2778						
ADD	619	621	623	625	761	846	931	1016	2777	2951	3021	3030	3175	3178	
ASL	2817	2819	2821	3029											
ASR	3019														
BCS	687	692	698	704	2818	2820	2822								
BEQ	751	757	781	785	789	793	797	801	805	836	842	856	870	874	878
	882	886	890	921	927	951	955	959	963	967	971	975	1006	1012	1036
	1040	1044	1048	1052	1056	1060	1084	1092	1096	1100	1107	1111	1115	1119	1123
	1127	1131	1155	1163	1167	1171	1178	1182	1186	1190	1194	1198	1202	1226	1234
	1238	1242	1250	1254	1258	1262	1266	1270	1274	1298	1306	1310	1314	1321	1325
	1329	1333	1337	1341	1345	1369	1377	1381	1385	1392	1396	1400	1404	1408	1412
	1416	1440	1448	1452	1456	1463	1467	1471	1475	1479	1483	1487	1511	1519	1523
	1527	1534	1538	1542	1546	1550	1554	1558	1582	1590	1594	1598	1605	1609	1613
	1617	1621	1625	1629	1653	1661	1665	1669	1676	1680	1684	1688	1692	1696	1700
	1724	1732	1736	1740	1748	1752	1756	1760	1764	1768	1772	1796	1804	1808	1812
	1819	1823	1827	1831	1835	1839	1843	1867	1875	1879	1883	1890	1894	1898	1902
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	2107	2111	2115	2119	2123	2127	2151	2159	2163	2167	2174	2178	2182	2186	2190
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BIC	2957	3018	3179												
BICB	3145														
BIS	760	845	930	1015	2789	2823									
BISB	772	857	942	1027	3210										
BIT	758	843	928	1013	2781	2785	2815	2837	2852	2867	2880	2888	2954	2960	2962
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BNE	693	699	705	744	755	759	771	829	840	844	856	914	925	929	941
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	3214	3239													
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BR	670	768	777	853	862	938	947	1023	1032	1103	1174	1246	1317	1388	1459
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	3191	3197	3252												
BVC	763	848	933	1018											
CCC	764	849	934	1019	2912	2917									
CLC	2814														
CLR	659	660	661	664	665	666	707	708	709	710	2784	2810	2840	2841	2855
	2856	2870	2871	2891	2892	3192	3237								
CLRB	2920	3003	3154	3196	3198	3216									
CLZ	2826	2883													
CMP	743	750	754	756	780	784	788	792	796	800	804	828	835	839	841
	865	869	873	877	881	885	889	913	920	924	926	950	954	958	962
	966	970	974	998	1005	1009	1011	1035	1039	1043	1047	1051	1055	1059	1083
	1091	1095	1099	1106	1110	1114	1118	1122	1126	1130	1154	1162	1166	1170	1177

	1181	1185	1189	1193	1197	1201	1225	1233	1237	1241	1245	1249	1253	1257	1261
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	2787														
	3098														
CMPB															
COM															
DEC															
DECB	618														
EMT	512														
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FSUB	824	1150	1435	1719	2004	2289	2573								
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IOT	513														
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	3097														
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	657	658	662	663	735	741	742	749	766	767	779	808	820	826	827
	834	851	852	864	893	905	911	912	919	936	937	949	978	990	996
	997	1004	1021	1022	1034	1063	1075	1081	1082	1090	1105	1134	1146	1152	1153
	1161	1176	1205	1217	1223	1224	1232	1248	1277	1289	1295	1296	1304	1319	1348
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	1517	1532	1561	1573	1579	1580	1588	1603	1632	1644	1650	1651	1659	1674	1703
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	2071	2077	2078	2086	2101	2130	2142	2148	2149	2157	2172	2201	2213	2219	2220
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	2861	2862	2869	2875	2876	2877	2884	2885	2890	2896	2897	2898	2906	2914	2919
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	2971	2975	2978	2991	2993	3008	3009	3011	3014	3016	3017	3020	3024	3027	3035
	3038	3041	3044	3047	3050	3053	3056	3062	3065	3069	3073	3076	3079	3082	3085
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DBKEBA.P11 CROSS REFERENCE TABLE -- PERMANENT SYMBOLS

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ROL	3199	3201	3203												
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RTI	633	809	894	979	1064	1135	1206	1278	1349	1420	1491	1562	1633	1704	1776
	1847	1918	1989	2060	2131	2202	2274	2345	2416	2487	2558	2629	2700	2772	2791
	2793	2972	2979	3002	3182	3249									
RTS	627	2827	2904	2942	2952	3103	3157	3219							
RTT	630														
SBC	774	859	944	1029											
SCC	2882														
SEC	2825														
SEV	765	850	935	1020	2913										
SUB	773	858	943	1028	2813	2922	2934	2992							
SWAB	2921														
TRAP	511														
TST	2835	2844	2850	2859	2865	2874	2886	2895	2924	2976	2997	3068			
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	1818	1869	1885	1889	1940	1956	1960	2011	2027	2031	2082	2098	2102	2153	2169
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	2529	2580	2596	2600	2651	2667	2671	2722	2738	2743	2783	2784	2794	2988	2990
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	1866	1878	1886	1937	1949	1957	2008	2020	2028	2079	2091	2099	2150	2162	2170
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