

RK611
RK06, RK07

RK611/06 SS VFY1
CZR6ME0

AH-9138E-MC
FICHE 1 OF 2

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A large grid of approximately 15 columns and 25 rows of small, illegible data tables. Each cell in the grid contains a small table with multiple columns and rows of text, likely representing individual data points or records from a larger dataset. The text is too small to be read accurately.

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FICHE 2 OF 2

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IDENTIFICATION

PRODUCT CODE: AC-9136E-MC
PRODUCT NAME: CZR6MEO RK611/06 SS VFY1
DATE: JUNE 1980
MAINTAINER: DIAGNOSTIC GROUP

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1.0 ABSTRACT

THIS PROGRAM PERFORMS PART 1 OF THE RK06/RK06-RK07 SUBSYSTEM VERIFICATION TESTS, WHICH PROVIDE A FUNCTIONAL SHAKEDOWN OF THE ENTIRE SUBSYSTEM, INCLUDING THE UNIBUS INTERFACE AND ACCESS TO MAIN MEMORY. THE TESTING IN PART 1 EMPLOYS WORST-CASE SITUATIONS INVOLVING MECHANICAL POSITIONING, DISK ADDRESSING AND DATA TRANSFER. IN ADDITION, MEASUREMENTS ARE MADE PERTAINING TO DRIVE OPERATIONAL TIMING.

WITHIN THE VARIOUS SUBSYSTEM TESTS, EMPHASIS IS GIVEN TO USEFUL SCOPE LOOPS AND OPERATOR SPECIFICATION OF TEST PARAMETER VALUES. AT THE BEGINNING OF TESTING, THE FOLLOWING OPTIONS MAY BE SPECIFIED BY THE USER:

- RK611 REGISTER ADDRESS
- RK06/07 VECTOR ADDRESS
- RK06/07 PRIORITY LEVEL
- DRIVE(S) TO BE TESTED
- TEST(S) TO BE RUN
- NUMBER OF TEST ITERATIONS
- DISK ADDRESS LIMITS
- DISK ADDRESS INCREMENTS
- DATA PATTERNS USED
- PHYSICAL MEMORY ADDRESS ON DATA TRANSFERS
- WORD COUNT ON DATA TRANSFERS
- STALL TIME BETWEEN OPERATIONS

IN ADDITION, STANDARD SWITCH REGISTER OPTIONS ARE PROVIDED, FOR A VARIETY OF PROGRAM LOOPING, RUNNING, AND REPORTING MODES.

ERRORS WILL BE REPORTED AS THEY OCCUR. THE REPORT WILL INCLUDE A TEST NUMBER AND DESCRIPTION, CURRENT AND PREVIOUS OPERATIONS, GOOD AND BAD TEST DATA, AND APPLICABLE DEVICE REGISTER CONTENTS.

2.0 HARDWARE REQUIREMENTS

THE FOLLOWING HARDWARE IS REQUIRED TO RUN PART 1 OF THE SUBSYSTEM VERIFICATION TESTS:

- PDP-11/04, (05,10 MFG. ONLY), 20,34,35,40,45,50,70 OR PDD
- 16 K MEMORY
- CONSOLE TELETYPE
- KW11-L OR KW11-P CLOCK
- RK06/07 UNIBUS CONTROLLER (RK611)
- 1 TO 8 RK06/07 DRIVES
- 1 TO 8 RK06/07 DISK CARTRIDGES (FORMATTED IN 20 OR 22 SECTOR FORMAT)

NOTE

IF NEITHER KW11-L OR P CLOCK IS PROVIDED, THE TIMING TESTS IN SECTION

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9.2 WILL BE BYPASSED, AND A MESSAGE WILL
INFORM THE OPERATOR.

3.0 PRELIMINARY PROGRAM REQUIREMENTS

BEFORE RUNNING THE SUBSYSTEM VERIFICATION PROGRAM, THE RK611
CONTROLLER DIAGNOSTIC (CZR6A THRU DZR6E AND CZR6K) AND RK06
DRIVE DIAGNOSTIC (CZR6H THRU CZR6J) SHOULD FIRST BE RUN, TO
RESOLVE BASIC, SOLID HARDWARE FAULTS.

4.0 GENERAL PROGRAM CONSIDERATIONS

4.1 SYSMAC

THIS PROGRAM USES PORTIONS OF THE SYSMAC DIAGNOSTIC SYSTEM MACRO
PACKAGE.

4.2 XXDP

THIS PROGRAM MAY BE LOADED UNDER XXDP. SUBSYSTEM TESTS 1-21 MAY BE
RUN IN CHAIN OR DUMP MODE, BUT DUAL-ACCESS TEST 22 MAY ONLY BE RUN IN
DUMP MODE.

4.2.1 CHAIN MODE

THIS PROGRAM IS DESIGNED TO BE RUN IN THE DEFAULT MODE WHEN CHAINED
UNDER XXDP. THUS, INPUT DIALOGUE IS BYPASSED, AND RK06 UNIBUS ADDRESS
AND INTERRUPT VECTOR ARE DEFAULTED TO STANDARD VALUES. ALL DRIVES
PRESENT AND READY WILL BE TESTED WITH THE EXCEPTION OF DRIVE 0, IF THE
RK06 CONTAINS THE XXDP MEDIUM.

4.2.2 DUMP MODE

THE PROGRAM CAN BE RUN IN DUMP MODE, WITH OR WITHOUT DEFAULT
PARAMETERS. DRIVE 0 MAY BE TESTED, BUT IF IT CONTAINS THE XXDP
MEDIUM, THE OPERATOR MUST REPLACE THE XXDP PACK WITH A FORMATTED
SCRATCH PACK PRIOR TO TESTING. A MESSAGE WILL INFORM THE OPERATOR
WHEN THIS IS NECESSARY.

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4.3 ACT/APT

THE PROGRAM IS COMPATIBLE WITH ACT/APT CONVENTIONS. SUBSYSTEM TESTS 1-21 MAY BE RUN IN AUTOMATIC OR DUMP MODE.

4.3.1 AUTOMATIC MODE

THE PROGRAM PROVIDES FOR AUTOMATIC APT/ACT OPERATION. IN THIS MODE, PARAMETERS REVERT TO DEFAULT VALUES, AND ALL DRIVES WHICH ARE PRESENT AND READY ARE TESTED.

4.3.2 DUMP MODE

IN DUMP MODE, PARAMETERS MAY BE INPUT, OR DEFAULTED, AND SUBSYSTEM TESTS 1-21 OR DUAL-ACCESS DATA TEST 26 MAY BE RUN.

4.3.3 APT ETABLE DEFINITIONS

THE FOLLOWING DEFINITIONS ARE VALID FOR SPECIFYING APT ENVIRONMENTAL TABLE (ETABLE) ENTRIES, VIA THE APT UTILITY PROGRAMS :

1. SOFTWARE ENVIRONMENT

- =1 IF APT SCRIPT MODE
- =0 IF STANDALONE MODE

2. ENVIRONMENT MODE BYTE

- BIT 7 = 1 ETABLE DOES SIZING
- = 0 PROGRAM DOES SIZING
- BIT 6 = 1 SPOOL MESSAGES TO APT IF SCRIPT MODE
- = 0 DON'T SPOOL TO APT
- BIT 5 = 1 SUPPRESS CONSOLE OUTPUT
- = 0 ALLOW CONSOLE OUTPUT
- BITS 4-0 NOT USED

3. SWITCH 1 (SOFTWARE SWITCH REGISTER)

IF ENVIRONMENT MODE BIT 7 (SIZING BIT) IS SET TO 1, THE SOFTWARE SWITCH REGISTER WILL BE USED, INSTEAD OF THE HARDWARE CONSOLE SWITCH REGISTER. REGARDLESS OF WHICH ONE IS USED, ALL BITS DEFINED IN SECTION 6.2 (SWITCH REGISTER OPTIONS) MAY BE USED WHEN RUNNING IN STANDALONE MODE. IN APT SCRIPT MODE, HOWEVER, BIT 14 (LOOP ON TEST) MUST ALWAYS BE SET TO 0.

4. SWITCH 2 (USER SWITCH REGISTER)

NOT USED

5. MEMORY TYPES 1-4 AND MAX MEMORY ADDRESSES

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NOT USED

6. INTERRUPT VECTOR 1
USED WHEN ENVIRONMENT MODE BIT 7 = 1. DEFAULT = 210

7. BUS PRIORITY 1
USED WHEN ENVIRONMENT MODE BIT 7 = 1. DEFAULT = 5

8. INTERRUPT VECTOR 2
NOT USED

9. BUS PRIORITY 2
NOT USED

10. BASE ADDRESS
USED WHEN ENVIRONMENT MODE BIT 7 = 1. DEFAULT = 177440

11. DEVICE MAP
USED WHEN ENVIRONMENT MODE BIT 7 = 1. EACH BIT SET TO 1 IN BITS
0-7 WILL SELECT THE CORRESPONDING DRIVE TO BE TESTED.
BITS 8-15 ARE NOT USED.

12. REMAINING ETABLE ENTRIES ARE NOT USED.

4.4 DUAL-ACCESS

THIS PROGRAM IS DESIGNED TO UTILIZE DUAL-ACCESS IN TEST 22 ONLY
(DUAL-ACCESS DATA TEST - SEE SECTION 9.6). FOR THE PURPOSES OF ALL
OTHER TESTS (1-21). THE OPERATOR MUST GUARANTEE THAT THERE IS NO
INTERFERENCE FROM THE UNUSED PORT. IF FAILURES ARE ENCOUNTERED IN
TESTS 1-21 DUE TO INTERFERENCE FROM THE OTHER PORT, THE OPERATOR IS
ADVISED TO SWITCH THAT PORT OFF-LINE AND RE-RUN THE TESTS.

4.5 MEMORY MANAGEMENT

THIS PROGRAM SUPPORTS MEMORY MANAGEMENT OPTIONS KT11C,D AND PDP11/70,
FOR THE PURPOSES OF TESTS 21 AND 22 ONLY. IN THESE TESTS, NPR
TRANSFERS BETWEEN THE RK06 AND ANY SPECIFIED PHYSICAL MEMORY ABOVE THE
PROGRAM, ARE PERFORMED, DURING READ/WRITE TESTING. (NOTE - TESTS 21
AND 22 ALSO SUPPORT 22-BIT ADDRESSING AND THE UNIBUS MAP, IF
INSTALLED). IN ALL OTHER TESTS, MEMORY MANAGEMENT IS DISABLED.

4.6 MEMORY PARITY CHECK

IF THE MEMORY PARITY CHECK OPTION IS AVAILABLE ON THE SYSTEM, ALL
TESTING IS DONE WITH PARITY CHECK ENABLED. PARITY ERRORS ARE
REPORTED, AND TESTING IS ABORTED, UNLESS THE BYTE LABELED MEMABT WAS

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PREVIOUSLY LOADED WITH A NON-ZERO VALUE.

4.7 BAD SECTORS

ACCORDING TO A CONTROL SWITCH OPTION (SEE SECTION 8.2.4.2) THE BAD SECTOR FILE ON EACH DRIVE TO BE TESTED IS TYPED AT THE CONSOLE PRIOR TO TESTING. ALL DATA ERRORS OCCURRING IN THE PROGRAM ARE MASKED OUT IF THEY OCCUR ON SECTORS DESIGNATED AS BAD (BY EITHER FACTORY OR SOFTWARE).

4.8 EXECUTION TIME

EXECUTION TIME IS DEPENDENT UPON PARAMETERS INPUT BY THE OPERATOR (SUCH AS STALL TIME OR ITERATION COUNT), AND TO A LESSER DEGREE, UPON THE PROCESSOR. HOWEVER, THE "AVERAGE" TIME REQUIRED TO RUN A QUICK-VERIFICATION (FIRST PASS) IS 14 MINUTES PER DRIVE. A SUBSEQUENT PASS WITH PARAMETERS DEFAULTED REQUIRES 21 MINUTES PER DRIVE.
NOTE: TIMES ARE APPROX. DOUBLED FOR RK07 TESTING.

5.0 PROGRAM LOADING

THE PROGRAM CAN BE LOADED FROM PAPER TAPE USING THE ABSOLUTE LOADER OR FROM ANY MEDIA SUPPORTED BY XXDP.

6.0 STARTING PROCEDURE

6.1 STARTING ADDRESSES

200 NORMAL STARTING ADDRESS OF TESTS 1-21 (PARAMETERS DEFAULTED)
204 SELECT OPERATING PARAMETERS, RK06/07 UNIBUS ADDR. AND INTERRUPT VECTOR FOR TESTS 1-21
220 DUAL-ACCESS DATA TEST 22 START ADDRESS

6.2 SWITCH REGISTER OPTIONS USED

THIS PROGRAM IS DESIGNED TO ALLOW THE USE OF THE HARDWARE SWITCH REGISTER IF PRESENT, OR THE SYSMAC-SUPPORTED SOFTWARE SWITCH REGISTER (IF HARDWARE SWR IS NOT PRESENT OR IS SET TO ALL ONES). IN EITHER CASE, THE FOLLOWING OPTIONS ARE IMPLEMENTED WHEN THE APPROPRIATE BITS ARE SET TO 1:

CZRMEO RK611/06 SS Vi Y1
CZR6ME.P11 16-JUN-80 09:00

MACY11 30A(1052) 23-JUN-80 10:15 J 1 PAGE 11

SEQ 0009

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BIT OPTION

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15 HALT ON ERROR
14 LOOP ON TEST
13 INHIBIT ERROR REPORTS
12 REPORT DESCRIPTION ONLY, ON ERRORS
11 INHIBIT ITERATIONS
10 BELL ON ERROR
09 LOOP ON ERROR
08 APPLY RANDOM STALL BETWEEN OPERATIONS
07 DO EXPLICIT SEEKS IN TESTS 1-12
06 REPORT ONE ERROR PER TRANSFER IN TESTS 17,21
05 INHIBIT WRITES IN TEST 21
04 INHIBIT WRITE CHECKS IN TEST 21
03 INHIBIT READS AND SOFTWARE COMPARES IN TEST 21
02 INHIBIT SOFTWARE COMPARES IN TEST 21
01 READ AFTER A WRITE CHECK ERROR IN TEST 21
00 REPORT ALL SOFTWARE COMPARE ERRORS IN TESTS 17,21

NOTE

FOR ADDITIONAL PROGRAM CONTROL OPTIONS,
SEE DESCRIPTION OF CONTROL SWITCH WORD
(CS), SECTION 8.2.4.2.

7.0 OPERATOR ACTION

1. LOAD PROGRAM INTO MEMORY (SEE SECTION 5.0)
2. LOAD A FORMATTED PACK ON EACH DRIVE TO BE TESTED.
3. BRING DRIVE (S) TO ONLINE STATE, WRITE ENABLED, AND LOCKED ON PORT.
4. LOAD THE DESIRED STARTING ADDRESS (SEE SECTION 6.1)
5. SET SWITCHES IF DESIRED (SEE SECTION 6.2)
6. PRESS START OR GIVE APPROPRIATE MONITOR START COMMAND.

8.0 PROGRAM ACTION

8.1 DESCRIPTION OF OPERATING PARAMETERS

AFTER THE PROGRAM IS STARTED, IT TYPES ITS IDENTIFICATION, AS FOLLOWS: "CZR6MD RK611/RK06-RK07 SUBSYSTEM VERIFICATION: PART 1," FOLLOWED BY: "LAST PHYS MEM ADR=XXXXXXXX". THEN, EITHER THE TESTS BEGIN EXECUTION WITH DEFAULT PARAMETERS, OR AN OPERATOR INTERACTIVE MODE IS ENTERED (SEE STARTING ADDRESSES, SECTION 6.1). IN THIS MODE OPERATING PARAMETERS MAY BE LISTED OR ALTERED BY VARIOUS MEANS. ALL PARAMETERS ARE TREATED AS OCTAL NUMBERS, AND THE PARAMETER LIST IS

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SHARED AMONG THE TESTS.

THE FOLLOWING IS THE LIST OF OPERATING PARAMETERS, (WHICH APPLY TO THE CURRENT SELECTION OF DRIVES AND TESTS). AFTER EACH, IS INDICATED THE VALID RANGE OF VALUES FOR THAT PARAMETER (IN OCTAL), AND ITS DEFAULT VALUE. ALL PARAMETERS ARE ENTERED AS OCTAL NUMBERS, AND THE PARAMETER MNEMONICS SHOWN ARE THOSE USED BY THE PROGRAM.

FC	FIRST CYLINDER ADDRESS IN TESTS WHICH ARE DONE OVER A RANGE OF CYLINDERS, FC=0-631, DEFAULT=0
LC	LAST CYLINDER ADDRESS IN TESTS WHICH ARE DONE OVER A RANGE OF CYLINDERS, LC=0-632, DEFAULT=632
IC	CYLINDER INCREMENT VALUE IN TESTS WHICH STEP THROUGH CYLINDER ADDRESSES, IC=1-632, DEFAULT=1
FT	FIRST TRACK ADDRESS IN TESTS WHICH ARE DONE OVER A RANGE OF TRACKS, FT=0-2, DEFAULT=0
LT	LAST TRACK ADDRESS IN TESTS WHICH ARE DONE OVER A RANGE OF TRACKS, LT=0-2, DEFAULT=2
IT	TRACK INCREMENT VALUE IN TESTS WHICH ARE DONE OVER A RANGE OF TRACKS, IT=1-2, DEFAULT=1
S0	FIRST SECTOR ADDRESS IN TESTS WHICH ARE DONE OVER A RANGE OF SECTORS, USED ON ANY DRIVE WHICH HAS A 20-SECTOR FORMATTED CARTRIDGE. S0=0-23, DEFAULT=0
S1	LAST SECTOR ADDRESS IN TESTS WHICH ARE DONE OVER A RANGE OF SECTORS, USED ON ANY DRIVE WHICH HAS A 20-SECTOR FORMATTED CARTRIDGE. S1=0-23, DEFAULT=23
S2	FIRST SECTOR ADDRESS IN TESTS WHICH ARE DONE OVER A RANGE OF SECTORS, USED ON ANY DRIVE WHICH HAS A 22-SECTOR FORMATTED CARTRIDGE. S2=0-25, DEFAULT=0
S3	LAST SECTOR ADDRESS IN TESTS WHICH ARE DONE OVER A RANGE OF SECTORS, USED ON ANY DRIVE WHICH HAS A 22-SECTOR FORMATTED CARTRIDGE. S3=0-25, DEFAULT=25
IS	SECTOR INCREMENT VALUE IN TESTS WHICH ARE DONE OVER A RANGE OF SECTORS, IS=1-25, DEFAULT=1
PT	DATA PATTERN SELECT WORD (SEE SECTION 8.2.4.1 FOR DETAILS) PT=0-177777, DEFAULT=0
CS	CONTROL SWITCH WORD (SEE SECTION 8.2.4.2 FOR DETAILS) CS=0-000070, DEFAULT=0
WC	WORD COUNT (NO. OF WORDS) USED IN DISK READ, WRITE, OR WRITE CHECK COMMANDS IN DATA TEST 20, WC=0-177777,

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DEFAULT=MAXIMUM BUFFER AVAILABLE, WC=0 IS INTERPRETED AS 65,536 WORDS

MA PHYSICAL MEMORY ADDRESS (STARTING ADDRESS OF DATA BUFFER) USED ON ALL DATA TRANSFERS IN TESTS 21, 22. MA MAY BE GREATER THAN OR EQUAL TO ADDRESS OF RWBUF (NOT EXCEEDING AVAILABLE MEMORY ON THE SYSTEM). DEFAULT = ADDRESS OF RWBUF.

ST NUMBER OF UNIT STALL TIMES WITH WHICH TO STALL (DELAY) BETWEEN RK06 COMMANDS

SM MAXIMUM NUMBER OF UNIT STALL TIMES USED IN TEST 12 ONLY

8.2 SELECTION OF OPERATING PARAMETERS (ADDRESS 204 START)

8.2.1 DRIVE SELECTION

AFTER AN ADDRESS 204 START, THE PROGRAM INDICATES INPUT MODE BY TYPING 'PARAMETER INPUT MODE'. THEN, THE RK611 REGISTER ADDRESS, RK06 VECTOR ADDRESS, AND RK06 PRIORITY ARE OPENED FOR POSSIBLE MODIFICATION AS FOLLOWS:

RK06-07 BUS ADR = 177440 NEW=(TYPE NEW VALUE HERE)
RK06-07 VEC ADR = 210 NEW=(TYPE NEW VALUE HERE)
RK06-07 PRIORITY = 5 NEW=(TYPE NEW VALUE HERE)

NEXT THE PROGRAM TYPES THE NUMBER(S) OF THE CURRENT DRIVES UNDER TEST, FOLLOWED BY A (*) ON THE NEXT LINE TO REQUEST NEW DRIVE NUMBERS. FOR EXAMPLE:

PARAMETER INPUT MODE
DRIVE(S)=0,1,2,4,7
* (INPUT, IF ANY, GOES HERE)

THE OPERATOR TYPES THE NEW NUMBERS (SEPARATED BY COMMAS) PLUS <CR> OR SIMPLY <CR> TO LEAVE THEM UNCHANGED. IF HE ENTERS DRIVE NUMBERS, THESE DRIVES ARE THEN CHECKED FOR VALID STATUS BY THE PROGRAM, AND ALL THOSE TYPED WHICH ARE ACCEPTABLE WILL NOW BE LISTED AS BEFORE. THE OPERATOR MAY MANUALLY RECONFIGURE THE DRIVES AND TYPE IN NEW DRIVE NUMBERS, AND THESE WILL BE CHECKED AND TYPED, UNTIL THE OPERATOR FINALLY TYPES JUST <CR>.

ON INITIAL ENTRY FROM ADDRESS 204, THE DRIVES WHICH ARE LISTED ARE ALL THOSE ON THE SUBSYSTEM WHICH ARE ON-LINE, READY, WRITE-ENABLED, AND LOADED WITH A CARTRIDGE OTHER THAN AN ALIGNMENT PACK. (THIS IS THE DEFAULT DRIVE SELECTION FOR ADDRESS 200 START). ON ALL OTHER ENTRIES TO THE DRIVE SELECTION ROUTINE (VIA <^C> AS DESCRIBED IN SECTIONS

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8.2.2.5 AND 8.2.3.6) THE PROGRAM FIRST TYPES:

TO TEST AL DRIVES TYPE 'A'<CR>, ELSE <CR>
* (CHARACTER GOES HERE)

NEXT, THE DRIVES ARE LISTED AS SHOWN ABOVE. IF (A) IS TYPED, ALL AVAILABLE DRIVES WILL BE LISTED, AND IF NOT, THE PREVIOUS SELECTION OF DRIVES WILL BE LISTED.

8.2.2 TEST SELECTION

WHEN THE OPERATOR FINALLY TYPES JUST <CR> IN RESPONSE TO THE DRIVE REQUEST, THE PROGRAM ENTERS THE TEST INPUT ROUTINE. THE FOLLOWING INSTRUCTIVE LINES ARE TYPED :

L = LIST TESTS
C = CHANGE TESTS
I = INPUT PARAMETERS AND RUN TESTS

THEN, THE FOLLOWING REQUEST IS MADE :

ENTER L,C, OR I
* (CHARACTER GOES HERE)

THE OPERATOR TYPES THE DESIRED CHARACTER PLUS <CR>.

8.2.2.1 LIST TESTS, (L)

IF (L) IS TYPED, THE PROGRAM TYPES THE CURRENT DESIRED TEST LIST, AS FOLLOWS :

TEST	ITERATIONS
1	0
2	177777
3	400
4	25
ETC.	

THE ITERATION NUMBER IS THE NUMBER OF TIMES THE TEST WILL BE RUN ON THIS PASS, AND IT MUST BE BETWEEN 0 AND 177777. IF IT IS ZERO, THAT TEST WILL NOT BE RUN. ON THE FIRST TIME THROUGH, ALL TESTS ARE LISTED WITH DEFAULT ITERATIONS. THE LIST OF DEFAULT ITERATION NUMBERS BEGINS AT ADDRESS 'DFLTST', AND CAN BE ALTERED IN CORE.

WHEN THE LIST IS COMPLETED, THE PROGRAM RETURNS TO TYPE : 'ENTER L,C, OR I' AGAIN.

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8.2.2.2 CHANGE TESTS, (C)

IF (C) IS TYPED, THE PROGRAM THEN ASKS: "TO DEFAULT TESTS TYPE D<CR>, ELSE <CR>". IF JUST <CR> IS TYPED, THE PROGRAM SEQUENTIALLY OPENS EACH TEST FOR POSSIBLE ALTERATION OF THE ITERATION NUMBER. EACH TEST IS OPENED AND A NEW LINE IS TYPED, GIVING THE TEST NO. AND ITER. NO. IN OCTAL, FOLLOWED BY (*) ON THE SAME LINE. FOR EXAMPLE :

```
TEST      ITERATIONS
0          0 * (INPUT, IF ANY, GOES HERE)
```

THE OPERATOR THEN TYPES THE NEW VALUE PLUS <CR>, OR JUST <CR> TO LEAVE IT UNCHANGED. IF THE OPERATOR TYPES A NUMBER FOLLOWED BY (!) (EXCLAMATION POINT) THE NUMBER JUST ENTERED WILL BE LOADED INTO THE ITERATION TABLE FOR ALL REMAINING TESTS. AFTER ALL TESTS HAVE BEEN OPENED, THE PROGRAM RETURNS TO TYPE : "ENTER L,C, OR I" AGAIN.

8.2.2.3 INPUT PARAMETERS AND RUN TESTS, (I)

IF (I) IS TYPED, THE PROGRAM ENTERS THE PARAMETER LIST ALTERATION ROUTINE, DESCRIBED IN SECTION 8.2.3, IN WHICH OPERATING PARAMETERS MAY BE INPUT, AND TESTING BEGUN.

8.2.2.4 CONTROL Z (^Z) FUNCTION

IF THE OPERATOR WISHES TO EXIT AT ANY TIME FROM L OR C MODE, AND RETURN TO SELECT A NEW MODE (L,C, OR I), CONTROL Z (^Z) MAY BE TYPED.

8.2.2.5 CONTROL C (^C) FUNCTION

IF THE OPERATOR WISHES TO TERMINATE L,C, OR I MODE, AND RETURN TO REQUEST NEW DRIVES AND TESTS, CONTROL C (^C) MAY BE TYPED.

8.2.3 PARAMETER LIST ALTERATION

THE PROGRAM NEXT TYPES THE FOLLOWING INSTRUCTIVE LINES:

```
T = TYPE LIST
O = OPEN LIST
S = SET INDIVIDUAL PARAM.
R = RUN TESTS
ENTER T,O,S, OR R
* (CHARACTER GOES HERE)
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THE OPERATOR TYPES THE DESIRED CHARACTER PLUS <CR>.

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8.2.3.1 TYPE LIST, (T)

IF (T) IS TYPED, THE PROGRAM TYPES THE CURRENT PARAMETER LIST, AS FOLLOWS:

```
FC = XXXXXX  
LC = XXXXXX  
ETC.
```

WHEN THE LIST IS COMPLETED, THE PROGRAM RETURNS TO TYPE 'ENTER T,O,S, OR R' AGAIN.

8.2.3.2 OPEN LIST, (O)

IF (O) IS TYPED THE PROGRAM THEN ASKS: "TO DEFAULT ALL PARAMETERS TYPE D<CR>, ELSE<CR>." IF JUST <CR> IS TYPED, THE PROGRAM SEQUENTIALLY OPENS EACH OPERATING PARAMETER IN THE LIST FOR ALTERATION (IN THE ORDER SHOWN IN SECTION 8.1). EACH PARAMETER IS OPENED AND A NEW LINE IS TYPED, GIVING THE PARAMETER AND ITS CURRENT VALUE (IN OCTAL), FOLLOWED BY (*) ON THE SAME LINE. FOR EXAMPLE:

```
IC=3 * (INPUT, IF ANY, GOES HERE)
```

THE OPERATOR THEN TYPES THE NEW VALUE PLUS A <CR> OR JUST <CR> TO LEAVE THAT PARAMETER UNCHANGED. AFTER ALL PARAMETERS HAVE BEEN OPENED FOR ALTERATION, THE PROGRAM RETURNS TO TYPE 'ENTER T,O,S, OR R' AGAIN.

THE LIST OF DEFAULT PARAMETERS BEGINS AT ADDR 3 'PRDFLT', AND CAN BE ALTERED IN CORE.

8.2.3.3 SET INDIVIDUAL PARAMETER, (S)

IF (S) IS TYPED, THE PROGRAM TYPES THE PROMPTER (>) ON THE NEXT LINE, AND WAITS FOR THE OPERATOR TO SET THE VALUE OF ANY INDIVIDUAL PARAMETER:

```
ENTER T,O,S, OR R  
*S  
> (ENTER PARAMETER AND VALUE HERE)
```

THE OPERATOR TYPES THE PARAMETER AND ITS NEW VALUE FOLLOWED BY <CR>, AND THE PROGRAM GIVES THE PROMPTER (>) AGAIN AS IN THE FOLLOWING EXAMPLE:

```
> FC = 600  
> FS = 12  
> IT = 1  
> ETC.
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TO TERMINATE INDIVIDUAL SETTING MODE, THE OPERATOR TYPES CONTROL Z (^Z). AND THE PROGRAM RETURNS TO TYPE 'ENTER T,O, S, OR R' AGAIN.

8.2.3.4 RUN TESTS, (R)

IF THE OPERATOR TYPES (R), EXECUTION OF TESTS BEGINS, USING ALL THE CURRENT PARAMETERS.

8.2.3.5 CONTROL Z (^Z) FUNCTION

IF THE OPERATOR WISHES TO EXIT AT ANY TIME FROM T, O, S, OR R MODE (RUNNING TESTS), AND RETURN TO SELECT A NEW MODE (T,O,S, OR R), CONTROL Z (^Z) MAY BE TYPED.

8.2.3.6 CONTROL C (^C) FUNCTION

IF THE OPERATOR WISHES TO TERMINATE T, O, OR S MODE, OR STOP TEST EXECUTION (R MODE), CONTROL C (^C) MAY BE TYPED, AND THE PROGRAM WILL RETURN TO REQUEST NEW DRIVE SELECTION (SEE SECTION 8.2.1). (ON AN ADDRESS 200 DEFAULT RUN, TYPING CONTROL C CAUSES THE PROGRAM TO HALT AFTER COMPLETING ANY DATA TRANSFER).

8.2.4 SPECIAL PARAMETER SPECIFICATIONS

8.2.4.1 PT-DATA PATTERN SELECT WORD

THE OPERATOR SPECIFIES A WORD OF SIXTEEN BITS (6 OCT. DIGITS), TO SELECT UP TO ALL SIXTEEN OF THE PATTERNS, LISTED IN SECTION 9.5.1. THE NO. OF ANY BIT SET IN PT CORRESPONDS TO THE NO. OF A PATTERN CHOSEN. IF THE HIGH BIT OF PT (BIT 15) IS SET, THE USER WILL BE ASKED TO TYPE UP TO SIXTEEN DATA WORDS WHICH WILL BE LOADED INTO THE BUFFER FOR PATTERN 15. THE PROGRAM TYPES:

"SELECT USER DEFINED PATTERN 15"

NEXT, EACH OF UP TO 16 WORDS ARE REQUESTED BY THE PROGRAM IN THE SAME MANNER IN WHICH THE OPERATING PARAMETERS ARE OPENED FOR ALTERATION (SEE SECT. 8.2.3.2):

WORD 0 = (OLD VALUE) * (NEW VALUE GOES HERE)
WORD 1 = (OLD VALUE) * (NEW VALUE GOES HERE)
ETC.

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FOR EACH WORD, THE USER TYPES THE NEW VALUE PLUS <CR>, OR JUST <CR>, (TO LEAVE IT UNCHANGED). TO INPUT LESS THAN 16 WORDS FOR PATTERN 15, THE OPERATOR MAY TYPE (!) (EXCLAMATION POINT) AND THE PROGRAM WILL FILL UP THE REST OF THE PATTERN 15 BUFFER WITH THE LAST WORD SPECIFIED. NEXT, THE PROGRAM WILL CONTINUE, IN THE PROPER PARAMETER INPUT MODE (O OR S).

8.2.4.2 CS- CONTROL SWITCH WORD

IN ADDITION TO THE OPTIONS PROVIDED BY THE SWITCH REGISTER (HARDWARE OR SOFTWARE), A CONTROL SWITCH WORD (CS) MAY BE SPECIFIED AMONG THE OPERATING PARAMETERS TO PROVIDE THE FOLLOWING OPTIONS:

BIT	OPTION
---	-----
05	DROP DRIVE IF 20(DEC) ERRORS EXCEEDED
04	TYPE BAD SECTOR FILES (BSF'S) ON FIRST PASS
03	INHIBIT TIMING REPORTS IN TESTS 13-16

NOTE

OTHER BITS UNUSED

9.0 DESCRIPTION OF TESTS

9.1 SEEK TESTS

THIS GROUP OF TESTS PERFORMS A VARIETY OF POSITIONING OPERATIONS. THROUGH THE EXECUTION OF READ HEADER COMMANDS, IMPLIED SEEKS ARE DONE TO SELECTED CYLINDERS, AND HEADER WORDS ARE READ AND CHECKED TO VERIFY THAT THE CORRECT CYLINDER WAS ADDRESSED. TESTING BEGINS WITH SIMPLE OPERATIONS WHICH VERIFY CYLINDER ADDRESSING CAPABILITY, AND PROCEEDS TO MORE INVOLVED SEEKING WHICH STRESSES THE SERVO MECHANISM. AT THE COMPLETION OF EACH SUBSYSTEM COMMAND, STATUS INDICATIONS AND ERROR BITS ARE CHECKED TO DETERMINE THE SUCCESS OF THE OPERATION. THROUGHOUT TESTING, SECTOR=FS, AND TRACK=FT.

9.1.1 TEST 1 - RECALIBRATE/SEEK TEST

THIS TEST WILL PERFORM A RECALIBRATE COMMAND (POSITION TO CYLINDER 0), FOLLOWED BY A SEEK TO CYLINDER LC. (AS IN ALL OF THE SEEK TESTS SEEKING IS IMPLIED, VIA THE READ HEADER COMMAND).

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9.1.2 TEST 2 - SEEK/SEEK TEST

THIS TEST WILL PERFORM A SEEK TO CYLINDER FC, FOLLOWED BY A SEEK TO CYLINDER LC.

9.1.3 TEST 3 - CYLINDER ADDRESSING TEST

THIS TEST VERIFIES THE CYLINDER ADDRESS BITS, BY SEEKING TO CYLINDERS 0, 1, 2, 4, 10, 20, 40, 100, 200, AND 400 (OCTAL). THEN, THE SEEKS ARE DONE IN REVERSE BACK TO CYLINDER 0. IN BINARY, THE CYLINDER ADDRESSES SEQUENCE AS FOLLOWS:

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9.1.4 TEST 4 - CYLINDER ADDRESS CROSSTALK TEST

THIS TEST PERFORMS SEEKS TO CYLINDERS WHOSE ADDRESSES ARE SUSCEPTIBLE TO BIT CROSSTALK WITHIN THE CYLINDER ADDRESSING LOGIC. FIRST A SEEK TO CYLINDER 0 IS DONE, FOLLOWED BY A SEEK TO 377 (OCTAL). THEN A SEEK TO 0 IS DONE AGAIN, AND A SEEK TO 376, ETC. THE CYLINDER ADDRESSES SEQUENCE AS FOLLOWS:

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BY SEEKING TO 0 BETWEEN CROSSTALK SEEKS, THE CROSSTALK PATTERNS ALSO TEST THE CYLINDER DIFFERENCE LOGIC.

9.1.5 TEST 5 - INCREMENT/DECREMENT SEEK TEST

IN THIS TEST SEEKS ARE DONE IN INCREMENTS OF IC CYLINDERS STARTING AT CYL FC, AND ENDING AT OR BEYOND LC. THEN, THE SEEKS ARE DONE IN REVERSE, BACK TO FC. IF FC IS CHOSEN > LC, THE SEEKS WILL PROCEED IN THE OPPOSITE DIRECTION.

9.1.6 TEST 6 - OSCILLATING SEEK TEST

THIS TEST FIRST SEEKS TO CYLINDER FC, WHICH IS INITIALLY EQUAL TO NC. THEN NC IS INCREMENTED BY IC, AND A SEEK FROM FC TO NC IS MADE, FOLLOWED BY A SEEK BACK TO FC. NC IS INCREMENTED AGAIN, A D SEEKING FROM FC TO NC TO FC IS REPEATED UNTIL NC EXCEEDS LC. THEN, THE REVERSE IS DONE, DECREMENTING NC UNTIL NC EXCEEDS FC.

9.1.7 TEST 7 - CONVERGING/DIVERGING SEEK TEST

THIS TEST PERFORMS SEEKS WHICH EXERCISE CYLINDER DIFFERENCE VALUES. INITIALLY, NCYL1 = FC AND NCYL2 = LC. A SEEK IS DONE TO NCYL1 AND THEN TO NCYL2, THEN, NCYL1 IS INCREMENTED BY IC IN THE DIRECTION OF LC, AND NCYL2 IS INCREMENTED BY IC IN THE DIRECTION OF FC, AND SEEKS ARE DONE TO NCYL1 AND NCYL2 AGAIN. THIS SEEKING IS CONTINUED WITH INCREMENTED VALUES OF NCYL1 AND NCYL2, UNTIL NCYL1 EXCEEDS LC AND NCYL2 EXCEEDS FC. (NOTE: FC > LC IS PERMISSIBLE.) THIS TEST CAUSES SEEKING TO CONVERGING AND THEN DIVERGING CYLINDER VALUES.

9.1.8 TEST 10 - PSEUDO-RANDOM SEEK TEST

THIS TEST PERFORMS A SEEK TO A PSEUDO-RANDOMLY CHOSEN CYLINDER WHICH IS WITHIN THE RANGE (0-632), (0-1456 FOR RK07)

9.1.9 TEST 11 - MAXIMUM VELOCITY REVERSAL SEEK TEST

THIS TEST PERFORMS A SEEK FROM CYLINDER 0 TO CYLINDER 201(OCT), AND BACK TO CYLINDER 0. THIS PARTICULAR SEEK CAUSES THE HEADS TO ACCELERATE TO MAXIMUM VELOCITY, AND THEN IMMEDIATELY DECELERATE, WITH NO APPRECIABLE PLATEAU OF CONSTANT VELOCITY. THIS OPERATION INDUCES HEATING IN THE SERVO MECHANISM.

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9.1.10 TEST 12 - MECHANICAL VIBRATION SEEK TEST

THIS TEST PERFORMS GEOMETRICALLY INCREASING SEEKS WITH GEOMETRICALLY DECREASING STALL TIME BETWEEN OPERATIONS WITH INTENT TO INDUCE VARYING VIBRATIONAL MODES UPON THE DRIVE. THE TEST BEGINS WITH LC = 1, AND ST = SM, THEN, THE FOLLOWING SEQUENCE IS PERFORMED: SEEKS ARE DONE BETWEEN 0 AND LC, 10(DEC) TIMES. THEN, ST IS DIVIDED BY 2 AND LC IS DOUBLED, AND SEEKS ARE DONE BETWEEN 0 AND LC AGAIN, 10(DEC) TIMES. THIS PROCESS IS CONTINUED FOR NEW VALUES OF ST AND LC, UNTIL LC EXCEEDS CYL 400 (OCT). THEN, THE WHOLE PROCESS IS REVERSED, WITH ST BEING DOUBLED AND LC DIVIDED BY 2, UNTIL LC BECOMES < 1.

9.2 TIMING TESTS

THIS GROUP OF TESTS PERFORMS TIMING MEASUREMENTS OF BASIC DRIVE OPERATIONS - SPINDLE ROTATION AND SEEKING. FOR EACH TEST THE FUNCTION BEING MEASURED IS TIMED FOR A GIVEN NUMBER OF OCCURENCES, AND THE MINIMUM, MAXIMUM, AND AVERAGE VALUES ARE TYPED ALONG WITH THE NUMBER OF OPERATIONS WHICH EXCEEDED THE LIMITS SPECIFIED IN THE RK06/07 DISK DRIVE SPECIFICATION.

NOTE- FOR 50 HZ OPERATION, PATCH 1 INTO LOCATION 166 (LABELED HZ:).

9.2.1 TEST 13 - MAXIMUM ROTATIONAL LATENCY MEASUREMENT

THIS TEST MEASURES THE INTERVAL OF TIME BETWEEN 2 INDEX MARKS IN A SINGLE DISK ROTATION. THE SPECIFIED MAXIMUM ROTATIONAL LATENCY = 25 MILLI-SEC + OR - 2.5%. THIS MEASUREMENT IS MADE 128 TIMES.

SAMPLE PRINTOUT --

ROTATIONAL TIMES :
MIN = 24086 US 103 OF 128 BELOW SPEC'D MIN OF 24375 US
MAX = 25065 US 0 OF 128 ABOVE SPEC'D MAX OF 25625 US
AVG = 24285 US

9.2.2 TEST 14 - ONE CYLINDER SEEK TIME MEASUREMENT

THIS TEST MEASURES THE TIME REQUIRED TO SEEK BETWEEN 2 ADJACENT CYLINDERS, BOTH IN THE FORWARD AND REVERSE DIRECTIONS. SEEKS ARE DONE TO 0,1,2,...,631,632 AND THEN TO 631,630,...,2,1,0 AND THE RESULTS ARE TYPED FOR EACH DIRECTION. THE SPECIFIED ONE CYL SEEK TIME IS < 8 MILLI-SEC. NOTE: SEEKS ARE TO CYL 1456 FOR THE RK07.

SAMPLE PRINTOUT --

ONE CYL SEFK TIMES :

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FORWARD DIRECTION
MIN = 6332 US
MAX = 6749 US 0 OF 410 ABOVE SPEC'D MAX OF 8000 US
AVG = 6536 US
REVERSE DIRECTION
MIN = 6314 US
MAX = 6749 US 0 OF 410 ABOVE SPEC'D MAX OF 8000 US
AVG = 6532 US

9.2.3 TEST 15 - AVERAGE SEEK TIME MEASUREMENT

THIS TEST MEASURES THE TRUE AVERAGE SEEK TIME IN BOTH THE FORWARD AND REVERSE DIRECTIONS. THE AVERAGE TIME IS CALCULATED FROM THE FOLLOWING FORMULA :

$$T \text{ AVG} = [T1(410)(2) + T2(409)(2) + \dots + T410(1)(2)] / (410)(410)$$

WHERE TX = THE MEASURED TIME TO SEEK X CYLINDERS. FORWARD AND REVERSE TIMES ARE MEASURED AND TYPED, SEPARATELY. THE AVERAGE SEEK TIME IS SPECIFIED TO BE < 38 MILLI-SEC.

SAMPLE PRINTOUT --

AVERAGE SEEK TIMES :
FORWARD DIRECTION
AVG = 35673 US SPEC'D MAX IS 38000 US
REVERSE DIRECTION
AVG = 35823 US SPEC'D MAX IS 38000 US

9.2.4 TEST 16 - MAXIMUM SEEK TIME MEASUREMENT

THIS TEST MEASURES THE TIME REQUIRED TO SEEK FROM CYL 0 TO CYL 632 OCT. (410 DEC.). THIS TIME REPRESENTS THE MAXIMUM SEEK TIME, AND IT IS MEASURED 128 TIMES IN EACH DIRECTION. THE SPECIFIED MAXIMUM SEEK TIME IS < 75 MILLI-SEC.

SAMPLE PRINTOUT --

MAXIMUM SEEK TIMES :
FORWARD DIRECTION
MIN = 68950 US
MAX = 69286 US 0 OF 128 ABOVE SPEC'D MAX OF 75000 US
AVG = 69122 US
REVERSE DIRECTION
MIN = 70194 US
MAX = 70667 US 0 OF 128 ABOVE SPEC'D MAX OF 75000 US
AVG = 70407 US

9.3 TEST 17 - SECTOR ADDRESSING TEST

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IN THIS TEST, ALL SECTORS OF FC, FT ARE WRITTEN, EACH WITH 256(DEC) WORDS OF ITS OWN SECTOR NO. + 100(OCT). NEXT, FOR EACH SECTOR WRITTEN A WRITE CHECK IS DONE, FOLLOWED BY A RE-F AND SOFTWARE COMPARE OF THE DATA.

9.4 TEST 20 - TRACK ADDRESSING TEST

IN THIS TEST, SECTOR FS OF CYL F: IS WRITTEN WITH 256 (DEC) WORDS OF THE TRACK NO. + 100(OCT), FOR EACH OF TRACKS 0,1,2. THEN, A WRITE CHECK OF EACH SECTOR IS DONE TO VERIFY THE WRITES. THEN, EACH OF THE 3 SECTORS IS RE-WRITTEN, AND AFTER EACH WRITE ALL OF THE THREE SECTORS ARE WRITE-CHECKED TO DETECT TRACK ADDRESSING PROBLEMS.

9.5 TEST 21 - READ/WRITE DATA TEST

THE READ/WRITE DATA TEST HAS 2 DIFFERENT VERSIONS, DEPENDING ON THE VALUE OF THE DATA PATTERN PARAMETER (PT).

FOR PT = 0 :

THIS TEST IS THE QUICK VERIFY DEFAULT DATA TEST, WHICH IS RUN WHEN PT=0, DUE TO PARAMETER INPUT CHOICE, OR ADDRESS 200 START. IN THIS TEST, THE ENTIRE PACK IS TESTED WITH A SINGLE DATA PATTERN. THIS PATTERN IS COMPRISED OF THE DATA PATTERNS 00-15, WHICH REPEATS EVERY 256(DEC) WORDS. EACH TRACK IS WRITTEN IN 4 SEGMENTS, FOLLOWED BY A WRITE-CHECK, READ, AND SOFTWARE COMPARE. THE SEGMENTS ARE 6 SECTORS EACH, WHICH MEANS THAT TRACK SPIRALING OCCURS ON THE LAST SEGMENT WRITTEN ON EACH TRACK.

FOR PT NOT = 0 :

THIS TEST PERFORMS READ/WRITE FUNCTIONS ON THE ENTIRE RANGE OF CYLINDERS (FC-LC), TRACKS (FT-LT), AND SECTORS (FS-LS) SPECIFIED. AT EACH SPECIFIED SECTOR WC WORDS OF THE CURRENT REPEATING DATA PATTERN ARE WRITTEN, AND THEN WRITE-CHECKED, FOLLOWED BY A READ AND SOFTWARE COMPARE. THIS IS DONE FOR ALL THE SPECIFIED SECTORS ON ALL THE TRACKS USING THE SECTOR INCREMENT IS, AND TRACK INCREMENT IT. AND THEN IT IS REPEATED USING EACH OF THE OTHER DATA PATTERNS CHOSEN IN PARAMETER PT, THEN, EACH OF THE ABOVE OPERATIONS ARE REPEATED AT EACH OF THE REMAINING CYLINDERS, IN THE SPECIFIED RANGE, USING THE CYLINDER INCREMENT IC. NOTE THAT FS MUST BE CHOSEN < OR = LS, AND FT MUST BE < OR = LT. HOWEVER, FC MAY BE <, =, OR > LC, AND IF FC>LC, THE CYLINDER ADDRESS WILL BE DECREMENTED BY IC (INSTEAD OF INCREMENTED) TO OBTAIN EACH NEW CYLINDER ADDRESS.

NOTE:

THE PACK ADDRESS LIMITS

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(FT,LT,FS,LS,FC,LC) REFER TO THE RANGE OF DATA SECTORS AT WHICH TRANSFERS MAY BEGIN, USING THE SPECIFIED WORD COUNT WC. IF WC IS LARGE ENOUGH, HOWEVER, THE TRANSFERS MAY EXTEND BEYOND THE ABOVE LIMITS.

9.5.1 DATA PATTERNS

EACH DATA PATTERN IS COMPRISED OF 16 WORDS. PATTERNS ARE REPEATED AS NECESSARY TO OBTAIN THE DESIRED WORD COUNT ON THE WRITE DATA COMMANDS. THE MAXIMUM ALLOWABLE WORD COUNT IS DETERMINED BY THE AMOUNT OF AVAILABLE BUFFER SPACE AT THE END OF THE PROGRAM, AND COULD BE AS LARGE AS 65,536 (DEC) WORDS. DATA SPIRALING AND TRACK SWITCHING IS THUS POSSIBLE DURING THE DATA TESTS.

THE FOLLOWING IS THE LIST OF 16 SELECTABLE DATA PATTERNS (IN OCTAL), WITH NOTABLE FEATURES DESCRIBED:

	0	1	2	3
	HIGH-LOW FREQUENCY MIX	HIGH FREQUENCY PHASE MIX	LOW FREQUENCY PHASE MIX	MAXIMUM PRECOMPENSATION PHASE MIX
	-----	-----	-----	-----
1177				
1178				
1179				
1180				
1181				
1182				
1183				
1184				
1185				
1186	177777	000000	052525	133333
1187	177777	000000	052525	066666
1188	177777	000000	052525	155555
1189	052525	177777	125252	155555
1190	052525	177777	125252	133333
1191	052525	177777	125252	066666
1192	177777	000000	052525	066666
1193	177777	000000	052525	155555
1194	052525	177777	125252	155555
1195	052525	177777	125252	133333
1196	177777	000000	052525	133333
1197	052525	177777	125252	133333
1198	177252	000000	052525	133333
1199	177252	177777	125252	133333
1200	172765	000000	052525	133333
1201	172765	177777	125252	133333
1202				
1203				
1204				
	4	5	6	7
	ROTATING BOUNDARY PULSE PRECOMPENSATION	ROTATING CELL PULSE PRECOMPENSATION	ALL ZEROS	ALL ONES
	-----	-----	-----	-----
1205				
1206				
1207				
1208				
1209				
1210	121105	026455	000000	177777
1211	150442	113226	000000	177777
1212	064221	045513	000000	177777
1213	132110	122645	000000	177777
1214	055044	151322	000000	177777
1215	026422	064551	000000	177777
1216	013211	132264	000000	177777
1217	105504	055132	000000	177777
1218	042642	026455	000000	177777
1219	021321	113226	000000	177777
1220	110550	045513	000000	177777
1221	044264	122645	000000	177777
1222	022132	151322	000000	177777
1223	011055	064551	000000	177777
1224	104426	132264	000000	177777
1225	042213	055132	000000	177777

	8 SHIFTED 1 IN FIELD OF 0'S	9 SHIFTED 0 IN FIELD OF 1'S	10 ALTERNATING 0-1	11 ALTERNATING 1-0

1226				
1227				
1228				
1229				
1230				
1231				
1232				
1233				
1234				
1235	000001	177776	052525	125252
1236	000002	177775	052525	125252
1237	000004	177773	052525	125252
1238	000010	177767	052525	125252
1239	000020	177757	052525	125252
1240	000040	177737	052525	125252
1241	000100	177677	052525	125252
1242	000200	177577	052525	125252
1243	000400	177377	052525	125252
1244	001000	176777	052525	125252
1245	002000	175777	052525	125252
1246	004000	173777	052525	125252
1247	010000	167777	052525	125252
1248	020000	157777	052525	125252
1249	040000	137777	052525	125252
1250	100000	077777	052525	125252
1251				
1252				
1253				
	12 SHIFTING 0'S AND 1'S	13 COMPOSITE ROTATING	14 PSEUDO- RANDOM	15 USER- DEFINED

1254				
1255				
1256				
1257	000001	072307		
1258	000003	135143		
1259	000007	156461		
1260	000017	167230		
1261	000037	073514		
1262	000077	035646		
1263	000177	016723		
1264	000377	107351		
1265	000777	143564		
1266	001777	061672		
1267	003777	030735		
1268	007777	114356		
1269	017777	046167		
1270	037777	123073		
1271	077777	151453		
1272	177777	164616		

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9.6 TEST 22 - DUAL-ACCESS DATA TEST

THIS TEST IS DESIGNED TO RUN ON 2 DIFFERENT PROCESSORS, SIMULTANEOUSLY AND INDEPENDENTLY PERFORMING DYNAMIC DATA OPERATIONS ON THE SAME DRIVE, THROUGH 2 DIFFERENT CONTROLLERS. TEST 22 HAS A SEPARATE STARTING ADDRESS = 220 AND IT IS NEVER RUN WITH THE OTHER TESTS IN AN AUTOMATIC MANNER. ALSO, BOTH PORTS MUST BE SWITCHED ON-LINE, AT THE DRIVE.

THIS TEST IS A MODIFIED VERSION OF READ/WRITE DATA TEST 21, IN WHICH A DRIVE RELEASE COMMAND IS DONE IMMEDIATELY AFTER COMPLETION OF READ WRITE OPERATIONS AT A PARTICULAR PACK ADDRESS. IN ALL OTHER RESPECTS, TESTS 21 AND 22 ARE IDENTICAL, AS FAR AS EACH PROCESSOR IS CONCERNED. THIS MEANS THAT THE TEST PARAMETERS (ADDRESS LIMITS, DATA PATTERNS, ETC.) CAN BE DEFINED DIFFERENTLY FOR EACH PROCESSOR. IF DIFFERENT DATA PATTERNS ARE USED, FOR EXAMPLE, THE DATA WHICH APPEARS ON THE DISK AT TIME OF FAILURE CAN IDENTIFY THE CONTROLLER FROM WHICH IT ORIGINATED. LIKEWISE, THE WORD COUNT MAY BE CHOSEN APPROPRIATELY SMALL (WC=1) TO CAUSE THE GREATEST AMOUNT OF CONTENTION FOR THE USE OF THE DRIVE.

NOTE THAT IF BOTH PROCESSORS ARE PERFORMING TRANSFERS WITH DIFFERENT DATA, AT THE SAME PACK ADDRESS, INTERFERENCE MAY OCCUR, IF ONE CONTROLLER SHOULD LOSE THE DRIVE PREMATURELY TO REPORT AN ERROR.

WHEN THE TEST IS STARTED (AT ADDRESS 220) PARAMETER INPUT MODE IS ENTERED, AND OPERATIONAL PARAMETERS AND THE DRIVE NUMBER MAY BE TYPED IN (SEE SECTION 8.1) AS WELL AS RK06 UNIBUS ADDRESS AND INTERRUPT VECTOR. THE TEST NUMBER IS AUTOMATICALLY SET TO 22, AND THE PROGRAM TYPES 'ENTER T,O,S, OR R' AS DESCRIBED IN SECTION 8.2.3. AT THIS POINT, THE OPERATION OF THE PROGRAM IS THE SAME AS PREVIOUSLY DESCRIBED IN CONJUNCTION WITH THE OTHER TESTS. HOWEVER, ONLY TEST 22 MAY BE RUN, AND ONLY ON THE SINGLE SELECTED DRIVE. ALSO, THE OPERATOR MUST INDEPENDENTLY LOAD AND START THE TEST IN EACH PROCESSOR AND SELECT INPUT PARAMETERS, INDEPENDENTLY. THE DRIVE NUMBER TYPED AT EACH PROCESSOR CONSOLE MUST BE THE SAME, IN ORDER TO PERFORM THE TESTS ON THE SAME DRIVE, SIMULTANEOUSLY.

10.0 ERROR REPORTING

10.1 COMMON ERRORS

THE FOLLOWING IS A LIST OF COMMON ERROR MESSAGES WHICH ACCOMPANY ERROR TYPEOUTS FROM THE SUBSYSTEM VERIFICATION PROGRAM. THE ERRORS ARE SELF-EXPLANATORY.

1. UNIBUS PARITY ERROR
2. NON-EXISTANT MEMORY ERROR
3. NON-EXISTANT DRIVE ERROR

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1471
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034606 000000 000121 000000 000000 000000 036000
HI BA LO BA
000000 074000

PACK ADDRESS OF ERROR(S) :
CYLNR TRACK SECTOR
000000 000000 000000

WD #	GOOD	BAD	HI PHY	LO PHY	VRT AD	KIPAR6
000000	000001	125252	000000	074000	154000	000600
000001	000002	125252	000000	074002	154002	000600
000002	000003	125252	000000	074004	154004	000600
000003	000004	125252	000000	074006	154006	000600
000004	000005	125252	000000	074010	154010	000600
000005	000006	125252	000000	074012	154012	000600
000006	000007	125252	000000	074014	154014	000600
000007	000010	125252	000000	074016	154016	000600
000010	000011	125252	000000	074020	154020	000600
000011	000012	125252	000000	074022	154022	000600

11.0 REVISION HISTORY

- REVED
- 1) SEEK-TIMING TESTS MODIFIED IN ACCORDANCE WITH RK07 SPEC. CHANGE.
 - 2) CORRECTED BUG IN "INITSS" ROUTINE WHICH HAD CAUSED INTERMITTENT "CONT. ERR. DURING DRIVER SERVICING" ERRORS.
 - 3) CORRECTED BUG IN "CONERR" ROUTINE WHICH HAD CAUSED INTERMITTENT "UNDEFINED ERRORS."

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APPEN IX A

SAMPLE ADDRESS 200 DEFAULT RUN

THE FOLLOWING PRINTOUT WAS THE RESULT OF RUNNING SUBSYSTEM VERIFICATION, PART 1, ON A SINGLE DRIVE (DRIVE 0), FOR THE FIRST PASS OF THE PROGRAM (QUICK-VERIFY PASS). IN THIS MODE, ALL MESSAGES APPEARING HERE WERE OUTPUTS - NO OPERATOR INPUTS ARE PROVIDED ON AN ADDRESS 200 RUN.

CZR6M-D - RK611/RK06-RK07 SUBSYSTEM VERIFICATION : PART 1

LAST PHYS MEM ADR = 377776

DRIVE 1 NON-EXISTENT
DRIVE 2 NON-EXISTENT
DRIVE 3 NON-EXISTENT
DRIVE 4 NON-EXISTENT
DRIVE 5 NON-EXISTENT
DRIVE 6 NON-EXISTENT
DRIVE 7 NON-EXISTENT

DRIVE(S) = 0

TESTING DRIVE 0
DRIVE SER. NO. 8
CART. SER. NO. 334

ROTATIONAL TIMES :
MIN = 24153 US 99 OF 128 BELOW SPEC'S MIN OF 24375 US
MAX = 25125 US 0 OF 128 ABOVE SPEC'D MAX OF 25625 US
AVG = 24374 US

ONE CYL SEEK TIMES :
FORWARD DIRECTION
MIN = 6323 US
MAX = 6686 US 0 OF 410 (814 FOR RK07) ABOVE SPEC'D MAX OF 8000 US
AVG = 6518 US
REVERSE DIRECTION
MIN = 6332 US
MAX = 6713 US 0 OF 410 (814 FOR RK07) ABOVE SPEC'D MAX OF 8000 US
AVG = 6513 US

AVERAGE SEEK TIMES

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FORWARD DIRECTION
AVG = 35673 US SPEC'D MAX IS 38000 US
REVERSE DIRECTION
AVG = 35823 US SPEC'D MAX IS 38000 US

MAXIMUM SEEK TIMES :
FORWARD DIRECTION
MIN = 68950 US
MAX = 69150 US 0 OF 128 ABOVE SPEC'D MAX OF 75000 US
AVG = 69064 US
REVERSE DIRECTION
MIN = 70222 US
MAX = 70530 US 0 OF 128 ABOVE SPEC'D MAX OF 75000 US
AVG = 70379 US

END PASS # 1

APPENDIX B

SAMPLE ADDRESS 204 RUN

THE FOLLOWING PRINTOUT WAS THE RESULT OF RUNNING SUBSYSTEM VERIFICATION, PART 1, ON A SINGLE DRIVE (DRIVE 0), WITH PARTICULAR TESTS SELECTED TO BE RUN FOR SPECIFIED NUMBERS OF TIMES, WITH SPECIFIED PARAMETERS, AND A SPECIFIED NO. OF PROGRAM PASSES. SPECIFICALLY, IT WAS DESIRED THAT TEST 2 BE RUN 25(OCTAL) TIMES, TEST 7 FOUR TIMES, TEST 14 RUN 2 TIMES, AND TEST 21 ONCE. THESE TESTS WERE SPECIFIED TO RUN FOR 2 PROGRAM PASSES, USING PARAMETER VALUES TYPED BY THE OPERATOR. IN THIS CASE PACK ADDRESS PARAMETERS AND DATA TRANSFER WORD COUNT WERE SPECIFIED, FOR TESTS WHICH UTILIZE THEM.

CZR6M-D - RK611/RK06-RK07 SUBSYSTEM VERIFICATION : PART 1

LAST PHYS MEM ADR = 377776

PARAMETER INPUT MODE

RK06 BUS ADR = 177440 NEW =
RK06 VEC ADR = 210 NEW =
RK06 PRIORITY = 5 NEW =

DRIVE 1 NON-EXISTENT
DRIVE 2 NON-EXISTENT
DRIVE 3 NON-EXISTENT
DRIVE 4 NON-EXISTENT
DRIVE 5 NON-EXISTENT
DRIVE 6 NON-EXISTENT
DRIVE 7 NON-EXISTENT

DRIVE(S) = 0

L = LIST TESTS
C = CHANGE TESTS
I = INPUT PARAMETERS AND RUN TESTS

ENTER L, C, OR I

* C
TO DEFAULT TESTS TYPE D<CR>, ELSE <CR>

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1663

*

TEST	ITERATIONS
1	10 * 0
2	200 * 25
3	10 * 00
4	10 * 00
5	2 * 00
6	2 * 00
7	2 * 4
10	400 * 00
11	500 * 00
12	2 * 00
13	1 * 00
14	1 * 2
15	1 * 00
16	1 * 00
17	10 * 00
20	10 * 0
21	1 *

ENTER L, C, OR I
* L

TEST	ITERATIONS
1	0
2	25
3	00
4	00
5	00
6	00
7	4
10	00
11	00
12	00
13	00
14	2
15	00
16	00
17	00
20	00
21	1

ENTER L, C, OR I
* I

T = TYPE PARAMETER LIST
O = OPEN PARAMETER LIST
S = SET INDIVIDUAL PARAM
R = RUN TESTS

ENTER T, O, S, OR R
* O

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TO DEFAULT ALL PARAMETERS TYPE D<CR>, ELSE <CR>

*
FC=0 *
LC=632 * 2
IC=1 *
FT=0 * 1
LT=2 * 1
IT=1 *
SO=0 *
S1=23 *
S2=0 * 10
S3=25 * 10
IS=1 *
PT=0 * 20
MA=61566 *
WC=403 * 1100
CS=0 *
ST=0 *
SM=1000 *

ENTER T, O, S, OR R

* T
FC=0
LC=2
IC=1
FT=1
LT=1
IT=1
SO=0
S1=23
S2=10
S3=10
IS=1
PT=20
MA=61566
WC=1100
CS=0
ST=0
SM=1000

ENTER T, O, S, OR R

* R
ENTER NO. OF PASSES (1-77777) :
* 2

TESTING DRIVE 0
DRIVE SER. NO. 8
CART. SER. NO. 334

ONE CYL SEEK TIMES :
FORWARD DIRECTION
MIN = 6319 US
MAX = 6691 US 0 OF 410 ABOVE SPEC'D MAX OF 8000 US

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AVG = 6502 US
REVERSE DIRECTION
MIN = 6301 US
MAX = 6682 US 0 OF 410 ABOVE SPEC'D MAX OF 8000 US
AVG = 6498 US

ONE CYL SEEK TIMES :
FORWARD DIRECTION
MIN = 6332 US
MAX = 6695 US 0 OF 410 ABOVE SPEC'D MAX OF 8000 US
AVG = 6506 US
REVERSE DIRECTION
MIN = 6314 US
MAX = 6686 US 0 OF 410 ABOVE SPEC'D MAX OF 8000 US
AVG = 6503 US

ONE CYL SEEK TIMES :
FORWARD DIRECTION
MIN = 6319 US
MAX = 6691 US 0 OF 410 ABOVE SPEC'D MAX OF 8000 US
AVG = 6504 US
REVERSE DIRECTION
MIN = 6301 US
MAX = 6700 US 0 OF 410 ABOVE SPEC'D MAX OF 8000 US
AVG = 6500 US

ONE CYL SEEK TIMES :
FORWARD DIRECTION
MIN = 6326 US
MAX = 6698 US 0 OF 410 ABOVE SPEC'D MAX OF 8000 US
AVG = 6511 US
REVERSE DIRECTION
MIN = 6317 US
MAX = 6698 US 0 OF 410 ABOVE SPEC'D MAX OF 8000 US
AVG = 6507 US

END PASS # 2
TO TEST ALL DRIVES TYPE 'A'<CR>, ELSE <CR>
*

1766 000000

PART=0
:*** IF 'PART' IS DEFINED = 0, PART 1 WILL BE ASSEMBLED. ***
:*** IF 'PART' IS DEFINED NOT = 0, PART 2 WILL BE ASSEMBLED. ***
:*** REV 004 ***

1767
1768
1769
1770
1771
1772
1773
1774
1775 167000

.NLIST MC,MD,CND
.LIST ME
.ENABL ABS,AMA
\$SWR= 167000
:.....
.SBTTL STARTING ADDRESSES
:
: 200 DEFAULT PARAMETERS FOR TESTS 1-21
: 204 SELECT PARAMETERS FOR TESTS 1-21
: 220 DUAL-ACCESS DATA TEST
:.....

1776
1777
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1783

1784 000001

\$TN=1
.TITLE CZR6MEU RK611/06 SS Vfy1
: *COPYRIGHT (C) 1976,1980
: *DIGITAL EQUIPMENT CORP.
: *MAYNARD, MASS. 01754
:
: *THIS PROGRAM WAS ASSEMBLED USING THE PDP-11 MAINDEC SYSMAC
: *PACKAGE (MAINDEC-11-DZQAC-C3), JAN 19, 1977.

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.SBTTL OPERATIONAL SWITCH SETTINGS
:
: SWITCH USE
:-----
: 15 HALT ON ERROR
: 14 LOOP ON TEST
: 13 INHIBIT ERROR TYPEOUTS
: 12 REPORT DESCRIPTION ONLY, ON ERRORS
: 11 INHIBIT ITERATIONS
: 10 BELL ON ERROR
: 9 LOOP ON ERROR
: 8 APPLY RANDOM STALL BETWEEN OPERATIONS
: 7 DO EXPLICIT SEEKS IN TESTS 1-12
: 6 REPORT 1 ERROR PER TRANSFER IN TESTS 17,21
: 5 INHIBIT WRITES IN TEST 21
: 4 INHIBIT WRITE CHECKS IN TEST 21
: 3 INHIBIT READS AND SOFTWARE COMPARES IN TEST 21
: 2 INHIBIT SOFTWARE COMPARES IN TEST 21
: 1 READ AFTER A WRITE CHECK ERROR IN TEST 21
: 0 REPORT ALL SOFTWARE COMPARE ERRORS IN TESTS 17,21

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1815
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1821

.SBTTL CONTROL SWITCH SETTINGS (PARAMETER CS)
:
: SWITCH USE
:-----
: 05 DROP DRIVE IF 20(DEC) ERRORS EXCEEDED
: 04 TYPE BAD SECTOR FILES (BSF'S) ON FIRST PASS
: 03 INHIBIT TIMING REPORTS IN TESTS 13-16

```
1822      .SBTTL  BASIC DEFINITIONS
1823
1824      ;*INITIAL ADDRESS OF THE STACK POINTER *** 1100 ***
1825      001100  STACK= 1100
1826      .EQUIV  EMT,ERROR      ;;BASIC DEFINITION OF ERROR CALL
1827      .EQUIV  IOT,SCOPE     ;;BASIC DEFINITION OF SCOPE CALL
1828
1829      ;*MISCELLANEOUS DEFINITIONS
1830      000011  MT= 11          ;;CODE FOR HORIZONTAL TAB
1831      000012  LF= 12          ;;CJDE FOR LINE FEED
1832      000015  CR= 15          ;;CODE FOR CARRIAGE RETURN
1833      000200  CRLF= 200       ;;CODE FOR CARRIAGE RETURN-LINE FEED
1834      177776  PS= 177776     ;;PROCESSOR STATUS WORD
1835      .EQUIV  PS,PSW
1836      177774  STKLMT= 177774  ;;STACK LIMIT REGISTER
1837      177772  PIRQ= 177772   ;;PROGRAM INTERRUPT REQUEST REGISTER
1838      177570  DSWR= 177570   ;;HARDWARE SWITCH REGISTER
1839      177570  DDISP= 177570  ;;HARDWARE DISPLAY REGISTER
1840
1841      ;*GENERAL PURPOSE REGISTER DEFINITIONS
1842      000000  R0= 0           ;;GENERAL REGISTER
1843      000001  R1= 1           ;;GENERAL REGISTER
1844      000002  R2= 2           ;;GENERAL REGISTER
1845      000003  R3= 3           ;;GENERAL REGISTER
1846      000004  R4= 4           ;;GENERAL REGISTER
1847      000005  R5= 5           ;;GENERAL REGISTER
1848      000006  R6= 6           ;;GENERAL REGISTER
1849      000007  R7= 7           ;;GENERAL REGISTER
1850      000006  SP= 6          ;;STACK POINTER
1851      000007  PC= 7          ;;PROGRAM COUNTER
1852
1853      ;*PRIORITY LEVEL DEFINITIONS
1854      000000  PR0= 0          ;;PRIORITY LEVEL 0
1855      000040  PR1= 40         ;;PRIORITY LEVEL 1
1856      000100  PR2= 100       ;;PRIORITY LEVEL 2
1857      000140  PR3= 140       ;;PRIORITY LEVEL 3
1858      000200  PR4= 200       ;;PRIORITY LEVEL 4
1859      000240  PR5= 240       ;;PRIORITY LEVEL 5
1860      000300  PR6= 300       ;;PRIORITY LEVEL 6
1861      000340  PR7= 340      ;;PRIORITY LEVEL 7
1862
1863      ;*'SWITCH REGISTER' SWITCH DEFINITIONS
1864      100000  SW15= 100000
1865      040000  SW14= 40000
1866      020000  SW13= 20000
1867      010000  SW12= 10000
1868      004000  SW11= 4000
1869      002000  SW10= 2000
1870      001000  SW09= 1000
1871      000400  SW08= 400
1872      000200  SW07= 200
1873      000100  SW06= 100
1874      000040  SW05= 40
1875      000020  SW04= 20
1876      000010  SW03= 10
1877      000004  SW02= 4
```

```
1878      000002      SW01= 2
1879      000001      SW00= 1
1880      .EQUIV SW09,SW9
1881      .EQUIV SW08,SW8
1882      .EQUIV SW07,SW7
1883      .EQUIV SW06,SW6
1884      .EQUIV SW05,SW5
1885      .EQUIV SW04,SW4
1886      .EQUIV SW03,SW3
1887      .EQUIV SW02,SW2
1888      .EQUIV SW01,SW1
1889      .EQUIV SW00,SW0
1890
1891      ;*DATA BIT DEFINITIONS (BIT00 TO BIT15)
1892      100000      BIT15= 100000
1893      040000      BIT14= 40000
1894      020000      BIT13= 20000
1895      010000      BIT12= 10000
1896      004000      BIT11= 4000
1897      002000      BIT10= 2000
1898      001000      BIT09= 1000
1899      000400      BIT08= 400
1900      000200      BIT07= 200
1901      000100      BIT06= 100
1902      000040      BIT05= 40
1903      000020      BIT04= 20
1904      000010      BIT03= 10
1905      000004      BIT02= 4
1906      000002      BIT01= 2
1907      000001      BIT00= 1
1908      .EQUIV BIT09,BIT9
1909      .EQUIV BIT08,BIT8
1910      .EQUIV BIT07,BIT7
1911      .EQUIV BIT06,BIT6
1912      .EQUIV BIT05,BIT5
1913      .EQUIV BIT04,BIT4
1914      .EQUIV BIT03,BIT3
1915      .EQUIV BIT02,BIT2
1916      .EQUIV BIT01,BIT1
1917      .EQUIV BIT00,BIT0
1918
1919      ;*BASIC "CPU" TRAP VECTOR ADDRESSES
1920      000004      ERRVEC= 4          ;; TIME OUT AND OTHER ERRORS
1921      000010      RESVEC= 10         ;; RESERVED AND ILLEGAL INSTRUCTIONS
1922      000014      TBITVEC=14        ;; "T" BIT
1923      000014      TRTVEC= 14         ;; TRACE TRAP
1924      000014      BPTVEC= 14         ;; BREAKPOINT TRAP (BPT)
1925      000020      IOTVEC= 20         ;; INPUT/OUTPUT TRAP (IOT) **SCOPE**
1926      000024      PWRVEC= 24         ;; POWER FAIL
1927      000030      EMTVEC= 30         ;; EMULATOR TRAP (EMT) **ERROR**
1928      000034      TRAPVEC=34        ;; "TRAP" TRAP
1929      000060      TKVEC= 60          ;; TTY KEYBOARD VECTOR
1930      000064      TPVEC= 64          ;; TTY PRINTER VECTOR
1931      000240      PIRQVEC=240        ;; PROGRAM INTERRUPT REQUEST VECTOR
1932      .SBTTL MEMORY MANAGEMENT DEFINITIONS
1933
```



```
1934 ;*KT11 VECTOR ADDRESS
1935
1936 000250 MMVEC= 250
1937
1938 ;*KT11 STATUS REGISTER ADDRESSES
1939
1940 177572 SR0= 177572
1941 177574 SR1= 177574
1942 177576 SR2= 177576
1943 172516 SR3= 172516
1944
1945 ;*KERNEL 'I' PAGE DESCRIPTOR REGISTERS
1946
1947 172300 KIPDR0= 172300
1948 172302 KIPDR1= 172302
1949 172304 KIPDR2= 172304
1950 172306 KIPDR3= 172306
1951 172310 KIPDR4= 172310
1952 172312 KIPDR5= 172312
1953 172314 KIPDR6= 172314
1954 172316 KIPDR7= 172316
1955
1956 ;*KERNEL 'I' PAGE ADDRESS REGISTERS
1957
1958 172340 KIPAR0= 172340
1959 172342 KIPAR1= 172342
1960 172344 KIPAR2= 172344
1961 172346 KIPAR3= 172346
1962 172350 KIPAR4= 172350
1963 172352 KIPAR5= 172352
1964 172354 KIPAR6= 172354
1965 172356 KIPAR7= 172356
1966
1967 170200 MAPL00=170200
1968 170202 MAPH00=170202
1969 172100 MEMCSR=172100 ;MEMORY CSR REG START ADRS
1970 177740 LOERAD=177740 ;11/70 MEM LO ERROR ADRS REG
1971 177742 HIERAD=177742 ;11/70 MEM HI ERROR ADRS REG
1972 177744 MEMSYS=177744 ;11/70 MEM SYSTEM REG
1973 .SBTTL TRAP CATCHER
1974
1975 000000 .=0
1976 ;*ALL UNUSED LOCATIONS FROM 4 - 776 CONTAIN A ".+2,HALT"
1977 ;*SEQUENCE TO CATCH ILLEGAL TRAPS AND INTERRUPTS
1978 ;*LOCATION 0 CONTAINS 0 TO CATCH IMPROPERLY LOADED VECTORS
1979 .=174
1980 000174 000000 DISPREG: .WORD 0 ;:SOFTWARE DISPLAY REGISTER
1981 000176 000000 SWREG: .WORD 0 ;:SOFTWARE SWITCH REGISTER
1982
1983 000200 000137 012450 .SBTTL STARTING ADDRESS(ES)
1984 000166 000166 JMP @#DFSTRT ;:JUMP TO STARTING ADDRESS OF PROGRAM
1985 000166 000000 .=166
1986 000204 000204 HZ: .WORD 0 ;:LINE FREQ. FLAG - 0 - 60 HZ
1987 000204 000137 012522 .=204
1988 000220 000220 JMP @#PSTART
1989 000220 000137 012504 .=220
1989 000220 000137 012504 JMP @#DASTRT
```


2028
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2030
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2034 001100
2035 001100
2036 001100 000000
2037 001102 000
2038 001103 000
2039 001104 000000
2040 001106 000000
2041 001110 000000
2042 001112 000000
2043 001114 000
2044 001115 001
2045 001116 000000
2046 001120 000000
2047 001122 000000
2048 001124 000000
2049 001126 000000
2050 001130 000000
2051 001132 000000
2052 001134 000
2053 001135 000
2054 001136 000000
2055 001140 177570
2056 001142 177570
2057 001144 177560
2058 001146 177562
2059 001148 177564
2060 001150 177566
2061 001154 000
2062 001155 002
2063 001156 012
2064 001157 000
2065 001160 000000
2066
2067 001162 000000
2068 001164 000000
2069 001166 000000
2070 001170 000000
2071 001172 000000
2072 001174 000000
2073 001176 000000
2074 001200 000000
2075 001202 000000
2076 001204 000000
2077 001206 000000
2078 001210 000000
2079 001212 000000
2080 001214 000000
2081 001216 000000
2082 001220 000000
2083 001222 000000

.SBTTL COMMON TAGS

*THIS TABLE CONTAINS VARIOUS COMMON STORAGE LOCATIONS
*USED IN THE PROGRAM.

SCMTAG: .=1100
\$STNM: .WORD 0
\$ERFLG: .BYTE 0
\$ICNT: .WORD 0
\$LPADR: .WORD 0
\$LPERR: .WORD 0
\$ERTTL: .WORD 0
\$ITEMB: .BYTE 0
\$ERMAX: .BYTE 1
\$ERRPC: .WORD 0
\$GDADR: .WORD 0
\$BDADR: .WORD 0
\$GDDAT: .WORD 0
\$BDDAT: .WORD 0
\$AUTOB: .BYTE 0
\$INTAG: .BYTE 0
\$SWR: .WORD DSWR
\$DISPLAY: .WORD DDISP
\$TKS: 177560
\$TKB: 177562
\$TPS: 177564
\$TPB: 177566
\$NULL: .BYTE 0
\$FILLS: .BYTE 2
\$FILLC: .BYTE 12
\$TPFLG: .BYTE 0
\$REGAD: .WORD 0
\$REG0: .WORD 0
\$REG1: .WORD 0
\$REG2: .WORD 0
\$REG3: .WORD 0
\$REG4: .WORD 0
\$REG5: .WORD 0
\$REG6: .WORD 0
\$REG7: .WORD 0
\$REG10: .WORD 0
\$REG11: .WORD 0
\$REG12: .WORD 0
\$REG13: .WORD 0
\$REG14: .WORD 0
\$REG15: .WORD 0
\$REG16: .WORD 0
\$REG17: .WORD 0
\$REG20: .WORD 0

:::START OF COMMON TAGS
:::CONTAINS THE TEST NUMBER
:::CONTAINS ERROR FLAG
:::CONTAINS SUBTEST ITERATION COUNT
:::CONTAINS SCOPE LOOP ADDRESS
:::CONTAINS SCOPE RETURN FOR ERRORS
:::CONTAINS TOTAL ERRORS DETECTED
:::CONTAINS ITEM CONTROL BYTE
:::CONTAINS MAX. ERRORS PER TEST
:::CONTAINS PC OF LAST ERROR INSTRUCTION
:::CONTAINS ADDRESS OF 'GOOD' DATA
:::CONTAINS ADDRESS OF 'BAD' DATA
:::CONTAINS 'GOOD' DATA
:::CONTAINS 'BAD' DATA
:::RESERVED--NOT TO BE USED
:::AUTOMATIC MODE INDICATOR
:::INTERRUPT MODE INDICATOR
:::ADDRESS OF SWITCH REGISTER
:::ADDRESS OF DISPLAY REGISTER
:::TTY KBD STATUS
:::TTY KBD BUFFER
:::TTY PRINTER STATUS REG. ADDRESS
:::TTY PRINTER BUFFER REG. ADDRESS
:::CONTAINS NULL CHARACTER FOR FILLS
:::CONTAINS # OF FILLER CHARACTERS REQUIRED
:::INSERT FILL CHARS. AFTER A 'LINE FEED'
:::'TERMINAL AVAILABLE' FLAG (BIT<07>=0=YES)
:::CONTAINS THE ADDRESS FROM
:::WHICH (\$REG0) WAS OBTAINED
:::CONTAINS ((\$REGAD)+0)
:::CONTAINS ((\$REGAD)+2)
:::CONTAINS ((\$REGAD)+4)
:::CONTAINS ((\$REGAD)+6)
:::CONTAINS ((\$REGAD)+10)
:::CONTAINS ((\$REGAD)+12)
:::CONTAINS ((\$REGAD)+14)
:::CONTAINS ((\$REGAD)+16)
:::CONTAINS ((\$REGAD)+20)
:::CONTAINS ((\$REGAD)+22)
:::CONTAINS ((\$REGAD)+24)
:::CONTAINS ((\$REGAD)+26)
:::CONTAINS ((\$REGAD)+30)
:::CONTAINS ((\$REGAD)+32)
:::CONTAINS ((\$REGAD)+34)
:::CONTAINS ((\$REGAD)+36)
:::CONTAINS ((\$REGAD)+40)

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001400

001400 056325
001402 060751
001404 062450
001406 062564

001410 056344
001412 060751
001414 062450
001416 062564

001420 056362
001422 060751
001424 062450
001426 062564

001430 056400
001432 060751
001434 062450
001436 062564

001440 056417
001442 060751
001444 062450
001446 062614

001450 056436
001452 060751
001454 062450
001456 062650

001460 056455
001462 060751
001464 062450

.SBTTL ERROR POINTER TABLE

;*THIS TABLE CONTAINS THE INFORMATION FOR EACH ERROR THAT CAN OCCUR.
;*THE INFORMATION IS OBTAINED BY USING THE INDEX NUMBER FOUND IN
;*LOCATION \$ITEMB. THIS NUMBER INDICATES WHICH ITEM IN THE TABLE IS PERTINENT.
;*NOTE1: IF \$ITEMB IS 0 THE ONLY PERTINENT DATA IS (\$ERRPC).
;*NOTE2: EACH ITEM IN THE TABLE CONTAINS 4 POINTERS EXPLAINED AS FOLLOWS:

;* EM ;;POINTS TO THE ERROR MESSAGE
;* DH ;;POINTS TO THE DATA HEADER
;* DT ;;POINTS TO THE DATA
;* DF ;;POINTS TO THE DATA FORMAT

\$ERRTB:

;ERROR 1 ;UNIBUS PARITY ERROR
EM1
DH100
DT100
DF01

;ERROR 2 ;NON-EXISTANT MEMORY
EM2
DH100
DT100
DF01

;ERROR 3 ;NON-EXISTANT DRIVE
EM3
DH100
DT100
DF01

;ERROR 4 ;UNIT FIELD ERROR
EM4
DH100
DT100
DF01

;ERROR 5 ;SUBSYSTEM TIMEOUT
EM5
DH100
DT100
DF02

;ERROR 6 ;D TO C PARITY ERROR
EM6
DH100
DT100
DF03

;ERROR 7 ;DRIVE DETECTED PARITY ERROR
EM7
DH100
DT100

2219	001466	062650	DF03	
2220				
2221			:ERROR 10	
2222	001470	056474	EM10	:AC LOW
2223	001472	060751	DH100	
2224	001474	062450	DT100	
2225	001476	062614	DF02	
2226				
2227			:ERROR 11	
2228	001500	056503	EM11	:SPEED LOSS
2229	001502	060751	DH100	
2230	001504	062450	DT100	
2231	001506	062614	DF02	
2232				
2233			:ERROR 12	
2234	001510	056516	EM12	:ILLEGAL FUNCTION
2235	001512	060751	DH100	
2236	001514	062450	DT100	
2237	001516	062614	DF02	
2238				
2239			:ERROR 13	
2240	001520	056530	EM13	:PROGRAMMING ERROR
2241	001522	060751	DH100	
2242	001524	062450	DT100	
2243	001526	062564	DF01	
2244				
2245			:ERROR 14	:NON-EXISTANT FUNCTION
2246	001530	056540	EM14	
2247	001532	060751	DH100	
2248	001534	062450	DT100	
2249	001536	062614	DF02	
2250				
2251			:ERROR 15	
2252	001540	056560	EM15	:DRIVE TYPE ERROR
2253	001542	060751	DH100	
2254	001544	062450	DT100	
2255	001546	062614	DF02	
2256				
2257			:ERROR 16	
2258	001550	056574	EM16	:FORMAT ERROR
2259	001552	060751	DH100	
2260	001554	062450	DT100	
2261	001556	062614	DF02	
2262				
2263			:ERROR 17	
2264	001560	056604	EM17	:WRITE LOCK ERROR
2265	001562	060751	DH100	
2266	001564	062450	DT100	
2267	001566	062614	DF02	
2268				
2269			:ERROR 20	
2270	001570	056621	EM20	:DRIVE UNSAFE
2271	001572	060751	DH100	
2272	001574	062450	DT100	
2273	001576	062614	DF02	
2274				

2275			:ERROR 21	
2276	001600	056634	EM21	:SEEK INCOMPLETE
2277	001602	060751	DH100	
2278	001604	062450	DT100	
2279	001606	062614	DF02	
2280				
2281			:ERROR 22	
2282	001610	056645	EM22	:CYLINDER OVERFLOW
2283	001612	060751	DH100	
2284	001614	062450	DT100	
2285	001616	062614	DF02	
2286				
2287			:ERROR 23	
2288	001620	056660	EM23	:ILLEGAL CYLINDER
2289	001622	060751	DH100	
2290	001624	062450	DT100	
2291	001626	062614	DF02	
2292				
2293			:ERROR 24	
2294	001630	056675	EM24	:DRIVE OFF TRACK
2295	001632	060751	DH100	
2296	001634	062450	DT100	
2297	001636	062614	DF02	
2298				
2299			:ERROR 25	
2300	001640	056711	EM25	:DRIVE TIMING ERROR
2301	001642	060751	DH100	
2302	001644	062450	DT100	
2303	001646	062614	DF02	
2304				
2305			:ERROR 26	
2306	001650	056730	EM26	:DATA LATE
2307	001652	060751	DH100	
2308	001654	062450	DT100	
2309	001656	062614	DF02	
2310				
2311			:ERROR 27	
2312	001660	056742	EM27	:CONTROLLER TIMEOUT
2313	001662	060751	DH100	
2314	001664	062450	DT100	
2315	001666	062614	DF02	
2316				
2317			:ERROR 30	
2318	001670	056757	EM30	:OPERATION INCOMPLETE
2319	001672	060751	DH100	
2320	001674	062450	DT100	
2321	001676	062724	DF05	
2322				
2323			:ERROR 31	
2324	001700	056771	EM31	:HEADER VRC ERROR
2325	001702	060751	DH100	
2326	001704	062450	DT100	
2327	001706	062724	DF05	
2328				
2329			:ERROR 32	
2330	001710	057005	EM32	:DATA CHECK ERROR

2331	001712	060751	DH100	
2332	001714	062450	DT100	
2333	001716	062760	DF07	
2334				
2335				:ERROR 33
2336	001720	057022	EM33	;WRITE CHECK ERROR
2337	001722	060751	DH100	
2338	001724	062450	DT100	
2339	001726	063014	DF10	
2340				
2341				:ERROR 34
2342	001730	057036	EM34	;DATA MISCOMPARE(S)
2343	001732	060751	DH100	
2344	001734	062450	DT100	
2345	001736	062710	DF04	
2346				
2347				:ERROR 35
2348	001740	057056	EM35	;NO DRIVE RESPONSE-UFE & NXD
2349	001742	060751	DH100	
2350	001744	062450	DT100	
2351	001746	062564	DF01	
2352				
2353				:ERROR 36
2354	001750	057110	EM36	;DRIVE ERROR WILL NOT CLEAR
2355	001752	000000	0	
2356	001754	000000	0	
2357	001756	000000	0	
2358				
2359				:ERROR 37
2360	001760	057137	EM37	;DRIVE STATUS CHANGE WILL NOT CLEAR
2361	001762	000000	0	
2362	001764	000000	0	
2363	001766	000000	0	
2364				
2365				:ERROR 40
2366	001770	057200	EM40	;ATTENTION BUT NO STATUS CHANGE OR FAULT
2367	001772	060751	DH100	
2368	001774	062450	DT100	
2369	001776	062614	DF02	
2370				
2371				:ERROR 41
2372	002000	057244	EM41	;ATTENTION BUT DRIVE NOT AVAILABLE
2373	002002	060751	DH100	
2374	002004	062450	DT100	
2375	002006	062614	DF02	
2376				
2377				:ERROR 42
2378	002010	057274	EM42	;ATTENTION WHEN NOT EXPECTED
2379	002012	060751	DH100	
2380	002014	062450	DT100	
2381	002016	062614	DF02	
2382				
2383				:ERROR 43
2384	002020	057324	EM43	;ERROR WHILE GATHERING DRIVE STATUS
2385	002022	060751	DH100	
2386	002024	062450	DT100	

2387	002026	063060	DF12	
2388				
2389			:ERROR 44	
2390	002030	057524	EM63	:CLEAR CONTROLLER DID NOT CLEAR ERROR
2391	002032	060751	DH100	
2392	002034	062450	DT100	
2393	002036	063060	DF12	
2394				
2395			:ERROR 45	
2396	002040	057562	EM64	:NO ATTENTION IN ATTENTION SUMMARY REG
2397	002042	060751	DH100	
2398	002044	062450	DT100	
2399	002046	063060	DF12	
2400				
2401			:ERROR 46	
2402	002050	057610	EM65	:UNSOLICITED ATTENTION
2403	002052	060751	DH100	
2404	002054	062450	DT100	
2405	002056	063060	DF12	
2406				
2407			:ERROR 47	
2408	002060	057632	EM66	:UNEXPECTED DATA TYPE ERROR
2409	002062	060751	DH100	
2410	002064	062450	DT100	
2411	002066	063060	DF12	
2412				
2413			:ERROR 50	
2414	002070	057651	EM67	:ATTENTION DID NOT RESET WITH CLEAR
2415	002072	060751	DH100	
2416	002074	062450	DT100	
2417	002076	063060	DF12	
2418				
2419			:ERROR 51	
2420	002100	057710	EM70	:SUBSYSTEM CLEAR DID NOT CLEAR DRIVE ATTENTION
2421	002102	060751	DH100	
2422	002104	062450	DT100	
2423	002106	063060	DF12	
2424				
2425			:ERROR 52	
2426	002110	057355	EM52	:MULTIPLE DRIVE SELECT
2427	002112	060751	DH100	
2428	002114	062450	DT100	
2429	002116	063060	DF12	
2430				
2431			:ERROR 53	
2432	002120	057372	EM53	:ABREVIATED HCE ERROR
2433	002122	060751	DH100	
2434	002124	062450	DT100	
2435	002126	063110	DF13	
2436				
2437			:ERROR 54	
2438	002130	056757	EM30	:OPERATION INCOMPLETE ERROR
2439	002132	060751	DH100	
2440	002134	062450	DT100	
2441	002136	063140	DF14	
2442				

2443			:ERROR 55	
2444	002140	056771	EM31	:ABREVIATED HVRC ERROR
2445	002142	060751	DH100	
2446	002144	062450	DT100	
2447	002146	063110	DF13	
2448				
2449			:ERROR 56	
2450	002150	057407	EM56	:2 TIMEOUT ERROR
2451	002152	060751	DH100	
2452	002154	062450	DT100	
2453	002156	063170	DF15	
2454				
2455			:ERROR 57	:2ND LEVEL IN SUBSYSTEM TIMEOUT
2456	002160	057407	EM56	
2457	002162	060751	DH100	
2458	002164	062450	DT100	
2459	002166	063234	DF16	
2460				
2461			:ERROR 60	
2462	002170	057426	EM60	:ERROR IN RECAL FOR RECOVERY
2463	002172	000000	0	
2464	002174	000000	0	
2465	002176	000000	0	
2466				
2467			:ERROR 61	
2468	002200	057460	EM61	:ABORT MESSAGE
2469	002202	000000	0	
2470	002204	000000	0	
2471	002206	000000	0	
2472				
2473			:ERROR 62	
2474	002210	057510	EM62	:CYLINDER MISCOMPARE
2475	002212	060751	DH100	
2476	002214	062450	DT100	
2477	002216	063300	DF17	
2478				
2479			:ERROR 63	:DATA ERROR WORDS
2480	002220	000000	0	
2481	002222	000000	0	
2482	002224	062542	DT602	
2483	002226	063410	DF25	
2484				
2485			:ERROR 64	
2486	002230	057524	EM63	:CLEAR CONTROLLER DID NOT CLEAR ERROR
2487	002232	060751	DH100	
2488	002234	062450	DT100	
2489	002236	062614	DF02	
2490				
2491			:ERROR 65	
2492	002240	057562	EM64	:NO ATTENTION IN ATTENTION SUMMARY REG
2493	002242	060751	DH100	
2494	002244	062450	DT100	
2495	002246	062614	DF02	
2496				
2497			:ERROR 66	
2498	002250	057610	EM65	:UNSOLICITED ATTENTION

2499	002252	060751	DH100	
2500	002254	062450	DT100	
2501	002256	062614	DF02	
2502				
2503			:ERROR 67	
2504	002260	057632	EM66	:UNEXPECTED DATA TYPE ERROR
2505	002262	060751	DH100	
2506	002264	062450	DT100	
2507	002266	062614	DF02	
2508				
2509			:ERROR 70	
2510	002270	057651	EM67	:ATTENTION DID NOT RESET WITH CLEAR
2511	002272	060751	DH100	
2512	002274	062450	DT100	
2513	002276	062614	DF02	
2514				
2515			:ERROR 71	
2516	002300	057710	EM70	:SUBSYSTEM CLEAR DID NOT CLEAR ATT
2517	002302	060751	DH100	
2518	002304	062450	DT100	
2519	002306	062614	DF02	
2520				
2521			:ERROR 72	
2522	002310	057755	EM71	:DATA LATE WHEN UNLOADING HEADER
2523	002312	060751	DH100	
2524	002314	062450	DT100	
2525	002316	062614	DF02	
2526				
2527			:ERROR 73	
2528	002320	060010	EM72	:CONTROLLER ERROR DURING DRIVER SERVICE
2529	002322	060751	DH100	
2530	002324	062450	DT100	
2531	002326	062614	DF02	
2532				
2533			:ERROR 74	
2534	002330	060043	EM73	:DRIVE DETECTED PARITY ERROR
2535	002332	060751	DH100	
2536	002334	062450	DT100	
2537	002336	062614	DF02	
2538				
2539			:ERROR 75	
2540	002340	060063	EM74	:UNDEFINED ERROR
2541	002342	060751	DH100	
2542	002344	062450	DT100	
2543	002346	062614	DF02	
2544				
2545			:ERROR 76	
2546	002350	060075	EM75	:MARKING SECTOR BAD MESSAGE
2547	002352	000000	0	
2548	002354	000000	0	
2549	002356	000000	0	
2550				
2551			:ERROR 77	
2552	002360	060125	EM76	:BAD DATA VERIFICATION WITH READ
2553	002362	061663	DH605	
2554	002364	062534	DT601	

2555	002366	063334	DF21	
2556				
2557			:ERROR 100	
2558	002370	060207	EM77	;RETRY SUCCESSFUL MESSAGE
2559	002372	000000	0	
2560	002374	062534	DT601	
2561	002376	063374	DF23	
2562				
2563			:ERROR 101	
2564	002400	060207	EM77	;ANOTHER RETRY SUCCESSFUL MESSAGE
2565	002402	000000	0	
2566	002404	062534	DT601	
2567	002406	063374	DF23	
2568				
2569			:ERROR 102	
2570	002410	060230	EM100	;RETRY UNSUCCESSFUL MESSAGE
2571	002412	000000	0	
2572	002414	062534	DT601	
2573	002416	063374	DF23	
2574				
2575			:ERROR 103	
2576	002420	060253	EM101	;NO VALID HEADERS IN TRACK JUST READ
2577	002422	061645	DH6042	
2578	002424	062534	DT601	
2579	002426	063404	DF24	
2580				
2581			:ERROR 104	
2582	002430	060331	EM102	;BSE ERROR ON SECTOR NOT LISTED AS BAD
2583	002432	060751	DH100	
2584	002434	062450	DT100	
2585	002436	062614	DF02	
2586				
2587			:ERROR 105	
2588	002440	060375	EM103	;TIMED-OUT ON READ HEADER
2589	002442	060751	DH100	
2590	002444	062450	DT100	
2591	002446	062650	DF03	
2592				
2593			:ERROR 106	
2594	002450	060423	EM104	;TIMED-OUT ON SEEK
2595	002452	060751	DH100	
2596	002454	062450	DT100	
2597	002456	062650	DF03	
2598				
2599			:ERROR 107	
2600	002460	060445	EM105	;DRIVE SIEZED BY OTHER PORT
2601	002462	060751	DH100	
2602	002464	062450	DT100	
2603	002466	062614	DF02	
2604				
2605			:ERROR 110	
2606	002470	060476	EM106	; 'DATA MISCMPR WHILE BAI SET'
2607	002472	060751	DH100	
2608	002474	062450	DT100	
2609	002476	063424	DF27	
2610				

2611			:ERROR 111	
2612	002500	060531	EM107	;'NO NEM WHEN EXPECTED''
2613	002502	000000	0	
2614	002504	000000	0	
2615	002506	000000	0	
2616				
2617			:ERROR 112	
2618	002510	060574	EM110	;'INTRPT WHEN CNTRLR NOT READY''
2619	002512	060751	DH100	
2620	002514	062450	DT100	
2621	002516	062564	DF01	
2622				
2623			:ERROR 113	
2624	002520	060627	EM111	;'NO ATT'N ON SEEK''
2625	002522	060751	DH100	
2626	002524	062450	DT100	
2627	002526	062614	DF02	
2628				
2629			:ERROR 114	
2630	002530	060650	EM112	;DRIVE'S CYLINDER INCORRECT
2631	002532	062072	DH702	
2632	002534	062510	DT202	
2633	002536	063414	DF26	
2634				
2635			:ERROR 115	
2636	002540	000000	0	;TYPE ADRS OF DATA MISCOMPARE(S)
2637	002542	000000	0	
2638	002544	062534	D1601	
2639	002546	063040	DF11	
2640				
2641			:ERROR 116	
2642	002550	057036	EM34	;DATA MISCOMPARE (11/70)
2643	002552	061227	DH103	
2644	002554	062560	DT103	
2645	002556	063324	DF20	
2646				
2647			:ERROR 117	
2648	002560	000000	0	;PART OF DATA MISCOMPARE
2649	002562	000000	0	
2650	002564	062450	DT100	
2651	002566	063344	DF22	
2652				
2653			:ERROR 120	
2654	002570	060672	EM113	;ABORT- CAN'T READ BSF
2655	002572	060751	DH100	
2656	002574	062450	DT100	
2657	002576	062564	DF01	
2658				
2659			:ERROR 121	
2660	002600	060720	EM114	;KT11 FAILURE
2661	002602	060751	DH100	
2662	002604	062450	DT100	
2663	002606	063450	DF30	
2664				
2665			:ERROR 122	
2666	002610	060735	EM115	.MEM PARITY ERROR

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ERROR POINTER TABLE

SEQ 0053

2667 002612 060751
2668 002614 062450
2669 002616 063474
2670
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2672 002620 056362
2673 002622 000000
2674 002624 000000
2675 002626 000000
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DH100
DT100
DF31
:ERROR 123
EM3
0
0
0

;NED WHEN SIZING UNDER APT DRIVE TABLE

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.SBTTL BIT ASSIGNMENTS IN THE RK611 REGISTERS

:RKCS1							
15	14	13	12	11	10	9	8
CERR	DI	DCPAR	CFMT	CTO	CDT	BA17	BA16
CCLR							
R/W	RO	RO	R/W	RO	R/W	R/W	R/W

7	6	5	4	3	2	1	0
RDY	IE	SPARE	F4	F3	F2	F1	GO
R/W	R/W	R/W	R/W	R/W	R/W	R/W	R/W

:RKCS2							
15	14	13	12	11	10	9	8
DLT	WCE	UPE	NED	NEM	PGE	MDS	UFE
RO	RO	RO	RO	RO	RO	RO	RO

7	6	5	4	3	2	1	0
OR	IR	CLR	BAI	DESL	DS2	DS1	DS0
RO	RO	WO	R/W	R/W	R/W	R/W	R/W

:RKDC (DESIRED CYLINDER)							
15	14	13	12	11	10	9	8
NU	NU	NU	NU	NU	NU	DC9	DC8
READ ONLY							

7	6	5	4	3	2	1	0
DC7	DC6	DC5	DC4	DC3	DC2	DC1	DC0
READ ONLY							

:RKDA (DESIRED TRACK AND SECTOR)							
15	14	13	12	11	10	9	8
NU	UN	UN	UN	UN	TA2	TA1	TA0
READ ONLY							

7	6	5	4	3	2	1	0
UN	UN	UN	SA4	SA3	SA2	SA1	SA0
READ ONLY							

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:RKDB (DATA BUFFER)							
15	14	13	12	11	10	9	8
DB15	DB14	DB13	DB12	DB11	DB10	DB9	DB8
READ/WRITE							
7	6	5	4	3	2	1	0
DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
READ/WRITE							
:RKASOF (ATTENTION SUMMARY AND OFFSET)							
15	14	13	12	11	10	9	8
ATN7	ATN6	ATN5	ATN4	ATN3	ATN2	ATN1	ATN0
READ ONLY							
7	6	5	4	3	2	1	0
UN	OF6	OF5	OF4	OF3	OF2	OF1	OF0
?	READ/WRITE						
:RKWC (WORD COUNT)							
15	14	13	12	11	10	9	8
WC15	WC14	WC13	WC12	WC11	WC10	WC9	WC8
READ/WRITE							
7	6	5	4	3	2	1	0
WC7	WC6	WC5	WC4	WC3	WC2	WC1	WC0
READ/WRITE							
:RKBA (BUS ADDRESS)							
15	14	13	12	11	10	9	8
BA15	BA14	BA13	BA12	BA11	BA10	BA9	BA8
READ/WRITE							
7	6	5	4	3	2	1	0
BA7	BA6	BA5	BA4	BA3	BA2	BA1	BA0
READ/WRITE							ALWYSO.

2787	:RKER (ERROR REGISTER)							
2788	15	14	13	12	11	10	9	8
2789	-----							
2790	DCK	UNS	OPI	DTE	WLE	IDAE	COE	HVRC
2791	READ ONLY							
2792	-----							
2793	: 7 6 5 4 3 2 1 0							
2794	-----							
2795	BSE	ECH	DTYPE	FMTE	DRPAR	ILF	SKI	ILC
2796	READ ONLY							
2797	-----							
2798	:RKDS (DRIVE STATUS)							
2799	15	14	13	12	11	10	9	8
2800	-----							
2801	SVAL	DSC	PIP	SPON	WRL	UN	UN	DTP
2802	READ ONLY							
2803	-----							
2804	: 7 6 5 4 3 2 1 0							
2805	-----							
2806	DRDY	VV	DROT	SPLS	ACLO	OFFSET	UN	DRA
2807	READ ONLY							
2808	-----							
2809	:RKMR1 (MAINTENANCE REG 1)							
2810	15	14	13	12	11	10	9	8
2811	-----							
2812	RD	WRT	ECCW	PCD	PCA	MEWD	MERD	MCLK
2813	GATE	GATE	READ ONLY				READ/WRITE	
2814	-----							
2815	: 7 6 5 4 3 2 1 0							
2816	-----							
2817	MIND	MSP	DMD	PAT	MS3	MS2	MS1	MS0
2818	READ/WRITE							
2819	-----							
2820	: 7 6 5 4 3 2 1 0							
2821	-----							
2822	:RKMR2 (MAINTENANCE REG 2)							
2823	15	14	13	12	11	10	9	8
2824	-----							
2825	: 7 6 5 4 3 2 1 0							
2826	-----							

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```
:RKECC/PAT
: 15 14 13 12 11 10 9 8
-----
: UN ! UN ! UN ! UN ! UN ! EPA10! EPA9 ! EPA8 !
: READ ONLY
-----

: 7 6 5 4 3 2 1 0
-----
: EPA7 ! EPA6 ! EPA5 ! EPA4 ! EPA3 ! EPA2 ! EPA1 ! EPA0 !
: READ ONLY
-----

:RKECC/POS
: 15 14 13 12 11 10 9 8
-----
: UN ! UN ! UN ! EPO12! EPO11! EPO10! EPO9 . EPO8 !
: READ ONLY
-----

: 7 6 5 4 3 2 1 0
-----
: EPO7 ! EPO6 ! EPO5 ! EPO4 ! EPO3 ! EPO2 ! EPO1 . EPO0 .
: READ ONLY
-----

:DRIVE STATUS INFORMATION

:LINE A MESSAGE 00
: 15 14 13 12 11 10 9 8
-----
: PAR ! DSC ! PIP ! SPON ! WRLK ! OFFON! FMT ! DRTP !
-----

: 7 6 5 4 3 2 1 0
-----
: DRDY ! VV DRA . NU NU ! DRIVE SELECT CODE !
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2901

: LINE B MESSAGE 00							
15	14	13	12	11	10	9	8
PAR	UNS	DROT	DCLO	WLE	SKI	PERR	ILF

7	6	5	4	3	2	1	0
FLT	ACLO	IVDA	UN	UN	UN	0	0

: LINE A MESSAGE 01							
15	14	13	12	11	10	9	8
PAR	UNLDG	RTZ	LDG	REV	FWD	SPEED	CART
	HDS		HDS			OK	PRES

7	6	5	4	3	2	1	0
DOOR	BRUSH	HEADS	TRK	UN	DRIVE SELECT CODE		
LTCH	HOME	HOME	FOLOW				
			OK				

: LINE B MESSAGE 01							
15	14	13	12	11	10	9	8
PAR	SERVO	LIMIT	SEEK	PLO	DIBIT	INDEX	MULTI
	BRAKE	ON SK	NO MO	ERR	ERR	ERR	HD SEL

7	6	5	4	3	2	1	0
HEAD	WR GTE	WR CUR	SEC	NU	NU	0	1
FAULT	AND NO	AND NO	ERR				
	XISTON	WR GTE					

```

2902 :LINE A MESSAGE 10
2903 : 15 14 13 12 11 10 9 8
2904 -----
2905 : PAR ! NU ! NU ! CYLINDER DIFF/OFFSET VALUE !
2906 -----
2907 : 7 6 5 4 3 2 1 0
2908 -----
2909 : CYLINDER DIFF/OFFSET VALUE! NU ! DRIVE SELECT CODE !
2910 -----
2911 :
2912 :LINE B MESSAGE 10
2913 : 15 14 13 12 11 10 9 8
2914 -----
2915 : PAR ! ALIGN! NU ! CYLINDER ADDRESS !
2916 : ! SIGN !
2917 -----
2918 : 7 6 5 4 3 2 1 0
2919 -----
2920 : CYLINDER ADDRESS ! UN ! UN ! 1 ! 0 !
2921 -----
2922 :
2923 :LINE A MESSAGE 11
2924 : 15 14 13 12 11 10 9 8
2925 -----
2926 : PAR ! DRIVE SERIAL NUMBER !
2927 -----
2928 : 7 6 5 4 3 2 1 0
2929 -----
2930 : DRIVE SERIAL NUMBER ! DRIVE SELECT CODE !
2931 -----
2932 :
2933 :LINE B MESSAGE 11
2934 : 15 14 13 12 11 10 9 8
2935 -----
2936 : PAR ! NU ! NU ! DECODED HEAD ! SECTOR!
2937 : ! ADDRESS ! COUNT !
2938 -----
2939 : 7 6 5 4 3 2 1 0
2940 -----
2941 : SECTOR COUNT . UN ! UN ! 1 ! 1 !
2942 -----
2943 :
2944 :
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2951 :
2952 :

```

.SBTTL RK06 CONTROLLER REGISTER DEFINITION

```

2953 000000 RKCS1= 0 ;CONTROL AND STATUS REGISTER 1
2954 000002 RKWC= 2 ;WORD COUNT REGISTER
2955 000004 RKBA= 4 ;BUS ADDRESS REGISTER
2956 000006 RKDA= 6 ;DESIRED TRACK SECTOR REGISTER
2957 000010 RKCS2= 10 ;CONTROL AND STATUS REGISTER 2

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```
2958      000012      RKDS= 12      ;DRIVE STATUS REGISTER
2959      000014      RKER= 14      ;ERROR REGISTER
2960      000016      RKASOF= 16     ;ATTENTION SUMMARY AND OFFSET REGISTER
2961      000020      RKDC= 20      ;DESIRED CYLINDER REGISTER
2962      000020      RKDCYL= 20     ;DESIRED CYLINDER REGISTER
2963      000024      RKDB= 24      ;DATA BUFFER
2964      000026      RKMR1= 26     ;MAINTENANCE REGISTER 1
2965      000034      RKMR2= 34     ;MAINTENANCE REGISTER 2
2966      000036      RKMR3= 36     ;MAINTENANCE REGISTER 3
2967      000030      RKPOS= 30     ;ECC POSITION INFORMATION
2968      000030      RKECPS= 30    ;ECC POSITION INFORMATION
2969      000032      FKPAT= 32     ;ECC PATTERN INFORMATION
2970      000032      RKECPT= 32    ;ECC PATTERN INFORMATION
2971
2972      .SBTTL  DRIVE COMMANDS
2973
2974      000101      SELDRV= 101    ;SELECT DRIVE
2975      000103      PACK= 103     ;PACK ACKNOWLEDGE
2976      000105      CLEAR= 105    ;DRIVE CLEAR
2977      000107      UNLOAD= 107   ;UNLOAD
2978      000111      SRTSPL= 111   ;START SPINDLE
2979      000113      RECAL= 113    ;RECALIBRATE
2980      000115      OFFSET= 115   ;OFFSET
2981      000117      SEEK= 117    ;SEEK
2982      000121      RDDATA= 121  ;READ DATA
2983      000123      WRDATA= 123  ;WRITE DATA
2984      000125      RDHEAD= 125  ;READ HEADER
2985      000127      WRHEAD= 127  ;WRITE HEADER AND DATA
2986      000131      WRTCHK= 131  ;WRITE CHECK
2987
2988      ;          THE FOLLOWING ARE NOT DRIVE COMMANDS BUT ARE USED BY THE DRIVER
2989      ;          TO SIMULATE A SPECIFIC DESIRED OPERATION
2990
2991      000140      RELEAS= 140   ;RELEASE DRIVE
2992      000141      RDSTAT= 141  ;GET ALL STATUS FROM DRIVE
2993      000164      RDALHD= 164  ;READ ALL HEADERS
2994      000176      CONCLR= 176  ;CONTROLLER CLEAR (BIT 15 OF CS1)
2995      000177      SUBCLR= 177  ;SUBSYSTEM CLEAR (BIT 5 OF CS2)
2996      000300      INTR= 300    ;GENERATE INTERRUPT TO CPU
2997
2998      ;          DRIVER ISSUED SERVICE COMMANDS
2999
3000      000001      DR.SEL= 001   ;DRIVE SELECT
3001      000005      DR.CLR= 005   ;DRIVE CLEAR
3002
3003      .SBTTL  CONTROL AND STATUS REGISTER 1 BITS
3004
3005      000001      GO= BIT0    ;GO BIT
3006      000100      IE= BIT6    ;INTERRUPT ENABLE
3007      000200      RDY= BIT7    ;CONTROLLER READY
3008      000400      BA16= BIT8    ;BUS ADDRESS BIT 16
3009      001000      BA17= BIT9    ;BUS ADDRESS BIT 17
3010      002000      CDT= BIT10   ;CONTROLLER DRIVE TYPE (0-RK06,1-RK07)
3011      004000      CTO= BIT11   ;CONTROLLER TIMED OUT WAITING FOR
3012      ;          DRIVE RESPONSE
3013      010000      CFMT= BIT12  ;CONTROLLER DRIVE FORMAT (0=22 SECTOR, 1-20 SECTOR)
```

```
3014      020000      SPAR=  BIT13      ;DRIVE BUS PARITY ERROR DETECTED BY CONTROLLER
3015      040000      DI=    BIT14      ;DRIVE INTERRUPT
3016      100000      CERR=  BIT15      ;CONTROLLER ERROR
3017      100000      CCLR=  BIT15      ;CONTROLLER CLEAR
3018
3019      ;          THESE BIT DEFINITIONS ARE USED FOR ADDRESS
3020      ;          THE HIGH BYTE OF RKCS1
3021
3022      000001      B.BA16= BIT0      ;BUS ADDRESS BIT 16
3023      000002      B.BA17= BIT1      ;BUS ADDRESS BIT 17
3024      000004      B.CDT=  BIT2      ;CONTROLLER DRIVE TYPE (0=RK06,1=RK07)
3025      000020      E.CFMT= BIT4      ;CONTROLLER DRIVE FORMAT (0=22 SECTOR, 1=20 SECTOR)
3026
3027      .SBTTL CONTROL AND STATUS REGISTER 2 BITS
3028
3029      000007      DRVMSK= 7      ;MASK FOR DRIVE SELECTION CODE
3030      000010      DESL=  BIT3      ;DESELECT OR RELEASE DRIVE IN BITS 0-2
3031      000010      RLS=  BIT3      ;DESELECT OR RELEASE DRIVE IN BITS 0-2
3032      000020      BAI=  BIT4      ;BUS ADDRESS INCREMENT INHIBIT
3033      000040      CLR=  BIT5      ;CLEAR CONTROLLER AND ALL DRIVES
3034      000040      SCLR= BIT5      ;CLEAR CONTROLLER AND ALL DRIVES
3035      000100      IR=   BIT6      ;INPUT READY
3036      000200      OR=   BIT7      ;OUTPUT READY
3037      000400      UFE=  BIT8      ;UNIT FIELD ERROR
3038      001000      MDS=  BIT9      ;MULTIPLE DRIVE SELECT
3039      002000      PGE=  BIT10     ;PROGRAMMING ERROR
3040      004000      NEM=  BIT11     ;NON-EXISTENT MEMORY
3041      010000      NED=  BIT12     ;NON-EXISTENT DRIVE
3042      020000      UPE=  BIT13     ;UNIBUS PARITY ERROR
3043      040000      WCE=  BIT14     ;WRITE CHECK ERROR
3044      100000      DLT=  BIT15     ;DATA LATE ERROR
3045
3046      .SBTTL ERROR REGISTER BIT DEFINITION
3047
3048      000001      ILC=  BIT0      ;ILLEGAL FUNCTION CODE
3049      ;*ILF= BIT0      ;ILLEGAL FUNCTION CODE
3050      000002      SKI=  BIT1      ;SEEK INCOMPLETE
3051      000004      ILF=  BIT2      ;ILLEGAL DRIVE FUNCTION
3052      000004      NXF=  BIT2      ;ILLEGAL DRIVE FUNCTION
3053      000010      DRPAR= BIT3      ;DRIVE DETECTED DRIVE BUS PARITY ERROR
3054      000020      FMTE= BIT4      ;FORMAT ERROR
3055      000040      DTYE= BIT5      ;DRIVE TYPE ERROR
3056      000100      ECH=  BIT6      ;ECC HARD
3057      000200      BSE=  BIT7      ;BAD SECTOR ERROR
3058      000400      HCRC= BIT8      ;HEADER CRC ERROR
3059      000400      HVRC= BIT8      ;HEADER VRC ERROR
3060      001000      COE=  BIT9      ;CYLINDER ADDRESS OVERFLOW ERROR
3061      002000      IDAE= BIT10     ;INVALID DISK ADDRESS ERROR
3062      004000      WLE=  BIT11     ;WRITE LOCK ERROR
3063      010000      DTE=  BIT12     ;DRIVE TIMING ERROR
3064      020000      OPI=  BIT13     ;OPERATION (SEARCH) INCOMPLETE
3065      040000      UNS=  BIT14     ;DRIVE UNSAFE
3066      100000      DCK=  BIT15     ;DATA CHECK
3067
3068      .SBTTL STATUS REGISTER BIT DEFINITION
3069
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3070      000001      DRA=   BIT0      ;DRIVE AVAILABLE (CONTROLLER IS SET IF
3071      ;          ; THIS BIT IS RESET)
3072      000004      OFST=   BIT2      ;DRIVE OFFSET
3073      000010      ACLO=   BIT3      ;AC LOW
3074      000020      SPDLS=  BIT4      ;SPEED LOSS
3075      000020      DCLO=   BIT4      ;DC LOW
3076      000040      DROT=   BIT5      ;DRIVE OFF TRACK
3077      000100      VV=     BIT6      ;VOLUME VALID
3078      000200      DRY=    BIT7      ;DRIVE READY
3079      000200      DRDY=   BIT7      ;DRIVE READY
3080      000400      DDT=    BIT8      ;DRIVE TYPE (0=RK06,1=RK07)
3081      004000      WRL=    BIT11     ;WRITE LOCK
3082      020000      PIP=    BIT13     ;POSITIONING IN PROGRESS
3083      040000      DSC=    BIT14     ;DRIVE STATUS CHANGE
3084      100000      SVAL=   BIT15     ;STATUS VALID
3085
3086      .SBTTL  MAINTENANCE REGISTER 1 BIT DEFINITION
3087
3088      000017      MESMSK= 17      ;MESSAGE MASK
3089
3090      000020      PAT=    BIT4      ;FORCE EVEN PARITY ON DRIVE BUS MESSAGE LINES
3091      000040      DMD=    BIT5      ;DIAGNOSTIC MODE
3092      000100      MSP=    BIT6      ;MAINTENANCE SECTOR PULSE
3093      000200      MIND=   BIT7      ;MAINTENANCE INDEX
3094      000400      MCLK=   BIT8      ;MAINTENANCE CLOCK
3095      001000      MERD=   BIT9      ;MAINTENANCE ENCODED READ DATA
3096      002000      MEWD=  BIT10     ;MAINTENANCE ENCODED WRITE DATA
3097      004000      PCA=    BIT11     ;PRECOMPENSATION ADVANCE
3098      010000      PCD=    BIT12     ;PRECOMPENSATION DELAY
3099      020000      ECCW=   BIT13     ;ECC WORD IS BEING READ OR WRITTEN
3100      040000      WRGAT=  BIT14     ;WRITE GATE
3101      100000      RDGATE= BIT15     ;READ GATE
3102
3103      .SBTTL  DEFINITION OF DRIVE STATUS BYTE 00 MESSAGE A
3104
3105      000040      S.DRA=  BITS      ;DRIVE AVAILIABLE
3106      000100      S.VV=   BIT6      ;VOLUME VALID
3107      000200      S.DRY=  BIT7      ;DRIVE READY
3108      000400      S.TYPE= BIT8      ;DRIVE TYPE
3109      001000      S.FORM= BIT9      ;DRIVE FORMAT
3110      002000      S.OFF=  BIT10     ;OFFSET
3111      004000      S.WRL=  BIT11     ;WRITE LOCK
3112      010000      S.SPIN= BIT12     ;SPINDLE ON
3113      020000      S.PIP=  BIT13     ;POSITIONING IN PROGRESS
3114      040000      S.DSC=  BIT14     ;DRIVE STATUS CHANGE
3115
3116      .SBTTL  DEFINITION OF DRIVE STATUS BYTE 00 MESSAGE B
3117
3118      000040      S.ICYL=  BITS      ;ILLEGAL CYLINDER ADDRESS
3119      000100      S.ACLO=  BIT6      ;AC LOW
3120      000200      S.FLT=  BIT7      ;DRIVE FAULT
3121      000400      S.ILF=  BIT8      ;ILLEGAL FUNCTION
3122      001000      S.PAR=  BIT9      ;DRIVE DETECTED DRIVE BUS PARITY ERROR
3123      002000      S.SKI=  BIT10     ;SEEK INCOMPLETE
3124      004000      S.WLE=  BIT11     ;WRITE LOCK ERROR
3125      010000      S.SPLS= BIT12     ;SPEED LOSS
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3126      010000      S.DCLO= BIT12      ;DC LOW
3127      020000      S.DROT= BIT13      ;DRIVE OFF TRACK
3128      040000      S.UNS= BIT14       ;DRIVE UNSAFE
3129
3130      .SBTTL  DEFINITION OF DRIVE STATUS BYTE 01 MESSAGE A
3131
3132      000020      S.XDOK= BIT4       ;TRANSDUCER OK
3133      000040      S.HDHM= BIT5       ;HEADS HOME
3134      000100      S.BRHM= BIT6       ;BRUSHES HOME
3135      000200      S.DOOR= BIT7       ;DOOR INTERLOCKED
3136      000400      S.CART= BIT8       ;CARTRAGE INTERLOCK
3137      001000      S.SPOK= BIT9       ;SPEED OK
3138      002000      S.FWD= BIT10      ;FORWARD
3139      004000      S.REV= BIT11      ;REVERSE
3140      010000      S.LOAD= BIT12     ;HEADS LOADING
3141      020000      S.RTZ= BIT13     ;RETURN TO ZERO
3142      040000      S.UNLD= BIT14     ;HEADS UNLOADING
3143
3144      .SBTTL  DEFINITION OF DRIVE STATUS BYTE 01 MESSAGE B
3145
3146      000020      S.SECT= BIT4       ;SECTOR ERROR
3147      000040      S.WCLK= BIT5       ;WRITE CLOCK AND NO WRITE GATE
3148      000100      S.WGAT= BIT6       ;WRITE GATE AND NO TRANSISTIONS
3149      000200      S.HDFL= BIT7       ;HEAD FAULT
3150      000400      S.MHD= BIT8        ;MULTIPLE HEAD SELECT
3151      001000      S.XERR= BIT9       ;INDEX ERROR
3152      002000      S.DIB= BIT10      ;DIBIT ERROR
3153      004000      S.PLO= BIT11      ;PLO ERROR
3154      010000      S.NMOV= BIT12     ;SEEK AND NO MOTION
3155      020000      S.LIMD= BIT13     ;LIMIT DETECT ON SEEK
3156      040000      S.BRKE= BIT14     ;SERVO-BRAKE
3157
3158      .SBTTL  COMMON MASKS
3159
3160      000007      M.DRV= 7          ;DRIVE CODE
3161      100000      M.PAR= BIT15     ;PARITY
3162      000003      M.ID= 3         ;BYTE ID
3163      017760      M.CDIF= 17760   ;CYLINDER DIFFERENCE/OFFSET
3164      017760      M.CADD= 17760   ;CYLINDER ADDRESS
3165      077770      M.SER= 77770    ;DRIVE SERIAL NUMBER
3166      000760      M.SECT= 760     ;SECTOR COUNT
3167      007000      M.HEAD= 7000    ;HEAD DECODE
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.SBTTL PARAMETER BLOCK ALLOCATION

1	COMMAND	DRIVE NO.	0
3	CYLINDER ADDRESS		2
5	TRACK	SECTOR	4
7	BA16-17,FORMAT,DRV TYPE	OFFSET	6
11	BUS ADDRESS (LOW 16 BITS)		10
13	WORD COUNT (2'S COMPLEMENT)		12
15	PROGRAM DRIVE STATUS INFORMATION		14
17	COMMAND AND STATUS REGISTER 1		16
21	COMMAND AND STATUS REGISTER 2		20
23	WORD COUNT REGISTER		22
25	BUS ADDRESS REGISTER		24
27	DESIRED TRACK AND SECTOR		26
31	DESIRED CYLINDER		30
33	ATTENTION SUMMARY AND DRIVE OFFSET		32
35	ERROR REGISTER		34
37	STATUS REGISTER		36
41	MESSAGE LINE A STATUS BYTE	00	40
43	MESSAGE LINE B STATUS BYTE	00	42
45	MESSAGE LINE A STATUS BYTE	01	44
47	MESSAGE LINE B STATUS BYTE	01	46
51	MESSAGE LINE A STATUS BYTE	10	50
53	MESSAGE LINE B STATUS BYTE	10	52
55	MESSAGE LINE A STATUS BYTE	11	54
57	MESSAGE LINE B STATUS BYTE	11	56
61	ECC POSITION INFORMATION		60
63	ECC PATTERN INFORMATION		62

.SBTTL PARAMETERS PASSED TO THE DRIVER

: THE FOLLOWING DEFINITIONS ARE USED TO PASS PARAMETERS
: TO THE RK06/RK07 DRIVER

000000	P.DRVN= 0	:DRIVE NUMBER
000001	P.CMND= 1	:COMMAND
000002	P.CYLN= 2	:CYLINDER ADDRESS
000004	P.SECT= 4	:SECTOR
000005	P.TRCK= 5	:TRACK
000006	P.OFST= 6	:OFFSET
000007	P.CS1H= 7	:RKCS1 BITS 8-15
000007	P.BAHI= 7	:BUS ADDRESS (BITS 16 AND 17)
000010	P.BALO= 10	:BUS ADDRESS (BITS 0-15)
000012	P.WC= 12	:WORD COUNT (2'S COMPLEMENT)
000014	P.PRST= 14	:PROGRAM DRIVE STATUS INFORMATION

.SBTTL PROGRAM DEVICE STATUS REGISTER DEFINITION

000001	DRVUSE= BIT0	:DRIVE IN USE
000002	DRVPOS= BIT1	:DRIVE POSITIONING
000004	DRV PDT= BIT2	:DRIVE POSITIONED FOR DATA TRANSFER
000010	UEXATT= BIT3	:UNEXPECTED ATTENTION
000020	DRVHRD= BIT4	:DRIVE HAS HARD ERROR
000040	DRV DSC= BITS	:DRIVE STATUS CHANGE DID NOT CLEAR

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3224          000100          CMDTO= BIT6          ;NO TERMINATION TO COMMAND FOR AT
3225          ;              ;LEAST 1 SECOND
3226          000200          W.WCK= BIT7          ;WRITE FOR WRITE WRITE CHECK
3227          000400          NOCHK= BIT8          ;NO CHECK, DO NOT SET INTERRUPT ENABLE
3228          001000          PBSVAL= BIT9          ;PARAMETER STATUS WORDS VALID
3229          ;              ;(SET WHEN ERROR TERMINATION OR
3230          ;              ;READ STATUS COMMAND)
3231          002000          DRPDRV= BIT10         ;DROP DRIVE FROM TEST SEQUENCE
3232          004000          NODSC= BIT11         ;ATTENTION SET BUT DCS AND FAULT RESET
3233          010000          DRVSZD= BIT12         ;DRIVE SEIZED BY OTHER PORT
3234          020000          E.UNLD= BIT13         ;DRIVE UNLOADED DUE TO ERROR
3235          040000          C.INIT= BIT14         ;PARAMETER BLOCK ENQUEUED IN INITIATION QUEUE
3236          100000          DTBAII= BIT15         ;INHIBIT BUS ADDRESS INCREMENT
3237
3238          .SBTTL  PARAMETERS PASSED FROM DRIVER TO PROGRAM
3239
3240          ;              THE FOLLOWING DEFINITIONS ARE USED FOR REGISTER RETURNS
3241          ;              FROM THE DRIVER TO THE CALLING PROGRAM
3242
3243          000016          P.CS1= 16          ;COMMAND AND STATUS REGISTER 1
3244          000020          P.CS2= 20          ;COMMAND AND STATUS REGISTER 2
3245          000022          P.WCR= 22          ;WORD COUNT REGISTER
3246          000024          P.BAR= 24          ;BUS ADDRESS REGISTER
3247          000026          P.DTS= 26          ;DESIRED TRACK SECTOR REGISTER
3248          000030          P.DCYL= 30         ;DESIRED CYLINDER REGISTER
3249          000032          P.ASOF= 32         ;ATTENTION SUMMARY/OFFSET REGISTER
3250          000034          P.ER= 34          ;ERROR REGISTER
3251          000036          P.DS= 36          ;STATUS REGISTER
3252          000040          P.A00= 40         ;MESSAGE A STATUS BYTE 00
3253          000042          P.B00= 42         ;MESSAGE B STATUS BYTE 00
3254          000044          P.A01= 44         ;MESSAGE A STATUS BYTE 01
3255          000046          P.B01= 46         ;MESSAGE B STATUS BYTE 01
3256          000050          P.A10= 50         ;MESSAGE A STATUS BYTE 10
3257          000052          P.B10= 52         ;MESSAGE B STATUS BYTE 10
3258          000054          P.A11= 54         ;MESSAGE A STATUS BYTE 11
3259          000056          P.B11= 56         ;MESSAGE B STATUS BYTE 11
3260          000060          P.EPOS= 60        ;ECC POSITION INFORMATION
3261          000062          P.EPAT= 62        ;ECC PATTERN INFORMATION
3262
3263          .SBTTL  PARAMETER BLOCK 0 FOR DRIVE
3264
3265          002630          000          PARM0: .BYTE 0          ;DRIVE NUMBER
3266          002631          000          .BYTE 0          ;COMMAND
3267          002632          000000        .WORD 0          ;CYLINDER ADDRESS
3268          002634          000          .BYTE 0          ;SECTOR ADDRESS
3269          002635          000          .BYTE 0          ;TRACK ADDRESS
3270          002636          000          .BYTE 0          ;OFFSET VALUE
3271          002637          000          .BYTE 0          ;BUS ADDRESS (BITS 16 AND 17)
3272          002640          000000        .WORD 0          ;BUS ADDRESS (BITS 0 - 15)
3273          002642          000000        .WORD 0          ;WORD COUNT (2'S COMPLEMENT)
3274          002644          000000        .WORD 0          ;PROGRAM DRIVE STATUS INFORMATION
3275          002646          000000        .WORD 0          ;COMMAND AND STATUS REGISTER 1
3276          002650          000000        .WORD 0          ;COMMAND AND STATUS REGISTER 2
3277          002652          000000        .WORD 0          ;WORD COUNT REGISTER
3278          002654          000000        .WORD 0          ;BUS ADDRESS REGISTER
3279          002656          000000        .WORD 0          ;DESIRED TRACK AND SECTOR REGISTER
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3280	002660	000000	.WORD	0	: DESIRED CYLINDER REGISTER
3281	002662	000000	.WORD	0	: ATTENTION SUMMARY/OFFSET REGISTER
3282	002664	000000	.WORD	0	: ERROR REGISTER
3283	002666	000000	.WORD	0	: STATUS REGISTER
3284	002670	000000	.WORD	0	: MESSAGE LINE A STATUS BYTE 00
3285	002672	000000	.WORD	0	: MESSAGE LINE B STATUS BYTE 00
3286	002674	000000	.WORD	0	: MESSAGE LINE A STATUS BYTE 01
3287	002676	000000	.WORD	0	: MESSAGE LINE B STATUS BYTE 01
3288	002700	000000	.WORD	0	: MESSAGE LINE A STATUS BYTE 10
3289	002702	000000	.WORD	0	: MESSAGE LINE B STATUS BYTE 10
3290	002704	000000	.WORD	0	: MESSAGE LINE A STATUS BYTE 11
3291	002706	000000	.WORD	0	: MESSAGE LINE B STATUS BYTE 11
3292	002710	000000	.WORD	0	: ECC POSITION INFORMATION
3293	002712	000000	.WORD	0	: ECC PATTERN INFORMATION

3294
3295 .SBTTL PARAMETER BLOCK 1 FOR DRIVE

3296					
3297	002714	000	PARM1: .BYTE	0	: DRIVE NUMBER
3298	002715	000	.BYTE	0	: COMMAND
3299	002716	000000	.WORD	0	: CYLINDER ADDRESS
3300	002720	000	.BYTE	0	: SECTOR ADDRESS
3301	002721	000	.BYTE	0	: TRACK ADDRESS
3302	002722	000	.BYTE	0	: OFFSET VALUE
3303	002723	000	.BYTE	0	: BUS ADDRESS (BITS 16 AND 17)
3304	002724	000000	.WORD	0	: BUS ADDRESS (BITS 0 - 15)
3305	002726	000000	.WORD	0	: WORD COUNT (2'S COMPLEMENT)
3306	002730	000000	.WORD	0	: PROGRAM DRIVE STATUS INFORMATION
3307	002732	000000	.WORD	0	: COMMAND AND STATUS REGISTER 1
3308	002734	000000	.WORD	0	: COMMAND AND STATUS REGISTER 2
3309	002736	000000	.WORD	0	: WORD COUNT REGISTER
3310	002740	000000	.WORD	0	: BUS ADDRESS REGISTER
3311	002742	000000	.WORD	0	: DESIRED TRACK AND SECTOR REGISTER
3312	002744	000000	.WORD	0	: DESIRED CYLINDER REGISTER
3313	002746	000000	.WORD	0	: ATTENTION SUMMARY/OFFSET REGISTER
3314	002750	000000	.WORD	0	: ERROR REGISTER
3315	002752	000000	.WORD	0	: STATUS REGISTER
3316	002754	000000	.WORD	0	: MESSAGE LINE A STATUS BYTE 00
3317	002756	000000	.WORD	0	: MESSAGE LINE B STATUS BYTE 00
3318	002760	000000	.WORD	0	: MESSAGE LINE A STATUS BYTE 01
3319	002762	000000	.WORD	0	: MESSAGE LINE B STATUS BYTE 01
3320	002764	000000	.WORD	0	: MESSAGE LINE A STATUS BYTE 10
3321	002766	000000	.WORD	0	: MESSAGE LINE B STATUS BYTE 10
3322	002770	000000	.WORD	0	: MESSAGE LINE A STATUS BYTE 11
3323	002772	000000	.WORD	0	: MESSAGE LINE B STATUS BYTE 11
3324	002774	000000	.WORD	0	: ECC POSITION INFORMATION
3325	002776	000000	.WORD	0	: ECC PATTERN INFORMATION

3326
3327 .SBTTL TEMPORARY CONTROLLER REGISTER STORAGE

3328					
3329	003000	000000	T.CS1: .WORD	0	: TEMPORARY STORAGE FOR COMMAND AND STATUS REGISTER 1
3330					
3331	003002	000000	T.CS2: .WORD	0	: TEMPORARY STORAGE FOR COMMAND AND STATUS REGISTER 2
3332					
3333	003004	000000	T.WCR: .WORD	0	: TEMPORARY STORAGE FOR WORD COUNT REGISTER
3334	003006	000000	T.BA: .WORD	0	: TEMPORARY STORAGE FOR BUS ADDRESS REGISTER
3335	003010	000000	T.DA: .WORD	0	: TEMPORARY STORAGE FOR DISK TRACK AND SECTOR

3336 003012 000000 T.DC: .WORD 0 ;TEMPORARY STORAGE FOR DRIVE CYLINDER
3337 003014 000000 T.ASOF: .WORD 0 ;TEMPORARY STORAGE FOR ATTENTION SUMMARY
3338 ; AND OFFSET
3339 003016 000000 T.ER: .WORD 0 ;TEMPORARY STORAGE FOR ERROR REGISTER
3340 003020 000000 T.DS: .WORD 0 ;TEMPORARY STORAGE FOR DRIVE STATUS REGISTER
3341 003022 000000 T.MR1: .WORD 0 ;TEMPORARY STORAGE FOR MAINTENANCE REGISTER 1
3342 003024 000000 T.MR2: .WORD 0 ;TEMPORARY STORAGE FOR MAINTENANCE REGISTER 2
3343 003026 000000 T.MR3: .WORD 0 ;TEMPORARY STORAGE FOR MAINTENANCE REGISTER 3
3344 003030 000000 T.POS: .WORD 0 ;TEMPORARY STORAGE FOR ECC POSITION
3345 003032 000000 T.PAT: .WORD 0 ;TEMPORARY STORAGE FOR ECC PATTERN
3346 003034 000000 T.DB: .WORD 0 ;TEMPORARY STORAGE FOR DATA BUFFER REGISTER

3347
3348 .SBTTL DRIVER PARAMETERS

3349
3350 003036 177440 RKBAS: .WORD 177440 ;ADDRESS OF RK611 UNIBUS ADDRESS BLOCK
3351 003040 000210 RKVEC: .WORD 210 ;ADDRESS OF R611 VECTOR
3352 003042 000240 RKPRI: .WORD PR5 ;RK611 INTERRUPT PRIORITY
3353 003044 040652 A.NORM: ERRFRE ;ADDRESS OF NORMAL RETURN FROM DRIVER
3354 003046 042130 A.ABNL: ERRHDL ;ADDRESS OF ABNORMAL RETURN FROM DRIVER
3355 003050 041414 A.CONT: CONERR ;ADDRESS OF CONTROLLER ERROR RETURN
3356 003052 000000 E.CONT: .WORD 0 ;CONTROLLER ERROR STATUS

3357 ; THIS LOCATION IS CLEARED WHEN EVERY COMMAND
3358 ; IS INITIATED. IF A CONTROLLER ERROR
3359 ; OCCURS THE FOLLOWING BIT ASSIGNMENT IS
3360 ; USED:

3361
3362 000001 E.CCLR= BIT0 ;CLEAR CONTROLLER DID NOT CLEAR ERROR
3363 000002 E.NOAT= BIT1 ;NO ATTENTION IN ATTENTION SUMMARY REG
3364 000004 E.UATT= BIT2 ;UNSOLICATED ATTENTION (SEQUENTIAL ONLY)
3365 000010 E.UDAT= BIT3 ;UNEXPECTED DATA TYPE ERROR
3366 000020 E.CLAT= BIT4 ;ATTENTION DID NOT RESET WITH CLEAR
3367 000040 E.SCLR= BIT5 ;SUBSYSTEM CLEAR DID NOT CLEAR DRIVE
3368 ; ATTENTION
3369 000100 E.ILLD= BIT6 ;ILLEGAL DRIVER COMMAND
3370 000400 E.DLT= BIT8 ;DATA LATE WHEN UNLOADING HEADER
3371 001000 E.CERR= BIT9 ;CONTROLLER ERROR DURING DRIVER SERVICING
3372 002000 E.DPAR= BIT10 ;DRIVE DETECTED PARITY ERROR
3373 040000 E.CMTO= BIT14 ;CONTROLLER COMMAND TIME OUT (QUEUFD ONLY)
3374 100000 E.MDS= BIT15 ;MULTIPLE DRIVE SELECT

3375
3376 003054 000000 O.WAIT: .WORD 0 ;PARAMETER BLOCK OF THE DRIVE
3377 ;WAITING FOR COMMAND COMPLETION
3378 003056 000400 W.MTIM: .WORD 400 ;LOOP COUNTER FOR MILLISECOND SCAN OF DRIVE
3379 003060 000400 W.MILI: .WORD 400 ;16 MILLISECOND TIME FOR PROGRAM

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CPU	VALUE
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11/05	100
11/10	
11/20	
11/34	
11/40	
11/45	400
11/50	
11/70	

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3392 003062 000300 W.SEC: .WORD 300 ;SECOND COUNT COUNT FOR ALL COMMANDS
3393 ; EXCEPT START SPINDLE
3394 003064 003000 W.8SEC: .WORD 3000 ;8 SECOND FOR DRIVE CYCLE DOWN
3395 003066 030000 W.MIN: .WORD 30000 ;MINUTE TIME FOR START SFINDLE
3396 003070 000000 HDR.AD: .WORD 0 ;ADDRESS USED FOR READ ALL HEADERS
3397 003072 000000 HDR.CT: .WORD 0 ;NUMBER OF HEADERS LEFT TO READ FOR READ
3398 ; ALL HEADERS
3399 003074 000 I.ISRL: .BYTE 0 ;INTERRUPT OR RELEASED COMMAND ISSUED
3400 003075 002 004 010 H.HEAD: .BYTE 2,4,10 ;HEAD DECODES
3401 003100 000 W.TIME: .BYTE 0 ;DRIVES BEING WATCH-DOG TIMED
3402
3403 .SBTTL INTERRUPT MASKS
3404
3405 003101 000 INTMSK: .BYTE 0 ;INTERRUPT MASKS FOR DRIVE IN PARAMETER BLOCK
3406
3407 ; INTERRUPT MASK TABLE
3408
3409 003102 001 I.DRV: .BYTE 1 ;INTERRUPT MASK FOR DRIVE 0
3410 003103 002 .BYTE 2 ;INTERRUPT MASK FOR DRIVE 1
3411 003104 004 .BYTE 4 ;INTERRUPT MASK FOR DRIVE 2
3412 003105 010 .BYTE 10 ;INTERRUPT MASK FOR DRIVE 3
3413 003106 020 .BYTE 20 ;INTERRUPT MASK FOR DRIVE 4
3414 003107 040 .BYTE 40 ;INTERRUPT MASK FOR DRIVE 5
3415 003110 100 .BYTE 100 ;INTERRUPT MASK FOR DRIVE 6
3416 003111 200 .BYTE 200 ;INTERRUPT MASK FOR DRIVE 7
3417
3418 .SBTTL PARAMETER BLOCK TABLE
3419
3420 003112 002630 PBLKT: PARMO ;ADDRESS OF PARAMETER BLOCK GIVEN WITH
3421 ;DRIVE CALL. MUST BE LOADED INTO PBLKT
3422
3423
3424 .SBTTL TIME FOR WATCH-DOG TIMER
3425
3426 003114 000000 W.DRV: .WORD 0 ;TIME FOR INSTRUCTION IN PARAMETER BLOCK
3427 .SBTTL PROGRAM SPECIFIC RESERVED LOCATIONS
3428 003116 000 MDFLAG: .BYTE 0 ;FLAG TO INDIC. DEFLT OR PARAM MODE
3429 003117 000 XXDPCH: .BYTE 0 ;XXDP CHAIN MODE FLAG
3430 003120 000 TSTING: .BYTE 0 ;CURRENTLY RUNNING TESTS IF = 1
3431 003121 000 DERCNT: .BYTE 0 ;DATA ERROR COUNT
3432 003122 000 OPCOMP: .BYTE 0 ;OPERATION COMPLETE FLAG
3433 003123 000 DONE: .BYTE 0 ;DONE SWITCH
3434 003124 000 TYPFMT: .BYTE 0 ;DRIVE TYPE & FORMAT CONTROL
3435 003125 000 FORMAT: .BYTE 0 ;DRIVE FORMAT IN BIT 4 OF BYTE
3436 003126 000 ERRCNT: .BYTE 0 ;ERROR COUNT
3437 003127 004 ERRLMT: .BYTE 4 ;ERROR LIMIT
3438 003130 000 DRVERS: .BYTE 0 ;ERROR COUNT FOR CURRENT DRIVE
3439 003131 000 OPCONT: .BYTE 0 ;OPERATION CONTROL SWITCHES
3440 003132 000 PCLKF: .BYTE 0 ;IF BYTE=1, KW11-P CLOCK IS PRESENT
3441 003133 000 DOTIM: .BYTE 0 ;IF BYTE=1, DO TIMING TESTS
3442 003134 000 XOVLD: .BYTE 0 ;FLAG = 1 IF XXDP IS POSSIBLY OVERLAID
3443 003135 000 XDPSVD: .BYTE 0 ;FLAG = 1 IF XXDP IS SAVED
3444 003136 000 DULACS: .BYTE 0 ;FLAG=1 IF DUAL ACCESS TEST
3445 003137 000 DRNAFG: .BYTE 0 ;=1 INDICATES DRIVE SIEZED BY OTHER PORT
3446 003140 000 REISSU: .BYTE 0 ;DUAL-ACC FLAG TO RE-ISSUE COMMAND
3447 003141 000 WCEFLG: .BYTE 0 ;WRITE CHECK ERROR FLAG
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3448	003142	000			DLTFLG: .BYTE	0		: DATA LATE ERROR FLAG
3449	003143	000			NORTRY: .BYTE	0		: 'NO-RETRY' FLAG
3450	003144	000			UBMPRS: .BYTE	0		: UNIBUS MAP PRESENT IF = 1
3451	003145	000			MEMABT: .BYTE	0		: SET BYTE = 1 FOR NO ABORT
3452								: ON MEMORY PARITY ERRORS
3453	003146	000			HLPOVL: .BYTE	0		: HELP FILE OVERLAID INDICATOR
3454	003147	000			NOTYPE: .BYTE	0		: INDICATES PROGRAM JUST LOADED IF 0
3455		000001			WHDSW=BIT0			: WRITE HEADER & DATA SWITCH
3456		000002			VFHDSW=BIT1			: VERIFY HEADERS SWITCH
3457		000004			WCDASW=BIT2			: WRITE CHECK DATA SWITCH
3458		000010			RCDASW=BIT3			: READ CHECK DATA SWITCH
3459		000020			CREQSW=BIT4			: OFFSET REQUIRED SWITCH
3460								
3461					.EVEN			
3462	003150	177546			LKS: .WORD	177546		: KW11-L CLOCK STATUS REGISTER
3463	003152	172540			PKS: .WORD	172540		: KW11-P CONTROL AND STAT'S REGISTER
3464	003154	172542			PKSB: .WORD	172542		: KW11-P COUNT SET BUFFER REGISTER
3465	003156	172544			PKRB: .WORD	172544		: KW11-P COUNTER REGISTER
3466	003160	000100			LCVEC: .WORD	100		: KW11-L VECTOR STORAGE
3467	003162	000104			PCVEC: .WORD	104		: KW11-P VECTOR STORAGE
3468		000114			MEMVEC=114			: MEMORY PARITY TRAP VECTOR
3469	003164	000000			TCONLO: .WORD	0		: LO BITS OF CALIBRATION TIME CONSTANT
3470	003166	000000			TCONHI: .WORD	0		: HI BITS OF CALIB TIME CONST
3471	003170	000000			BLWMIN: .WORD	0		: COUNT OF TIMES BELOW MIN
3472	003172	000000			ABVMX1: .WORD	0		: COUNT OF FORWARD TIMES ABOVE MAX
3473	003174	000000			ABVMX2: .WORD	0		: COUNT OF REVERSE TIMES ABOVE MAX
3474	003176	000000			SUML01: .WORD	0		: LO BITS OF FORWARD TIME SUM
3475	003200	000000			SUMH11: .WORD	0		: HI BITS OF FORWARD TIME SUM
3476	003202	000000			SUML02: .WORD	0		: LO BITS OF REVERSE TIME SUM
3477	003204	000000			SUMH12: .WORD	0		: HI BITS OF REVERSE TIME SUM
3478	003206	000000			SAVPAR: .WORD	0		: SAVE WORD FOR PAR CONSTANT
3479	003210	000000			SAVWRD: .WORD	0		: SCRATCH WORD
3480	003212	000000			MINIL1: .WORD	0		: LO BITS OF MIN MEAS'D TIME (FORWARD)
3481	003214	000000			MINIH1: .WORD	0		: HI BITS OF MIN MEAS'D TIME (FORWARD)
3482	003216	000000			MAXIL1: .WORD	0		: LO BITS OF MAX MEAS'D TIME (FORWARD)
3483	003220	000000			MAXIH1: .WORD	0		: HI BITS OF MAX MEAS'D TIME (FORWARD)
3484	003222	000000			MINIL2: .WORD	0		: LO BITS OF MIN MEAS'D TIME (REVERSE)
3485	003224	000000			MINIH2: .WORD	0		: HI BITS OF MIN MEAS'D TIME (REVERSE)
3486	003226	000000			MAXIL2: .WORD	0		: LO BITS OF MAX MEAS'D TIME (REVERSE)
3487	003230	000000			MAXIH2: .WORD	0		: HI BITS OF MAX MEAS'D TIME (REVERSE)
3488	003232	000400			BSSOFT: .BLKW	*D256		: RECORD OF BAD SECTORS FROM SOFTWARE
3489	004232	000400			BSFACT: .BLKW	*D256		: RECORD OF BAD SECTORS FROM FACTORY
3490	005232	000000	000000	000000	PRVCMD: .WORD	0,0,0,0,0,0		: PREVIOUS COMMAND STORAGE
3491	005240	000000	000000	000000				
3492	005246	000000	000000	000000	COMSTR: .WORD	0,0,0,0,0,0		: CURRENT COMMAND STORAGE
3493	005254	000000	000000	000000				
3494	005262	000000			PRMPLO: .WORD	0		: PREV. U.B. MAP REG 0
3495	005264	000000			PRMPHO: .WORD	0		
3496	005266	000000			CRMPLO: .WORD	0		: CURRENT U.B. MAP REG 0
3497	005270	000000			CRMPHO: .WORD	0		
3498	005272	000102			BUFF0: .BLKW	*D66		: OUTPUT BUFFER 1
3499	005476	000000			LOWOCT: .WORD	0		: LOW 16 BITS OF CONVERTED BINARY NUMBER
3500	005500	000000			HIGOCT: .WORD	0		: HIGH BITS OF CONVERTED BINARY NO.
3501	005502	000000			BUFPRT: .WORD	0		: BUFFER POINTER
3502	005504	000000			RECODE: .WORD	0		: RECOVERY CODE WORD
3503	005506	000000			ERRCOM: .WORD	0		: ERROR COMMAND

3504	005510	000000	DRIVE:	.WORD	0	:NO. OF DRIVE IN USE
3505	005512	000000	STALLS:	.WORD	0	:CURRENT NO. OF UNIT STALLS TO APPLY
3506	005514	000000	CYLNR:	.WORD	0	:CURRENT CYLINDER NUMBER
3507	005516	000000	FS:	.WORD	0	:FIRST SECTOR LIMIT
3508	005520	000000	LS:	.WORD	0	:LAST SECTOR LIMIT
3509	005522	000000	NCYL1:	.WORD	0	:NEXT CYL SCRATCH WORD
3510	005524	000000	NCYL2:	.WORD	0	:NEXT CYL SCRATCH WORD
3511	005526	000000	OFINUS:	.WORD	0	:OFFSET IN USE
3512	005530	000000	TRACK:	.WORD	0	:TRACK IN USE
3513	005532	000000	INTCHR:	.WORD	0	:TTY INTERRUPT INPUT WORD
3514	005534	000000	SELECT:	.WORD	0	:ANY BIT SET IN LOW BYTE GIVES AUTO-SELECTED DRIVE NO.
3515	005536	000000	CNLINE:	.WORD	0	:ANY BIT SET IN LOW BYTE INDICATES A DRIVE WAS ON-LINE
3516	005540	000000	NEWON:	.WORD	0	:NEW LOOK AT ONLINE DRIVES
3517	005542	000000	SCRACH:	.WORD	0	:ALL-PURPOSE SCRATCH WORD
3518	005544	000000	PATRN:	.WORD	0	:DATA PATTERN LIST WORD
3519	005546	000000	XXDPAD:	.WORD	0	:STARTING ADDRESS OF XXDP LOADER
3520	005550	000002	XDPSAV:	.BLKW	2	:XXDP PHYSICAL SAVE ADDRESS
3521	005554	000000	PLOFST:	.WORD	0	:(+) OFFSET VALUE
3522	005556	000000	NGOFST:	.WORD	0	:(-) OFFSET VALUE
3523	005560	000005	SAVPRS:	.BLKW	5	:SAVE INITIAL PARAMS FOR XFER
3524	005572	000000	WDSXFR:	.WORD	0	:NO. OF WORDS ACTUALLY XFERRED
3525	005574	000000	FINCYL:	.WORD	0	:FINAL CYLINDER ADRS
3526	005576	000	FINTRK:	.BYTE	0	:FINAL TRACK ADRS
3527	005577	000	FINSEC:	.BYTE	0	:FINAL SECTOR ADRS
3528	005600	000000	LASTWC:	.WORD	0	:ACTUAL FINAL WC
3529	005602	000002	PMA:	.BLKW	2	:PARTIAL TRANSFER MEMORY START ADRS

3530
3531
3532
3533 ;LIST OF DESIRED DRIVES TO TEST (IF BYTE=0, DON'T TEST DRIVE)

3534	005606		DRVLST:			
3535	005606	001		.BYTE	1	:DRIVE 0
3536	005607	001		.BYTE	1	:DRIVE 1
3537	005610	001		.BYTE	1	:DRIVE 2
3538	005611	001		.BYTE	1	:DRIVE 3
3539	005612	001		.BYTE	1	:DRIVE 4
3540	005613	001		.BYTE	1	:DRIVE 5
3541	005614	001		.BYTE	1	:DRIVE 6
3542	005615	001		.BYTE	1	:DRIVE 7

3543
3544 ;LIST OF DRIVE TYPES 0=RK06, NON 0=RK07

3545	005616		DTYLIST:			
3546	005616	000		.BYTE	0	:DRV 0
3547	005617	000		.BYTE	0	:DRV 1
3548	005620	000		.BYTE	0	:DRV 2
3549	005621	000		.BYTE	0	:DRV 3
3550	005622	000		.BYTE	0	:DRV 4
3551	005623	000		.BYTE	0	:DRV 5
3552	005624	000		.BYTE	0	:DRV 6
3553	005625	000		.BYTE	0	:DRV 7

3554
3555
3556
3557 ;LIST OF ITERATION COUNTS FOR DESIRED TESTS (IF=0, TEST NOT RUN)

3558 ;MAXIMUM ALLOWABLE NUMBER OF ITERATIONS = 77776 (OCT)

3559 005626 TSTLIST:

3560	005626	000010	.WORD	10	:TEST	01
3561	005630	000200	.WORD	200	:TEST	02
3562	005632	000010	.WORD	10	:TEST	03
3563	005634	000010	.WORD	10	:TEST	04
3564	005636	000002	.WORD	2	:TEST	05
3565	005640	000002	.WORD	2	:TEST	06
3566	005642	000002	.WORD	2	:TEST	07
3567	005644	000400	.WORD	400	:TEST	10
3568	005646	000500	.WORD	500	:TEST	11
3569	005650	000002	.WORD	2	:TEST	12
3570	005652	000001	.WORD	1	:TEST	13
3571	005654	000001	.WORD	1	:TEST	14
3572	005656	000001	.WORD	1	:TEST	15
3573	005660	000001	.WORD	1	:TEST	16
3574	005662	000010	.WORD	10	:TEST	17
3575	005664	000010	.WORD	10	:TEST	20
3576	005666	000001	.WORD	1	:TEST	21

3577
3578
3579

:LIST OF DEFAULT ITERATION COUNTS FOR ALL TESTS
DFLTST:

3581	005670		.WORD	10	:TEST	01
3582	005670	000010	.WORD	200	:TEST	02
3583	005672	000200	.WORD	10	:TEST	03
3584	005674	000010	.WORD	10	:TEST	04
3585	005676	000010	.WORD	10	:TEST	05
3586	005700	000002	.WORD	2	:TEST	06
3587	005702	000002	.WORD	2	:TEST	07
3588	005704	000002	.WORD	2	:TEST	07
3589	005706	000400	.WORD	400	:TEST	10
3590	005710	000500	.WORD	500	:TEST	11
3591	005712	000002	.WORD	2	:TEST	12
3592	005714	000001	.WORD	1	:TEST	13
3593	005716	000001	.WORD	1	:TEST	14
3594	005720	000001	.WORD	1	:TEST	15
3595	005722	000001	.WORD	1	:TEST	16
3596	005724	000010	.WORD	10	:TEST	17
3597	005726	000010	.WORD	10	:TEST	20
3598	005730	000001	.WORD	1	:TEST	21

3599
3600

3601 000021

NMTSTS=<DFLTST-TSTLST>/2 ;TOTAL NO. OF AUTOMATIC TESTS

3602
3603
3604

:OPERATING PARAMETER LIST
PRMLST:

3605	005732		FC:	.WORD	0	:FIRST CYLINDER
3606	005732	000000	LC:	.WORD	0	:LAST CYLINDER. 632 FOR RK06, 1456 FOR RK07
3607	005734	000000	IC:	.WORD	1	:CYLINDER INCREMENT
3608	005736	000001	FT:	.WORD	0	:FIRST TRACK
3609	005740	000000	LT:	.WORD	2	:LAST TRACK
3610	005742	000002	IT:	.WORD	1	:TRACK INCREMENT
3611	005744	000001	SO:	.WORD	0	:FIRST SECTOR IF 20(DEC) SECTOR FMT
3612	005746	000000	S1:	.WORD	23	:LAST SECTOR IF 20(DEC) SECTOR FMT
3613	005750	000023	S2:	.WORD	0	:FIRST SECTOR IF 22(DEC) SECTOR FMT
3614	005752	000000	S3:	.WORD	25	:LAST SECTOR IF 22(DEC) SECTOR FMT
3615	005754	000025				

3616	005756	000001	IS:	.WORD	1	:SECTOR INCREMENT
3617	005760	000000	PT:	.WORD	0	:DATA PATTERN SELECT WORD
3618	005762	063520	MA:	.WORD	RWBUF	:LO BITS OF PHYS. MEM. ADDR. (BITS 0-15)
3619	005764	000000		.WORD	0	:HI BITS OF PHYS. MEM. ADDR. (BITS 16-21)
3620	005766	000403	WC:	.WORD	403	:WORD COUNT (IF WC=0, WRD CNT IS 65,536 DEC)
3621	005770	000000	CS:	.WORD	0	:CONTROL SWITCH WORD
3622	005772	000000	ST:	.WORD	0	:NUMBER OF UNIT STALLS
3623	005774	001000	SM:	.WORD	1000	:MAX. STALLS, TEST 11

3624
3625

:DEFAULT OPERATING PARAMETER VALUES
PRDFLT:

3627	005776			.WORD	0	:FC DEFAULT
3628	005776	000000		.WORD	0	:LC DEFAULT
3629	006000	000000		.WORD	0	:IC DEFAULT
3630	006002	000001		.WORD	1	:FT DEFAULT
3631	006004	000000		.WORD	0	:LT DEFAULT
3632	006006	000002		.WORD	2	:IT DEFAULT
3633	006010	000001		.WORD	1	:SO DEFAULT
3634	006012	000000		.WORD	0	:S1 DEFAULT
3635	006014	000023		.WORD	23	:S2 DEFAULT
3636	006016	000000		.WORD	0	:S3 DEFAULT
3637	006020	000025		.WORD	25	:IS DEFAULT
3638	006022	000001		.WORD	1	:PT DEFAULT
3639	006024	000000		.WORD	0	:LOW MA (0-15) DEFAULT
3640	006026	063520		.WORD	RWBUF	:HIGH MA (16-21) DEFAULT
3641	006030	000000		.WORD	0	:WC DEFAULT
3642	006032	000403		.WORD	403	:CS DEFAULT
3643	006034	000000		.WORD	0	:ST DEFAULT
3644	006036	000000		.WORD	0	:SM DEFAULT
3645	006040	001000		.WORD	1000	

3646
3647

:OPERATING PARAMETER VALUE LOW AND HIGH LIMITS
PRMLIM:

3649				.WORD	0	:FC LIMITS
3650	006042			.WORD	0	:LC LIMITS
3651	006042	000000		.WORD	0	:IC LIMITS
3652	006044	000000		.WORD	0	:FT LIMITS
3653	006046	000000		.WORD	0	:LT LIMITS
3654	006050	000000		.WORD	0	:IT LIMITS
3655	006052	000001		.WORD	1	:SO LIMITS
3656	006054	000000		.WORD	0	:S1 LIMITS
3657	006056	000000		.WORD	0	:S2 LIMITS
3658	006060	000002		.WORD	2	:S3 LIMITS
3659	006062	000000		.WORD	0	:IS LIMITS
3660	006064	000002		.WORD	2	
3661	006066	000001		.WORD	1	
3662	006070	000002		.WORD	2	
3663	006072	000000		.WORD	0	
3664	006074	000023		.WORD	23	
3665	006076	000000		.WORD	0	
3666	006100	000023		.WORD	23	
3667	006102	000000		.WORD	0	
3668	006104	000025		.WORD	25	
3669	006106	000000		.WORD	0	
3670	006110	000025		.WORD	25	
3671	006112	000001		.WORD	1	

3672	006114	000025	.WORD	25	
3673	006116	000000	.WORD	0	;PT LIMITS
3674	006120	177777	.WORD	177777	
3675	006122	063520	.WORD	RWBUF	;MA LOWER LIMIT
3676	006124	000000	.WORD	0	
3677	006126	157776	MAHILM: .WORD	157776	;MA UPPER LIMIT
3678	006130	000077	.WORD	77	
3679	006132	000000	.WORD	0	;WC LIMITS
3680	006134	177777	.WORD	177777	
3681	006136	000000	.WORD	0	;CS LIMITS
3682	006140	000070	.WORD	000070	
3683	006142	000000	.WORD	0	;ST LIMITS
3684	006144	177777	.WORD	177777	
3685	006146	000000	.WORD	0	;SM LIMITS
3686	006150	177777	.WORD	177777	

3687
3688
3689
3690
3691 006152
3692 006152 041506
3693 006154 041514
3694 006156 041511
3695 006160 052106
3696 006162 052114
3697 006164 052111
3698 006166 030123
3699 006170 030523
3700 006172 031123
3701 006174 031523
3702 006176 051511
3703 006200 052120
3704 006202 040515
3705 006204 000000
3706 006206 041527
3707 006210 051503
3708 006212 052123
3709 006214 046523
3710
3711
3712

;ASCII PARAMETER MNEMONICS
PRMNM:

.ASCII /FC/
.ASCII /LC/
.ASCII /IC/
.ASCII /FT/
.ASCII /LT/
.ASCII /IT/
.ASCII /SO/
.ASCII /S1/
.ASCII /S2/
.ASCII /S3/
.ASCII /IS/
.ASCII /PT/
.ASCII /MA/
.WORD 0
.ASCII /WC/
.ASCII /CS/
.ASCII /ST/
.ASCII /SM/

;TABLE FILLER FOR MA

3713
3714
3715 006216
3716 006216 177777
3717 006220 177777
3718 006222 177777
3719 006224 052525
3720 006226 052525
3721 006230 052525
3722 006232 177777
3723 006234 177777
3724 006236 052525
3725 006240 052525
3726 006242 177777
3727 006244 052525

;DATA PATTERN 00
HI-LO FREQ. MIX
PAT00:

3728	006246	177252	177252
3729	006250	177252	177252
3730	006252	172765	172765
3731	006254	172765	172765

3732
3733
3734

:DATA PATTERN 01
: HI FREQ. PHASE MIX
PAT01:

3737	006256		000000
3738	006256	000000	000000
3739	006260	000000	000000
3740	006262	000000	000000
3741	006264	177777	177777
3742	006266	177777	177777
3743	006270	177777	177777
3744	006272	000000	000000
3745	006274	000000	000000
3746	006276	177777	177777
3747	006300	177777	177777
3748	006302	000000	000000
3749	006304	177777	177777
3750	006306	000000	000000
3751	006310	177777	177777
3752	006312	000000	000000
3753	006314	177777	177777

3754
3755
3756

:DATA PATTERN 02
: LO FREQ. PHASE MIX
PAT02:

3759	006316		052525
3760	006316	052525	052525
3761	006320	052525	052525
3762	006322	052525	052525
3763	006324	125252	125252
3764	006326	125252	125252
3765	006330	125252	125252
3766	006332	052525	052525
3767	006334	052525	052525
3768	006336	125252	125252
3769	006340	125252	125252
3770	006342	052525	052525
3771	006344	125252	125252
3772	006346	052525	052525
3773	006350	125252	125252
3774	006352	052525	052525
3775	006354	125252	125252

3776
3777
3778

:DATA PATTERN 03
: MAX. PRECOMP. PHASE MIX
PAT03:

3781	006356		133333
3782	006356	133333	133333
3783	006360	066666	066666

3784	006362	155555	155555
3785	006364	155555	155555
3786	006366	133333	133333
3787	006370	066666	066666
3788	006372	066666	066666
3789	006374	155555	155555
3790	006376	155555	155555
3791	006400	133333	133333
3792	006402	133333	133333
3793	006404	133333	133333
3794	006406	133333	133333
3795	006410	133333	133333
3796	006412	133333	133333
3797	006414	133333	133333
3798			
3799			
3800			

:DATA PATTERN 04
: ROTATING BOUNDARY PULSE PRECOMP.

3801			
3802			
3803	006416		
3804	006416	121105	121105
3805	006420	150442	150442
3806	006422	064221	064221
3807	006424	132110	132110
3808	006426	055044	055044
3809	006430	026422	026422
3810	006432	013211	013211
3811	006434	105504	105504
3812	006436	042642	042642
3813	006440	021321	021321
3814	006442	110550	110550
3815	006444	044264	044264
3816	006446	022132	022132
3817	006450	011055	011055
3818	006452	104426	104426
3819	006454	042213	042213
3820			
3821			
3822			

:DATA PATTERN 05
: ROTATING CELL PULSE PRECOMP.

3823			
3824			
3825	006456		
3826	006456	026455	026455
3827	006460	113226	113226
3828	006462	045513	045513
3829	006464	122645	122645
3830	006466	151322	151322
3831	006470	064551	064551
3832	006472	132264	132264
3833	006474	055132	055132
3834	006476	026455	026455
3835	006500	113226	113226
3836	006502	045513	045513
3837	006504	122645	122645
3838	006506	151322	151322
3839	006510	064551	064551

3840 006512 132264 132264
3841 006514 055132 055132

3842
3843
3844 ;DATA PATTERN 06
3845 ; ALL ZEROS
3846 006516 PAT06:
3847 006516 000000 000000
3848 006520 000000 000000
3849 006522 000000 000000
3850 006524 000000 000000
3851 006526 000000 000000
3852 006530 000000 000000
3853 006532 000000 000000
3854 006534 000000 000000
3855 006536 000000 000000
3856 006540 000000 000000
3857 006542 000000 000000
3858 006544 000000 000000
3859 006546 000000 000000
3860 006550 000000 000000
3861 006552 000000 000000
3862 006554 000000 000000

3863
3864
3865
3866 ;DATA PATTERN 07
3867 ; ALL ONES
3868 006556 PAT07:
3869 006556 177777 177777
3870 006560 177777 177777
3871 006562 177777 177777
3872 006564 177777 177777
3873 006566 177777 177777
3874 006570 177777 177777
3875 006572 177777 177777
3876 006574 177777 177777
3877 006576 177777 177777
3878 006600 177777 177777
3879 006602 177777 177777
3880 006604 177777 177777
3881 006606 177777 177777
3882 006610 177777 177777
3883 006612 177777 177777
3884 006614 177777 177777

3885
3886 ;DATA PATTERN 08
3887 ; SHIFTED 1 IN FIELD OF ZEROS
3888 PAT08:
3889 006616
3890 006616 000001 000001
3891 006620 000002 000002
3892 006622 000004 000004
3893 006624 000010 000010
3894 006626 000020 000020
3895 006630 000040 000040

3896	006632	000100	000100
3897	006634	000200	000200
3898	006636	000400	000400
3899	006640	001000	001000
3900	006642	002000	002000
3901	006644	004000	004000
3902	006646	010000	010000
3903	006650	020000	020000
3904	006652	040000	040000
3905	006654	100000	100000

:DATA PATTERN 09
: SHIFTED 0 IN FIELD OF ONES
: PAT09:

3911	006656		
3912	006656	177776	177776
3913	006660	177775	177775
3914	006662	177773	177773
3915	006664	177767	177767
3916	006666	177757	177757
3917	006670	177737	177737
3918	006672	177677	177677
3919	006674	177577	177577
3920	006676	177377	177377
3921	006700	176777	176777
3922	006702	175777	175777
3923	006704	173777	173777
3924	006706	167777	167777
3925	006710	157777	157777
3926	006712	137777	137777
3927	006714	077777	077777

:DATA PATTERN 10
: ALTERNATING 0-1
: PAT10:

3933	006716		
3934	006716	052525	052525
3935	006720	052525	052525
3936	006722	052525	052525
3937	006724	052525	052525
3938	006726	052525	052525
3939	006730	052525	052525
3940	006732	052525	052525
3941	006734	052525	052525
3942	006736	052525	052525
3943	006740	052525	052525
3944	006742	052525	052525
3945	006744	052525	052525
3946	006746	052525	052525
3947	006750	052525	052525
3948	006752	052525	052525
3949	006754	052525	052525
3950			
3951			

3952
3953
3954
3955 006756
3956 006756 125252
3957 006760 125252
3958 006762 125252
3959 006764 125252
3960 006766 125252
3961 006770 125252
3962 006772 125252
3963 006774 125252
3964 006776 125252
3965 007000 125252
3966 007002 125252
3967 007004 125252
3968 007006 125252
3969 007010 125252
3970 007012 125252
3971 007014 125252

```
;DATA PATTERN 11  
: ALTERNATING 1-0  
PAT11:  
125252  
125252  
125252  
125252  
125252  
125252  
125252  
125252  
125252  
125252  
125252  
125252  
125252  
125252  
125252  
125252  
125252  
125252
```

3972
3973
3974
3975
3976
3977 007016
3978 007016 000001
3979 007020 000003
3980 007022 000007
3981 007024 000017
3982 007026 000037
3983 007030 000077
3984 007032 000177
3985 007034 000377
3986 007036 000777
3987 007040 001777
3988 007042 003777
3989 007044 007777
3990 007046 017777
3991 007050 037777
3992 007052 077777
3993 007054 177777

```
;DATA PATTERN 12  
: SHIFTING ZEROS AND ONES  
PAT12:  
000001  
000003  
000007  
000017  
000037  
000077  
000177  
000377  
000777  
001777  
003777  
007777  
017777  
037777  
077777  
177777
```

3994
3995
3996
3997
3998
3999 007056
4000 007056 072307
4001 007060 135143
4002 007062 156461
4003 007064 167230
4004 007066 073514
4005 007070 035646
4006 007072 016723
4007 007074 107351

```
;DATA PATTERN 13  
: COMPOSITE ROTATING  
PAT13:  
072307  
135143  
156461  
167230  
073514  
035646  
016723  
107351
```


4008	007076	143564	143564
4009	007100	061672	061672
4010	007102	030735	030735
4011	007104	114356	114356
4012	007106	046167	046167
4013	007110	123073	123073
4014	007112	151453	151453
4015	007114	164616	164616

4016
4017
4018

:DATA PATTERN 14
: PSEUDO-RANDOM (COMPUTED BY PROGRAM)

4020			
4021	007116		
4022	007116	000000	.WORD
4023	007120	000000	.WORD
4024	007122	000000	.WORD
4025	007124	000000	.WORD
4026	007126	000000	.WORD
4027	007130	000000	.WORD
4028	007132	000000	.WORD
4029	007134	000000	.WORD
4030	007136	000000	.WORD
4031	007140	000000	.WORD
4032	007142	000000	.WORD
4033	007144	000000	.WORD
4034	007146	000000	.WORD
4035	007150	000000	.WORD
4036	007152	000000	.WORD
4037	007154	000000	.WORD

4038
4039

:USER-DEFINED DATA PATTERN 15
PAT15:

4040				
4041	007156			
4042	007156	072307	072307	:WORD 00
4043	007160	135143	135143	:WORD 01
4044	007162	156461	156461	:WORD 02
4045	007164	167230	167230	:WORD 03
4046	007166	073514	073514	:WORD 04
4047	007170	035646	035646	:WORD 05
4048	007172	016723	016723	:WORD 06
4049	007174	107351	107351	:WORD 07
4050	007176	143564	143564	:WORD 10
4051	007200	061672	061672	:WORD 11
4052	007202	030735	030735	:WORD 12
4053	007204	114356	114356	:WORD 13
4054	007206	046167	046167	:WORD 14
4055	007210	123073	123073	:WORD 15
4056	007212	151453	151453	:WORD 16
4057	007214	164616	164616	:WORD 17

4058
4059
4060

4061	057467	MINLO1= [^] D24375	
4062	000000	MINHI1=0	
4063	062031	MAXLO1= [^] D25625	

:LO BITS OF SPEC'D MIN ROT. LATENCY
:HI BITS OF SPEC'D MIN ROT. LATENCY
:LO BITS OF SPEC'D MAX ROT. LATENCY

```

4064          000000          MAXHI1=0          ;HI BITS OF SPEC'D MAX ROT. LATENCY
4065          ;MAXLO2=*D8000          ;LO BITS OF SPEC'D MAX 1 CYL SEEK TIME
4066          ;MAXHI2=0          ;HI BITS OF SPEC'D MAX 1 CYL SEEK TIME
4067 007216 000000          MAXLO2: .WORD 0          ;LOAD FROM NEW DRIVE ROUTINE
4068 007220 000000          MAXHI2: .WORD 0          ;LOAD FROM NEWDRIVE ROUTINE
4069          112160          MAXLO3=*D38000          ;LO BITS OF SPEC'D MAX AVG SEEK TIME
4070          000000          MAXHI3=0          ;HI BITS OF SPEC'D MAX AVG SEEK TIME
4071          ;MAXLO4=*D9464          ;LO BITS OF SPEC'D MAX 410 CYL SEEK TIME
4072          ;MAXHI4=1          ;HI BITS OF SPEC'D MAX 410 CYL SEEK TIME
4073 007222 000000          MAXLO4: .WORD 0          ;LOAD FROM THE DEWDRIE ROUTINE
4074 007224 000000          MAXHI4: .WORD 0          ;LOAD FROM THE NEWDRIVE ROUTINE
4075          002734          FNDSHT=*D1500          ;SHORT RANDOM SEEKS = 1500(10)
4076          016514          RNDLNG=*D7500          ;LONG RANDOM SEEKS = 7500(10)
4077          000002          LSTTRK=2          ;LAST TRACK = 2
4078          000365          ALNCYL=365          ;ALIGNMENT CYLINDER =245(10)
4079          000002          BSERR=BIT1          ;BSE ERROR
4080          000004          HVRCER=BIT2          ;HVRC ERROR
4081          000010          OPIERR=BIT3          ;OPI ERROR
4082          000020          DCKERR=BIT4          ;DATA CHECK ERROR
4083          000040          ECCNC=BIT5          ;ECC NON-CORRECTABLE
4084          000100          WCERR=BIT6          ;WRITE CHECK ERROR
4085          000200          ABORT=BIT7          ;ABORT
4086          000400          LEV2ER=BIT8          ;LEVEL TWO ERROR
4087          001000          BADSEC=BIT9          ;BAD SECTOR FLAG
4088          002000          TWOTOS=BIT10          ;TWO TIME OUTS
4089          004000          RCLREQ=BIT11          ;RECALIBRATE REQUIRED
4090          010000          DRNAVL=BIT12          ;DRIVE NOT RELSD BY OTHER PORT
4091          100000          ANYDER=BIT15          ;ANY ERROR DETECTED FLAG
4092
4093 007226 005015 055103 033'22 DZR6M: .ASCIZ <15><12>/CZR6ME0/
4094 007234 042515 000060
4095 007240 005015 044103 047101 XXDFMG: .ASCII <CR><LF>/CHANGE XXDP PACK/
4096 007246 042507 054040 042130
4097 007254 020120 040520 045503
4098 007262 005015 046103 040505          .ASCIZ <CR><LF>/CLEAR LOC 40,RESTART PROGRAM/
4099 007270 020122 047514 020103
4100 007276 030064 051054 051505
4101 007304 040524 052122 050040
4102 007312 047522 051107 046501
4103 007320          000
4104 007321          040 020055 045522 SUBVER: .ASCIZ & - RK611/06 SS VFY&
4105 007326 030466 027461 033060
4106 007334 051440 020123 043126
4107 007342 000131
4108 007344 006461 000012          PART1: .ASCIZ /1/<15><12>
4109 007350 006462 000012          PART2: .ASCIZ /2/<15><12>
4110 007354 005015 046012 051501          LSTMEM: .ASCIZ <15><12><12>/LAST PHYS MEM ADR = /
4111 007362 020124 044120 051531
4112 007370 046440 046505 040440
4113 007376 051104 036440 000040
4114 007404 053117 051105 040514          OVLODR: .ASCIZ /OVERLAY LOADER ? (Y OR N) * /
4115 007412 020131 047514 042101
4116 007420 051105 037440 024040
4117 007426 020131 051117 047040
4118 007434 020051 020052          000
4119 007441          015 050012 051101          PRMINP: .ASCIZ <15><12>/PARAM INPUT MODE/<15><12><12>

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4120	007446	046501	044440	050116	
4121	007454	052125	046440	042117	
4122	007462	006505	005012	000	
4123	007467	122	030113	026466	RKBADR: .ASCIZ /RK06-07 BUS ADR = /
4124	007474	033460	041040	051525	
4125	007502	040440	051104	036440	
4126	007510	000040			
4127	007512	045522	033060	030055	RKVADR: .ASCIZ /RK06-07 VEC ADR = /
4128	007520	020067	042526	020103	
4129	007526	042101	020122	020075	
4130	007534	000			
4131	007535	122	030113	026466	FKPRTY: .ASCIZ /RK06-07 PRIOR = /
4132	007542	033460	050040	044522	
4133	007550	051117	036440	000	
4134	007555	123	051127	036440	SWRMSG: .ASCIZ /SWR = /
4135	007562	000040			
4136	007564	020040	042516	020127	NEWMMSG: .ASCIZ / NEW = /
4137	007572	020075	000		
4138	007575	015	042012	053122	DRVSEQ: .ASCIZ <15><12>/DRV(S) = /
4139	007602	051450	020051	020075	
4140	007610	000			
4141	007611	104	044522	042526	BADDRV: .ASCIZ /DRIVE /
4142	007616	020040	000040		
4143	007622	047516	026516	054105	NXDRIV: .ASCIZ /NON-EXIST/<15><12>
4144	007630	051511	006524	000012	
4145	007636	047516	020124	042122	NTREDY: .ASCIZ /NOT RDY/<15><12>
4146	007644	006531	000012		
4147	007650	051127	026524	047514	WRTLOK: .ASCIZ /WRT-LOCK/<15><12>
4148	007656	045503	005015	000	
4149	007663	114	040517	042504	ALNPAK: .ASCIZ /LOADED WITH ALIGN PACK/<15><12>
4150	007670	020104	044527	044124	
4151	007676	040440	044514	047107	
4152	007704	050040	041501	006513	
4153	007712	000012			
4154	007714	043111	054040	042130	REPLPK: .ASCIZ /IF XXDP PACK ON DRV 0, TYPE 'Y <CR>', & REPLACE IT : /
4155	007722	020120	040520	045503	
4156	007730	047440	020116	051104	
4157	007736	020126	026060	052040	
4158	007744	050131	020105	054442	
4159	007752	036040	051103	021076	
4160	007760	020054	020046	051040	
4161	007766	050105	040514	042503	
4162	007774	044440	020124	020072	
4163	010002	000			
4164	010003	015	005012	025052	NODRTS: .ASCII <15><12><12>/** NO DRVS TO TEST/<15><12>
4165	010010	047040	020117	051104	
4166	010016	051526	052040	020117	
4167	010024	042524	052123	005015	
4168	010032	051120	051505	020123	CNTRDY: .ASCIZ /PRESS 'CONT' WHEN RDY/<15><12>
4169	010040	041442	047117	021124	
4170	010046	053440	042510	020116	
4171	010054	042122	006531	000012	
4172	010062	040510	052114	051040	HLTRQD: .ASCIZ /HALT REQ/<15><12>
4173	010070	050505	005015	000	
4174	010075	104	053122	030040	DROXDP: .ASCIZ /DRV 0 IS LOAD MEDIUM/<15><12>
4175	010102	044440	020123	047514	

4176	010110	042101	046440	042105	
4177	010116	052511	006515	000012	
4178	010124	005015	052012	020117	ALDRVS: .ASCIZ <15><12><12>/TO TEST ALL DRVS TYPE 'A' <CR>, ELSE <CR>/<15><12>/* /
4179	010132	042524	052123	040440	
4180	010140	046114	042040	053122	
4181	010146	020123	054524	042520	
4182	010154	021040	021101	036040	.
4183	010162	051103	026076	042440	
4184	010170	051514	020105	041474	
4185	010176	037122	005015	020052	
4186	010204	000			
4187	010205	015	046012	036440	TSTMDS: .ASCII <15><12>/L = LIST TESTS/<15><12>
4188	010212	046040	051511	020124	
4189	010220	042524	052123	006523	
4190	010226	012			
4191	010227	103	036440	041440	.ASCII /C = CHANGE TESTS/<15><12>
4192	010234	040510	043516	070105	
4193	010242	042524	052123	006523	
4194	010250	012			
4195	010251	111	036440	044440	.ASCIZ /I = INPUT PARAMS, RUN TESTS/<15><12>
4196	010256	050116	052125	050040	
4197	010264	051101	046501	026123	
4198	010272	051040	047125	052040	
4199	010300	051505	051524	005015	
4200	010306	000			
4201	010307	015	042412	052116	ENTLCI: .ASCIZ <15><12>/ENTER L,C, OR I/<15><12>/* /
4202	010314	051105	046040	041454	
4203	010322	020054	051117	044440	
4204	010330	005015	020052	000	
4205	010335	124	020117	042504	DFTEST: .ASCIZ /TO DEFAULT TESTS TYPE D <CR>, ELSE <CR>/<15><12>/* /
4206	010342	040506	046125	020124	
4207	010350	042524	052123	020123	
4208	010356	054524	042520	042040	
4209	010364	036040	051103	026076	
4210	010372	042440	051514	020105	
4211	010400	041474	037122	005015	
4212	010406	020052	000		
4213	010411	015	052012	051505	TLSTHD: .ASCIZ <15><12>/TEST ITERATIONS/<15><12>
4214	010416	020124	020040	020040	
4215	010424	052111	051105	052101	
4216	010432	047511	051516	005015	
4217	010440	000			
4218	010441	015	052012	036440	EXPLAN: .ASCII <15><12>/T = TYPE PARAM LIST/<15><12>
4219	010446	052040	050131	020105	
4220	010454	040520	040522	020115	
4221	010462	044514	052123	005015	
4222	010470	020117	020075	050117	.ASCII /O = OPEN PARAM LIST/<15><12>
4223	010476	047105	050040	051101	
4224	010504	046501	046040	051511	
4225	010512	006524	012		
4226	010515	123	036440	051440	.ASCII /S = SET INDIVIDUAL PARAM/<15><12>
4227	010522	052105	044440	042116	
4228	010530	053111	042111	040525	
4229	010536	020114	040520	040522	
4230	010544	006515	012		
4231	010547	122	036440	051040	.ASCIZ /R = RUN TESTS/<15><12>

4232	010554	047125	052040	051505	
4233	010562	051524	005015	000	
4234	010567	015	042412	052116	PARMDE: .ASCIZ <15><12>/ENTER T,O,S, OR R/<15><12>
4235	010574	051105	052040	047454	
4236	010602	051454	020054	051117	
4237	010610	051040	005015	000	
4238	010615	015	025012	042040	DUACES: .ASCIZ <15><12>/* DUAL-PORT DATA TEST */<15><12>
4239	010622	040525	026514	047520	
4240	010630	052122	042040	052101	
4241	010636	020101	042524	052123	
4242	010644	025040	005015	000	
4243	010651	115	054101	053440	MAWRDC: .ASCIZ /MAX WORD CT = /
4244	010656	051117	020104	052103	
4245	010664	036440	000040		
4246	010670	025052	020040	030123	SECNL1: .ASCII /** S0>S1/
4247	010676	051476	061		
4248	010701	040	047516	020124	NOTALD: .ASCIZ / NOT ALLOWED/<15><12>
4249	010706	046101	047514	042527	
4250	010714	006504	000012		
4251	010720	025052	020040	031123	SECNL2: .ASCIZ /** S2>S3/
4252	010726	051476	000063		
4253	010732	025052	020040	052106	TRKNLW: .ASCIZ /** FT>LT/
4254	010740	046076	000124		
4255	010744	025052	020040	041527	WC2BIG: .ASCIZ /** WC OR MA TOO LARGE/<15><12>
4256	010752	047440	020122	040515	
4257	010760	052040	047517	046040	
4258	010766	051101	042507	005015	
4259	010774	000			
4260	010775	124	020117	042504	DFQUES: .ASCIZ /TO DEFAULT ALL PARAMS, TYPE D <CR>, ELSE <CR>/<15><12>/* /
4261	011002	040506	046125	020124	
4262	011010	046101	020114	040520	
4263	011016	040522	051515	020054	
4264	011024	054524	042520	042040	
4265	011032	036040	051103	026076	
4266	011040	042440	051514	020105	
4267	011046	041474	037122	005015	
4268	011054	020052	000		
4269	011057	015	052412	042523	PFIFTN: .ASCIZ <15><12>/USER-DEF PATT 15 :/<15><12>
4270	011064	026522	042504	020106	
4271	011072	040520	052124	030440	
4272	011100	020065	006472	000012	
4273	011106	005015	047515	020104	SELP15: .ASCIZ <15><12>/MOD PATT 15 :/<15><12>
4274	011114	040520	052124	030440	
4275	011122	020065	006472	000012	
4276	011130	047524	046440	042117	MDFY15: .ASCIZ /TO MOD PATT 15, TYPE M <CR>, ELSE <CR>/<15><12>/* /
4277	011136	050040	052101	020124	
4278	011144	032461	020054	054524	
4279	011152	042520	046440	036040	
4280	011160	051103	026076	042440	
4281	011166	051514	020105	041474	
4282	011174	037122	005015	020052	
4283	011202	000			
4284	011203	015	042412	052116	ENTPAS: .ASCIZ <15><12>/ENIER NO. OF PASSES (1-77777) :/<15><12>/* /
4285	011210	051105	047040	027117	
4286	011216	047440	020106	040520	
4287	011224	051523	051505	024040	

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SEQ 0084

4288	011232	026461	033467	033467	
4289	011240	024467	035040	005015	
4290	011246	020052	000		
4291	011251	015	025012	020052	DROPDR: .ASCIZ <15><12>/** DROPPING DRV - 20 ERRORS/<15><12>
4292	011256	042040	047522	050120	
4293	011264	047111	020107	051104	
4294	011272	020126	020055	030062	
4295	011300	042440	051122	051117	
4296	011306	006523	000012		
4297	011312	005015	052012	051505	TSTDNR: .ASCII <15><12><12>/TESTING DRV /
4298	011320	044524	043516	042040	
4299	011326	053122	040		
4300	011331	040	005015	000	DRVNO: .ASCIZ / /<15><12>
4301	011335	104	053122	000040	DRIV: .ASCIZ /DRV /
4302	011342	040503	052122	020056	CART: .ASCIZ /CART. /
4303	011350	000			
4304	011351	123	051105	020056	SERNM: .ASCIZ /SER. NO. /
4305	011356	047516	020056	000040	
4306	011364	005015	040506	052103	FACTBS: .ASCIZ <15><12>/FACTORY /
4307	011372	051117	020131	000	
4308	011377	012	047523	052106	SOFTBS: .ASCII <12>/SOFTWARE /
4309	011404	040527	042522	040	
4310	011411	102	042101	051440	BDSECT: .ASCIZ /BAD SECTORS :/<15><12>
4311	011416	041505	047524	051522	
4312	011424	035040	005015	000	
4313	011431	040	047040	047117	NOFALS: .ASCIZ / NONE/
4314	011436	000105			
4315					
4316					:MESSAGES USED IN TIMING TESTS
4317	011440	025052	020040	047516	NOCLKS: .ASCII /** NO CLOCK/
4318	011446	041440	047514	045503	
4319	011454	026440	020040	044524	TIMSKP: .ASCIZ / - TIMING TESTS WILL BE SKIPPED/<15><12>
4320	011462	044515	043516	052040	
4321	011470	051505	051524	053440	
4322	011476	046111	020114	042502	
4323	011504	051440	044513	050120	
4324	011512	042105	005015	000	
4325	011517	052	020052	045440	CLKFAL: .ASCIZ /** KW11 FAILURE/
4326	011524	030527	020061	040506	
4327	011532	046111	051125	000105	
4328	011540	005015	047522	020124	ROTIMS: .ASCIZ <15><12>/ROT TIMES :/<15><12>
4329	011546	044524	042515	020123	
4330	011554	006472	000012		
4331	011560	005015	053101	020107	AVGSEK: .ASCIZ <15><12>/AVG SEEK TIMES :/<15><12>
4332	011566	042523	045505	052040	
4333	0.574	046511	051505	035040	
4334	011602	005015	000		
4335	011605	015	047412	042516	ONECYL: .ASCIZ <15><12>/ONE CYL SEEK TIMES :/<15><12>
4336	011612	041440	046131	051440	
4337	011620	042505	020113	044524	
4338	011626	042515	020123	006472	
4339	011634	000012			
4340	011636	005015	040515	020130	MAXSEK: .ASCIZ <15><12>/MAX SEEK TIMES :/<15><12>
4341	011644	042523	045505	052040	
4342	011652	046511	051505	035040	
4343	011660	005015	000		

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SEQ 0085

4344	011663	052	043052	042127	FORWRD: .ASCIZ	/**FWD DIR**/<15><12>
4345	011670	042040	051111	025052		
4346	011676	005015	000			
4347	011701	052	051052	053105	REVRSE: .ASCIZ	/**REV DIR**/<15><12>
4348	011706	042040	051111	025052		
4349	011714	005015	000			
4350	011717	115	047111	036440	MINEQ: .ASCIZ	/MIN = /
4351	011724	000040				
4352	011726	040515	020130	020075	MAXEQ: .ASCIZ	/MAX = /
4353	011734	000				
4354	011735	101	043526	036440	AVGEQ: .ASCIZ	/AVG = /
4355	011742	000040				
4356	011744	052440	000123		MICROS: .ASCIZ	/ US/
4357	011750	047440	020106	031061	BELOW: .ASCIZ	/ OF 128 BELOW SPEC'D MIN OF /
4358	011756	020070	042502	047514		
4359	011764	020127	050123	041505		
4360	011772	042047	046440	047111		
4361	012000	047440	020106	000		
4362	012005	040	043117	030440	ABOVE: .ASCIZ	/ OF 128 ABOVE SPEC'D MAX OF /
4363	012012	034062	040440	047502		
4364	012020	042526	051440	042520		
4365	012026	023503	020104	040515		
4366	012034	020130	043117	000040		
4367	012042	047440	020106	030464	ABOVE1: .ASCII	/ OF 410 (814 FOR RK07) ABOVE SPEC'D MAX OF 8000 US/
4368	012050	020060	034050	032061		
4369	012056	043040	051117	051040		
4370	012064	030113	024467	040440		
4371	012072	047502	042526	051440		
4372	012100	042520	023503	020104		
4373	012106	040515	020130	043117		
4374	012114	034040	030060	020060		
4375	012122	051525				
4376	012124	033050	030065	026460	.ASCIZ	/(6500-RK07)/<CR><LF>
4377	012132	045522	033460	006451		
4378	012140	000012				
4379	012142	047440	020106	031061	ABOVE3: .ASCII	/ OF 128 ABOVE SPEC'D MAX OF 75000 US/
4380	012150	020070	041101	053117		
4381	012156	020105	050123	041505		
4382	012164	042047	046440	054101		
4383	012172	047440	020106	032467		
4384	012200	030060	020060	051525		
4385	012206	033450	030063	030060	.ASCIZ	/(73000-RK07)/<CR><LF>
4386	012214	051055	030113	024467		
4387	012222	005015	000			
4388	012225	040	020040	050123	SPCDMX: .ASCIZ	/ SPEC'D MAX IS 38000 US/<15><12>
4389	012232	041505	042047	046440		
4390	012240	054101	044440	020123		
4391	012246	034063	030060	020060		
4392	012254	051525	005015	000		
4393	012261	062	031464	032467	LIM1: .ASCIZ	/24375 US/<15><12>
4394	012266	052440	006523	000012		
4395	012274	032462	031066	020065	LIM2: .ASCIZ	/25625 US/<15><12>
4396	012302	051525	005015	000		
4397						
4398	012307	052	020052	041440	BAD632: .ASCIZ	/** CANNOT READ BAD SECTOR TRACK!/<15><12>
4399	012314	047101	047516	020124		

4400	012322	042522	042101	041040		
4401	012330	042101	051440	041505		
4402	012336	047524	020122	051124		
4403	012344	041501	020513	005015		
4404	012352	000				
4405	012353	136	006503	000012	CNTRLC:	.ASCIZ /^C/<15><12>
4406	012360	055136	005015	000	CNTRLZ:	.ASCIZ /^Z/<15><12>
4407	012365	136	006522	000012	CNTRLR:	.ASCIZ /^R/<15><12>
4408	012372	052536	005015	000	CNTRLU:	.ASCIZ /^U/<15><12>
4409	012377	136	006507	000012	CNTRLG:	.ASCIZ /^G/<15><12>
4410	012404	005015	000012		CR2LF:	.ASCIZ <15><12><12>
4411	012410	000134			EXSLSH:	.ASCIZ /\ /
4412	012412	000054			COMMA:	.ASCIZ /, /
4413	012414	020040			SPACE6:	.ASCII / /
4414	012416	040			SPACE4:	.ASCII / /
4415	012417	040			SPACE3:	.ASCII / /
4416	012420	040			SPACE2:	.ASCII / /
4417	012421	040	000		SPACE1:	.ASCIZ / /
4418		012424				.EVEN
4419	012424	020040	000075		PRMBUF:	.ASCIZ / = /
4420	012430	047527	042122	000040	WORDSP:	.ASCIZ /WORD /
4421	012436	036440	000040		EQUALS:	.ASCIZ / = /
4422	012442	020052	000		PROMPT:	.ASCIZ /* /
4423	012445	076	000040		PRMPSP:	.ASCIZ /> /
4424						
4425						.EVEN
4426						
4427						
4428						
4429						
4430	012450	105037	003116		DFSTRT:	CLRB MDFLAG ;SET FLAG FOR DEFAULT MODE
4431	012454	105037	003136		CLRB	DULACS ;CLEAR DUAL-ACCESS FLAG
4432	012460	105037	003126		CLRB	ERRCNT ;CLEAR ERROR COUNT FOR RESTARTS
4433	012464	022737	013660	000042	CMP	#DRVST,@#42 ;SEE IF EOP RETURN ADRS = DRVST
4434	012472	001003			BNE	4\$;BR IF NOT DRVST
4435	012474	012737	016742	000042	MOV	#NEWPAS,@#42 ;SET RETURN ADRS = NEWPAS
4436	012502	000414			4\$:	BR CMSTRT ;PROCEED
4437						
4438	012504	112737	000001	003136	DASTRT:	MOVB #1,DULACS ;SET FLAG FOR DUAL-ACCESS DATA TEST
4439	012512	112737	000001	003116	MOVB	#1,MDFLAG ;SET FLAG FOR PARAMETER MODE
4440	012520	000405			BR	CMSTRT ;PROCEED
4441						
4442	012522	112737	000001	003116	PSTART:	MOVB #1,MDFLAG ;SET FLAG FOR PARAMETER MODE
4443	012530	105037	003136		CLRB	DULACS ;CLEAR DUAL-ACCESS TEST FLAG
4444						
4445	012534	012737	000340	177776	CMSTRT:	MOV #PR7,@#PS ;BLOCK ALL INTERRUPTS
4446					.SBTTL	INITIALIZE THE COMMON TAGS
4447					::	CLEAR THE COMMON TAGS (\$CMTAG) AREA
4448	012542	012706	001100		MOV	#\$CMTAG,R6 ;:FIRST LOCATION TO BE CLEARED
4449	012546	005026			CLR	(R6)+ ;:CLEAR MEMORY LOCATION
4450	012550	022706	001140		CMP	#SWR,R6 ;:DONE?
4451	012554	001374			BNE	.-6 ;:LOOP BACK IF NO
4452	012556	012706	001100		MOV	#STACK,SP ;:SETUP THE STACK POINTER
4453					::	INITIALIZE A FEW VECTORS
4454	012562	012737	055120	000020	MOV	#\$SCOPE,@#IOTVEC ;:IOT VECTOR FOR SCOPE ROUTINE
4455	012570	012737	000340	000022	MOV	#340,@#IOTVEC+2 ;:LEVEL 7


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4456 012576 012737 054414 000030      MOV      # $ERROR,@#EMTVEC  ;; EMT VECTOR FOR ERROR ROUTINE
4457 012604 012737 000340 000032      MOV      #340,@#EMTVEC+2  ;; LEVEL 7
4458 012612 012737 056226 000034      MOV      # $TRAP,@#TRAPVEC ;; TRAP VECTOR FOR TRAP CALLS
4459 012620 012737 000340 000036      MOV      #340,@#TRAPVEC+2 ;; LEVEL 7
4460 012626 012737 054764 000024      MOV      # $PWRDN,@#PWRVEC ;; POWER FAILURE VECTOR
4461 012634 012737 000340 000026      MOV      #340,@#PWRVEC+2  ;; LEVEL 7
4462 012642 013737 025430 025422      MOV      $ENDCT,$EOPCT    ;; SETUP END-OF-PROGRAM COUNTER
4463 012650 005037 001304      CLR      $TIMES           ;; INITIALIZE NUMBER OF ITERATIONS
4464 012654 005037 001306      CLR      $ESCAPE         ;; CLEAR THE ESCAPE ON ERROR ADDRESS
4465 012660 112737 000001 001115      MOV      #1,$ERMAX       ;; ALLOW ONE ERROR PER TEST
4466 012666 012737 012666 001106      MOV      #.,$LPADR       ;; INITIALIZE THE LOOP ADDRESS FOR SCOPE
4467 012674 012737 012674 001110      MOV      #.,$LPERR       ;; SETUP THE ERROR LOOP ADDRESS
4468                                     ;; SIZE FOR A HARDWARE SWITCH REGISTER. IF NOT FOUND OR IT IS
4469                                     ;; EQUAL TO A "-1", SETUP FOR A SOFTWARE SWITCH REGISTER.
4470 012702 013746 000004      MOV      @#ERRVEC,-(SP)   ;; SAVE ERROR VECTOR
4471 012706 012737 012742 000004      MOV      #64,$@#ERRVEC   ;; SET UP ERROR VECTOR
4472 012714 012737 177570 001140      MOV      #DSWR,$SWR      ;; SETUP FOR A HARDWARE SWICH REGISTER
4473 012722 012737 177570 001142      MOV      #DDISP,$DISPLAY ;; AND A HARDWARE DISPLAY REGISTER
4474 012730 022777 177777 166202      CMP      #-1,@$SWR       ;; TRY TO REFERENCE HARDWARE SWR
4475 012736 001012      BNE      66$             ;; BRANCH IF NO TIMEOUT TRAP OCCURRED
4476                                     ;; AND THE HARDWARE SWR IS NOT = -1
4477 012740 000403      BR       65$            ;; BRANCH IF NO TIMEOUT
4478 012742 012716 012750 64$:      MOV      #65,$(SP)      ;; SET UP FOR TRAP RETURN
4479 012746 000002      RTI
4480 012750 012737 000176 001140 65$:      MOV      #SWREG,$SWR    ;; POINT TO SOFTWARE SWR
4481 012756 012737 000174 001142      MOV      #DISPREG,$DISPLAY
4482 012764 012637 000004 66$:      MOV      (SP)+,@#ERRVEC ;; RESTORE ERROR VECTOR
4483
4484 012770 005037 001326      CLR      $PASS           ;; CLEAR PASS COUNT
4485 012774 132737 000200 001341      BITB    #APTSIZE,$ENVM   ;; TEST USER SIZE UNDER APT
4486 013002 001403      BEQ     67$             ;; YES,USE NON-APT SWITCH
4487 013004 012737 001342 001140      MOV      #$$SWREG,$SWR  ;; NO,USE APT SWITCH REGISTER
4488 013012 67$:
4489 013012 000005      RESET          ;; CLEAR THE UNIBUS
4490 013014 012737 000006 000004      MOV      #6,@#ERRVEC    ;; SET TIME-OUT VECTOR
4491 013022 005037 000006      CLR      @#ERRVEC+2
4492 013026 112737 000144 001115      MOV      #100.,$ERMAX   ;; SET MAX ERROR CNT TO 100 FOR $SCOPE
4493 013034 105037 003130      CLR      DRVERS         ;; CLEAR ERROR COUNT FOR CURRENT DRIVE
4494 013040 112737 000001 003146      MOV      #1,$HLPVOL     ;; SET HLP FILE OVLD INDICTR
4495 013046 104401 007226      TYPE    ,DZR6M         ;; TYPE PROGRAM I.D. FOR PART 1
4496 013052 104401 007321      TYPE    ,SUBVER
4497 013056 104401 007344      TYPE    ,PART1
4498 013062 105737 003147      TSTB    NOTYPE         ;; SEE IF OPERATOR NOTE SHOULD BE TYPED
4499 013066 001004      BNE     39$            ;; BR IF NOT
4500 013070 104401 063520      TYPE    ,NOTMSG        ;; TYPE OPERATOR NOTE
4501 013074 105237 003147      INCB    NOTYPE         ;; SET FLAG FOR NEXT TIME
4502 013100 105737 003136 39$:      TSTB    DULACS         ;; SEE IF DUAL-ACCESS DATA TEST
4503 013104 001402      BEQ     40$            ;; BR IF NOT
4504 013106 104401 010615      TYPE    ,DUACES        ;; TYPE '* DUAL-ACCESS DATA TEST *'
4505 013112 012737 025626 000060 40$:      MOV      #KBDHDL,@#TKVEC ;; LOAD VECTOR FOR TTY KBD
4506 013120 012737 000200 000062      MOV      #PR4,@#TKVEC+2 ;; SET KBD PRIORITY = 4
4507 013126 013701 003040      MOV      RKVEC,R1       ;; ADDR. OF RK06 VECTOR STORAGE
4508 013132 012721 045234      MOV      #1,$INTR,(R1)+ ;; SET IT TO RK06 HANDLER
4509 013136 013711 003042      MOV      RKPRI,(R1)     ;; SET RK06 PRIORITY
4510 013142 012737 026436 000250      MOV      #KTERHD,@#MMVEC ;; VECT FOR KT11 FAILURE
4511 013150 012737 000340 000252      MOV      #PR7,@#MMVEC+2 ;; SET PRIOR. = 7 FOR HNDLER
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4512 013156 105037 003120          CLRB   TSTING          ;CLEAR 'RUNNING TESTS' FLAG
4513 013162 012737 000000 177776    MOV    #?RO,@#PS      ;ALLOW ALL INTERRUPTS AGAIN
4514 013170 012701 005232          MOV    #PRVCM,R1      ;ZERO OUT PREVIOUS COMMAND
4515 013174 012700 000006          MOV    #6,RO          ;SET COUNTER
4516 013200 005021          42$:  CLR    (R1)+         ;ZERO A WORD
4517 013202 005300          DEC    RO
4518 013204 001375          BNE    42$            ;BR IF NOT DONE YET
4519 013206 005037 005512          CLR    STALLS         ;DON'T ALLOW STALLS YET
4520 013212 023727 000042 016742    CMP    @#42,#NEWPAS   ;SEE IF CHAIN MODE
4521 013220 101002          BHI    44$            ;BR IF YES
4522 013222 004737 026316          JSR    PC,GTSWRG      ;OPEN SOFTWARE SWR FOR MODIFICATION
4523 013226 012737 176543 052662 44$:  MOV    #176543,$HNUM  ;INIT. PSEUDO-RANDOM NOS.
4524 013234 012737 123456 052664    MOV    #123456,$LNUM
4525          ;CHECK FOR PRESENCE OF KW11-L OR P CLOCK, AND SET FLAGS
4526 013242 105037 003132          CLRB   PCLKF          ;INIT. P CLOCK FLAG
4527 013246 105037 003133          CLRB   DOTIM          ;INIT. TIMING TESTS FLAG
4528 013252 012737 013312 000004    MOV    #6$,@#4        ;SET TIME-OUT ERROR VECTOR
4529 013260 005777 167666          TST    @PKS           ;SEE IF P-CLOCK IS PRESENT
4530 013264 105237 003132          INCB   PCLKF          ;PRESENT, SET FLAG
4531 013270 013700 003162          MOV    PCVEC,RO       ;LOAD KW11-P VECTOR ADDRESS
4532 013274 012720 032620          4$:  MOV    #CLOCK,(RO)+ ;ADDR OF CLOCK SERVICE ROUTINE
4533 013300 012710 000340          MOV    #PR7,(RO)      ;SET CLOCK HANDLER PRIORITY = 7
4534 013304 105237 003133          INCB   DOTIM          ;SET FLAG TO ALLOW TIMING TESTS
4535 013310 000414          BR     8$             ;BR TO CONTINUE
4536 013312 022626          6$:  CMP    (SP)+,(SP)+   ;P-CLK NOT THERE, RESET THE STACK
4537 013314 012737 013334 000004    MOV    #7$,@#4        ;SET TIME-OUT ERROR VECTOR
4538 013322 005777 167622          TST    @LKS           ;SEE IF L-CLK PRESENT
4539 013326 013700 003160          MOV    LCVEC,RO       ;LOAD KW11-L VECTOR ADDRESS
4540 013332 000760          BR     4$             ;BR TO SET UP L-CLK VECTOR
4541 013334 022626          7$:  CMP    (SP)+,(SP)+   ;L-CLK NOT THERE, RESET THE STACK
4542 013336 104401 011440          TYPE   ,NOCLKS        ;SAY TIMING TESTS WON'T BE RUN
4543 013342 012737 000006 000004 8$:  MOV    #6,@#4         ;SET TIME-OUT VECTOR TO LOCATION 6
4544 013350 004737 026020          JSR    PC,SIZMEM      ;SIZE MEMORY, FIX MA LIMIT IN PRMLIM
4545 013354 105737 003135          TSTB   XDPSVD         ;SEE IF XXDP PREVIOUSLY SAVED
4546 013360 001404          BEQ    9$             ;BR IF NOT
4547 013362 004737 027210          JSR    PC,GETXDP      ;RESTORE SAVED XXDP
4548 013366 105037 003135          CLRB   XDPSVD         ;CLEAR THE FLAG
4549 013372          9$:
4550 013372 122737 000013 000041    CMPB   #13,@#41      ;LOAD FROM XXDP ?
4551 013400 001003          BNE    99$            ;BRANCH IF NOT
4552 013402 104401 007240          TYPE   ,XXDPMG
4553 013406 000000          HALT
4554 013410          99$:
4555 013410 105737 003116          TSTB   MDFLAG        ;SEE IF DEFAULT MODE
4556 013414 001031          BNE    10$           ;BR IF NOT DEFAULT MODE
4557 013416 013700 001370          MOV    $VECT1,RO     ;GET APT RK06 VECTOR AND PRTY
4558 013422 013737 001374 003036    MOV    $BASE,RKBAS   ;GET APT RK06 BASE ADDRESS
4559 013430 132737 000200 001341    BITB   #BIT7,$ENVM   ;SEE IF NO SIZING
4560 013436 001005          BNE    18$           ;BR IF NO SIZING
4561 013440 012700 120210          MOV    #AVECT1,RO    ;GET DEFAULT VECTOR AND PRIORITY
4562 013444 012737 177440 003036    MOV    #ABASE,RKBAS  ;GET DEFAULT BASE ADDRESS
4563 013452 110037 003040          MOV    RO,RKVEC      ;STORE VECTOR
4564 013456 105037 003041          CLRB   RKVEC+1       ;CLEAR HI BYTE
4565 013462 000300          SWAB   RO             ;GET PRTY INTO BITS 5-7
4566 013464 042700 177437          BIC    #177437,RO    ;CLEAR OTHER BITS
4567 013470 010037 003042          MOV    RO,RKPRI      ;STORE RK06 PRIORITY
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4568 013474 000137 014372          JMP      ALLDRV          ;GO CHECK ALL DRIVES
4569
4570
4571
4572          ;BEGIN PARAMETER INPUT MODE
4573 013500 104401 007441 10$:      TYPE      ,PRMINP          ;TYPE 'PARAMETER INPUT MODE'
4574
4575          ;OPEN RK06 UNIBUS ADDRESS FOR MODIFICATION
4576 013504 013746 003036      MOV      RKBAS,-(SP)      ;PUT OLD VALUE ON STACK
4577 013510 104401 007467      TYPE      ,RKBADR          ;TYPE 'RK06 BUS ADR = ''
4578 013514 004737 026206      JSR      PC,GETPRM          ;TYPE OLD, GET NEW RKBAS VALUE
4579 013520 012637 003036      MOV      (SP)+,RKBAS      ;STORE NEW VALUE
4580          ;OPEN RK06 VECTOR ADDRESS FOR MODIFICATION
4581 013524 013746 003040      MOV      RKVEC,-(SP)      ;PUT OLD VALUE ON STACK
4582 013530 104401 007512      TYPE      ,RKVADR          ;TYPE 'RK06 VEC ADR = ''
4583 013534 004737 026206      JSR      PC,GETPRM          ;TYPE OLD, GET NEW RKVEC VALUF
4584 013540 012637 003040      MOV      (SP)+,RKVEC      ;STORE NEW VALUE
4585          ;OPEN RK06 INTERRUPT HANDLER PRIORITY LEVEL FOR MODIFICATION
4586 013544 013746 003042 11$:      MOV      RKPRI,-(SP)      ;GET OLD VALUE OF PRIORITY
4587 013550 006316          ASL      (SP)              ;GET IT INTO BITS 0-2
4588 013552 006316          ASL      (SP)
4589 013554 006316          ASL      (SP)
4590 013556 000316          SWAB     (SP)
4591 013560 104401 007535      TYPE      ,RKPRTY          ;TYPE 'RK06 PRIORITY = ''
4592 013564 004737 026206      JSR      PC,GETPRM          ;TYPE OLD, GET NEW RKPRI VALUE
4593 013570 012600          MOV      (SP)+,R0
4594 013572 020027 000004      CMP      R0,#4              ;SEE IF AT LEAST LEVEL 4
4595 013576 002414          BLT      12$                ;BR IF NEW VALUE TOO SMALL
4596 013600 020027 000007      CMP      R0,#7              ;SEE IF LEVEL 7 OR LESS
4597 013604 003011          BGT      12$                ;BR IF NEW VALUE TOO LARGE
4598 013606 000300          SWAB     R0                  ;GET PRIORITY INTO BITS 5-7
4599 013610 006200          ASR      R0
4600 013612 006200          ASR      R0
4601 013614 006200          ASR      R0
4602 013616 010037 003042      MOV      R0,RKPRI          ;STORE NEW VALUE
4603 013622 104401 001315      TYPE      ,$CRLF
4604 013626 000405          BR       16$
4605 013630 104401 005272 12$:      TYPE      ,BUFF0          ;ECHO BAD INPUT
4606 013634 104401 001314      TYPE      ,$QUES
4607 013640 000741          BR       11$                ;GO ASK AGAIN
4608
4609 013642 105037 003126 16$:      CLRB     ERRCNT            ;CLEAR ERROR CNT FOR RESTARTS
4610 013646 012737 013660 000042      MOV      #DRVST,@#42      ;SET UP DUMP MODE RETURN FROM $EOP
4611          ; UPON COMPLETION OF REQUESTED PASSES
4612 013654 000137 014450          JMP      CHKLST            ;GO CHECK STATUS OF MARKED DRIVES
4613
4614
4615
4616          ;*****
4617          ;SBTTL TO - DESIRED DRIVE INPUT ROUTINE
4618          ;*THIS ROUTINE TYPES THE CURRENT LIST OF DRIVES TO TEST,
4619          ;*WITH DRIVE NUMBERS SEPARATED BY COMMAS. THEN, A NEW
4620          ;*CHOICE OF DRIVES IS REQUESTED BY TTY INPUT, AND THESE
4621          ;*DRIVES ARE CHECKED FOR VALIDITY AND PROPER STATUS,
4622          ;*AND IF THEY ARE VALID, THEY ARE LOADED INTO THE DRIVE
4623          ;*LIST (DRVLST).
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4624 ;:*****
4625
4626 013660 DRVTST:
4627 013660 012706 001100 MOV #STACK,SP ;RESET THE STACK
4628 013664 005037 005512 CLR STALLS ;INHIBIT STALL BETWEEN OPERATIONS
4629 013670 104401 010124 TYPE ,ALDRVS ;ASK IF ALL DRIVES DESIRED
4630 013674 004737 030560 JSR PC,RDCHRS ;READ RESPONSE
4631 013700 013660 DRVTST ;(^C) RETURN ADDRESS
4632 013702 013660 DRVTST ;(^Z) RETURN ADDRESS
4633 013704 013660 DRVTST ;(^U) OR ERROR RETURN ADDRESS
4634 013706 005700 TST R0 ;SEE IF NULL INPUT
4635 013710 001413 BEQ TELDRV ;BR IF NULL INPUT
4636 013712 022737 000101 005272 CMP #'A,BUFFO ;SEE IF ALL DRIVES REQUESTED
4637 013720 001002 BNE 4$ ;BR IF NOT ALL DRIVES
4638 013722 000137 014372 JMP ALLDRV ;CHECK ALL DRIVES
4639 013726 104401 005272 4$: TYPE ,BUFFO ;ECHO BAD INPUT
4640 013732 104401 001314 TYPE ,SQUES ;TYPE <?> AND <CR>,<LF>
4641 013736 .000750 BR DRVTST ;GO ASK AGAIN
4642
4643 013740 104401 007575 ;TYPE CURRENT DRIVE LIST
4644 013744 005001 TELDRV: TYPE ,DRVSEQ ;TYPE 'DRIVE(S) = '
4645 013746 005000 CLR R1 ;INITIALIZE DRIVE NUMBER
4646 013750 012703 000001 CLR R0 ;INIT. COUNT OF LISTED DRIVES
4647 013754 105761 005606 4$: MOV #BIT0,R3 ;INIT BIT POINTER
4648 013760 001414 TSTB DRVLST(R1) ;SEE IF THIS DRIVE IS LISTED
4649 013762 005700 BEQ 8$ ;BR IF DRIVE NOT LISTED
4650 013764 001402 TST R0 ;SEE IF FIRST TIME HERE
4651 013766 104401 012412 BEQ 6$ ;BR IF FIRST TIME HERE
4652 013772 010137 005542 6$: MOV R1,SCRACH ;TYPE A COMMA
4653 013776 052737 000060 005542 BIS #'0,SCRACH ;USE SCRACH FOR BUFFER
4654 014004 104401 005542 TYPE ,SCRACH ;CONVFT DRIVE NO. TO ASCII
4655 014010 005200 INC R0 ;TYPE DRIVE NO.
4656 014012 005201 8$: INC R1 ;INCREMENT NO. OF LISTED DRIVES
4657 014014 006303 ASL R3 ;INCREMENT DRIVE NO.
4658 014016 022701 000010 CMP #10,R1 ;SHIFT BIT POINTER
4659 014022 001354 BNE 4$ ;SEE IF DONE YET
4660 014024 005700 TST R0 ;BR IF NOT DONE
4661 014026 001016 BNE 26$ ;SEE IF ANY DRIVES WERE LISTED
4662 014030 104401 011431 TYPE ,NOFALS ;BR IF YES
4663 014034 104401 010003 TYPE ,NODRTS ;TYPE ' NONE'
4664 ; 'PRESS 'CONT' WHEN RDY'
4665 014040 012703 000401 MOV #401,R3 ;PATTERN TO MARK DRIVES
4666 014044 012701 005606 MOV #DRVLST,R1 ;DRIVE LIST ADDRESS
4667 014050 010321 MOV R3,(R1)+ ;MARK ALL DRIVES IN LIST
4668 014052 010321 MOV R3,(R1)+
4669 014054 010321 MOV R3,(R1)+
4670 014056 010311 MOV R3,(R1)
4671 014060 000137 044100 JMP HLTPRG ;HALT PROGRAM
4672 014064 104401 001315 26$: TYPE ,$CRLF ;TYPE <CR>,<LF>
4673 014070 105737 003116 TSTB MDFLAG ;SEE IF DEFAULT MODE
4674 014074 001002 BNE 20$ ;BR IF NOT DEFAULT MODE
4675 014076 000137 015010 JMP LODFLS ;GO LOAD DEFLT ITER. COUNTS
4676 014102 122737 000013 000041 20$: CMPB #13,@#41 ;SEE IF RK06 IS XXDP MEDIUM
4677 014110 001032 BNE 19$ ;BR IF NOT
4678 014112 105737 005606 TSTB DRVLST ;SEE IF DRIVE 0 LISTED TO TEST
4679 014116 001427 BEQ 19$ ;BR IF NOT
```

4680	014120	104401	007714	24\$:	TYPE	,REPLK	:TYPE 'IF XXDP PACK ON DRV 0, TYPE Y <CR>
4681							: AND REPLACE IT'
4682	014124	004737	030560		JSR	PC,RDCHRS	:READ RESPONSE
4683	014130	014120			24\$:(^C) RETURN
4684	014132	014120			24\$:(^Z) RETURN
4685	014134	014120			24\$:(^U) RETURN
4686	014136	005700			TST	RO	:SEE IF NULL INPUT
4687	014140	001416			BEQ	19\$:BR IF JUST <CR> TYPED
4688	014142	022737	000131 005272		CMP	#'Y,BUFFO	:SEE IF 'Y <CR>' TYPED
4689	014150	001363			BNE	24\$:BR IF NOT, TO ASK AGAIN
4690	014152	104401	010032		TYPE	,CNTRDY	:TYPE 'PRESS CONT WHEN DRV RDY'
4691	014156	042762	000100 000000		BIC	#IE,RKCS1(R2)	:DISABLE RK06 INTERRUPT
4692	014164	000000			HALT		:HALT FOR PACK CHANGE
4693	014166	004737	027450		JSR	PC,INITSS	:INIT THE SUB-SYS
4694	014172	000137	014450		JMP	CHKLST	:GO CHECK STATUS OF LISTED DRIVES
4695	014176	104401	012442	19\$:	TYPE	,PROMPT	:TYPE ASTERISK AND SPACE
4696							
4697	014202	004737	030560		:READ AND CHECK	NEW DRIVE NUMBERS	
4698	014206	013660			JSR	PC,RDCHRS	:READ IN INPUT STRING
4699	014210	013660			DRVTST		:(^C) RETURN ADDRESS
4700	014212	013740			DRVTST		:(^Z) RETURN ADDRESS
4701	014214	005700			TELDRV		:(^U) OR ERROR RETURN ADDRESS
4702	014216	001007			TST	RO	:SEE IF NULL INPUT
4703	014220	105737	003136		BNE	9\$:BR IF NEW DRIVES WILL BE SELECTED
4704	014224	001002			TSTB	DULACS	:SEE IF DUAL-ACCESS FLAG SET
4705	014226	000137	014522		BNE	22\$:BR IF YES
4706	014232	000137	015266	22\$:	JMP	ASKTST	:JUMP TO THE TEST INPUT ROUTINE
4707	014236	022700	000017	9\$:	JMP	INPUTP	:GO ASK FOR INPUT PARAMETERS
4708	014242	002005			CMP	#15.,RO	:SEE IF TOO MANY CHARACTERS TYPED
4709	014244	104401	005272	10\$:	BGE	12\$:BR IF NOT TOO MANY
4710	014250	104401	001314		TYPE	,BUFFO	:ECHO BAD INPUT
4711	01	000631			TYPE	,\$QUES	:TYPE <?> AND <CR>,<LF>
4712	014	005001			BR	TELDRV	:GO TYPE DRIVES AND ASK AGAIN
4713	014251	126127	005272 000060	12\$:	CLR	R1	:CLEAR CHAR. POINTER
4714	260	002766		14\$:	CMPB	BUFFO(R1),#'0	:SEE IF DRIVE NO. < 0
4715	014270	126127	005272 000067		BLT	10\$:BR TO ECHO BAD INPUT
4716	014276	003362			CMPB	BUFFO(R1),#'7	:SEE IF > 7
4717	014300	005201			BGT	10\$:BR TO ECHO BAD INPUT
4718	014302	020100			INC	R1	:INCREMENT CHAR POINTER
4719	014304	001406			CMP	R1,RO	:SEE IF MORE CHARS TO CHECK
4720	014306	126127	005272 000054		BEQ	16\$:BR IF ALL CHARS CHECKED
4721	014314	001353			CMPB	BUFFO(R1),#',	:SEE IF THIS IS COMMA
4722	014316	005201			BNE	10\$:BR IF NOT COMMA
4723	014320	000757			INC	R1	:INCREMENT CHAR POINTER
4724					BR	14\$:BR TO CONTINUE CHECKING CHARS
4725	014322	012701	005606		:GET NEW DRIVE NUMBERS INTO LIST		
4726	014326	005021		16\$:	MOV	#DRVLST,R1	:GET DRIVE LIST ADDRESS
4727	014330	005021			CLR	(R1)+	:CLEAR OUT THE DRIVE LIST
4728	014332	005021			CLR	(R1)+	
4729	014334	005011			CLR	(R1)+	
4730	014336	005000			CLR	(R1)	
4731	014340	116001	005272	18\$:	CLR	RO	:CLEAR BUFFER POINTER
4732	014344	042701	000060		MOVB	BUFFO(RO),R1	:GET A DRIVE NUMBER
4733	014350	112761	000001 005606		BIC	#'0,R1	:STRIP ASCII BITS
4734	014356	062700	000002		MOVB	#1,DRVLST(R1)	:MARK THIS DRIVE IN DRIVE LIST
4735	014362	105760	005271		ADD	#2,RO	:POINT TO NEXT DRIVE NUMBER
					TSTB	BUFFO-1(RO)	:SEE IF NULL CHAR YET

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4736 014366 001364      BNE      18$      ;BR IF NOT DONE YET
4737 014370 000427      BR        CHKLST  ;GO CHECK NEW DRIVES FOR VALID STATUS
4738                      ;COME HERE IF ALL DRIVES REQUESTED
4739 014372 005000      ALLDRV: CLR      R0      ;CLEAR DRIVE NUMBER
4740 014374 013703 001376  MOV      $DEVN,R3    ;GET APT DEVICE MAP
4741 014400 132737 000200 001341  BITB     #BIT7,$ENVM ;SEE IF NO SIZING
4742 014406 001002      BNE      1$      ;BR IF NO SIZING
4743 014410 012703 000377  MOV      #377,R3    ;SET UP FOR SIZING
4744 014414 012701 000001  1$:     MOV      #BIT0,R1 ;SET BIT POINTER
4745 014420 105060 005606  2$:     CLRB    DRVLST(R0) ;INITIALIZE DRIVE ENTRY
4746 014424 030103      BIT      R1,R3     ;SEE IF THIS DRIVE IS REQUESTED
4747 014426 001403      BEQ      4$      ;BR IF NOT
4748 014430 112760 000001 005606  MOVB    #1,DRVLST(R0) ;MARK THIS DRIVE IN DRIVE LIST
4749 014436 006301      4$:     ASL      R1      ;SHIFT BIT POINTER
4750 014440 005200      INC      R0      ;INCREMENT DRIVE NUMBER
4751 014442 022700 000010  CMP      #10,R0    ;SEE IF DONE MARKING ALL DRIVES
4752 014446 001364      BNE      2$      ;BR IF NOT DONE YET
4753                      ;CHECK ALL DRIVES MARKED IN DRIVE LIST FOR PROPER STATUS
4754 014450 005000      CHKLST: CLR     R0      ;CLEAR DRIVE NUMBER
4755 014452 105760 005606  2$:     TSTB    DRVLST(R0) ;SEE IF THIS DRIVE IS MARKED IN LIST
4756 014456 001410      BEQ      4$      ;BR IF NOT MARKED
4757 014460 010037 005510  MOV      R0,DRIVE  ;SET DRIVE NUMBER FOR SCNDRV
4758 014464 004737 027604  JSR      PC,SCNDRV ;CHECK STATUS OF THIS DRIVE
4759                      ;IF NOT VALID, TYPE MESSAGE
4760                      ; AND REMOVE IT FROM DRIVE LIST
4761 014470 014514      6$:     ERROR RETURN ADDRESS FOR SCNDRV
4762 014472 113760 003124 005616  MOVB    TYPFMT,DTYLST(R0) ;SET DRV TYPE. 0=RK06, 4=RK07
4763 014500 005200      4$:     INC      R0      ;RETURN HERE IF DRIVE IS USEABLE
4764 014502 022700 000010  CMP      #10,R0    ;SEE IF DONE CHECKING LIST
4765 014506 001361      BNE      2$      ;BR IF NOT DONE YET
4766 014510 000137 013740  JMP      TELDRV   ;GO BACK TO LIST SELECTED DRIVES
4767 014514 105060 005606  5$:     (LRB    DRVLST(R0) ;REMOVE INVALID DRIVE FROM LIST
4768 014520 000767      BR        4$      ;CONTINUE CHECKING THE LIST
4769
4770
4771                      ;*****
4772                      ;SBTTL TO - DESIRED TEST INPUT ROUTINE
4773                      ;*THIS ROUTINE ALLOWS INPUT OF DESIRED TESTS AND ITERATION
4774                      ;*COUNT FOR EACH TEST. THE INPUT IS CHECKED FOR VALIDITY,
4775                      ;*AND IF VALID, THE ITERATION COUNTS ARE LOADED INTO THE
4776                      ;*TEST LIST (TSTLST). AN ITERATION VALUE OF 0 INDICATES
4777                      ;*THAT THE TEST IS NOT TO BE RUN.
4778                      ;*THIS ROUTINE REQUESTS 'ENTER L,C, OR I'. TYPING (L)
4779                      ;*CAUSES THE CURRENT LIST OF TESTS AND ITERATIONS TO BE
4780                      ;*TYPED, (C) ALLOWS TESTS AND ITERATIONS TO BE CHANGED,
4781                      ;*AND (I) ALLOWS THE OPERATOR TO PROCEED WITH SELECTION
4782                      ;*OF OPERATING PARAMETERS, AND RUN TESTS.
4783                      ;*TYPING (^C) CAUSES IMMEDIATE RETURN TO DRVTST. TYPING
4784                      ;* (^Z) CAUSES RETURN TO ASKTMD.
4785                      ;*****
4786
4787                      ;DETERMINE L,C, OR I MODE
4788 014522 104401 010205  ASKTST: TYPE     ,TSTMDS ;ASK FOR TEST INPUT MODE
4789 014526 104401 010307  ASKTMD: TYPE     ,ENTLCI ;TYPE 'ENTER L,C, OR I'
4790 014532 004737 030560  JSR      PC,RDCHRS ;READ RESPONSE
4791 014536 013660      DRVTST ;(^C) RETURN ADDRESS
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4792 014540 014526 ASKTMD ;(^Z) RETURN ADDRESS
4793 014542 014526 ASKTMD ;(^U) OR ERROR RETURN ADDRESS
4794 014544 005700 TST RO ;SEE IF ANY INPUT
4795 014546 001005 BNE 4$ ;BR IF ANY INPUT
4796 014550 104401 005272 2$: TYPE ,BUFFO ;ECHO BAD INPUT
4797 014554 104401 001314 TYPE ,SQUES ;TYPE <?> AND <CR>,<LF>
4798 014560 000762 BR ASKTMD ;GO ASK AGAIN
4799 014562 022737 000114 005272 4$: CMP #L,BUFFO ;SEE IF (L) TYPED
4800 014570 001002 BNE 6$ ;BR IF NOT (L)
4801 014572 000137 014626 JMP LSTTST ;JUMP TO LIST TESTS
4802 014576 022737 000103 005272 6$: CMP #'C,BUFFO ;SEE IF (C) TYPED
4803 014604 001002 BNE 8$ ;BR IF NOT (C)
4804 014606 000137 014744 JMP CHGTST ;JUMP TO CHANGE TESTS
4805 014612 022737 000111 005272 8$: CMP #'I,BUFFO ;SEE IF (I) TYPED
4806 014620 001353 BNE 2$ ;BR IF NOT (I), TO ECHO BAD INPUT
4807 014622 000137 015266 JMP INPUTP ;GO INPUT PARAMS AND RUN TESTS
4808
4809 ;LIST CURRENT TESTS AND ITERATION COUNTS
4810 014626 104401 010411 LSTTST: TYPE ,TLSTHD ;TYPE HEADING 'TEST ITERATIONS'
4811 014632 005001 CLR R1 ;INITIALIZE TEST INDEX
4812 014634 004737 025766 2$: JSR PC,PREPKB ;PREPARE FOR POSSIBLE KBD INPUT
4813 014640 004737 031072 JSR PC,TYPTST ;TYPE CURRENT TEST AND ITERATION NUMBER
4814 014644 104401 001315 TYPE ,$CRLF ;TYPE <CR>,<LF>
4815 014650 005737 005532 TST INTCHR ;SEE IF ANY INPUT
4816 014654 001416 BEQ 8$ ;BR IF NO INPUT
4817 014656 122737 000003 005532 CMPB #003,INTCHR ;SEE IF (^C) TYPED
4818 014664 001002 BNE 4$ ;BR IF NOT (^C)
4819 014666 000137 013660 JMP DRVTST ;JUMP TO ASK FOR DRIVES AGAIN
4820 01472 122737 000032 005532 4$: CMPB #032,INTCHR ;SEE IF (^?) TYPED
4821 014700 001002 BNE 6$ ;BR IF NOT (^Z)
4822 014702 000137 014526 JMP ASKTMD ;JUMP TO ASK FOR NEW TEST INPUT MODE
4823 014706 004737 026006 6$: JSR PC,ECOBAD ;ECHO BAD INPUT
4824 014712 062701 000002 8$: ADD #2,R1 ;INCREMENT TEST INDEX
4825 014716 010102 MOV R1,R2 ;GET COPY OF INDEX
4826 014720 062702 005626 ADD #TSTLST,R2 ;GET POSITION IN LIST
4827 014724 022702 005670 CMP #DFLTST,R2 ;SEE IF DONE WITH LIST
4828 014730 001341 BNE 2$ ;BR IF NOT DONE YET
4829 014732 042777 000100 164204 B!C #BIT6,@$TKS ;DISABLE KBD INTERRUPT
4830 014740 000137 014526 JMP ASKTMD ;GO ASK FOR NEW TEST INPUT MODE
4831
4832 ;CHANGE CURRENT TESTS AND ITERATION COUNTS
4833 014744 104401 010335 CHGTST: TYPE ,DFTEST ;ASK IF TESTS SHOULD BE DEFAULTED
4834 014750 004737 030560 JSR PC,RDCHRS ;READ RESPONSE
4835 014754 013660 DRVTST ;(^C) RETURN ADDRESS
4836 014756 014526 ASKTMD ;(^Z) RETURN ADDRESS
4837 014760 014744 CHGTST ;(^U) OR ERROR RETURN ADDRESS
4838 014762 005700 TST RO ;SEE IF NULL INPUT
4839 014764 001426 BEQ NULINP ;BR IF NULL INPUT
4840 014766 022737 000104 005272 CMP #'D,BUFFO ;SEE IF (D) TYPED
4841 014774 001405 BEQ LODFLS ;BR IF DEFAULTS REQUESTED
4842 014776 104401 005272 TYPE ,BUFFO ;ECHO BAD INPUT
4843 015002 104401 001314 TYPE ,SQUES ;TYPE <?> AND <CR>,<LF>
4844 015006 000756 BR CHGTST ;GO ASK AGAIN
4845 ;LOAD DEFAULT ITERATION COUNTS INTO TEST LIST
4846 015010 012700 005670 LODFLS: MOV #DFLTST,R0 ;LOAD DEFAULT LIST ADDRESS
4847 015014 012702 005626 MOV #TSTLST,R2 ;LOAD TEST LIST ADDRESS
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4848 015020 012022          4$:  MOV    (R0)+,(R2)+      ;LOAD A DEFAULT VALUE
4849 015022 022702 005670    CMP    #DFLTST,R2        ;SEE IF DONE YET
4850 015026 001374          BNE    4$                ;BR IF NOT DONE YET
4851 015030 105737 003116    TSTB  MDFLAG             ;SEE IF DEFAULT MODE
4852 015034 001234          BNE    ASKTM             ;BR IF NOT DEFAULT MODE
4853 015036 000137 016172    JMP    LODFPT            ;GO LOAD DEFAULT PARAMETERS
4854 015042 005001          NULINP: CLR   R1          ;INITIALIZE TEST INDEX
4855 015044 104401 010411    TYPE  ,TLSTHD           ;TYPE HEADING 'TEST  ITERATIONS'
4856                                ;TYPE CURRENT TEST AND ITERATION NUMBER
4857 015050 004737 031072    8$:  JSR    PC,TYPTST      ;TYPE A TEST NO. AND ITERATION NO.
4858 015054 104401 012421    TYPE  ,SPACE1           ;TYPE A SPACE
4859 015060 104401 012442    TYPE  ,PROMPT           ;TYPE ASTERISK AND SPACE
4860                                ;READ AND CHECK INPUT, IF ANY
4861 015064 004737 030560    JSR    PC,RDCHRS        ;READ OCTAL DIGITS TYPED
4862 015070 013660          DRVTST                  ;(^C) RETURN ADDRESS
4863 015072 014526          ASKTM                    ;(^Z) RETURN ADDRESS
4864 015074 015050          8$                       ;(^U) OR ERROR RETURN ADDRESS
4865 015076 005700          TST    R0                ;SEE IF ANY INPUT
4866 015100 001012          BNE    12$               ;BR IF ANY INPUT
4867 015102 062701 000002    10$: ADD    #2,R1            ;INCREMENT INDEX
4868 015106 010102          MOV    R1,R2             ;COPY INDEX
4869 015110 062702 005626    ADD    #TSTLST,R2        ;GET POSITION IN LIST
4870 015114 022702 005670    CMP    #DFLTST,R2        ;SEE IF DONE WITH LIST
4871 015120 001353          BNE    8$                ;BR IF NOT DONE YET
4872 015122 000137 014526    JMP    ASKTM             ;GO ASK FOR NEW TEST INPUT MODE
4873 015126 022737 000041 005272 12$:  CMP    #'!',BUFF0        ;SEE IF (!) TYPED
4874 015134 001431          BEQ    16$               ;BR TO PROPAGATE CURRENT ITER. NO.
4875 015136 005002          CLR    R2                ;INITIALIZE (!) INDICATOR
4876 015140 122760 000041 005271  CMPB   #'!',BUFF0-1(R0)  ;SEE IF LAST CHAR IN BUF IS (!)
4877 015146 001004          BNE    14$               ;BR IF NOT (!)
4878 015150 105060 005271    CLRB  BUFF0-1(R0)        ;INSERT TERMINATOR BYTE
4879 015154 005300          DEC    R0                ;DECREMENT CHAR COUNT
4880 015156 005202          INC    R2                ;SET (!) INDICATOR
4881 015160 022700 000005    14$:  CMP    #5,R0             ;SEE HOW MANY CHARS NOW
4882 015164 002433          BLT    20$               ;BR IF TOO MANY (MAX ITER. NO. = 77776)
4883 015166 012746 005272    MOV    #BUFF0,-(SP)      ;GET BUF ADDR. ON STACK FOR OCTBIN
4884 015172 004737 052430    JSR    PC,OCTBIN         ;CHECK DIGITS AND CONVERT TO BINARY
4885 015176 015254          20$                       ;ERROR RETURN ADDRESS FOR OCTBIN
4886 015200 012600          MOV    (SP)+,R0          ;GET ITERATION NUMBER
4887 015202 020027 077776    CMP    R0,#77776         ;SEE IF > 77776
4888 015206 101022          BHI    20$               ;BR IF TOO BIG
4889 015210 010061 005626    MOV    R0,TSTLST(R1)     ;PUT ITERATION NUMBER INTO TEST LIST
4890 015214 005702          TST    R2                ;SEE IF (!) WAS TYPED
4891 015216 001731          BEQ    10$               ;BR IF NOT (!)
4892                                ;PROPAGATE CURRENT ITERATION NO. TO THE END OF TSTLST
4893 015220 010102          16$:  MOV    R1,R2             ;COPY INDEX
4894 015222 062702 005626    ADD    #TSTLST,R2        ;GET POSITION IN LIST
4895 015226 022702 005666    CMP    #DFLTST-2,R2      ;SEE IF AT END OF LIST
4896 015232 001002          BNE    17$               ;BR IF NOT DONE
4897 015234 000137 014526    JMP    ASKTM             ;GO ASK FOR NEW TEST INPUT MODE
4898 015240 016161 005626 005630 17$:  MOV    TSTLST(R1),TSTLST+2(R1) ;PROPAGATE TO NEXT WORD
4899 015246 062701 000002    ADD    #2,R1             ;INCREMENT INDEX
4900 015252 000762          BR    16$                ;BR TO CONTINUE
4901 015254 104401 005272    20$:  TYPE  ,BUFF0            ;ECHO BAD INPUT
4902 015260 104401 001314    TYPE  ,$QUES            ;TYPE <?> AND <CR>,<LF>
4903 015264 000671          BR    8$                 ;GO ASK AGAIN
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4904
4905 ;ASK FOR DESIRED OPERATIONAL PARAMETER INPUT MODE
4906 015266 104401 010441 INPUTP: TYPE ,EXPLAN ;EXPLAIN INPUT MODES
4907 015272 005037 005512 ASKMDE: CLR STALLS ;INHIBIT STALL BETWEEN OPERATIONS
4908 015276 104401 010567 TYPE ,PARMDE ;ASK FOR DESIRED INPUT MODE
4909 015302 104401 012442 TYPE ,PROMPT ;TYPE "*"
4910 015306 004737 030560 JSR PC,RDCHRS ;READ RESPONSE TO MODE QUESTION
4911 015312 013660 DRVTST ;(^C) RETURN ADDRESS
4912 015314 015272 ASKMDE ;(^Z) RETURN ADDRESS
4913 015316 015272 ASKMDE ;(^U) OR ERROR RETURN ADDRESS
4914 015320 005700 TST RO ;SEE IF NULL INPUT
4915 015322 001005 BNE 4$ ;BR IF ANY INPUT
4916 015324 104401 005272 2$: TYPE ,BUFFO ;ECHO BAD INPUT
4917 015330 104401 001314 TYPE ,SQUES ;TYPE <?> AND <CR>,<LF>
4918 015334 000756 BR ASKMDE ;GO ASK AGAIN
4919 015336 022737 000124 005272 4$: CMP #'T,BUFFO ;SEE IF (T) TYPED
4920 015344 001002 BNE 6$ ;BR IF NOT (T)
4921 015346 000137 015654 JMP TYPLST ;JUMP TO THE TYPE LIST ROUTINE
4922 015352 022737 000117 005272 6$: CMP #'O,BUFFO ;SEE IF (O) TYPED
4923 015360 001002 BNE 8$ ;BR IF NOT (O)
4924 015362 000137 016126 JMP OPNLST ;JUMP TO THE OPEN LIST ROUTINE
4925 015366 022737 000123 005272 8$: CMP #'S,BUFFO ;SEE IF (S) TYPED
4926 015374 001002 BNE 10$ ;BR IF NOT (S)
4927 015376 000137 016412 JMP SETPRM ;JUMP TO THE SET INDIV. PARAM. ROUTINE
4928 015402 022737 000122 005272 10$: CMP #'R,BUFFO ;SEE IF (R) TYPED
4929 015410 001345 BNE 2$ ;BR IF NOT (R), TO ECHO BAD INPUT
4930 015412 023737 005746 005750 CMP S0,S1 ;COMPARE 20(DEC) SECTOR LIMITS
4931 015420 003403 P.E 12$ ;BR IF S0 NOT > S1
4932 015422 104401 010670 TYPE ,SECNL1 ;TYPE "S0>S1 NOT ALLOWED"
4933 015426 000721 BR ASKMDE ;GO ASK AGAIN FOR PARAMETERS
4934 015430 023737 005752 005754 12$: CMP S2,S3 ;COMPARE 22(DEC) SECTOR LIMITS
4935 015436 003405 BLE 14$ ;BR IF S2 NOT > S3
4936 015440 104401 010720 TYPE ,SECNL2 ;TYPE "S2>S3"
4937 015444 104401 010701 TYPE ,NOTALD ;TYPE "NOT ALLOWED"
4938 015450 000710 BR ASKMDE ;GO ASK AGAIN FOR PARAMETERS
4939 015452 023737 005740 005742 14$: CMP FT,LT ;COMPARE CHOSEN TRACK LIMITS
4940 015460 003405 BLE 20$ ;BR IF FT NOT > LT
4941 015462 104401 010732 TYPE ,TRKNLW ;TYPE "FT>LT"
4942 015466 104401 010701 TYPE ,NOTALD ;TYPE "NOT ALLOWED"
4943 015472 000677 BR ASKMDE ;GO ASK AGAIN FOR PARAMETERS
4944 ;CHECK WORD COUNT AGAINST PHYSICAL MEMORY AVAILABLE
4945 015474 013700 005766 20$: MOV WC,RO ;WORD COUNT LO BITS
4946 015500 005300 DEC RO ;DECREMENT BY 1
4947 015502 005001 CLR R1 ;WORD COUNT HI BITS
4948 015504 000241 CLC ;CONVERT WORD COUNT TO BYTES
4949 015506 006100 ROL RO
4950 015510 006101 ROL R1
4951 015512 063700 005762 ADD MA,RO ;ADD WORD COUNT (WC) TO MEM. ADR. (MA)
4952 015516 005501 ADC R1
4953 015520 063701 005764 ADD MA+2,R1
4954 015524 020137 006130 CMP R1,MAHILM+2 ;COMPARE HI BITS
4955 015530 101004 BHI 26$ ;BR IF WC TOO BIG
4956 015532 001006 BNE 28$ ;BR IF WC IS OK
4957 015534 020037 006126 CMP RO,MAHILM ;COMPARE LO BITS
4958 015540 101403 BLOS 28$ ;BR IF WC IS OK
4959 015542 104401 010744 26$: TYPE ,WC2BIG ;TYPE "*" WC OR MA TOO LARGE
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4960 015546 000651          BR      ASKME      ;GO BACK TO ASK AGAIN FOR PARAMS
4961          :SAVE XXDP LOADER, IF NECESSARY
4962 015550 004737 026726    28$:   JSR      PC,CHKXDP ;GO SAVE XXDP LOADER IF NECESSARY
4963 015554 015562          36$           ;'NO SAVE' RETURN ADDRESS
4964 015556 000137 016646    30$:   JMP      RUNTST ;JUMP TO THE RUN TESTS ROUTINE
4965 015562 105737 000041    36$:   TSTB     @#41     ;SEE IF LOADED BY XXDP
4966 015566 001773          BEQ      30$       ;BR IF NOT
4967 015570 104401 007404          TYPE     ,OVLODR   ;TYPE 'OVERLAY LOADER ? (Y OR N) *'
4968 015574 004737 030560          JSR      PC,RDCHRS ;READ RESPONSE
4969 015600 015562          36$           ;(^C) RETURN ADDRESS
4970 015602 015562          36$           ;(^Z) RETURN ADDRESS
4971 015604 015562          36$           ;(^U) OR ERROR RETURN ADDRESS
4972 015606 022700 000001          CMP      #1,R0     ;SEE IF 1 CHAR TYPED
4973 015612 001405          BEQ      38$       ;BR IF 1 CHAR TYPED
4974 015614 104401 005272    37$:   TYPE     ,BUFFO   ;ECHO BAD INPUT
4975 015620 104401 001314          TYPE     ,SQUES
4976 015624 000756          BR      36$       ;GO ASK AGAIN
4977 015626 122737 000131 005272 38$:   CMPB     #'Y,BUFFO ;SEE IF (Y) TYPED
4978 015634 001001          BNE     40$       ;BR IF NOT (Y)
4979 015636 000747          BR      30$       ;GO RUN TESTS
4980 015640 122737 000116 005272 40$:   CMPB     #'N,BUFFO ;SEE IF (N) TYPED
4981 015646 001362          BNE     37$       ;BR IF NOT (N)
4982 015650 000137 015272          JMP      ASKME     ;GO ASK FOR PARAMETERS AGAIN
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.SBTTL TO - TYPE (T) LIST ROUTINE
; *THIS ROUTINE TYPES THE ENTIRE PARAMETER LIST ON THE
; *CONSOLE, LINE BY LINE. EACH LINE HAS THE FOLLOWING
; *FORMAT: XX=YYYYYY, WHERE XX IS A PARAMETER MNEMONIC
; *AND YYYYYY IS THE VALUE OF THE PARAMETER IN OCTAL.
; *TYPING (^C) CAUSES IMMEDIATE RETURN TO DRVTST,
; *AND (^Z) CAUSES RETURN TO ASKME.
```

```
4994 015654          TYPLST:
4995 015654 005001          CLR      R1        ;INITIALIZE INDEX
4996 015656 004737 025766    1$:   JSR      PC,PREPKB ;PREPARE FOR POSSIBLE KBD INPUT
4997 015662 004737 031126          JSR      PC,TYPPRM ;TYPE CURRENT PARAMETER AND VALUE
4998 015666 104401 001315          TYPE     ,%CR LF   ;TYPE <CR>, <LF>
4999 015672 005737 005532          TST     INTCHR    ;SEE IF ANY INPUT AT KBD
5000 015676 001420          BEQ      8$        ;BR IF NO INPUT
5001 015700 122737 000003 005532    CMPB     #003,INTCHR ;SEE IF (^C) TYPED
5002 015706 001002          BNE     4$        ;BR IF NOT (^C)
5003 015710 000137 013660          JMP      DRVTST   ;JUMP TO ASK FOR DRIVE(S) AGAIN
5004 015714 122737 000032 005532 4$:   CMPB     #032,INTCHR ;SEE IF (^Z) TYPED
5005 015722 001002          BNE     6$        ;BR IF NOT (^Z)
5006 015724 000137 015272          JMP      ASKME     ;JUMP TO ASK FOR NEW MODE
5007 015730 004737 026006    6$:   JSR      PC,ECOBAD ;ECHO BAD INPUT
5008 015734 004737 025766          JSR      PC,PREPKB ;PREPARE FOR POSSIBLE KBD INPUT
5009 015740 022761 040515 006152 8$:   CMP      #'MA,PRMNM(R1) ;SEE IF PARAM. IS (MA)
5010 015746 001002          BNE     10$       ;BR IF NOT (MA)
5011 015750 062701 000002          ADD     #2,R1     ;INCREMENT PARAMETER INDEX
5012 015754 062701 00G002    10$:  ADD     #2,R1     ;INCREMENT PARAMETER INDEX
5013 015760 010102          MOV     R1,R2     ;GET COPY OF INDEX
5014 015762 062702 005732          ADD     #PRMLST,R2 ;COMPUTE POSITION IN LIST
5015 015766 022702 005776          CMP     #PRDFLT,R2 ;SEE IF DONE WITH LIST
```

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5016 015772 001333          BNE      1$          ;BR IF NOT DONE YET
5017 015774 032737 100000 005760 BIT      #BIT15,PT  ;SEE IF PATTERN 15 SPECIFIED
5018 016002 001005          BNE      12$        ;BR IF PATTERN SPECIFIED
5019 016004 042777 000100 163132 BIC      #BIT6,@$TKS ;DISABLE KBD INTERRUPT
5020 016012 000137 015272 JMP      ASKMDE     ;JUMP TO ASK FOR NEW MODE
5021                                ;TYPE OUT USER-DEFINED PATTERN 15
5022 016016 104401 011057 12$: TYPE    ,PFIFTN  ;TYPE 'USER-DEFINED PATTERN 15 :''
5023 016022 005001          CLR      R1         ;INITIALIZE WORD INDEX
5024 016024 005737 005532 14$: TST     INTCHR  ;SEE IF ANY INPUT
5025 016030 001420          BEQ      20$        ;BR IF NO INPUT
5026 016032 122737 000003 005532 CMPB     #003,INTCHR ;SEE IF (^C) TYPED
5027 016040 001002          BNE      16$        ;BR IF NOT (^C)
5028 016042 000137 013660 JMP      DRVTST     ;JUMP TO ASK FOR DRIVE(S) AGAIN
5029 016046 122737 000032 005532 16$: CMPB     #032,INTCHR ;SEE IF (^Z) TYPED
5030 016054 001002          BNE      18$        ;BR IF NOT (^Z)
5031 016056 000137 015272 JMP      ASKMDE     ;JUMP TO ASK FOR NEW MODE
5032 016062 004737 026006 18$: JSR     PC,ECOBAD ;ECHO BAD INPUT
5033 016066 004737 025766 JSR     PC,PREPKB  ;PREPARE FOR POSSIBLE KBD INPUT
5034 016072 004737 031206 20$: JSR     PC,TYPAT  ;TYPE WORD XX = YYYYYY
5035 016076 104401 001315 TYPE     ,$CRLF    ;TYPE <CR>,<LF>
5036 016102 062701 000002 ADD      #2,R1     ;INCREMENT INDEX
5037 016106 022701 000040 CMP      #32.,R1  ;SEE IF 16 WORDS TYPED
5038 016112 001344          BNE      14$        ;BR IF NOT DONE YET
5039 016114 042777 000100 163022 BIC      #BIT6,@$TKS ;DISABLE KBD INTERRUPT
5040 016122 000137 015272 JMP      ASKMDE     ;JUMP TO ASK FOR NEW MODE
```

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5041
5042
5043
5044 .SBTTL TO - OPEN (O) LIST ROUTINE
5045 ;*THIS ROUTINE IS USED TO EITHER REQUEST LOADING OF ALL
5046 ;*DEFAULT PARAMETERS INTO THE PARAMETER LIST, OR TO
5047 ;*SEQUENTIALLY OPEN EACH PARAMETER FOR ALTERATION BY
5048 ;*TTY INPUT. TYPING (^C) CAUSES IMMEDIATE RETURN TO
5049 ;*DRVTST, AND (^Z) CAUSES RETURN TO ASKMDE.
```

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5050
5051
5052 OPNLST:
5053 016126 104401 010775 TYPE     ,DFQUES  ;ASK IF ALL DEFAULT VALUES DESIRED
5054 016132 004737 030560 JSR     PC,RDCHRS ;READ RESPONSE TO DEFAULT QUESTION
5055 016136 013660 DRVTST          ;(^C) RETURN ADDRESS
5056 016140 015272 ASKMDE         ;(^Z) RETURN ADDRESS
5057 016142 016126 OPNLST          ;(^U) OR ERROR RETURN ADDRESS
5058 016144 005700 TST     RO      ;SEE IF NULL INPUT
5059 016146 001433 BEQ      NOTDFT  ;BR IF DEFAULTS NOT REQUESTED
5060 016150 022737 000104 005272 CMP      #'D,BUFFO ;SEE IF (D) TYPED
5061 016156 001405 BEQ      LODFPT  ;BR IF DEFAULTS DESIRED
5062 016160 104401 005272 TYPE     ,BUFFO  ;ECHO BAD INPUT
5063 016164 104401 001314 TYPE     ,$QUES
5064 016170 000756 BR       OPNLST  ;GO ASK AGAIN
5065 ;LOAD ALL DEFAULT VALUES INTO PARAMETER LIST
5066 016172 012700 005776 LODFPT: MOV    #PRDFLT,R0 ;LOAD DEFAULT LIST ADDRESS
5067 016176 012702 005732 MOV    #PRMLST,R2  ;LOAD PARAMETER LIST ADDRESS
5068 016202 012022 4$: MOV    (R0)+,(R2)+ ;LOAD A DEFAULT VALUE
5069 016204 022702 005776 CMP    #PRDFLT,R2 ;SEE IF DONE YET
5070 016210 001374 BNE     4$      ;BR IF MORE VALUES TO LOAD YET
5071 016212 105737 003116 TSTB   MDFLAG   ;SEE IF DEFAULT MODE
```

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5072 016216 001005          BNE      6$          ;BR IF NOT DEFAULT MODE
5073 016220 012737 000001 025422  MOV      #1,$EGPCT  ;SET PASS COUNT = 1
5074 016226 000137 016730      JMP      STPASS     ;GO RUN DFLT TESTS
5075 016232 000137 015272      6$:      JMP      ASKMDE   ;JUMP TO ASK FOR NEW MODE
5076 016236 005001      NOTDFT: CLR      R1   ;INITIALIZE INDEX
5077          ;TYPE CURRENT PARAMETER AND VALUE
5078 016240 004737 031126      8$:      JSR      PC,TYPPRM ;TYPE PARAMETER AND VALUE
5079 016244 104401 012421      TYPE    ,SPACE1    ;TYPE A SPACE
5080 016250 104401 012442      TYPE    ,PROMPT    ;TYPE ASTERISK AND SPACE
5081          ;PEAD AND CHECK INPUT, IF ANY
5082 016254 004737 030560      JSR      PC,RDCHRS  ;READ OCTAL DIGITS TYPED
5083 016260 013660          DRVTST          ;(^C) RETURN ADDRESS FOR RDCHRS
5084 016262 015272          ASKMDE          ;(^Z) RETURN ADDRESS FOR RDCHRS
5085 016264 016240          8$           ;(^U) OR ERROR RETURN ADDR. FOR RDCHRS
5086 016266 005700          TST      R0       ;SEE IF ANY INPUT
5087 016270 001007          BNE      10$      ;BR IF ANY INPUT
5088 016272 022761 040515 006152  CMP      #'MA,PRMNM(R1) ;SEE IF (MA) JUST DEFAULTED
5089 016300 001023          BNE      12$      ;BR IF NOT (MA)
5090 016302 062701 000002      ADD      #2,R1    ;INCREMENT INDEX
5091 016306 000420          BR       12$      ;BR TO MOVE ON TO NEXT PARAMETER
5092 016310 022700 000010      10$:     CMP      #10,R0  ;SEE IF 8 CHARACTERS TYPED
5093 016314 002431          BLT      14$      ;BR IF MORE THAN 8 TYPED
5094 016316 012746 005272      MOV      #BUFFO,-(SP) ;PUT BUFFER ADDR. ON STACK FOR OCTBIN
5095 016322 004737 052430      JSR      PC,OCTBIN ;CHECK DIGITS AND CONVERT TO BINARY
5096 016326 016400          14$           ;ERROR RETURN ADDRESS FOR OCTBIN
5097 016330 012637 005476      MOV      (SP)+,LOWOCT ;GET LOW BINARY BITS
5098 016334 013737 052562 005500  MOV      $HIOCT,HIGOCT ;GET HIGH BINARY BITS
5099          ;CHECK PARAMETER VALUE AND PUT INTO LIST
5100 016342 004737 031564      JSR      PC,CHKPRM ;CHECK VALIDITY OF PARAM VALUE
5101 016346 016400          14$           ;ERROR RETURN ADDR. FOR CHKPRM
5102          ;OPEN USER-DEFINED PATTERN 15 FOR MODIFICATION, IF NECESSARY
5103 016350 004737 031240      12$:     JSR      PC,MODP15
5104          ;MOVE ON TO NEXT PARAMETER
5105 016354 062701 000002      ADD      #2,R1    ;INCREMENT THE PARAMETER INDEX
5106 016360 010102          MOV      R1,R2    ;GET COPY OF INDEX
5107 016362 062702 005732      ADD      #PRMLST,R2 ;COMPUTE POSITION IN LIST
5108 016366 022702 005776      CMP      #PRDFLT,R2 ;SEE IF DONE WITH LIST
5109 016372 001322          BNE      8$       ;BR IF NOT DONE YET
5110 016374 000137 015272      JMP      ASKMDE   ;JUMP TO ASK FOR NEW MODE
5111 016400 104401 005272      14$:     TYPE    ,BUFFO  ;ECHO BAD INPUT
5112 016404 104401 001314      TYPE    ,$QUES    ;TYPE <?> AND <CR>,<LF>
5113 016410 000713          BR       8$       ;BR TO ASK AGAIN
5114
5115
5116
5117          .SBTTL TO - SET (S) INDIVIDUAL PARAM ROUTINE
5118          ;*THIS ROUTINE IS USED TO ALLOW SETTING OF ANY INDIVIDUAL
5119          ;*PARAMETER VALUE BY TTY INPUT. A PROMPTER (>) IS
5120          ;*TYPED AND THE ROUTINE WAITS FOR INPUT OF THE FORM
5121          ;* XX=YYYYYY, WHERE XX IS THE MNEMONIC FOR ANY VALID
5122          ;*PARAMETER IN THE LIST, AND YYYYYY IS THE DESIRED PARAMETER
5123          ;*VALUE IN OCTAL DIGITS. THE PROGRAM THEN ASKS FOR ANOTHER
5124          ;*PARAMETER BY TYPING '>' AGAIN. TYPING (^C) CAUSES
5125          ;*IMMEDIATE RETURN TO DRVTST, AND (^Z) CAUSES RETURN TO
5126          ;*ASKMDE.
5127

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5128 016412 SETPRM:
5129 016412 104401 012445 ;TYPE '>' PROMPTER
5130 ;READ AND CHECK INPUT, IF ANY
5131 016416 004737 030560 JSR PC, RDCHRS ;READ INPUT LINE
5132 016422 013660 DRVTST ;(^C) RETURN ADDRESS FOR RDCHRS
5133 016424 015272 ASKMDE ;(^Z) RETURN ADDRESS FOR RDCHRS
5134 016426 016412 SETPRM ;(^U) OR ERROR RETURN ADDR. FOR RDCHRS
5135 016430 020027 000004 CMP R0, #4 ;SEE IF AT LEAST 4 CHARACTERS TYPED
5136 016434 002005 BGE 6$ ;BR IF AT LEAST 4 TYPED
5137 016436 104401 005272 4$: TYPE ,BUFFO ;ECHO BAD INPUT
5138 016442 104401 001314 TYPE ,$QUES ;TYPE <?> AND <CR>, <LF>
5139 016446 000761 BR SETPRM ;BR TO ASK FOR INPUT AGAIN
5140 016450 020027 000013 6$: CMP R0, #11. ;SEE IF ELEVEN OR LESS CHARS TYPED
5141 016454 003370 BGT 4$ ;BR IF MORE THAN ELEVEN TYPED
5142 ;SEE IF THIS MNEMONIC RESIDES IN PARAM MNEMONIC TABLE
5143 016456 005001 CLR R1 ;INITIALIZE PARAMETER INDEX
5144 016460 023761 005272 006152 8$: CMP BUFFO, PRMNE(R1) ;TRY TO MATCH 2-CHAR MNEMONIC
5145 016466 001411 BEQ 10$ ;BR IF PARAMETER FOUND IN TABLE
5146 016470 062701 000002 ADD #2, R1 ;INCREMENT INDEX
5147 016474 010102 MOV R1, R2 ;GET COPY OF INDEX
5148 016476 062702 005732 ADD #PRMLST, R2 ;SEE IF ALL ENTRIES HAVE BEEN CHECKED
5149 016502 022702 005776 CMP #PRDFLT, R2
5150 016506 001364 BNE 8$ ;BR IF MORE TO CHECK YET
5151 016510 000752 BR 4$ ;BR TO ECHO BAD INPUT
5152 ;CHECK FOR VALID LINE FORMAT
5153 016512 012702 000002 10$: MOV #2, R2 ;INITIALIZE BUFFER POINTER
5154 016516 122762 000075 005272 CMFB #'=, BUFFO(R2) ;SEE IF NEXT CHAR IS '='
5155 016524 001411 BEQ 12$ ;BR IF IT IS '='
5156 016526 122762 000040 005272 CMPB #040, BUFFO(R2) ;SEE IF NEXT CHAR IS A SPACE
5157 016534 001340 BNE 4$ ;BR TO ECHO BAD INPUT
5158 016536 005202 INC R2 ;INCREMENT BUFFER POINTER
5159 016540 122762 000075 005272 CMFB #'=, BUFFO(R2) ;SEE IF NEXT CHAR IS '='
5160 016546 001333 BNE 4$ ;BR TO ECHO INVALID INPUT
5161 016550 005202 12$: INC R2 ;INCREMENT BUFFER POINTER
5162 016552 020200 CMP R2, R0 ;SEE IF MORE CHARS LEFT IN BUFFER
5163 016554 002330 BGE 4$ ;BR TO ECHO INVALID INPUT
5164 016556 122762 000040 005272 CMPB #040, BUFFO(R2) ;SEE IF NEXT CHARACTER IS SPACE
5165 016564 001003 BNE 14$ ;BR IF NOT A SPACE
5166 016566 005202 INC R2 ;INCREMENT BUFFER POINTER
5167 016570 020200 CMP R2, R0 ;SEE IF MORE CHARS LEFT IN BUFFER
5168 016572 002321 BGE 4$ ;BR IF NONE LEFT
5169 016574 160200 14$: SUB R2, R0 ;SUBTRACT POINTER FROM CHAR COUNT
5170 016576 020027 000010 CMP R0, #10 ;SEE HOW MANY CHARS LEFT
5171 016602 003315 BGT 4$ ;BR IF TOO MANY LEFT
5172 ;CHECK FOR LEGAL PARAMETER VALUE
5173 016604 062702 005272 ADD #BUFFO, R2 ;GET ADDRESS OF DIGITS
5174 016610 010246 MOV R2, -(SP) ;PUT IT ON STACK FOR OCTBIN
5175 016612 004737 052430 JSR PC, OCTBIN ;CHECK DIGITS AND CONVERT TO BINARY
5176 016616 016436 4$ ;ERROR RETURN ADDR. FOR OCTBIN
5177 016620 012637 005476 MOV (SP)+, LOWOCT ;GET LOW BINARY BITS
5178 016624 013737 052562 005500 MOV $HIOCT, HIGOCT ;GET HIGH BINARY BITS
5179 016632 004737 031564 JSR PC, CHKPRM ;CHECK VALIDITY OF PARAMETER VALUE
5180 016636 016436 4$ ;ERROR RETURN ADDR. FOR CHKPRM
5181 ;OPEN USER-DEFINED PATTERN 15 FOR MODIFICATION, IF NECESSARY
5182 016640 004737 031240 JSR PC, MODP15
5183 016644 000662 BR SETPRM ;RETURN TO ASK FOR ANOTHER PARAMETER
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5184
5185
5186
5187      .SBTTL TO - RUN (R) TESTS ROUTINE
5188      ;*THIS ROUTINE IS ENTERED TO SET UP THE RUNNING OF TESTS,
5189      ;*WHOSE PARAMETERS HAVE BEEN DETERMINED. IT FIRST ASKS
5190      ;*FOR THE DESIRED NUMBER OF PROGRAM PASSES, AND STORES
5191      ;*THIS IN $EOPCT FOR THE END OF PASS ROUTINE. THEN, THE
5192      ;*DRIVE LIST IS SCANNED FOR THE FIRST DESIRED DRIVE, AND
5193      ;*THE HEADER ON SECTOR 0 IS READ TO GET THE FORMAT FOR THIS
5194      ;*DRIVE. THIS IS STORED IN THE FORMAT BYTE, AND A JUMP IS
5195      ;*MADE TO THE FIRST TEST.
5196      ;*THE END OF PASS ROUTINE RETURNS TO 'NEWDRV' TO SELECT
5197      ;*EACH SUCCESSIVE DRIVE FOR TESTING, UNTIL THE ENTIRE PASS
5198      ;* (ALL DRIVES) IS COMPLETED.
5199
5200      016646
5201      RUNTST:
5202      ;INPUT THE DESIRED NUMBER OF PROGRAM PASSES
5203      4$:      TYPE      ,ENTPAS      ;ASK FOR NUMBER OF PASSES
5204      JSR      PC,RDCHRS      ;READ RESPONSE
5205      DRVTST      ;(^C) RETURN ADDRESS
5206      ASKMDE      ;(^Z) RETURN ADDRESS
5207      4$      ;(^U) OR ERROR RETURN ADDRESS
5208      TST      RO      ;SEE IF NULL INPUT
5209      BEQ      6$      ;GO ASK AGAIN
5210      CMP      #5,RO      ;SEE HOW MANY CHARS TYPED
5211      BGE      8$      ;BR IF 5 OR LESS
5212      6$:      TYPE      ,BUFFO      ;ECHO BAD INPUT
5213      TYPE      ,SQUES
5214      BR      4$      ;GO ASK AGAIN
5215      8$:      MOV      #BUFFO,-(SP) ;GET BUF ADDR ON STACK FOR OCTBIN
5216      JSR      PC,OCTBIN      ;CHECK DIGITS AND CONVERT TO BINARY
5217      6$      ;ERROR RETURN ADDRESS FOR OCTBIN
5218      MOV      (SP)+,$EOPCT      ;SET DESIRED NO. OF PASSES (1-77777)
5219      BEQ      6$      ;BR IF 0, TO ASK AGAIN
5220      ;THIS IS THE ACTUAL START OF A RUN WITH X PASSES
5221      STPASS:  CLR      $PASS      ;INIT. THE PASS NUMBER
5222      MOV      #1,TSTING      ;SET 'RUNNING TESTS' FLAG
5223      NEWPAS:  MOV      #17777,DRIVE ;INITIALIZE DRIVE NO. TO -1
5224      CLR      $DEVCT      ;INIT APT DEVICE COUNT TO 0
5225      ;CHECK DRIVE LIST FOR MORE DRIVES TO TEST ON THIS PASS
5226      NEWDRV:  MOV      #PRO,@#PS      ;RE-ESTABLISH PRIORITY 0
5227      MOV      #STACK,SP      ;RESTORE THE STACK
5228      CLRB      DRVERS      ;CLEAR ERROR COUNT FOR CURRENT DRIVE
5229      CLR      INTCHR      ;CLEAR TTY INPUT BUFFER WORD
5230      INC      DRIVE      ;INCREMENT DRIVE NUMBER
5231      CMP      #10,DRIVE      ;SEE IF DONE WITH THIS PASS
5232      BNE      2$      ;BR IF NOT DONE CHECKING LIST
5233      JMP      DUNPAS      ;JUMP IF DONE WITH THIS PASS
5234      2$:      MOV      DRIVE,RO      ;GET NEW DRIVE NUMBER
5235      TSTB      DRVLST(RO)      ;SEE IF THIS DRIVE IS MARKED IN LIST
5236      BEQ      NEWDRV      ;BR IF NOT MARKED, TO CHECK ANOTHER
5237      CLRB      $STNM      ;INIT TEST NUMBER TO 0
5238      CLR      $TIMES      ;INIT ITERATION COUNT
5239      TSTB      DTYLST(RO)      ;SEE IF RK07
5240      BNE      1$      ;BR IF YES

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5240 017046 105037 003124          CLR8      TYPFMT          ;ELSE LOAD ALL RK06 PARAMETERS
5241 017052 012737 000625 017702    MOV       #625,LCM5
5242 017060 012737 000631 017704    MOV       #631,LCM1
5243 017066 012737 000631 006044    MOV       #631,PRMLIM+2
5244 017074 012737 000632 005734    MOV       #632,LC
5245 017102 012737 000632 017706    MOV       #632,LSTCYL
5246 017110 012737 000632 006000    MOV       #632,PRDFLT+2
5247 017116 012737 000632 006050    MOV       #632,PRMLIM+6
5248 017124 012737 000632 006054    MOV       #632,PRMLIM+12
5249 017132 012737 000633 017710    MOV       #633,LCP1
5250 017140 012737 000400 017712    MOV       #400,HOLD1
5251 017146 012737 001000 017714    MOV       #1000,HOLD2
5252 017154 012737 000025 017716    MOV       #25,HOLD3
5253 017162 012737 010025 017720    MOV       #10025,HOLD4
5254 017170 012737 010125 017722    MOV       #10125,HOLD5
5255 017176 012737 000002 017724    MOV       #2,HOLD6
5256 017204 012737 110244 017726    MOV       #110244,HOLD7
5257 017212 012737 017500 007216    MOV       #8000.,MAXLO2    ;8000 US FOR RK06
5258 017220 012737 000000 007220    MOV       #0,MAXHI2      ;
5259 017226 012737 022370 007222    MOV       #9464.,MAXLO4  ;75000 US FOR RK06
5260 017234 012737 000001 007224    MOV       #1,MAXHI4      ;
5261 017242 000477          BR        3$
5262
5263 017244 152737 000004 003124 1$:  BISB     #8.CDT,TYPFMT    ;LOAD ALL RK07 PARAMETERS
5264 017252 012737 001451 017702    MOV       #1451,LCM5
5265 017260 012737 001455 017704    MOV       #1455,LCM1
5266 017266 012737 001455 006044    MOV       #1455,PRMLIM+2
5267 017274 012737 001456 005734    MOV       #1456,LC
5268 017302 012737 001456 017706    MOV       #1456,LSTCYL
5269 017310 012737 001456 006000    MOV       #1456,PRDFLT+2
5270 017316 012737 001456 006050    MOV       #1456,PRMLIM+6
5271 017324 012737 001456 006054    MOV       #1456,PRMLIM+12
5272 017332 012737 001457 017710    MOV       #1457,LCP1
5273 017340 012737 001000 017712    MOV       #1000,HOLD1
5274 017346 012737 002000 017714    MOV       #2000,HOLD2
5275 017354 012737 002025 017716    MOV       #2025,HOLD3
5276 017362 012737 012025 017720    MOV       #12025,HOLD4
5277 017370 012737 012125 017722    MOV       #12125,HOLD5
5278 017376 012737 000012 017724    MOV       #12,HOLD6
5279 017404 012737 016104 017726    MOV       #16104,HOLD7
5280
5281 017412 012737 014544 007216    MOV       #6500.,MAXLO2   ;6500 US FOR RK07
5282 017420 012737 000000 007220    MOV       #0,MAXHI2      ;
5283 017426 012737 016450 007222    MOV       #7464.,MAXLO4  ;73000 US FOR RK07
5284 017434 012737 000001 007224    MOV       #1,MAXHI4      ;
5285 017442 010037 001332          MOV       R0,SUNIT      ;SET DRIVE NO FOR APT
5286          ;READ THE HEADER ON SECTOR 0,TRACK 0,CYL 0, AND GET THE DRIVE FORMAT
5287 017446 004737 027450          JSR      PC,INITSS      ;INIT. DRIVER PARAMS AND S.S.
5288 017452 112765 000125 000001    MOV8     #RDHEAD,P.CMND(R5) ;SET READ HEADER COMMAND
5289 017460 004737 040252          JSR      PC,DRVCAL      ;DO READ HEADER
5290 017464 013712 017716          MOV     HOLD3,(R2)      ;ISSUE RD HDR WITH FMT BIT - 0
5291 017470 032712 000200 4$:  BIT     #BIT7,(R2)      ;CHECK FOR C. READY IN CS1
5292 017474 001775          BEQ     4$              ;BR IF NOT RDY YET
5293 017476 013712 017720          MOV     HOLD4,(R2)      ;ISSUE RD HDR WITH FMT BIT = 1
5294 017502 032712 000200 6$:  BIT     #BIT7,(R2)      ;CHEC FOR C. READY IN CS1
5295 017506 001775          BEQ     6$              ;BR IF NOT RDY YET
```

5296	017510	105037	003125		CLRB	FORMAT	: INITIALIZE FORMAT BYTE TO 0
5297	017514	013737	005752	005516	MOV	S2,FS	: INIT SECTOR LIMITS FOR 22(DEC) SECTOR FMT
5298	017522	013737	005754	005520	MOV	S3,LS	
5299	017530	005762	000024		TST	RKDB(R2)	: POP SILO ONE TIME
5300	017534	032762	001000	000024	BIT	#BIT9,RKDB(R2)	: TEST FORMAT BIT IN HEADER WORD 2
5301	017542	001411			BEQ	8\$: BR IF 22 SECTOR FORMAT
5302	017544	152737	000020	003125	BISB	#B.CFMT,FORMAT	: SET 20 SECTOR FORMAT FOR THIS DRIVE
5303	017552	013737	005746	005516	MOV	S0,FS	: INIT SECTOR LIMITS FOR 20(DEC) SECTOR FMT
5304	017560	013737	005750	005520	MOV	S1,LS	
5305	017566	013737	005772	005512	MOV	8\$: ST,STALLS	: SET NUMBER OF UNIT STALLS DESIRED
5306	017574	004737	027450		JSR	PC,INITSS	: INIT THE S.S.
5307	017600	004737	036630		JSR	PC,REDBSF	: READ BAD SECTOR FILE FOR THIS DRIVE
5308	017604	113737	005510	011331	MOVB	DRIVE,DRVNO	: GET DRIVE NO.
5309	017612	152737	000060	011331	BISB	#'0,DRVNO	: CONVERT TO ASCII
5310	017620	104401	011312		TYPE	,TSTDRM	: TYPE 'TESTING DRIVE X'
5311	017624	005737	001326		TST	\$PASS	: SEE IF THIS IS FIRST PASS
5312	017630	001012			BNE	10\$: BR IF NOT FIRST PASS
5313	017632	004737	032070		JSR	PC,DRVSR	: TYPE 'DRIVE SER. NO. xxx'
5314	017636	004737	032210		JSR	PC,CRTSER	: TYPE 'CART. SER. NO. XXXXXXXXXXXX'
5315	017642	032737	000020	005770	BIT	#BIT4,CS	: SEE IF BAD SECTORS SHOULD BE TYPED
5316	017650	001402			BEQ	10\$: BR IF NOT
5317	017652	004737	037120		JSR	PC,TYPBSF	: TYPE BAD SECTOR FILES
5318	017656	005037	005532		CLR	INTCHR	: INIT. TTY INPUT CHAR BUFFER
5319	017662	105737	003136		TSTB	DULACS	: SEE IF DUAL-ACCESS FLAG SET
5320	017666	001420			BEQ	TST1	: BR IF NOT
5321	017670	112737	000021	001102	MOVB	#21,\$STNMP	: SET TEST NO.
5322	017676	000137	024444		JMP	RWDTST	: JUMP TO R/W DATA TEST
5323							
5324	017702	000000			LCM5:	0	: LAST CYL-5
5325	017704	000000			LCM1:	0	: LAST CYL-1
5326	017706	000000			LSTCYL:	0	: LAST CYL 632 FOR RK06, 1456 FOR RK07
5327	017710	000000			LCP1:	0	: LAST CYL+1
5328	017712	000000			HOLD1:	0	
5329	017714	000000			HOLD2:	0	
5330	017716	000000			HOLD3:	0	
5331	017720	000000			HOLD4:	0	
5332	017722	000000			HOLD5:	0	
5333	017724	000000			HOLD6:	0	
5334	017726	000000			HOLD7:	0	
5335							
5336							
5337							
5338							


```
5339 .....  
5340 *TEST 1 RECALIBRATE/SEEK TEST  
5341 *  
5342 * THIS TEST PERFORMS A RECALIBRATE, FOLLOWED BY A  
5343 * SEEK TO CYLINDER LC.  
5344 *  
5345 .....  
5346 TST1: SCOPE  
5347 017730 000004 MOV #1,$TESTN ;;SET TEST NUMBER IN APT MAIL BOX  
5348 017732 012737 000001 001324 JSR PC,SETUP ;SET UP FOR LOOP ON ERROR  
5349 017740 004737 030416 JSR PC,CHKITR ;SEE IF ITER. NO. = 0 FOR THIS TEST  
5350 017744 004737 030464 JMP TST2 ;JUMP TO NEXT TEST  
5351 017750 000137 020010 ;RETURN HERE FROM CHKITR IF TEST SHOULD BE RUN  
5352 017754 004737 027450 JSR PC,INITSS ;INITIALIZE DRIVER PARAMS AND SUB-SYS  
5353 017760 004737 025766 JSR PC,PREPKB ;PREPARE FOR POSSIBLE KBD INPUT  
5354 ;RECALIBRATE THE DRIVE  
5355 017764 112765 000113 000001 MOVB #RECAL,P.CMND(R5) ;SET RECAL COMMAND  
5356 017772 004737 040252 JSR PC,DRVCAL ;RECALIBRATE DRIVE  
5357 ;DO SEEK TO CYLINDER LC  
5358 017776 013765 005734 000002 MOV LC,P.CYLN(R5) ;SET CYLINDER = LC  
5359 020004 004737 032330 JSR PC,SEEKER ;DO A READ HEADER OR EXPLICIT SEEK  
5360  
5361  
5362
```

```
5363 .....  
5364 *TEST 2 SEEK/SEEK TEST  
5365 *  
5366 * THIS TEST PERFORMS A SEEK TO CYLINDER FC, FOLLOWED  
5367 * BY A SEEK TO CYLINDER LC.  
5368 *  
5369 .....  
5370 TST2: SCOPE  
5371 020010 000004 MOV #2,$TESTN ;;SET TEST NUMBER IN APT MAIL BOX  
5372 020012 012737 000002 001324 JSR PC,SETUP ;SET UP FOR LOOP ON ERROR  
5373 020020 004737 030416 JSR PC,CHKITR ;SEE IF ITER. NO. = 0 FOR THIS TEST  
5374 020024 004737 030464 JMP TST3 ;JUMP TO NEXT TEST  
5375 020030 000137 020070 ;RETURN HERE FROM CHKITR IF TEST SHOULD BE RUN  
5376 020034 004737 027450 JSR PC,INITSS ;INITIALIZE DRIVER PARAMS AND SUB-SYS  
5377 020040 004737 025766 JSR PC,PREPKB ;PREPARE FOR POSSIBLE KBD INPUT  
5378 ;DO SEEK TO CYLINDER FC  
5379 020044 013765 005732 000002 MOV FC,P.CYLN(R5) ;SET CYL = FC  
5380 020052 004737 032330 JSR PC,SEEKER ;SEEK TO FC  
5381 ;DO SEEK TO CYLINDER LC  
5382 020056 013765 005734 000002 MOV LC,P.CYLN(R5) ;SET CYL = LC  
5383 020064 004737 032330 JSR PC,SEEKER ;SEEK TO LC  
5384  
5385
```

```
5386 .....  
5387 *TEST 3 CYLINDER ADDRESSING TEST  
5388 *  
5389 * THIS TEST VERIFIES THE CYLINDER ADDRESS BITS, BY  
5390 * SEEKING TO CYLINDERS 0,1,2,4,10,20,40,100,200,400(OCT).  
5391 * THEN, THE SEEKS ARE DONE IN REVERSE, TO 400,200,100,  
5392 * 40,20,10,4,2,1,0.  
5393 *  
5394
```

```
5395  
5396 020070 000004  
5397 020072 012737 000003 001324  
5398 020100 004737 030416  
5399 020104 004737 030464  
5400 020110 000137 020176  
5401  
5402 020114 004737 027450  
5403 020120 004737 025766  
5404  
5405 020124 005065 000002  
5406 020130 004737 032330  
5407  
5408 020134 005265 000002  
5409 02 140 004737 032330  
5410 020144 006365 000002  
5411 020150 033765 017714 000002  
5412 020156 001770  
5413  
5414 020160 006265 000002  
5415 020164 004737 032330  
5416 020170 005765 000002  
5417 020174 001371  
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5443  
5444 020176 000004  
5445 020200 012737 000004 001324  
5446 020206 004737 030416  
5447 020212 004737 030464  
5448 020216 000137 020342  
5449  
5450 020222 004737 027450
```

TST3: SCOPE
MOV #3,STESTN ;:SET TEST NUMBER IN APT MAIL BOX
JSR PC,SETUP ;:SET UP FOR LOOP ON ERROR
JSR PC,CHKITR ;:SEE IF ITER. NO. = 0 FOR THIS TEST
JMP TST4 ;:JUMP TO NEXT TEST
;RETURN HERE FROM CHKITR IF TEST SHOULD BE RUN
JSR PC,INITSS ;:INITIALIZE DRIVER PARAMS AND SUB-SYS
JSR PC,PREPKB ;:PREPARE FOR POSSIBLE KBD INPUT
;SEEK TO CYLINDER 0
CLR P.CYLN(R5) ;:SET CYL = 0
JSR PC,SEEKER ;:SEEK TO CYL 0
;SEEK TO CYLINDERS 1,2,4,10,20,40,100,200,400 (FORWARD DIRECTION)
INC P.CYLN(R5) ;:INITIALIZE CYL TO 1
2\$: JSR PC,SEEKER ;:SEEK TO CURRENT CYL
ASL P.CYLN(R5) ;:GET NEXT CYL NO.
5411 BIT HOLD2,P.CYLN(R5) ;:SEE IF DONE WITH FORWARD SEEKS
5412 BEQ 2\$;:BR IF NOT DONE YET
;SEEK TO CYLINDERS 400,200,100,40,20,10,4,2,1,0 (REVERSE DIRECTION)
4\$: ASR P.CYLN(R5) ;:GET NEXT CYL NO.
JSR PC,SEEKER ;:SEEK TO CURRENT CYLINDER
TST P.CYLN(R5) ;:SEE IF DONE IN REVERSE DIRECTION
BNE 4\$;:BR IF NOT DONE YET

```
*****  
*TEST 4 CYLINDER ADDRESS CROSSTALK TEST  
*  
* THIS TEST PERFORMS SEEKS TO CYLINDERS WHOSE ADDRESSES  
* ARE SUSCEPTIBLE TO BIT CROSSTALK WITHIN THE CYLINDER  
* ADDRESSING LOGIC. THE CYLINDER ADDRESS BITS SEQUENCE  
* AS FOLLOWS :  
* 000 000 000  
* 011 111 111  
* 000 000 000  
* 011 111 110  
* 000 000 000  
* 011 111 101  
* 000 000 000  
* :  
* :  
* 000 000 000  
* 011 111 111  
* 000 000 000  
* 101 111 111  
*****
```

```
5444 020176 000004  
5445 020200 012737 000004 001324  
5446 020206 004737 030416  
5447 020212 004737 030464  
5448 020216 000137 020342  
5449  
5450 020222 004737 027450
```

TST4: SCOPE
MOV #4,STESTN ;:SET TEST NUMBER IN APT MAIL BOX
JSR PC,SETUP ;:SET UP FOR LOOP ON ERROR
JSR PC,CHKITR ;:SEE IF ITER. NO. = 0 FOR THIS TEST
JMP TST5 ;:JUMP TO NEXT TEST
;RETURN HERE FROM CHKITR IF TEST SHOULD BE RUN
JSR PC,INITSS ;:INITIALIZE DRIVER PARAMS AND SUB-SYS

```
5451 020226 004737 025766 JSR PC,PREPKB ;PREPARE FOR POSSIBLE KBD INPUT
5452 ;SEEK TO CYL 0
5453 020232 005065 000002 CLR P,CYLN(R5) ;SET CYL = 0
5454 020236 004737 032330 JSR PC,SEEKER ;SEEK TO CYLINDER 0
5455 ;SEEK TO CYL 377 (OCT)
5456 020242 012765 000377 000002 MOV #377,P,CYLN(R5) ;SET CYL = 377
5457 020250 004737 032330 JSR PC,SEEKER ;SEEK TO CYL 377
5458 ;SEEK TO REMAINING CROSSTALK CYLINDERS
5459 020254 012737 000001 005514 MOV #1,CYLNDR ;INITIALIZE BIT MASK
5460 020262 005065 000002 2$: CLR P,CYLN(R5) ;SET CYL = 0
5461 020266 004737 032330 JSR PC,SEEKER ;SEEK TO CYL 0
5462 020272 032737 000200 005514 BIT #BIT07,CYLNDR ;SEE IF ABOUT TO DO LAST SEEK
5463 020300 001013 BNE 4$ ;BR IF THIS WILL BE LAST SEEK
5464 020302 012765 000377 000002 MOV #377,P,CYLN(R5) ;SET CYL = 377
5465 020310 043765 005514 000002 BIC CYLNDR,P,CYLN(R5) ;ZERO A BIT TO PROMOTE CROSSTALK
5466 020316 004737 032330 JSR PC,SEEKER ;SEEK TO THIS CYLINDER
5467 020322 006337 005514 ASL CYLNDR ;SHIFT BIT MASK
5468 020326 000755 BR 2$ ;GO SEEK TO CYL 0 AGAIN
5469 020330 012765 000577 000002 4$: MOV #577,P,CYLN(R5) ;SET CYL = 577
5470 020336 004737 032330 JSR PC,SEEKER ;SEEK TO CYL 577
5471
5472
5473
5474
5475 *****
5476 *TEST 5 INCREMENT/DECREMENT SEEK TEST
5477 *
5478 * IN THIS TEST SEEKS ARE DONE IN INCREMENTS OF 1C CYLINDERS
5479 * STARTING AT CYL FC, AND ENDING AT OR BEYOND LC. THEN, THE
5480 * SEEKS ARE DONE IN REVERSE, BACK TO FC. IF FC IS CHOSEN > LC,
5481 * THE SEEKS WILL PROCEED IN THE OPPOSITE DIRECTION.
5482 *****
5483 020342 000004 TST5: SCOPE
5484 020344 012737 000005 001324 MOV #5,$TESTN ;:SET TEST NUMBER IN APT MAIL BOX
5485 020352 004737 030416 JSR PC,SETUP ;SET UP FOR LOOP ON ERROR
5486 020356 004737 030464 JSR PC,CHKITR ;SEE IF ITER. NO. = 0 FOR THIS TEST
5487 020362 000137 020546 JMP TST6 ;JUMP TO NEXT TEST
5488 ;RETURN HERE FROM CHKITR IF TEST SHOULD BE RUN
5489 020366 004737 027450 JSR PC,INITSS ;INITIALIZE DRIVER PARAMS AND SUB-SYS
5490 020372 004737 025766 JSR PC,PREPKB ;PREPARE FOR POSSIBLE KBD INPUT
5491 ;SEEK IN INCREMENTS, FROM FC TO LC
5492 020376 013737 005732 005522 MOV FC,NCYL1 ;INITIALIZE NCYL1 TO FC
5493 020404 013765 005522 000002 2$: MOV NCYL1,P,CYLN(R5) ;SET CYL = NCYL1
5494 020412 004737 032330 JSR PC,SEEKER ;SEEK TO NCYL1
5495 020416 023737 005732 005734 CMP FC,LC ;SEE IF FC > LC
5496 020424 003010 BGT 4$ ;BR IF FC > LC
5497 020426 063737 005736 005522 ADD 1C,NCYL1 ;INCREMENT NCYL1
5498 020434 023737 005734 005522 CMP LC,NCYL1 ;SEE IF LC EXCEEDED
5499 020442 002360 BGE 2$ ;BR IF NOT YET
5500 020444 000407 BR 6$ ;LC REACHED - DO REVERSE SEEKS
5501 020446 163737 005736 005522 4$: SUB 1C,NCYL1 ;DECREMENT NCYL1
5502 020454 023737 005734 005522 CMP LC,NCYL1 ;SEE IF LC EXCEEDED
5503 020462 003750 BLE 2$ ;BR IF NOT YET
5504 ;SEEK IN INCREMENTS, BACK TO FC
5505 020464 023737 005732 005734 6$: CMP FC,LC ;SEE IF FC > LC
5506 020472 003010 BGT 8$ ;BR IF FC > LC
```

```
5507 020474 163737 005736 005522 SUB IC,NCYL1 ;DECREMENT NCYL1
5508 020502 023737 005732 005522 CMP FC,NCYL1 ;SEE IF FC EXCEEDED
5509 020510 003410 BLE 10$ ;BR IF NOT YET
5510 020512 006415 BR 12$ ;FC REACHED - ALL DONE
5511 020514 063737 005736 005522 8$: ADD IC,NCYL1 ;INCREMENT NCYL1
5512 020522 023737 005732 005522 CMP FC,NCYL1 ;SEE IF FC EXCEEDED
5513 020530 002406 BLT 12$ ;FC REACHED - ALL DONE
5514 020532 013765 005522 000002 10$: MOV NCYL1,P.CYLN(R5) ;SET CYL = NCYL1
5515 020540 004737 032330 JSR PC,SEEKER ;SEEK TO NCYL1
5516 020544 000747 BR 6$ ;CONTINUE
5517 020546 12$:
5518
5519
5520
5521 *****
5522 :*TEST 6 OSCILLATING SEEK TEST
5523 :*
5524 :* IN THIS TEST, SEEKS OF INCREASING LENGTH ARE DONE FROM FC
5525 :* TO NCYL1, AND BACK TO FC. NCYL1 IS INCREMENTED BY IC AFTER
5526 :* EACH SEEK, AND THE SEEKS TO NCYL1 ALWAYS PROCEED FROM FC
5527 :* TOWARD LC (FC > LC IS PERMISSIBLE). WHEN LC IS REACHED OR
5528 :* EXCEEDED, THE SAME SEEKS ARE DONE IN REVERSE ORDER, UNTIL
5529 :* NCYL1 EXCEEDS FC.
5530 *****
5531 020546 000004 TST6: SCOPE
5532 020550 012737 000006 001324 MOV #6,$TESTN ;:SET TEST NUMBER IN APT MAIL BOX
5533 020556 004737 030416 JSR PC,SETUP ;:SET UP FOR LOOP ON ERROR
5534 020562 004737 030464 JSR PC,CHKTR ;:SEE IF ITER. NO. = 0 FOR THIS TEST
5535 020566 000137 020776 JMP TST7 ;:JUMP TO NEXT TEST
5536 :RETURN HERE FROM CHKTR IF TEST SHOULD BE RUN
5537 020572 004737 027450 JSR PC,INITSS ;:INITIALIZE DRIVER PARAMS AND SUB-SYS
5538 020576 004737 025766 JSR PC,PREPKB ;:PREPARE FOR POSSIBLE KBD INPUT
5539
5540 :DO OSCILLATING SEEKS TOWARD LC
5541 020602 013737 005732 005522 MOV FC,NCYL1 ;:INITIALIZE NCYL1 TO FC
5542 020610 013765 005522 000002 2$: MOV NCYL1,P.CYLN(R5) ;:SET CYL = NCYL1
5543 020616 004737 032330 JSR PC,SEEKER ;:SEEK TO NCYL1
5544 020622 013765 005732 000002 MOV FC,P.CYLN(R5) ;:SET CYL = FC
5545 020630 004737 032330 JSR PC,SEEKER ;:SEEK TO FC
5546 020634 023737 005732 005734 CMP FC,LC ;:SEE IF FC > LC
5547 020642 003010 BGT 4$ ;:BR IF FC > LC
5548 020644 063737 005736 005522 ADD IC,NCYL1 ;:INCREMENT NCYL1
5549 020652 023737 005734 005522 CMP LC,NCYL1 ;:SEE IF LC EXCEEDED
5550 020660 002353 BGE 2$ ;:BR IF NOT YET
5551 020662 000407 BR 6$ ;:LC REACHED - DO REVERSE SEEKS
5552 020664 163737 005736 005522 4$: SUB IC,NCYL1 ;:DECREMENT NCYL1
5553 020672 023737 005734 005522 CMP LC,NCYL1 ;:SEE IF LC EXCEEDED
5554 020700 003743 BLE 2$ ;:BR IF NOT YET
5555 :DO OSCILLATING SEEKS TOWARD FC
5556 020702 023737 005732 005734 6$: CMP FC,LC ;:SEE IF FC > LC
5557 020710 003010 BGT 8$ ;:BR IF FC > LC
5558 020712 163737 005736 005522 SUB IC,NCYL1 ;:DECREMENT NCYL1
5559 020720 023737 005732 005522 CMP FC,NCYL1 ;:SEE IF FC EXCEEDED
5560 020726 003410 BLE 10$ ;:BR IF NOT YET
5561 020730 000422 BR 12$ ;:FC REACHED - ALL DONE
5562 020732 063737 005736 005522 8$: ADD IC,NCYL1 ;:INCREMENT NCYL1
5563 020740 023737 005732 005522 CMP FC,NCYL1 ;:SEE IF FC EXCEEDED
```

```
5563 020746 002413          BLT      12$          :FC REACHED - ALL DONE
5564 020750 013765 005522 000002 10$:  MOV      NCYL1,P.CYLN(R5) :SET CYL = NCYL1
5565 020756 004737 032330          JSR      PC,SEEKER    :SEEK TO NCYL1
5566 020762 013765 005732 000002  MOV      FC,P.CYLN(R5) :SET CYL = FC
5567 020770 004737 032330          JSR      PC,SEEKER    :SEEK TO FC
5568 020774 000742          BR       6$          :CONTINUE
5569 020776          12$:
```

5570
5571

```
5572 :*****
5573 :*TEST 7          CONVERGING/DIVERGING SEEK TEST
5574 :*
```

```
5575 :* THIS TEST PERFORMS SEEKS WHICH EXERCISE CYLINDER DIFFERENCE
5576 :* VALUES. INITIALLY, NCYL1 = FC AND NCYL2 = LC. A SEEK IS
5577 :* DONE TO NCYL1 AND THEN TO NCYL2. THEN, NCYL1 IS INCREMENTED
5578 :* BY IC IN THE DIRECTION OF LC, AND NCYL2 IS INCREMENTED BY
5579 :* IC IN THE DIRECTION OF FC, AND SEEKS ARE DONE TO NCYL1
5580 :* AND NCYL2 AGAIN. THIS SEEKING IS CONTINUED WITH INCREMENTED
5581 :* VALUES OF NCYL1 AND NCYL2, UNTIL NCYL1 EXCEEDS LC AND NCYL2
5582 :* EXCEEDS FC. (NOTE : FC > LC IS PERMISSIBLE.)
5583 :* THIS TEST CAUSES SEEKING TO CONVERGING AND THEN DIVERGING
5584 :* CYLINDER VALUES.
5585 :*
```

```
5586 :*****
5587 :*TEST 7: SCOPE
```

```
5588 021000 012737 000007 001324  MOV      #7,$TESTN    ;;SET TEST NUMBER IN APT MAIL BOX
5589 021006 004737 030416          JSR      PC,SETUP     :SET UP FOR LOOP ON ERROR
5590 021012 004737 030464          JSR      PC,CHKITR   :SEE IF ITER. NO. = 0 FOR THIS TEST
5591 021016 000137 021154          JMP      TST10       :JUMP TO NEXT TEST
5592 :RETURN HERE FROM CHKITR IF TEST SHOULD BE RUN
5593 021022 004737 027450          JSR      PC,INITSS   :INITIALIZE DRIVER PARAMS AND SUB-SYS
5594 021026 004737 025766          JSR      PC,PREPKB   :PREPARE FOR POSSIBLE KBD INPUT
5595 021032 013737 005732 005522  MOV      FC,NCYL1    :SET NCYL1 = FC
5596 021040 013737 005734 005524  MOV      LC,NCYL2    :SET NCYL2 = LC
5597 021046 013765 005522 000002 2$:  MOV      NCYL1,P.CYLN(R5) :SET CYL = NCYL1
5598 021054 004737 032330          JSR      PC,SEEKER   :SEEK TO NCYL1
5599 021060 013765 005524 000002  MOV      NCYL2,P.CYLN(R5) :SET CYL = NCYL2
5600 021066 004737 032330          JSR      PC,SEEKER   :SEEK TO NCYL2
5601 021072 023737 005732 005734  CMP      FC,LC       :SEE IF FC > LC
5602 021100 003013          BGT      4$          :BR IF FC > LC
5603 021102 063737 005736 005522  ADD      IC,NCYL1    :INCREMENT NCYL1
5604 021110 163737 005736 005524  SUB      IC,NCYL2    :DECREMENT NCYL2
5605 021116 023737 005734 005522  CMP      LC,NCYL1    :SEE IF LIMITS EXCEEDED
5606 021124 002350          BGE      2$          :BR IF NOT YET
5607 021126 000412          BR       6$          :LIMITS REACHED - ALL DONE
5608 021130 163737 005736 005522 4$:  SUB      IC,NCYL1    :DECREMENT NCYL1
5609 021136 063737 005736 005524  ADD      IC,NCYL2    :INCREMENT NCYL2
5610 021144 023737 005734 005522  CMP      LC,NCYL1    :SEE IF LIMITS EXCEEDED
5611 021152 003735          BLE      2$          :BR IF NOT YET
5612 021154          6$:
```

5613
5614

```
5615 :*****
5616 :*TEST 10         PSEUDO-RANDOM SEEK TEST
5617 :*
```

5618

5619 : * THIS TEST PERFORMS A SEEK TO A PSEUDO-RANDOMLY CHOSEN
5620 : * CYLINDER, WHICH IS WITHIN THE RANGE (0-632).
5621 : *
5622 : *****
5623 021154 000004 TST10: SCOPE
5624 021156 012737 000010 001324 MOV #10,\$TESTN ;;SET TEST NUMBER IN APT MAIL BOX
5625 021164 004737 030416 JSR PC,SETUP ;;SET UP FOR LOOP ON ERROR
5626 021170 004737 030464 JSR PC,CHKITR ;;SEE IF ITER. NO. = 0 FOR THIS TEST
5627 021174 000137 021226 JMP TST11 ;;JUMP TO NEXT TEST
5628 ;RETURN HERE FROM CHKITR IF TEST SHOULD BE RUN
5629 021200 004737 027450 JSR PC,INITSS ;;INITIALIZE DRIVER PARAMS AND SUB-SYS
5630 021204 004737 025766 JSR PC,PREPKB ;;PREPARE FOR POSSIBLE KBD INPUT
5631 021210 004737 032510 JSR PC,RNDADR ;;SELECT A PSEUDO-RANDOM CYLINDER
5632 021214 013765 005514 000002 MOV CYLDR,P.CYLN(R5) ;;SET CYL VALUE
5633 021222 004737 032330 JSR PC,SEEKER ;;SEEK TO THIS CYLINDER
5634
5635
5636
5637

: *TEST 11 MAXIMUM VELOCITY REVERSAL SEEK TEST
: *

: * THIS TEST PERFORMS A SEEK TO CYLINDER 0, FOLLOWED BY A
: * SEEK TO CYL 201 (OCT), AND THEN A RETURN SEEK TO 0. THIS
: * OPERATION REQUIRES THE HEADS TO DECELERATE UPON REACHING
: * THE POINT OF MAXIMUM VELOCITY, AND INDUCES HEATING IN
: * THE SERVO MECHANISM.
: *

5646 : *****
5647 021226 000004 TST11: SCOPE
5648 021230 012737 000011 001324 MOV #11,\$TESTN ;;SET TEST NUMBER IN APT MAIL BOX
5649 021236 004737 030416 JSR PC,SETUP ;;SET UP FOR LOOP ON ERROR
5650 021242 004737 030464 JSR PC,CHKITR ;;SEE IF ITER. NO. = 0 FOR THIS TEST
5651 021246 000137 021314 JMP TST12 ;;JUMP TO NEXT TEST
5652 ;RETURN HERE FROM CHKITR IF TEST SHOULD BE RUN
5653 021252 004737 027450 JSR PC,INITSS ;;INITIALIZE DRIVER PARAMS AND SUB-SYS
5654 021256 004737 025766 JSR PC,PREPKB ;;PREPARE FOR POSSIBLE KBD INPUT
5655 021262 005065 000002 CLR P.CYLN(R5) ;;SET CYL = 0
5656 021266 004737 032330 JSR PC,SEEKER ;;SEEK TO CYL 0
5657 021272 012765 000201 000002 MOV #201,P.CYLN(R5) ;;SET CYL = 201 (OCT)
5658 021300 004737 032330 JSR PC,SEEKER ;;SEEK TO CYL 201
5659 021304 005065 000002 CLR P.CYLN(R5) ;;SET CYL = 0
5660 021310 004737 032330 JSR PC,SEEKER ;;SEEK TO CYL 0
5661
5662
5663

: *TEST 12 MECHANICAL VIBRATION SEEK TEST
: *

: * THIS TEST PERFORMS GEOMETRICALLY INCREASING SEEKS WITH
: * GEOMETRICALLY DECREASING STALL TIME BETWEEN OPERATIONS
: * WITH INTENT TO INDUCE VARYING VIBRATIONAL MODES UPON
: * THE DRIVE. THE TEST BEGINS WITH LC = 1, AND ST = SM. THEN,
: * THE FOLLOWING SEQUENCE IS PERFORMED :
: * SEEKS ARE DONE BETWEEN 0 AND LC, TEN TIMES. THEN, ST IS
: * DIVIDED BY 2 AND LC IS DOUBLED, AND SEEKS ARE DONE BETWEEN
: * 0 AND LC AGAIN, TEN TIMES. THIS PROCESS IS CONTINUED FOR NEW
: *

5674

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5675          : *      VALUES OF ST AND LC, UNTIL LC EXCEEDS CYL 400 (OCT). THEN,  
5676          : *      THE WHOLE PROCESS IS REVERSED, WITH ST BEING DOUBLED AND  
5677          : *      LC DIVIDED BY 2, UNTIL LC BECOMES < 1.  
5678          : *  
5679          : *****  
5680 021314 000004 TST12: SCOPE  
5681 021316 012737 000012 001324 MOV #12,$TESTN ;;SET TEST NUMBER IN APT MAIL BOX  
5682 021324 004737 03C416 JSR PC,SETUP ;;SET UP FOR LOOP ON ERROR  
5683 021330 004737 030464 JSR PC,CHKITR ;;SEE IF ITER. NO. = 0 FOR THIS TEST  
5684 021334 000137 021554 JMP TST13 ;;JUMP TO NEXT TEST  
5685          :RETURN HERE FROM CHKITR IF TEST SHOULD BE RUN  
5686 021340 004737 027450 JSR PC,INITSS ;;INITIALIZE DRIVER PARAMS AND SUB-SYS  
5687 021344 004737 025766 JSR PC,PREPKB ;;PREPARE FOR POSSIBLE KBD INPUT  
5688 021350 012737 000001 005514 MOV #1,CYLNDR ;;INIT. CURRENT CYL TO 1  
5689 021356 013737 005774 001276 MOV SM,$TMP6 ;;INIT. STALLS TO SM  
5690          :DO INCREASING SEEKS  
5691 021364 012737 000012 005542 2$: MOV #10.,SCRACH ;;INIT. LOOP COUNTER TO 10(DEC)  
5692 021372 013737 001276 005512 MOV $TMP6,STALLS ;;SET CURRENT STALL IN STALL WORD  
5693 021400 005065 000002 4$: CLR P.CYLN(R5) ;;SET CYL = 0  
5694 021404 004737 032330 JSR PC,SEEKER ;;SEEK TO CYL 0  
5695 021410 013765 005514 000002 MOV CYLNDR,P.CYLN(R5) ;;SET CURRENT CYLINDER  
5696 021416 004737 032330 JSR PC,SEEKER ;;SEEK TO CURRENT CYLINDER  
5697 021422 005337 005542 DEC SCRACH ;;DECREMENT LOOP COUNTER  
5698 021426 001364 BNE 4$ ;;BR IF NOT 5 TIMES YET  
5699 021430 000241 CLC ;;CLEAR GARBAGE BIT  
5700 021432 006037 001276 ROR $TMP6 ;;DIVIDE STALLS BY 2  
5701 021436 006037 001300 ROR $TMP7 ;;SAVE THE DROPPED STALL BIT  
5702 021442 006337 005514 ASL CYLNDR ;;DOUBLE THE CURRENT CYLINDER NUMBER  
5703 021446 032737 001000 005514 BIT #BIT09,CYLNDR ;;SEE IF DONE WITH INCREASING SEEKS YET  
5704 021454 001743 BEQ 2$ ;;BR IF NOT DONE YET  
5705          :DO DECREASING SEEKS  
5706 021456 006237 005514 6$: ASR CYLNDR ;;DIVIDE CURRENT CYLINDER BY 2  
5707 021462 006137 001300 ROL $TMP7 ;;GET A SAVED STALL BIT INTO CARRY  
5708 021466 006137 001276 ROL $TMP6 ;;DOUBLE THE STALL  
5709 021472 012737 000012 005542 MOV #10.,SCRACH ;;INIT. LOOP COUNTER TO 10(DEC)  
5710 021500 013737 001276 005512 MOV $TMP6,STALLS ;;SET CURRENT STALL IN STALL WORD  
5711 021506 005065 000002 8$: CLR P.CYLN(R5) ;;SET CYL = 0  
5712 021512 004737 032330 JSR PC,SEEKER ;;SEEK TO CYL 0  
5713 021516 013765 005514 000002 MOV CYLNDR,P.CYLN(R5) ;;SET CURRENT CYL NUMBER  
5714 021524 004737 032330 JSR PC,SEEKER ;;SEEK TO CURRENT CYLINDER  
5715 021530 005337 005542 DEC SCRACH ;;DECREMENT LOOP COUNTER  
5716 021534 001364 BNE 8$ ;;BR IF NOT 5 TIMES YET  
5717 021536 032737 000002 005514 BIT #BIT01,CYLNDR ;;SEE IF DONE YET  
5718 021544 001744 BEQ 6$ ;;BR IF NOT DONE YET  
5719 021546 013737 005772 005512 MOV ST,STALLS ;;RESTORE DESIRED NO. OF UNIT STALLS  
5720  
5721  
5722  
5723          : *****  
5724          : *TEST 13 MAX ROTATIONAL LATENCY MEASUREMENT  
5725          : *THIS TEST MEASURES THE INTERVAL OF TIME BETWEEN 2 INDEX MARKS  
5726          : *IN A SINGLE DISK ROTATION. THE SPECIFIED MAXIMUM ROTATIONAL  
5727          : *LATENCY = 25 MILLI-SEC + OR - 2.5%. THIS MEASUREMENT IS MADE  
5728          : * 128 TIMES.  
5729          : *****  
5730 021554 000004 TST13: SCOPE
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5731 021556 012737 000013 001324      MOV    #13,$TESTN      ;;SET TEST NUMBER IN APT MAIL BOX
5732 021564 004737 030416      JSR    PC,SETUP        ;;SET UP FOR LOOP ON ERROR
5733 021570 004737 030464      JSR    PC,CHKITR      ;;SEE IF ITER. NO. = 0 FOR THIS TEST
5734 021574 000137 022366      JMP    TST14          ;;JUMP TO NEXT TEST
5735                                ;RETURN HERE FROM CHKITR IF TEST SHOULD BE RUN
5736 021600 004737 027450      JSR    PC,INITSS      ;;INITIALIZE DRIVER PARAMS AND SUB-SYS
5737 021604 004737 025766      JSR    PC,PREPKB     ;;PREPARE FOR POSSIBLE KBD INPUT
5738 021610 105737 003133      TSTB   DOTIM         ;;SEE IF TIMING TESTS ARE ALLOWED
5739 021614 001002                BNE    26$           ;;BR IF TEST ALLOWED
5740 021616 000177 000004      JMP    @1$           ;;SKIP TO NEXT TEST
5741 021622 004737 032646      26$: JSR    PC,CALBRT    ;;CALIBRATE SOFTWARE TIMER
5742 021626                                1$:
5743 021626 022366                TST14                ;ERROR RETURN ADDR FOR CALBRT
5744 02.630 004737 033140      JSR    PC,INIVRB     ;;INIT VARIABLES TO BE USED
5745 021634 105037 003120      CLRB   TSTING        ;INHIBIT ^C, ^Z ESCAPE
5746 021640 005003                CLR    R3            ;INIT MEASUREMENT COUNTER
5747                                ;READ HEADER TO INITIALIZE FORMAT BIT IN CONTROLLER
5748 021642 142765 000020 000007      BICB   #B.CFMT,P.CS1H(R5) ;SET FORMAT BIT = 0
5749 021650 112765 000125 000001      MOVB   #RDHEAD,P.CMND(R5) ;SET READ HDR COMMAND
5750 021656 004737 040252                JSR    PC,DRVCAL     ;DO READ HDR TO SET FORMAT TO 0
5751 021662 112737 000001 003120      MOVB   #1,TSTING     ;ALLOW ^C, ^Z ESCAPE
5752                                ;CHANGE FORMAT AND READ HDR TO FIND INDEX
5753 021670 005000                                2$: CLR    R0            ;INIT RK06 INTR INDICATOR
5754 021672 012777 022350 161140      MOV    #RHEDHD,@RKVEC ;SET SPECIAL RK06 HANDLER ADDRESS
5755 021700 005001                CLR    R1            ;INIT TIME-OUT INDICATOR
5756 021702 152765 000020 000007      BISB   #B.CFMT,P.CS1H(R5) ;COMPLEMENT FORMAT BIT TO 1
5757 021710 052765 010000 000016      BIS    #CFMT,P.CS1(R5) ;COMPLEMENT FORMAT BIT TO 1
5758 021716 004737 040542                JSR    PC,STRCMD     ;STORE PREV AND CURRENT COMMANDS
5759 021722 013712 017722                MOV    HOLD5,(R2)   ;ISSUE RD HDR WITH FMT BIT = 1
5760 021726 005700                                6$: TST    R0            ;SEE IF RK06 INTR REC'D YET
5761 021730 001012                BNE    8$           ;BR IF RK06 INTR REC'D
5762 021732 005201                INC    R1            ;INCREMENT TIME-OUT INDICATOR
5763 021734 001374                BNE    6$           ;BR IF NO TIME-OUT
5764 021736 004737 041650                                7$: JSR    PC,REPSUP    ;GET COMMAND FOR REPORT
5765 021742 004737 044316                JSR    PC,TOPROC     ;GATHER STATUS
5766 021746 104105                ERROR  10$          ;TIMED-OUT ON READ HEADER
5767 021750 000572                BR     36$          ;GO TO EXIT
5768 021752 104410                                28$: RESREG                ;RESTORE R0-R5
5769 021754 000770                BR     7$           ;TAKE ERROR EXIT
5770                                ;CHANGE FORMAT AND READ HDR TO MEASURE TIME TO NEXT INDEX
5771 021756 005000                                8$: CLR    R0            ;INIT RK06 INTERRUPT INDICATOR
5772 021760 005004                CLR    R4            ;INIT HI CYCLE COUNT
5773 021762 010501                MOV    R5,R1         ;SAVE PARAM BLK ADDRESS
5774 021764 010537 005542                MOV    R5,SCRACH
5775 021770 005005                CLR    R5
5776 021772 142761 000020 000007      BICB   #B.CFMT,P.CS1H(R1) ;COMPLEMENT FORMAT BIT TO 0
5777 022000 042761 010000 000016      BIC    #CFMT,P.CS1(R1) ;COMPLEMENT FORMAT BIT TO 0
5778 022006 016162 000016 000000      MOV    P.CS1(R1),RKCS1(R2) ;ISSUE READ HDR TO FIND NEXT INDEX
5779 022014 004737 033104                JSR    PC,TIMER      ;MEASURE THE TIME TO NEXT INDEX
5780 022020 021736                7$                ;ERROR RETURN ADDRESS
5781                                ;CONVERT MEAS'D CYCLES TO TIME AND CHECK AGAINST LIMITS
5782 022022 104407                SAVREG                ;SAVE R0-R5
5783 022024 004737 034072                JSR    PC,CNVTIM     ;CONVERT MEAS'D CYCLES TO TIME
5784 022030 021752                                28$                ;ERROR RETURN ADDRESS
5785 022032 060337 003176                ADD    R3,SUMLO1     ;ADD CONVERTED TIME TO SUM
5786 022036 005537 003200                ADC    SUMHI1
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5787 022042 060237 003200      ADD     R2,SUMHI1
5788 022046 004737 036266      JSR     PC,CTLOUT      ;CHECK FOR (^C) OR (^Z) KBD INPUT
5789                          ;COMPARE R2-R3 AGAINST CURRENT MIN AND MAX MEASUREMENTS
5790 022052 020237 003214      CMP     R2,MINIH1     ;COMP HI BITS TO HI MIN
5791 022056 101010                BHI     32$           ;BR IF > MIN
5792 022060 103403                BLO     30$           ;BR IF < MIN
5793 022062 020337 003212      CMP     R3,MINIL1     ;COMP LO BITS TO LO MIN
5794 022066 103004                BHS     32$           ;BR IF > OR = MIN
5795 022070 010237 003214      30$:   MOV     R2,MINIH1     ;SET NEW MIN
5796 022074 010337 003212      MOV     R3,MINIL1
5797 022100 020237 003220      32$:   CMP     R2,MAXIH1     ;COMP HI BITS TO HI MAX
5798 022104 103410                BLO     14$           ;BR IF < MAX
5799 022106 101003                BHI     34$           ;BR IF > MAX
5800 022110 020337 003216      CMP     R3,MAXIL1     ;COMP LO BITS TO LO MAX
5801 022114 101404                BLOS    14$           ;BR IF < OR = MAX
5802 022116 010237 003220      34$:   MOV     R2,MAXIH1     ;SET NEW MAX
5803 022122 010337 003216      MOV     R3,MAXIL1
5804                          ;COMPARE MEAS'D TIME TO SPECIFIED LIMITS
5805 022126 020227 000000      14$:   CMP     R2,#MINHI1     ;COMPARE HI BITS TO HI MINIMUM
5806 022132 101006                BHI     18$           ;BR IF > MIN
5807 022134 103403                BLO     16$           ;BR IF < MIN
5808 022136 020327 057467      CMP     R3,#MINLO1     ;COMPARE LO BITS TO LO MIN
5809 022142 103002                BHS     18$           ;BR IF > OR = MIN
5810 022144 005237 003170      16$:   INC     BLWMIN          ;INCREMENT COUNT OF TIMES BELOW MIN
5811 022150 020227 000000      18$:   CMP     R2,#MAXHI1     ;COMPARE HI BITS TO HI MAX
5812 022154 103406                BLO     22$           ;BR IF < MAX
5813 022156 101003                BHI     20$           ;BR IF > MAX
5814 022160 020327 062031      CMP     R3,#MAXLO1     ;COMPARE LO BITS TO LO MAX
5815 022164 101402                BLOS    22$           ;BR IF < OR = MAX
5816 022166 005237 003172      20$:   INC     ABVMX1          ;INCREMENT COUNT OF TIMES ABOVE MAX
5817 022172 104410                22$:   RESREG                    ;RESTORE R0-R5
5818 022174 005203                INC     R3              ;INCREMENT MEASUREMENT COUNTER
5819 022176 013705 005542      MOV     SCRACH,R5      ;RESTORE PARAM BLK ADDRESS
5820 022202 022703 000200      CMP     #128.,R3      ;SEE IF 128 MEASUREMENTS YET
5821 022206 001230                BNE     2$             ;BR IF NOT DONE WITH MEASUREMENTS YET
5822                          ;DIVIDE SUM BY 128. TO GET AVERAGE TIME OF ROTATION
5823 022210 012704 000007      MOV     #7,R4          ;SET LOOP COUNTER = 7
5824 022214 000241                24$:   CLC                      ;DIVIDE DOUBLE-WORD SUM BY 2
5825 022216 006037 003200      ROR     SUMHI1
5826 022222 006037 003176      ROR     SUMLO1
5827 022226 005304                DEC     R4              ;SEE IF DONE WITH DIVISION
5828 022230 001371                BNE     24$           ;BR IF NOT DONE YET
5829                          ;TYPE TIMING MEASUREMENT RESULTS (IN DECIMAL)
5830 022232 032737 000010 005770  BIT     #BIT03,CS      ;SEE IF TIMING TEST REPORTS SHOULD BE INHIBITED
5831 022240 001036                BNE     36$           ;BR IF REPORTS SHOULD BE INHIBITED
5832 022242 104401 011540      TYPE    ,ROTIMS        ;TYPE 'ROTATIONAL TIMES :''
5833 022246 012701 003212      MOV     #MINIL1,R1     ;POINTER TO LO MIN
5834 022252 004737 034144      JSR     PC,TYPMIN      ;TYPE MIN MEAS'D TIME
5835 022256 013746 003170      MOV     BLWMIN,-(SP)   ;GET COUNT OF TIMES BELOW MIN
5836 022262 104405                TYPDS                    ;CONVERT AND TYPE IT
5837 022264 104401 011750      TYPE    ,BELOW         ;TYPE '' OF 128 BELOW MIN OF ''
5838 022270 104401 012261      TYPE    ,LIM1          ;TYPE SPEC'D MIN LIMIT
5839 022274 012701 003216      MOV     #MAXIL1,R1     ;POINTER TO LO MAX
5840 022300 004737 034170      JSR     PC,TYPMAX      ;TYPE MAX MEAS'D TIME
5841 022304 013746 003172      MOV     ABVMX1,-(SP)   ;GET COUNT OF TIMES ABOVE MAX
5842 022310 104405                TYPDS                    ;CONVERT AND TYPE IT
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5843 022312 104401 012005 TYPE ,ABOVE ;TYPE '' OF 128 ABOVE MAX OF ''
5844 022316 104401 012274 TYPE ,LIM2 ;TYPE SPEC'D MAX LIMIT
5845 022322 012701 003176 MOV #SUMLO1,R1 ;POINTER TO AVG TIME
5846 022326 004737 034214 JSR PC,TYPAVG ;TYPE AVERAGE TIME
5847 022332 104401 001315 TYPE ,$CRLF ;TYPE <CR>,<LF>
5848 022336 012777 045234 160474 36$: MOV #I.INTR,@RKVEC ;RESTORE RK06 HANDLER ADDRESS
5849 022344 000177 177256 JMP @1$ ;PROCEED TO NEXT TEST
5850
5851
5852 ;*****
5853 ;*SPECIAL INTERRUPT HANDLER FOR READ HEADERS IN ROT. LAT. MEAS. TEST
5854 022350 005200 P4EDHD: INC R0 ;SET RK06 INTR HANDLER
5855 022352 005762 000000 TST RKCS1(R2) ;SEE IF CONTROLLER ERROR SET
5856 022356 100002 BPL 2$ ;BR IF NO ERROR
5857 022360 000137 045234 JMP I.INTR ;LET DRIVER PROCESS THE ERROR
5858 022364 000002 2$: RTI ;RETURN FROM INTR
5859
5860
5861
5862 ;*****
5863 ;*TEST 14 ONE CYLINDER SEEK TIME MEASUREMENT
5864 ;*THIS TEST MEASURES THE TIME REQUIRED TO SEEK BETWEEN 2 ADJACENT
5865 ;*CYLINDERS, BOTH IN THE FORWARD AND REVERSE DIRECTIONS. SEEKS
5866 ;*ARE DONE TO 0,1,2,...,631,632 AND THEN TO 631,630,...,2,1,0
5867 ;* AND THE RESULTS ARE TYPED FOR EACH DIRECTION.
5868 ;*THE SPECIFIED ONE CYL SEEK TIME IS < 8 MILLI-SEC.
5869 ;* SEEKS ARE TO & FROM CYL 1456 FOR THE RK07
5870 ;*****
5871 022366 000004 TST14: SCOPE
5872 022370 012737 000014 001324 MOV #14,$TESTN ;;SET TEST NUMBER IN APT MAIL BOX
5873 022376 004737 030416 JSR PC,SETUP ;SET UP FOR LOOP ON ERROR
5874 022402 004737 030464 JSR PC,CHKITR ;SEE IF ITER. NO. = 0 FOR THIS TEST
5875 022406 000137 022706 JMP TST15 ;JUMP TO NEXT TEST
5876 ;RETURN HERE FROM CHKITR IF TEST SHOULD BE RUN
5877 022412 004737 027450 JSR PC,INITSS ;INITIALIZE DRIVER PARAMS AND SUB-SYS
5878 022416 004737 025766 JSR PC,PREPKB ;PREPARE FOR POSSIBLE KBD INPUT
5879 022422 105737 003133 TSTB DOTIM ;SEE IF TIMING TESTS ARE ALLOWED
5880 022426 001002 BNE 2$ ;BR IF ALLOWED
5881 022430 000177 000004 1$: JMP @4$ ;SKIP TO NEXT TEST
5882 022434 004737 032646 2$: JSR PC,CALBRT ;CALIBRATE THE SOFTWARE TIMER
5883 022440 4$:
5884 022440 022706 TST15 ;ERROR RETURN ADDR FOR CALBRT
5885 022442 004737 033140 JSR PC,INIVRB ;INIT VARIABLES USED
5886 022446 112765 000117 000001 MOVB #SEEK,P.CMND(R5) ;SET SEEK COMMAND
5887 022454 005065 000002 CLR P.CYLN(R5) ;INIT CYL TO 0
5888 022460 004737 040252 JSR PC,DRVCAL ;DO INITIAL SEEK TO CYL 0
5889 ;MEASURE FORWARD SEEK TIME
5890 022464 005265 000002 6$: INC P.CYLN(R5) ;INCREMENT CYLINDER BY 1
5891 022470 004737 033246 JSR PC,TIMSEK ;MEASURE A SEEK TIME
5892 022474 022706 TST15 ;ERROR RETURN ADDR FOR CALBRT
5893 022476 060137 003176 ADD R1,SUMLO1 ;ADD MEAS'D TIME TO FORWARD SUM
5894 022502 005537 003200 ADC SUMHI1
5895 022506 060037 003200 ADD R0,SUMHI1
5896 022512 004737 036266 JSR PC,C7LOUT ;CHECK FOR (^C) OR (^Z) KBD INPUT
5897 ;COMPARE FORWARD TIME TO CURRENT FORWARD MIN AND MAX
5898 022516 012703 003212 MOV #MINI1,R3 ;SET POINTER TO CURRENT MIN AND MAX
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5955 022716 004737 030416 JSR PC,SETUP ;SET UP FOR LOOP ON ERROR
5956 022722 004737 030464 JSR PC,CHKITR ;SEE IF ITER. NO. = 0 FOR THIS TEST
5957 022726 000137 023244 JMP TST16 ;JUMP TO NEXT TEST
5958 ;RETURN HERE FROM CHKITR IF TEST SHOULD BE RUN
5959 022732 004737 027450 JSR PC,INITSS ;INITIALIZE DRIVER PARAMS AND SUB-SYS
5960 022736 004737 025766 JSR PC,PREPKB ;PREPARE FOR POSSIBLE KBD INPUT
5961 022742 105737 003133 TSTB DOTIM ;SEE IF TIMING TESTS ARE ALLOWED
5962 022746 001002 BNE 2$ ;BR IF ALLOWED
5963 022750 000177 000004 1$: JMP 24$ ;SKIP TO NEXT TEST
5964 022754 004737 032646 2$: JSR PC,CALBRT ;CALIBRATE THE SOFTWARE TIMER
5965 022760 4$:
5966 022760 023244 TST16 ;ERROR RETURN ADDR FOR CALBRT
5967 022762 004737 033140 JSR PC,INIVRB ;INIT VARIABLES
5968 022766 112765 000117 000001 MOVB #SEEK,P.CMND(R5) ;SET SEEK COMMAND
5969 022774 005065 000002 CLR P,CYLN(R5) ;SET CYL = 0
5970 023000 004737 040252 JSR PC,DRVCAL ;SEEK INITIALLY TO 0
5971 023004 005037 005522 CLR NCYL1 ;SET DESTINATION CYLINDER
5972 023010 013737 017710 005542 MOV LCP1,SCRACH ;INIT COEFFICIENT TO 411(DEC)/814(DEC) FOR RK07
5973 023016 005237 005522 8$: INC NCYL1 ;INCR DESTINATION CYL
5974 023022 005337 005542 DEC SCRACH ;DECREMENT COEFFICIENT
5975 023026 013765 005522 000002 MOV NCYL1,P,CYLN(R5)
5976 023034 004737 033246 JSR PC,TIMSEK ;MEASURE A FORWARD SEEK TIME
5977 023040 023244 TST16 ;ERROR RETURN ADDR FOR CALBRT
5978 023042 004737 033450 JSR PC,FDTERM ;COMPUTE AND ADD A FORWARD TERM
5979 023046 005065 000002 CLR P,CYLN(R5) ;SET CYL TO 0
5980 023052 004737 033246 JSR PC,TIMSEK ;MEASURE A REVERSE SEEK TIME
5981 023056 023244 TST16 ;ERROR RETURN ADDR FOR CALBRT
5982 023060 004737 033514 JSR PC,RVTERM ;COMPUTE AND ADD A REVERSE TERM
5983 023064 023737 005522 017706 CMP NCYL1,LSTCYL ;SEE IF DONE MEASURING YET
5984 023072 002751 BLT 8$ ;BR IF NOT DONE YET
5985 023074 104401 011560 TYPE ,AVGSEK ;TYPE 'AVERAGE SEEK TIMES'
5986 ;DIVIDE FORWARD SUM BY (410)(410)/(814)(814) FOR RK07
5987 023100 013700 003176 MOV SUMLO1,R0 ;GET DIVIDEND
5988 023104 013701 003200 MOV SUMHI1,R1
5989 023110 013702 003202 MOV SUMLO2,R2
5990 023114 013703 003204 MOV SUMHI2,R3
5991 023120 013704 017724 MOV HOLD6,R4 ;GET DIVISOR
5992 023124 013705 017726 MOV HOLD7,R5
5993 023130 004737 053210 JSR PC,M.DPID ;PERFORM DIVISION
5994 023134 010237 003200 MOV R2,SUMHI1 ;GET FORWARD AVG
5995 023140 010337 003176 MOV R3,SUMLO1
5996 ;DIVIDE REVERSE SUM BY (410)(410)
5997 023144 013700 003216 MOV MAXIL1,R0 ;GET DIVIDEND
5998 023150 013701 003220 MOV MAXIH1,R1
5999 023154 013702 003226 MOV MAXIL2,R2
6000 023160 013703 003230 MOV MAXIH2,R3
6001 023164 004737 053210 JSR PC,M.DPID ;PERFORM DIVISION
6002 023170 010237 003204 MOV R2,SUMHI2 ;GET REVERSE AVG
6003 023174 010337 003202 MOV R3,SUMLO2
6004 ;TYPE FORWARD AVERAGE
6005 023200 104401 011663 TYPE ,FORWRD ;TYPE '***FORWARD DIRECTION***'
6006 023204 012701 003176 MOV #SUMLO1,R1 ;POINTER TO FORWARD AVG
6007 023210 004737 034214 JSR PC,TYPAVG ;TYPE FORWARD AVERAGE
6008 023214 104401 012225 TYPE ,SPCDMX ;TYPE 'SPEC'D MAX IS 40000 US'
6009 ;TYPE REVERSE AVERAGE
6010 023220 104401 011701 TYPE ,REVRSE ;TYPE '***REVERSE DIRECTION***'
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6011 023224 012701 003202
6012 023230 004737 034214
6013 023234 104401 012225
6014 023240 004737 027450
6015
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6024
6025 023244 000004
6026 023246 012737 000016 001324
6027 023254 004737 030416
6028 023260 004737 030464
6029 023264 000137 023570
6030
6031 023270 004737 027450
6032 023274 004737 025766
6033 023300 105737 003133
6034 023304 001002
6035 023306 000177 000004
6036 023312 004737 032646
6037 023316
6038 023316 023570
6039 023320 004737 033140
6040 023324 112765 000117 000001
6041 023332 005065 000002
6042 023336 004737 040252
6043 023342 005037 005542
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6045 023346 013765 017706 000002
6046 023354 004737 033246
6047 023360 023570
6048 023362 060137 003176
6049 023366 005537 003200
6050 023372 060037 003200
6051 023376 004737 036266
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6053 023402 012703 003212
6054 023406 004737 033560
6055
6056 023412 020037 007224
6057 023416 103406
6058 023420 101003
6059 023422 020137 007222
6060 023426 101402
6061 023430 005237 003172
6062
6063 023434 005065 000002
6064 023440 004737 033246
6065 023444 023570
6066 023446 060137 003202

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MOV #SUMLO2,R1 ; POINTER TO REVERSE AVG
JSR PC,TYPAVG ; TYPE REVERSE AVERAGE
TYPE ,SPCDMX ; TYPE "SPEC'D MAX IS 40000 US"
JSR PC,INITSS ; INIT THE SUB-SYS

:*****
*TEST 16 MAXIMUM SEEK TIME MEASUREMENT
*THIS TEST MEASURES THE TIME REQUIRED TO SEEK FROM CYL 0 TO
*CYL 632 OCT. (410 DEC.). THIS TIME REPRESENTS THE MAXIMUM SEEK
*TIME, AND IT IS MEASURED 128 TIMES IN EACH DIRECTION. THE
*THE SPECIFIED MAXIMUM SEEK TIME IS < 75 MILLI-SEC.
* CYL 814 IS THE LAST CYL FOR THE RK07
:*****
TST16: SCOPE
MOV #16,$TESTN ; SET TEST NUMBER IN APT MAIL BOX
JSR PC,SETUP ; SET UP FOR LOOP ON ERROR
JSR PC,CHKITR ; SEE IF ITER. NO. = 0 FOR THIS TEST
JMP TST17 ; JUMP TO NEXT TEST
;RETURN HERE FROM CHKITR IF TEST SHOULD BE RUN
JSR PC,INITSS ; INITIALIZE DRIVER PARAMS AND SUB-SYS
JSR PC,PREPKB ; PREPARE FOR POSSIBLE KBD INPUT
TSTB DOTIM ; SEE IF TIMING TESTS ARE ALLOWED
BNE 2$ ; BR IF ALLOWED
1$: JMP 4$ ; SKIP TO NEXT TEST
2$: JSR PC,CALBRT ; CALIBRATE THE SOFTWARE TIMER
4$:
TST17 ; ERROR RETURN ADDR FOR CALBRT
JSR PC,INIVRB ; INIT VARIABLES USED
MOVB #SEEK,P.CMND(R5) ; SET SEEK COMMAND
CLR P.CYLN(R5) ; INIT CYL TO 0
JSR PC,DRVCAL ; DO INITIAL SEEK TO CYL 0
CLR SCRACH ; INIT MEASUREMENT COUNT
;MEASURE FORWARD SEEK TIME
6$: MOV LSTCYL,P.CYLN(R5) ; SET CYL = 632 OCT, 1456 FOR RK07
JSR PC,TIMSEK ; MEASURE A SEEK TIME
TST17 ; ERROR RETURN ADDR FOR CALBRT
ADD R1,SUMLO1 ; ADD MEAS'D TIME TO FORWARD SUM
ADC SUMHI1
ADD R0,SUMHI1
JSR PC,CTLOUT ; CHECK FOR (^C) OR (^Z) KBD INPUT
;COMPARE FORWARD TIME TO CURRENT FORWARD MIN AND MAX
MOV #MINI1,R3 ; SET POINTER TO CURRENT MIN AND MAX
JSR PC,CMPTIM ; COMPARE TO MEAS'D MIN AND MAX
;COMPARE FORWARD TIME TO SPEC'D MAX LIMIT
CMP R0,MAXHI4 ; COMPARE HI BITS TO HI MAX
BLO 12$ ; BR IF < MAX
BHI 10$ ; BR IF > MAX
CMP R1,MAXLO4 ; COMPARE LO BITS TO LO MAX
BLOS 12$ ; BR IF < OR = MAX
10$: INC ABVMX1 ; INCREMENT COUNT OF TIMES ABOVE SPEC'D MAX
;MEASURE A REVERSE SEEK TIME
12$: CLR P.CYLN(R5) ; SET CYL = 0
JSR PC,TIMSEK ; MEASURE A SEEK TIME
TST17 ; ERROR RETURN ADDR FOR CALBRT
ADD R1,SUMLO2 ; ADD MEASURED TIME TO REVERSE S M
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6067 023452 005537 003204      ADC      SUMH12
6068 023456 060037 003204      ADD      R0,SUMH12
6069 023462 004737 036266      JSR      PC,CTLOUT      ;CHECK FOR (^C) OR (^Z) KBD INPUT
6070      ;COMPARE REVERSE TIME TO CURRENT REVERSE MIN AND MAX
6071 023466 012703 003222      MOV      #MINIL2,R3      ;SET POINTER TO CURRENT MIN AND MAX
6072 023472 004737 033560      JSR      PC,CMPTIM      ;COMPARE TO MEAS'G MIN AND MAX
6073      ;COMPARE REVERSE TIME TO SPEC'D MAX LIMIT
6074 023476 020037 007224      CMP      R0,MAXHI4      ;COMPARE HI BITS TO HI MAX
6075 023502 103406      BLO      18$      ;BR IF < MAX
6076 023504 101003      BHI      16$      ;BR IF > MAX
6077 023506 020137 007222      CMP      R1,MAXLO4      ;COMPARE LO BITS TO LO MAX
6078 023512 101402      BLOS     18$      ;BR IF < OR = MAX
6079 023514 005237 003174      16$: INC      ABVMX2      ;INCR COUNT OF TIMES ABOVE SPEC'D MAX
6080 023520 005237 005542      18$: INC      SCRACH      ;INCREMENT MEASUREMENT COUNT
6081 023524 022737 000200      005542  CMP      #128.,SCRACH      ;SEE IF DONE WITH 128 MEASUREMENTS
6082 023532 001305      BNE      6$      ;BR IF NOT DONE YET
6083      ;COMPUTE FORWARD AND REVERSE SEEK TIME AVERAGES
6084 023534 012703 000200      MOV      #128.,R3      ;GET NO. OF MEASUREMENTS
6085 023540 004737 033632      JSR      PC,GETAVG      ;COMPUTE AVERAGES
6086      ;TYPE TIMING MEASUREMENT RESULTS (IN DECIMAL)
6087 023544 032737 000010      005770  BIT      #BIT03,CS      ;SEE IF TIMING TEST REPORTS SHOULD BE INHIBITED
6088 023552 001255      BNE      1$      ;BR IF REPORTS SHOULD BE INHIBITED
6089 023554 104401 011636      TYPE     ,MAXSEK      ;TYPE 'MAXIMUM SEEK TIMES'
6090 023560 012703 012142      MOV      #ABOVE3,R3      ;GET POINTER TO MAX SPEC'D LIMIT MSG
6091 023564 004737 033724      JSR      PC,TYPTMS      ;TYPE TIMING TEST RESULTS
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6096      ;*****
6097      ;*TEST 17      SECTOR ADDRESSING TEST
6098      ;*IN THIS TEST, ALL SECTORS OF FC, FT ARE WRITTEN, EACH WITH
6099      ;*256(DEC) WORDS OF ITS OWN SECTOR NO. + 100(OCT). NEXT, FOR
6100      ;*EACH SECTOR WRITTEN A WRITE CHECK IS DONE, FOLLOWED BY A
6101      ;*READ AND SOFTWARE COMPARE OF THE DATA.
6102      ;*****
6102 023570 000004      TST17: SCOPE
6103 023572 012737 000017      001324  MOV      #17,$TESTN      ;;SET TEST NUMBER IN APT MAIL BOX
6104 023600 004737 030416      JSR      PC,SETUP      ;SET UP FOR LOOP ON ERROR
6105 023604 004737 030464      JSR      PC,CHKITR      ;SEE IF ITER. NO. = 0 FOR THIS TEST
6106 023610 000137 024210      JMP      TST20      ;JUMP TO NEXT TEST
6107      ;RETURN HERE FROM CHKITR IF TEST SHOULD BE RUN
6108 023614 004737 027450      JSR      PC,INITSS      ;INITIALIZE DRIVER PARAMS AND SUB-SYS
6109 023620 004737 025766      JSR      PC,PREPKB      ;PREPARE FOR POSSIBLE KBD INPUT
6110 023624 112765 000123      000001  MOVB     #WRDATA,P.CMND(R5)      ;SET WRITE DATA COMMAND
6111 023632 013765 005732      000002  MOV      FC,P.CYLN(R5)      ;SET CYL = FC
6112 023640 113765 005740      000005  MOVB     FT,P.TRCK(R5)      ;SET TRACK = FT
6113 023646 013737 005732      001174  MOV      FC,$REG5      ;SET PACK ADR FOR ERROR PRINTOUT
6114 023654 013737 005740      001176  MOV      FT,$REG6
6115 023662 005037 001200      CLR      $REG7
6116 023666 105065 000004      CLRB     P.SECT(R5)      ;SET SECTOR = 0
6117 023672 012765 063520      000010  MOV      #RWBUF,P.BALO(R5)      ;SET DATA BUFFER ADDRESS
6118 023700 012765 177400      000012  MOV      #-256.,P.WC(R5)      ;SET WORD CNT FOR 256(DEC) WORDS
6119      ;SET NUMBER OF SECTORS IN R3
6120 023706 012703 000024      MOV      #20.,R3      ;SET R3 INITIALLY = 20(DEC)
6121 023712 105737 003125      TSTB     FORMAT      ;DETERMINE THE FORMAT
6122 023716 001002      BNE      4$      ;BR IF 20 SECTOR FORMAT
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6123 023720 012703 000026      MOV      #22.,R3      ;SET R3 = 22(DEC)
6124                               ;WRITE THE CURRENT SECTOR WITH THE SECTOR NUMBER + 100(OCT)
6125 023724 116500 000004      4$: MOVB     P.SECT(R5),RO ;PUT SECTOR NO. INTO RO
6126 023730 062700 000100      ADD      #100,RO      ;ADD 100(OCT) TO SECTOR NO.
6127 023734 004737 034240      JSR      PC,LODSEC    ;LOAD THE ENTIRE BUFFER WITH SECTOR NO. + 100(OCT)
6128 023740 004737 040252      JSR      PC,DRVCAL    ;WRITE THE SECTOR
6129 023744 105265 000004      INCB     P.SECT(R5)   ;INCREMENT THE SECTOR NO.
6130 023750 120365 000004      CMPB     R3,P.SECT(R5);SEE IF LAST SECTOR WRITTEN YET
6131 023754 001363                BNE      4$          ;BR IF NOT DONE YET
6132 023756 012737 023756 001110 MOV      #.,$LPERR    ;SET NEW LOOP ON ERROR ADRS
6133 023764 004737 030416      JSR      PC,SETUP     ;SET UP FOR LOOP ON ERROR
6134 023770 105065 000004      CLRB     P.SECT(R5)   ;INITIALIZE SECTOR TO 0
6135                               ;DO WRITE CHECK OF A SECTOR
6136 023774 116500 000004      8$: MOVB     P.SECT(R5),RO ;GET SECTOR INTO RO
6137 024000 062700 000100      ADD      #100,RO      ;ADD 100(OCT) TO SECTOR NO.
6138 024004 004737 034240      JSR      PC,LODSEC    ;LOAD BUFFER WITH SECTOR + 100(OCT)
6139 024010 112765 000131 000001 MOVB     #WRTCHK,P.CMND(R5);SET WRITE CHECK COMMAND
6140 024016 004737 040252      JSR      PC,DRVCAL    ;DO A WRITE CHECK
6141                               ;READ THE SECTOR AND COMPARE THE DATA TO SECTOR NUMBER + 100(OCT)
6142 024022 112765 000121 000001 MOVB     #RDATA,P.CMND(R5);SET READ COMMAND
6143 024030 004737 040252      JSR      PC,DRVCAL    ;READ THIS SECTOR INTO MEMORY
6144 024034 012701 063520      MOV      #RWBUF,R1    ;GET ADDR. OF DATA BUF INTO R1
6145 024040 005004                CLR      R4           ;CLEAR WORD NUMBER
6146 024042 005037 005542      CLR      SCRACH       ;INIT. COMPARE ERROR COUNT
6147 024046 116537 000004 001200 MOVB     P.SECT(R5),$REG7
6148 024054 032737 000002 005504 BIT      #BSERR,RECODE ;SEE IF BAD SECTOR ERROR OCCURRED
6149 024062 001045                BNE      22$          ;BR IF YES
6150 024064 020021 10$: CMP      RO,(R1)+    ;COMPARE BUF WORD TO SECTOR + 100(OCT)
6151 024066 001437                BEQ      20$          ;BR IF NO COMPARE ERROR
6152 024070 005737 005542      TST      SCRACH       ;CHECK ERROR COUNT
6153 024074 001006                BNE      12$          ;BR IF NOT FIRST ERROR IN SECTOR
6154 024076 105037 062051 063412 CLRB     DH701+38.    ;ADJUST DATA HEADER FOR MSG
6155 024102 012737 000005      MOV      #5,DF25+2    ;ADJ. ERROR DATA WORD COUNT
6156 024110 104034                ERROR    34          ;TYPE HEADING FOR ERROR MSG
6157 024112 005237 005542 12$: INC      SCRACH       ;INCREMENT ERROR COUNT
6158 024116 032777 000001 155014 BIT      #BIT0,@SWR   ;SEE IF ALL ERRORS SHOULD BE REPORTED
6159 024124 001004                BNE      14$          ;BR TO REPORT ALL ERRORS
6160 024126 022737 000012 005542 CMP      #10.,SCRACH  ;SEE IF 10(DEC) ERRORS YET
6161 024134 002420                BLT      22$          ;BR IF > 10.
6162 024136 010437 001202 14$: MOV      R4,$REG10    ;WORD NUMBER
6163 024142 010037 001204      MOV      RO,$REG11    ;GOOD DATA
6164 024146 016137 177776 001206 MOV      -2(R1),$REG12 ;BAD DATA
6165 024154 104063                ERROR    63          ;TYPE GOOD AND BAD DATA
6166 024156 032777 000100 154754 BIT      #BIT6,@SWR   ;SEE IF JUST 1 ERROR SHOULD BE REPORTED
6167 024164 001004                BNE      20$          ;BR IF JUST 1 ERROR SHOULD BE REPORTED
6168 024166 005204 20$: INC      R4           ;INCREMENT WORD NUMBER
6169 024170 022704 000400      CMP      #256.,R4     ;SEE IF DONE YET
6170 024174 001333                BNE      10$          ;BR IF NOT DONE COMPARING YET
6171 024176 105265 000004 22$: INCB     P.SECT(R5)   ;INCREMENT SECTOR NO.
6172 024202 120365 000004      CMPB     R3,P.SECT(R5);SEE IF ALL SECTORS CHECKED YET
6173 024206 001272                BNE      8$          ;BR IF NOT DONE YET
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:::.....  
: *TEST 20 TRACK ADDRESSING TEST
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6185 024210 000004
6186 024212 012737 000020 001324
6187 024220 004737 030416
6188 024224 004737 030464
6189 024230 000137 024444
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6191 024234 004737 027450
6192 024240 004737 025766
6193 024244 112765 000123 000001
6194 024252 013765 005732 000002
6195 024260 105065 000005
6196 024264 113765 005516 000004
6197 024272 012765 063520 000010
6198 024300 012765 177400 000012
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6200 024306 116500 000005
6201 024312 062700 000100
6202 024316 004737 034240
6203 024322 004737 040252
6204 024326 105265 000005
6205 024332 122765 000003 000005
6206 024340 001362
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6208 024342 012737 024342 001110
6209 024350 004737 030416
6210 024354 004737 034262
6211
6212 024360 012737 024360 001110
6213 024366 004737 030416
6214 024372 105065 000005
6215 024376 116500 000005
6216 024402 062700 000100
6217 024406 004737 034240
6218 024412 112765 000123 000001
6219 024420 004737 040252
6220 024424 004737 034262
6221 024430 105265 000005
6222 024434 122765 000003 000005
6223 024442 001355
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6227 024444
6228 000040
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;*IN THIS TEST, SECTOR FS OF CYL FC IS WRITTEN WITH 256 (DEC) WORDS
;*OF THE TRACK NO. + 100(OCT), FOR EACH OF TRACKS 0,1,2. THEN, A
;*WRITE CHECK OF EACH SECTOR IS DONE TO VERIFY THE WRITES. THEN, EACH
;*OF THE 3 SECTORS IS RE-WRITTEN, AND AFTER EACH WRITE ALL OF THE
;*THREE SECTORS ARE WRITE-CHECKED TO DETECT TRACK ADDRESSING PROBLEMS.
;*****
TST20: SCOPE
MOV #20,$TESTN ;;SET TEST NUMBER IN APT MAIL BOX
JSR PC,SETUP ;;SET UP FOR LOOP ON ERROR
JSR PC,CHKITR ;;SEE IF ITER. NO. = 0 FOR THIS TEST
JMP TST21 ;;JUMP TO NEXT TEST
;RETURN HERE FROM CHKITR IF TEST SHOULD BE RUN
JSR PC,INITSS ;;INITIALIZE DRIVER PARAMS AND SUB-SYS
JSR PC,PREPKB ;;PREPARE FOR POSSIBLE KBD INPUT
MOVB #WRDATA,P.CMND(R5) ;SET WRITE COMMAND
MOV FC,P.CYLN(R5) ;SET CYL = FC
CLRB P.TRCK(R5) ;INITIALIZE TRACK NO. TO 0
MOVB FS,P.SECT(R5) ;SET SECTOR =FS
MOV #RWBUFF,P.BAL0(R5) ;SET DATA BUFFER ADDRESS
MOV #-256.,P.WC(R5) ;SET WORD COUNT FOR 256(DEC) WORDS
;WRITE THE TRACK NO. + 100(OCT) AT SECTOR FS OF TRACKS 0,1,2
4$: MOVB P.TRCK(R5),R0 ;SET TRACK NO. IN R0
ADD #100,R0 ;ADD 100(OCT) TO TRACK NO.
JSR PC,LODSEC ;LOAD BUFFER WITH TRACK NO. + 100(OCT)
JSR PC,DRVCAL ;WRITE THE SECTOR
INCB P.TRCK(R5) ;INCREMENT THE TRACK NO.
CMPB #3,P.TRCK(R5) ;SEE IF ALL 3 TRACKS WRITTEN YET
BNE 4$ ;BR IF NOT DONE YET
;DO A WRITE-CHECK OF THE 3 SECTORS WRITTEN, TO VERIFY THE DATA
MOV #.,$LPERR ;SET NEW LOOP ON ERROR ADRS
JSR PC,SETUP ;SET UP FOR LOOP ON ERROR
JSR PC,TRKCHK
;WRITE A SECTOR AND WRITE-CHECK ALL 3 SECTORS
MOV #.,$LPERR ;SET NEW LOOP ON ERROR ADRS
JSR PC,SETUP ;SET UP FOR LOOP ON ERROR
CLRB P.TRCK(R5) ;INIT TRACK TO 0
6$: MOVB P.TRCK(R5),R0 ;GET TRACK NO. IN R0
ADD #100,R0 ;ADD 100(OCT) TO TRACK NO.
JSR PC,LODSEC ;LOAD BUF WITH TRACK + 100(OCT)
MOVB #WRDATA,P.CMND(R5) ;SET WRITE DATA COMMAND
JSR PC,DRVCAL ;RE-WRITE A SECTOR
JSR PC,TRKCHK ;WRITE-CHECK ALL 3 SECTORS
INCB P.TRCK(R5) ;INCR THE TRACK NO.
CMPB #3,P.TRCK(R5) ;SEE IF ALL 3 TRACKS RE-WRITTEN YET
BNE 6$ ;BR IF NOT DONE YET

RWDTST:
RWTINX=<$TN-1>*2 ;R/W TEST INDEX
;*****
;*TEST 21 READ/WRITE DATA TEST
;*THE READ/WRITE DATA TEST HAS 2 DIFFERENT VERSIONS, DEPENDING
;*ON THE VALUE OF THE DATA PATTERN PARAMETER (PT).
;*****
;* FOR PT = 0 :
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6260 024444 000004
6261 024446 012737 000021 001324
6262 024454 004737 030416
6263 024460 004737 030464
6264 024464 000137 025356
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6266 024470 004737 027450
6267 024474 004737 025766
6268 024500 105037 003134
6269 024504 032777 000040 154426
6270 024512 001002
6271 024514 004737 034346
6272 024520 013737 005760 005544
6273 024526 005737 005760
6274 024532 001020
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6276 024534 012765 063520 000010
6277 024542 005065 000002
6278 024546 105065 000005
6279 024552 012765 175000 000012
6280 024560 105065 000004
6281 024564 004737 035716
6282 024570 025332
6283 024572 000457
6284
6285 024574 013765 005762 000010
6286 024602 013700 005764
6287 024606 042700 177774
6288 024612 150065 000007
6289 024616 013737 005762 005602
6290 024624 013737 005764 005604

:*THIS TEST IS THE QUICK VERIFY DEFAULT DATA TEST, WHICH IS RUN
:*WHEN PT=0, DUE TO PARAMETER INPUT CHOICE, OR ADDRESS 200 START.
:*IN THIS TEST, THE ENTIRE PACK IS TESTED WITH A SINGLE DATA PATTERN.
:*THIS PATTERN IS COMPRISED OF THE DATA PATTERNS 00-15, WHICH REPEATS
:*EVERY 256(DEC) WORDS. EACH TRACK IS WRITTEN IN 4 SEGMENTS, FOLLOWED
:*BY A WRITE-CHECK, READ, AND SOFTWARE COMPARE. THE SEGMENTS ARE 6
:*SECTORS EACH, WHICH MEANS THAT SPIRALING OCCURS ON THE LAST
:*SEGMENT WRITTEN ON EACH TRACK.
:*
:* FOR PT NOT = 0 :
:*THIS TEST PERFORMS READ/WRITE FUNCTIONS ON THE ENTIRE RANGE OF
:*CYLINDERS (FC-LC), TRACKS (FT-LT), AND SECTORS (FS-LS) SPECIFIED.
:*AT EACH SPECIFIED SECTOR WC WORDS OF THE CURRENT REPEATING DATA
:*PATTERN ARE WRITTEN, AND THEN WRITE-CHECKED, FOLLOWED BY A READ
:*AND SOFTWARE COMPARE. THIS IS DONE FOR ALL THE SPEC'D SECTORS ON ALL
:*THE TRACKS, USING SECTOR INCR IS, AND TRACK INCR IT.
:* THEN IT IS REPEATED USING EACH OF THE OTHER DATA
:*PATTERNS CHOSEN IN PARAMETER PT. THEN, EACH OF THE ABOVE OPERATIONS
:*ARE REPEATED AT EACH OF THE REMAINING CYLINDERS, IN THE SPECIFIED
:*RANGE, USING THE CYLINDER INCREMENT IC.
:* NOTE THAT FS MUST BE CHOSEN < OR = LS, AND FT MUST BE < OR = LT.
:*HOWEVER, FC MAY BE <, =, OR > LC, AND IF FC>LC, THE CYLINDER
:*ADDRESS WILL BE DECREMENTED BY IC (INSTEAD OF INCREMENTED) TO
:*OBTAIN EACH NEW CYLINDER ADDRESS.
:*****
TST21: SCOPE
MOV #21,\$TESTN ;;SET TEST NUMBER IN APT MAIL BOX
JSR PC,SETTUP ;;SET UP FOR LOOP ON ERROR
JSR PC,CHKITR ;;SEE IF ITER. NO. = 0 FOR THIS TEST
JMP \$EOP ;;JUMP TO END-OF-PASS ROUTINE
;RETURN HERE FROM CHKITR IF TEST SHOULD BE RUN
JSR PC,INITSS ;;INITIALIZE DRIVER PARAMS AND SUB-SYS
JSR PC,PREPKB ;;PREPARE FOR POSSIBLE KBD INPUT
CLRB XOVLD ;;INIT XDP OVERLAID INDICATOR
BIT #BITS,@SWR ;;SEE IF WRITES INHIBITED
BNE 2\$;;IF YES,DON'T CHANGE RAND DATA PAT
JSR PC,LOC@14 ;;GENERATE PSEUDO-RAND PAT 14
2\$: MOV PT,PATRN ;;GET A COPY OF PT
TST PT ;;SEE IF QUICK VERIFY DATA TEST DESIRED
BNE 7\$;;BR IF NOT QUICK VERIFY
;SET PARAMETERS FOR QUICK VERIFY DEFAULT DATA TEST
MOV #RWBUF,P.BALO(R5) ;;SET R/W BUFFER ADDRESS
CLR P.CYLN(R5) ;;INIT CYL TO 0
4\$: CLRB P.TRCK(R5) ;;INIT TRACK TO 0
5\$: MOV #-1536,P.WC(R5) ;;SET WORD COUNT FOR 6 SECTORS
CLRB P.SECT(R5) ;;INIT SECTOR TO 0
6\$: JSR PC,CHKLIM ;;CHK WRD COUNT AND PACK ADR TO AVOID OVERFLOW
30\$;;RETURN ADDR FOR TRANSFER NOT ALLOWED
BR 18\$;;PROCEED
;SET PARAMETERS FOR NON-DEFAULT DATA TEST
7\$: MOV MA,P.BALO(R5) ;;SET BA BITS 0-15
MOV MA+2,R0 ;;GET MA BITS 16-21
BIC #177774,R0 ;;MASK FOR LO 2 BITS
BISB R0,P.BAHI(R5) ;;SET BA BITS 16,17
MOV MA,PMA ;;SET BUFFER ADDRESS
MOV MA+2,PMA+2

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6291 024632 105237 003134      INCB  XOVLAD      ;SET XXDP OVERLAID INDICATOR
6292 024636 005737 055662      TST   $KT11      ;SEE IF MEMORY MANAGEMENT PRESENT
6293 024642 100002                BPL   9$         ;BR IF NO MEM. MGT.
6294                ;PREPARE PAGE ADDRESS REGISTERS FOR RELOCATION
6295 024644 004737 026532      JSR   PC,PREPAR  ;PREPARE MEM MGT FOR RELOCATION
6296                ;INITIALIZE TEST PARAMETERS
6297 024650 013765 005732 000002 9$:   MOV   FC,P.CYLN(R5) ;INIT CYL TO FC
6298 024656 012701 000001      44$:  MOV   #1,R1    ;INIT PATTERN BIT POINTER
6299 024662 030137 005760      10$:  BIT   R1,PT     ;SEE IF THIS PATTERN SELECTED
6300 024666 001003                BNE   14$         ;BR IF THIS PATTERN IS SELECTED
6301 024670 006301      11$:  ASL   R1        ;SHIFT PATTERN BIT POINTER
6302 024672 001571                BEQ   25$         ;BR IF ALL PATTERNS CHECKED ON THIS CYLINDER
6303 024674 000772                BR    10$         ;GO CHECK ANOTHER PATTERN
6304 024676 113765 005740 000005 14$:  MOVB  FT,P.TRCK(R5) ;INIT TRACK TO FT
6305 024704 113765 005516 000004 16$:  MOVB  FS,P.SECT(R5) ;INITIALIZE SECTOR TO FS
6306 024712 013765 005766 000012 17$:  MOV   WC,P.WC(R5)  ;SET WORD COUNT FOR WC WORDS
6307 024720 005465 000012                NEG   P.WC(R5)
6308 024724 004737 035716      JSR   PC,CHKLIM  ;CHECK WRD CNT AND PACK ADR TO AVOID OVERFLOW
6309 024730 025256                25$:                ;RETURN ADDRESS FOR TRANSFER NOT ALLOWED
6310                ;PERFORM TESTS AT CURRENT ADDRESS
6311 024732 012737 024732 001110 18$:  MOV   #,$LPERR    ;SET NEW LOOP ON ERROR ADRS
6312 024740 004737 030416      JSR   PC,SETUP   ;SET UP FOR LOOP ON ERROR
6313 024744 004737 037562      JSR   PC,SVPRMS  ;STORE PARAMS FOR THIS XFER
6314 024750 004737 034562      JSR   PC,LODBUF  ;LOAD DATA INTO R/W BUFFER
6315 024754 105037 003140      CLRB  REISSU     ;DUAL-ACC COMMAND RE-ISSUE FLAG
6316 024760 032777 000040 154152  BIT   #BIT5,@SWR ;SEE IF DATA WRITES INHIBITED
6317 024766 001005                BNE   20$         ;BR IF INHIBITED
6318 024770 112765 000123 000001  MOVB  #WRDATA,P.CMND(R5) ;SET WRITE COMMAND
6319 024776 004737 037674      JSR   PC,TRNSFR  ;WRITE THE DATA
6320 025002 032777 000020 154130 20$:  BIT   #BIT4,@SWR ;SEE IF WRITE CHECKS INHIBITED
6321 025010 001014                BNE   22$         ;BR IF INHIBITED
6322 025012 112765 000131 000001  MOVB  #WRTCHK,P.CMND(R5) ;SET WRITE CHECK COMMAND
6323 025020 004737 037674      JSR   PC,TRNSFR  ;DO WRITE-CHECK OF DATA WRITTEN
6324 025024 105737 003141      TSTB  WCEFLG     ;SEE IF A WCE ERROR OCCURRED
6325 025030 001404                BEQ   22$         ;BR IF NOT
6326 025032 032777 000002 154100  BIT   #BIT1,@SWR ;SEE IF READ AND COMPARE SHOULD BE DONE
6327 025040 001417                BEQ   24$         ;BR IF NOT
6328 025042 032777 000010 154070 22$:  BIT   #BIT3,@SWR ;SEE IF READ AND COMPARE INHIBITED
6329 025050 001013                BNE   24$         ;BR IF INHIBITED
6330 025052 112765 000121 000001  MOVB  #RDDATA,P.CMND(R5) ;SET READ COMMAND
6331 025060 004737 037674      JSR   PC,TRNSFR  ;READ THE DATA
6332 025064 032777 000004 154046  BIT   #BIT2,@SWR ;SEE IF SOFTWARE COMPARES INHIBITED
6333 025072 001002                BNE   24$         ;BR IF INHIBITED
6334 025074 004737 035010      JSR   PC,CMPBUF  ;PERFORM SOFTWARE COMPARE OF DATA
6335 025100 105737 003136      24$:  TSTB  DULACS     ;SEE IF DUAL-ACCESS DATA TEST
6336 025104 001405                BEQ   27$         ;BR IF NOT DUAL-ACCESS
6337 025106 112765 000140 000001  MOVB  #RELEAS,P.CMND(R5) ;SET DRIVE RELEASE COMMAND
6338 025114 004737 040252      JSR   PC,DRVCL  ;RELEASE THE DRIVE
6339 025120 104411      27$:  SCOPER ;CHECK FOR INTERNAL LOOP ON ERROR
6340 025122 005737 005760      TST   PT        ;SEE IF QUICK VERIFY DATA TEST DESIRED
6341 025126 001031                BNE   40$         ;BR IF NOT QUICK VERIFY
6342                ;INCREMENT PACK ADDRESS FOR QUICK VERIFY DEFAULT DATA TEST
6343 025130 062765 000006 000004  ADD   #6,P.SECT(R5) ;INCREMENT SECTOR BY 6
6344 025136 126527 000004 000030  CMPB  P.SECT(R5),#24. ;SEE IF SECTOR LIMIT REACHED YET
6345 025144 002002                BGE   34$         ;BR IF LIMIT EXCEEDED
6346 025146 000137 024564      JMP   6$         ;GO BACK AND TEST

```

```
6347 025152 105265 000005 34$: INCB P.TRCK(R5) ;INCREMENT TRACK
6348 025156 122765 000003 000005 CMPB #3,P.TRCK(R5) ;SEE IF TRACK LIMIT EXCEEDED
6349 025164 001402 BEQ 36$ ;BR IF YES
6350 025166 000137 024552 JMP 5$
6351 025172 005265 000002 36$: INC P.CYLN(R5) ;INCREMENT CYL
6352 025176 023765 017710 000002 CMP LCP1,P.CYLN(R5) ;SEE IF CYL LIMIT EXCEEDED
6353 025204 001452 BEQ 30$ ;BR IF YES
6354 025206 000137 024546 JMP 4$
6355 ;INCREMENT PACK ADDRESS FOR NON-DEFAULT DATA TEST
6356 025212 063765 005756 000004 40$: ADD IS,P.SECT(R5) ;INCREMENT SECTOR BY IS
6357 025220 126537 000004 005520 CMPB P.SECT(R5),LS ;SEE IF LS EXCEEDED
6358 025226 003631 BLE 17$ ;BR IF NOT YET
6359 025230 116503 000005 MOVB P.TRCK(R5),R3 ;GET TRACK NO.
6360 025234 063703 005744 ADD IT,R3 ;INCREMENT TRACK
6361 025240 110365 000005 MOVB R3,P.TRCK(R5) ;PUT IT BACK
6362 025244 020337 005742 CMP R3,LT ;SEE IF LT EXCEEDED
6363 025250 003615 BLE 16$ ;BR IF NOT YET
6364 025252 000137 024670 JMP 11$ ;JMP TO CHECK FOR NEXT PATTERN
6365 025256 023737 005732 25$: CMP FC,LC ;SEE IF FC>LC
6366 025264 003011 BGT 26$ ;BR IF FC>LC
6367 025266 063765 005736 000002 ADD IC,P.CYLN(R5) ;INCREMENT THE CYLINDER
6368 025274 026537 000002 005734 CMP P.CYLN(R5),LC ;SEE IF LC EXCEEDED
6369 025302 003013 BGT 30$ ;BR IF LC EXCEEDED
6370 025304 000137 024656 JMP 44$
6371 025310 163765 005736 000002 26$: SUB IC,P.CYLN(R5) ;DECREMENT THE CYLINDER
6372 025316 026537 000002 005734 CMP P.CYLN(R5),LC ;SEE IF LC EXCEEDED
6373 025324 002402 BLT 30$ ;BR IF LC EXCEEDED
6374 025326 000137 024656 JMP 44$
6375 025332 105737 003134 30$: TSTB XQVLAD ;SEE IF XXDP MIGHT BE OVERLAID NOW
6376 025336 001402 BEQ 31$ ;BR IF NOT
6377 025340 004737 027210 JSR PC,GETXDP ;RESTORE XXDP LOADER, IF SAVED
6378 025344 105737 003144 31$: TSTB UBMPRS ;SEE IF UNIBUS MAP PRESENT
6379 025350 001402 BEQ 32$ ;BR IF NOT
6380 025352 005037 172516 CLR @#SR3 ;DISABLE 22-BIT MODE AND UNIBUS MAP
6381 025356
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.SBTTL END OF PASS ROUTINE

```
::*****
;*INCREMENT THE PASS NUMBER ($PASS)
;*TYPE 'END PASS #XXXXX' (WHERE XXXXX IS A DECIMAL NUMBER)
;*IF THERES A MONITOR GO TO IT
;*IF THERE ISN'T JUMP TO APTPAS
```

\$EOP:

```
SCOPE
INC $DEVCT ;INCREMENT DEVICE COUNT FOR APT
JMP NEWDRV ;GO SEE IF MORE DRIVES TO TEST ON THIS PASS
DUNPAS: ;THIS IS TRULY THE END OF A PASS
BIC #BITS,@$TKS ;DISABLE TTY KBD INTERRUPT
CLR $STNM ;ZERO THE TEST NUMBER
CLR $TIMES ;ZERO THE NUMBER OF ITERATIONS
INC $PASS ;INCREMENT THE PASS NUMBER
BIC #100000,$PASS ;DON'T ALLOW A NEG. NUMBER
```

```
6403 025420 005327          DEC      (PC)+          ;;LOOP?
6404 025422 000001          $EOPCT: .WORD      1
6405 025424 003022          BGT      $DOAGN          ;;YES
6406 025426 012737          MOV      (PC)+,@(PC)+  ;;RESTORE COUNTER
6407 025430 000001          $ENDCT: .WORD      1
6408 025432 025422          $EOPCT
6409 025434 104401 025501          TYPE     ,SENDMG        ;;TYPE 'END PASS #'
6410 025440 013746 001326          MOV      $PASS,-(SP)    ;;SAVE $PASS FOR TYPEOUT
6411 025444 104405          TYPDS
6412 025446 104401 025476          TYPE     ,SENULL        ;;GO TYPE--DECIMAL ASCII WITH SIGN
6413 025452 013700 000042          $GET42: MOV      @#42,R0  ;;TYPE A NULL CHARACTER
6414 025456 001405          BEQ      $DOAGN          ;;GET MONITOR ADDRESS
6415 025460 000005          RESET
6416 025462 004710          $ENDAD: JSR      PC,(R0)  ;;BRANCH IF NO MONITOR
6417 025464 000240          NOP
6418 025466 000240          NOP
6419 025470 000240          NOP
6420 025472          $DOAGN:
6421 025472 000137          JMP      @(PC)+          ;;CLEAR THE WORLD
6422 025474 025516          $RTNAD: .WORD      APTAS  ;;GO TO MONITOR
6423 025476 377 377 000          $ENULL: .BYTE     -1,-1,0  ;;SAVE ROOM
6424 025501 015 042412 042116          $ENDMG: .ASCIZ    <15><12>/END PASS #/  ;;FOR
6425 025506 050040 051501 020123          ;;ACT11
6426 025514 000043          APTPAS: CMPB     #APTENV,$ENV  ;;RETURN
6427 025516 122737 000001 001340          BNE      2$              ;;NULL CHARACTER STRING
6428 025524 001007          CMP      $PASS,#2       ;;RUN UNDER THE APT ?
6429 025526 023727 001326 000002          BLO      2$              ;;BRANCH IF NOT
6430 025534 103403          BLO      2$              ;;TWO PASSES ?
6431 025536 005237 001102          1$:     INC      $STNM     ;;BRANCH IF NOT
6432 025542 000775          BR       1$              ;;INCREMENT THE TEST NUMBER
6433 025544 000137 016742          2$:     JMP      NEWPAS    ;;LOOPING FOR APT TO DUMP NFXT PROG
6434
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6438          ;THIS IS THE ABNORMAL RETURN FROM THE DRIVER,
6439          ; WHICH IS USED WHEN NED INDICATION IS EXPECTED (AND VALID).
6440          ; (NED = NON-EXISTENT DRIVE)
6441 025550 032765 010000 000020          NEDHDL: BIT      #NED,P.CS2(R5)  ;;SEE IF NED ON DRIVE SELECT
6442 025556 001002          BNE      1$              ;;BR IF NED SET
6443 025560 000137 042130          JMP      ERRHDL          ;;GO HANDLE OTHER ERROR
6444 025564 010046          1$:     MOV      RO,-(SP)    ;;SAVE RO,R1
6445 025570 012700 000001          MOV      R1,-(SP)
6446 025574 005001          MOV      #1,R0          ;;SET BIT POINTER
6447 025576 120137 005510          2$:     CLR      R1          ;;CLEAR COUNTER
6448 025602 001403          CMPB     R1,DRIVE        ;;SEE IF R1 = CURRENT DRIVE NUMBER
6449 025604 005201          BEQ      3$              ;;BR IF =
6450 025606 006300          INC      R1              ;;INCREMENT COUNTER
6451 025610 000772          ASL      RO              ;;SHIFT BIT POINTER
6452 025612 040037 005540          3$:     BR       2$          ;;TRY AGAIN
6453 025616 012601          BIC      RO,NEWON        ;;CLEAR ONLINE BIT FOR THIS DRIVE
6454 025620 012600          MOV      (SP)+,R1        ;;RESTORE RO,R1
6455 025622 000137 044306          MOV      (SP)+,RO
6456          JMP      RETNML        ;;TAKE NORMAL RETURN
6457
6458          ;:*****
```

```
6459      .SBTTL KBDHDL - TTY KEYBOARD INTERRUPT HANDLER
6460      ;:*****
6461
6462 025626 010146      KBDHDL: MOV     R1,-(SP)           ;SAVE R1 ON STACK
6463 025630 017701 153312      MOV     @STKB,R1           ;READ A CHAR FROM KBD BUFFER
6464 025634 042701 177600      BIC     #177600,R1        ;MASK OUT UNUSED BITS
6465 025640 120127 000172      CMPB   R1,#172           ;SEE IF LOWER CASE TYPED
6466 025644 003005      BGT     20$              ;BR IF NOT
6467 025646 120127 000141      CMPB   R1,#141
6468 025652 002402      BLT     20$              ;BR IF NOT
6469 025654 042701 000040      BIC     #BITS,R1         ;MAKE IT UPPER CASE
6470 025660 010137 005532      20$:  MOV     R1,INTCHR     ;SAVE INPUT CHARACTER
6471 025664 122701 000003      CMPB   #003,R1          ;SEE IF (^C) TYPED
6472 025670 001007      BNE     2$              ;BR IF NOT (^C)
6473 025672 104401 012353      TYPE   ,CNTRLC         ;ECHO (^C)
6474 025676 042777 000100 153240 1$:  BIC     #BIT6,@STKS      ;CLEAR KBD INTERRUPT ENABLE BIT
6475 025704 012601      MOV     (SP)+,R1        ;RESTORE R1
6476 025706 000002      RTI
6477 025710 122701 000032      2$:  CMPB   #032,R1         ;SEE IF (^Z) TYPED
6478 025714 001003      BNE     3$              ;BR IF NOT (^Z)
6479 025716 104401 012360      TYPE   ,CNTRLZ         ;ECHO (^Z)
6480 025722 000765      BR      1$              ;RETURN
6481 025724 122701 000022      3$:  CMPB   #022,R1         ;SEE IF (^R) TYPED
6482 025730 001003      BNE     4$              ;BR IF NOT (^R)
6483 025732 104401 012365      TYPE   ,CNTRLR         ;ECHO (^R)
6484 025736 000757      BR      1$              ;RETURN
6485 025740 122701 000007      4$:  CMPB   #007,R1         ;SEE IF (^G) TYPED
6486 025744 001003      BNE     5$              ;BR IF NOT (^G)
6487 025746 104401 012377      TYPE   ,CNTRLG         ;ECHO (^G)
6488 025752 000751      BR      1$              ;RETURN
6489 025754 104401 005532      5$:  TYPE   ,INTCHR         ;ECHO INPUT
6490 025760 104401 001315      TYPE   ,$CRLF          ;DO <CR> AND <LF>
6491 025764 000744      BR      1$              ;RETURN
```

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6494
6495      ;:*****
```

```
6496      .SBTTL PREPKB - PREPARE FOR KEYBOARD INPUT
6497      ;*THIS SUBROUTINE CLEARS THE KEYBOARD BUFFER AND THE
6498      ;*DONE BIT, AND CLEARS THE TTY INTERRUPT INPUT WORD
6499      ;*INTCHR, IN PREPARATION FOR NEW TTY INPUT. IT ALSO
6500      ;*ENABLES KBD INTERRUPT.
6501      ;* CALL:
6502      ;*      JSR     PC,PREPKB
6503      ;:*****
```

```
6504
6505 025766 005077 153154      PREPKB: CLR     @STKB           ;CLEAR KBD BUFFER AND DONE BIT
6506 025772 005037 005532      CLR     INTCHR          ;CLEAR TTY INPUT WORD
6507 025776 052777 000100 153140      BIS     #BIT6,@STKS     ;ENABLE KBD INTERRUPT
6508 026004 000207      RTS      PC              ;RETURN
```

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```
6512      ;:*****
6513      .SBTTL ECOBAD - ECHO BAD TTY INPUT
6514      ;*THIS SUBROUTINE ECHOS A CHARACTER WHICH HAD BEEN
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6521 026006 104401 005532
6522 026012 104401 001314
6523 026016 000207
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6534 026020 104407
6535 026022 012737 000200 055662
6536 026030 004737 055624
6537 026034 005737 055662
6538 026040 100406
6539 026042 013737 056126 006126
6540 026050 005037 006130
6541 026054 000436
6542
6543 026056 013700 056130
6544 026062 005001
6545 026064 012702 000006
6546 026070 000241
6547 026072 006100
6548 026074 006101
6549 026076 005302
6550 026100 001373
6551
6552 026102 063700 056126
6553 026106 005501
6554 026110 010037 006126
6555 026114 010137 006130
6556 026120 000241
6557 026122 006100
6558 026124 006101
6559 026126 006100
6560 026130 006101
6561 026132 006100
6562 026134 006101
6563 026136 020127 000037
6564 026142 103403
6565 026144 112737 000001 003144
6566
6567 026152 104401 007354
6568 026156 012746 006126
6569 026162 004737 053366
6570 026166 004737 053702

```

;*TYPED IN AND WAS DETERMINED TO BE INVALID FOR SOME
;*REASON, FOLLOWED BY A QUESTION MARK (?). THEN, A <CR>
;*AND <LF> ARE DONE.
;* CALL - JSR PC,ECOBAD
;*****
ECOBAD: TYPE ,INTCHR ;ECHO BAD CHARACTER
        TYPE ,SQUES ;TYPE <?> AND <CR>, <LF>
        RTS PC ;RETURN
;*****
;*****
;*SIZMEM - SIZE MEMORY, SET LIMITS
;*THIS SUBROUTINE SIZES MEMORY BY CALLING SUBROUTINE $SIZE, AND
;*IT USES THE VALUES RETURNED, TO SET THE LIMITS ON PARAMETER MA.
;*IT ALSO TYPES 'LAST PHYS MEM ADR = XXXXXXXX' (LEAD ZEROS SUPRS'D),
;*AND SETS THE FLAG UBMPRS = 1 IF 22-BIT ADDRESSES ARE USED (11/70).
;*****
SIZMEM: SAVREG ;SAVE R0-R5
        MOV #200,$KT11 ;SET MEM MGT KEY FOR $SIZE
        JSR PC,$SIZE ;SIZE MEMORY
        TST $KT11 ;SEE IF MEM MGT PRESENT
        BMI 8$ ;BR IF MEM MGT PRESENT
        MOV $LSTAD,MAHILM ;SET MEM LIMIT LO BITS
        CLR MAHILM+2 ;CLEAR MEM LIMIT HI BITS
        BR 16$ ;GO TYPE LAST ADDRESS
;SHIFT SAF LEFT 6, AND PUT IN R1-R0
8$: MOV $LSTBK,R0 ;LO BITS
    CLR R1 ;HI BITS
    MOV #6,R2 ;SET LOOP COUNT = 6
12$: LCL ;ROTATE LOOP
    ROL R0
    ROL R1
    DEC R2
    BNE 12$
;ADD VIRTUAL ADDRESS TO SHIFTED SAF TO GET PHYSICAL ADDRESS
    ADD $LSTAD,R0 ;ADD LO BITS
    ADC R1 ;HI BITS
    MOV R0,MAHILM ;SET LO BITS OF MEM LIMIT
    MOV R1,MAHILM+2 ;SET HI BITS OF MEM LIMIT
    CLC
    ROL R0
    ROL R1
    ROL R0
    ROL R1
    ROL R0
    ROL R1
    ROL R0
    ROL R1
    CMP R1,#37 ;SEE IF 22 BIT ADDR
    BLO 16$ ;BR IF NO
    MOVB #1,UBMPRS ;SET 'UNIBUS MAP PRESENT' FLAG
;TYPE 'LAST PHYS MEM ADR = XXXXXXXX'
16$: TYPE ,LSTMEM ;TYPE 'LAST PHYS MEM ADR ='
    MOV #MAHILM,-(SP) ;PUT POINTER ON STACK
    JSR PC,@#$DB20 ;CONVERT BINARY TO OCTAL ASCII
    JSR PC,@#$SUPRS ;TYPE 'XXXXXXXX'

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6571 026172 104401 001315
6572 026176 104401 001315
6573 026202 104410
6574 026204 000207
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6584 026206 016646 000002
6585 026212 104403
6586 026214 006
6587 026215 000
6588 026216 104401 007564
6589 026222 004737 030560
6590 026226 026260
6591 026230 026260
6592 026232 026260
6593 026234 005700
6594 026236 001001
6595 026240 000207
6596 026242 020027 000006
6597 026246 003407
6598 026250 104401 005272
6599 026254 104401 001314
6600 026260 162716 000010
6601 026264 000207
6602 026266 012746 005272
6603 026272 004737 052430
6604 026276 026250
6605 026300 012600
6606 026302 005737 052562
6607 026306 001360
6608 026310 010066 000002
6609 026314 000207

```
TYPE ,SCLF ;TYPE <CR>,<LF>
TYPE ,SCLF
RESREG ;RESTORE R0-R5
RTS PC ;RETURN

*****
* GETPRM - INPUT A SIX-DIGIT OCTAL NO. ON TTY KBD
* ENTER SUBROUTINE WITH OLD PARAMETER VALUE ON STACK. SUBROUTINE
* TYPES OLD VALUE, AND INPUTS NEW VALUE, RETURNING NEW VALUE ON
* TOP OF STACK.
*****
GETPRM: MOV 2(SP),-(SP) ;GET OLD VALUE
        TYPOS ;TYPE IT
        .BYTE 6 ;SIX DIGITS
        .BYTE 0 ;SUPPRESS LEADING ZEROS
        TYPE ,NEWMSG ;TYPE " NEW = "
        JSR PC,RDCHRS ;READ NEW VALUE FROM KBD
        7$ ;(^C) RETURN ADDRESS
        7$ ;(^Z) RETURN ADDRESS
        7$ ;(^U) OR ERROR RETURN ADDRESS
        TST R0 ;SEE IF ANY CHARS TYPED
        BNE 4$ ;BR IF YES
        RTS PC ;RETURN - OLD VALUE UNCHANGED
4$: CMP R0,#6 ;SEE IF > 6 CHARS TYPED
    BLE 8$ ;BR IF NOT BAD
6$: TYPE ,BUFF0 ;ECHO BAD INPUT
    TYPE ,SQUES
7$: SUB #10,(SP) ;FIX ERROR RETURN PC
    RTS PC ;ERROR RETURN
8$: MOV #BUFF0,-(SP) ;PUT POINTER TO CHARS ON STACK
    JSR PC,OCTBIN ;CONVERT DIGITS TO BINARY
    6$ ;ERROR RETURN ADDRESS
    MOV (SP)+,R0 ;GET NEW BINARY VALUE
    TST $HIOCT ;SEE IF HI BITS ARE 0
    BNE 6$ ;BR IF NOT
    MOV R0,2(SP) ;PUT NEW VALUE ON STACK
    RTS PC ;RETURN
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6620 026316 022737 000176 001140
6621 026324 001010
6622 026326 013746 000176
6623 026332 104401 007555
6624 026336 004737 026206
6625 026342 012637 000176
6626 026346 000207

```
*****
SBTTL GTSWRG - OPEN SOFTWARE SWITCH REGISTER FOR MODIFICATION
* THIS SUBROUTINE ALLOWS THE CONTENTS OF THE SOFTWARE SWITCH
* REGISTER TO BE MODIFIED BY TTY INPUT. THE SUBROUTINE TYPES
* "SWR = XXXXXX NEW = " AND WAITS FOR A NEW OCTAL VALUE
* OF UP TO SIX DIGITS TO BE TYPED.
*****
GTSWRG: CMP #SWREG,SWR ;SEE IF SOFTWARE SWR SELECTED
        BNE 6$ ;BR IF NOT
        MOV SWREG,-(SP) ;PUT OLD VALUE ON STACK
        TYPE ,SWRMSG ;TYPE "SWR = "
        JSR PC,GETPRM ;TYPE OLD, GET NEW SWREG VALUE
        MOV (SP)+,SWREG ;STORE NEW VALUE
6$: RTS PC ;RETURN
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```
*****  
*INITMM - INITIALIZE MEMORY MANAGEMENT REGISTERS  
*THIS SUBROUTINE INITIALIZES KIPAR'S AND KIPDR'S, FOR  
*CONTIGUOUS 4K PAGES, STARTING AT PHYSICAL ADDRESS 0.  
*KIPAR7 IS LOADED WITH 177600. TO PRESERVE THE I/O PAGE.  
*22-BIT MODE AND THE UNIBUS MAP ARE ENABLED, IF PRESENT,  
*BUT MEM MGT IS NOT TURNED ON IN THIS SUBROUTINE. HOWEVER,  
*MEM MGT TRAPS ARE ENABLED.  
*****
```

```
*****  
INITMM: SAVREG ;SAVE R0-R5  
CLR R1 ;INIT FOR PAR LOADING  
MOV #KIPAR0,R2 ;ADDR OF FIRST PAR  
MOV #8,R3 ;LOAD 8 PAR'S AND 8 PDR'S  
4$: MOV #77406,-40(R2) ;PDR = 4K,UP,READ/WRITE  
MOV R1,(R2)+ ;LOAD A PAR  
ADD #200,R1 ;UPDATE FOR NEXT PAR  
DEC R3 ;DECREMENT LOOP COUNTER  
BNE 4$ ;LOOP UNTIL ALL 8 ARE LOADED  
MOV #177600,-(R2) ;SET UP KIPAR7 FOR I/O PAGE  
TSTB UBMPRS ;SEE IF 22-BIT ADDRESSES  
BEQ 10$ ;BR IF NOT  
MOV #60,@#SR3 ;ENABLE 22-BIT MODE,AND UNIBUS MAP  
10$: MOV #BIT9,@#SR0 ;ENABLE KT11 TRAPS  
RESREG ;RESTORE R0-R5  
RTS PC ;RETURN  
*****
```

```
*****  
*SERVICE ROUTINE FOR MEM MGT TRAPS  
*****  
KTERHD: JSR PC,REPSUP ;GATHER STATUS FOR PRINTOUT  
MOV @#SR0,$REG5 ;GET SR0  
MOV @#SR1,$REG6 ;GET SR1  
MOV @#SR2,$REG7 ;GET SR2  
MOV #8,@#ERRVEC ;SET TIME-OUT VECTOR  
MOV #PR7,@#ERRVEC+2  
MOV @#SR3,$REG10 ;GET SR3  
BR 10$  
8$: CMP (SP)+,(SP)+ ;CLEAN UP STACK  
MOVB #3,DF30+18. ;FIX PRINTOUT FOR NO SR3  
CLRB DH704+11.  
10$: ERROR 121 ;KT11 FAILURE  
JMP HLTPRG ;ABORT !!!  
*****
```

```
*****  
*PREPAR - PREPARE MEM MGT FOR RELOCATION  
*THIS SUBROUTINE CALLS INITMM, AND THEN SETS UP CONSTANT  
*FOR KIPAR6 IN SAVPAR, AND LOADS THE UNIBUS MAP REGISTERS,  
*IF PRESENT.  
*****
```



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6684 026532 104407
6685 026534 004737 026350
6686 026540 013700 005602
6687 026544 042700 017777
6688 026550 013701 005604
6689 026554 010003
6690 026556 010104
6691 026560 006100
6692 026562 006101
6693 026564 006100
6694 026566 006101
6695 026570 000301
6696 026572 006100
6697 026574 106001
6698 026576 010137 003206
6699 026602 105737 003144
6700 026606 001420
6701
6702 026610 012700 170200
6703 026614 012701 000037
6704 026620 010320
6705 026622 010420
6706 026624 062703 020000
6707 026630 005504
6708 026632 077106
6709 026634 042765 160000 000010
6710 026642 142765 000003 000007
6711 026650 104410
6712 026652 000207
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6720 026654 005737 006130
6721 026660 001404
6722 026662 012737 160000 005546
6723 026670 000412
6724 026672 023727 006126 157776
6725 026700 103370
6726 026702 013737 006126 005546
6727 026710 062737 000002 005546
6728 026716 162737 006000 005546
6729 026724 000207
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*****
PREPAR: SAVREG ;SAVE R0-R5
JSR PC,INITMM ;INIT MEM MGT REGISTERS
MOV PMA,R0 ;LO BITS OF MA
BIC #17777,R0 ;MASK FOR BITS 13-15
MOV PMA+2,R1 ;HI BITS OF MA
MOV R0,R3 ;SAVE THESE BITS
MOV R1,R4
ROL R0 ;GET MA BITS 13-21 INTO R1 BITS 7-15
ROL R1
ROL R0
ROL R1
SWAB R1
ROL R0
RORB R1
MOV R1,SAVPAR ;CONSTANT FOR LOADING PAR6 LATER
TSTB UBMPRS ;SEE IF UNIBUS MAP ENABLED
BEQ 9$ ;BR IF NOT (NO UNIBUS MAPPING)
;PREPARE FOR 22-BIT ADDRESSING - LOAD UNIBUS MAP REGISTERS
MOV #MAPLOO,R0 ;STARTING ADDR OF MAP REGISTERS
MOV #31,,R1 ;SET REGISTER COUNTER
4$: MOV R3,(R0)+ ;LOAD A MAP REGISTER
MOV R4,(R0)+
ADD #20000,R3 ;ADD 4K WORDS
ADC R4
SOB R1,4$ ;LOOP UNTIL 31(DEC) ARE LOADED
BIC #160000,P.BALO(R5) ;CLEAR BA BITS 13-17
BICB #3,P.BAHI(R5)
9$: RESREG ;RESTORE R0-R5
RTS PC ;RETURN

*****
;* FNDXDP - FIND STARTING ADR OF XXDP LOADER , AND STORE IT
;* IN XXDPAD.
*****
FNDXDP: TST MAHILM+2 ;TEST HI BITS OF UPPER MEMORY LIMIT
BEQ 6$ ;BR IF HI BITS ARE 0
4$: MOV #160000,XXDPAD ;START OF 28K IS END OF XXDP
BR 8$
6$: CMP MAHILM,#157776 ;SEE IF MEM LIMIT > OR = 157776
BHIS 4$ ;BR IF YES
MOV MAHILM,XXDPAD ;HI MEM LIMIT IS END OF XXDP
ADD #2,XXDPAD ;ADD 2 BYTES
8$: SUB #6000,XXDPAD ;COMPUTE START OF XXDP
RTS PC ;RETURN

*****
;*CHKXDP - CHECK FOR IMMINENT OVERLAY OF XXDP LOADER
;*THIS SUBROUTINE DETERMINES IF THE CURRENT MA AND WC COMBINATION
;*WOULD CAUSE OVERLAY OF THE XXDP LOADER ON A WRITE TRANSFER.
;*IF SO, AN ATTEMPT IS MADE TO MOVE THE LOADER INTO UNUSED MEMORY.
;*IF THIS IS NOT POSSIBLE, A RETURN IS MADE TO THE ADDRESS
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6739 ;*FOLLOWING THE CALL. THE SAVE ADDRESS IS STORED IN XXDPSAV AND XXDPSAV+2.
6740 ;*IF IT IS NOT NECESSARY TO MOVE THE LOADER, A NORMAL RETURN IS MADE.
6741 ;* CALL - JSR PC,CHKXDP
6742 ;*
6743 ;* <RETURN ADR FOR NO SAVE>
6744 ;*****
6744 026726 104407 CHKXDP: SAVREG ;SAVE R0-R5
6745 ;SEE IF LOADER MUST BE PROTECTED
6746 026730 105737 003136 TSTB DULACS ;SEE IF DUAL-ACCESS DATA TEST
6747 026734 001005 BNE 2$ ;BR IF YES
6748 026736 012700 005626 MOV #TSTLST,R0 ;GET ADRS OF TEST LIST
6749 026742 005760 000040 TST RWTINX(R0) ;SEE IF R/W DATA TEST TO BE RUN
6750 026746 001406 BEQ 4$ ;EXIT IF NOT TO BE RUN
6751 026750 005737 005760 2$: TST PT ;SEE IF DEFAULT DATA TEST
6752 026754 001403 BEQ 4$ ;BR IF DEFAULT DATA TEST
6753 026756 005737 005764 TST MA+2 ;SEE IF HI MEM ADR = 0
6754 026762 001404 BEQ 6$ ;BR IF 0
6755 026764 062716 000002 4$: ADD #2,(SP) ;FIX UP NORMAL RETURN PC
6756 026770 104410 5$: RESREG ;RESTORE R0-R5
6757 026772 000207 RTS PC ;RETURN
6758 026774 013700 005766 6$: MOV WC,R0 ;GET WORD COUNT LO BITS
6759 027000 005300 DEC R0 ;DECREMENT IT
6760 027002 005001 CLR R1 ;SET HI BITS = 0
6761 027004 000241 CLC ;DOUBLE IT FOR BYTES
6762 027006 006100 ROL R0
6763 027010 006101 ROL R1
6764 027012 063700 005762 ADD MA,R0 ;COMPUTE LAST ADR OF XFER IN R1-R0
6765 027016 005501 ADC R1
6766 027020 063701 005764 ADD MA+2,R1
6767 027024 105037 003135 CLR B XDPSVD ;INIT LOADER SAVE INDICATOR
6768 027030 004737 026654 JSR PC,FNDXDP ;COMPUTE START ADR OF XXDP
6769 027034 013703 005546 MOV XXDPAD,R3 ;GET START OF XXDP LOADER
6770 027040 062703 005776 ADD #5776,R3 ;COMPUTE LAST ADDRESS OF XXDP
6771 027044 023703 005762 CMP MA,R3 ;SEE IF MA > LAST ADR OF XXDP
6772 027050 101345 BHI 4$ ;BR IF YES - DON'T MOVE XXDP
6773 027052 005701 TST R1 ;CHECK HI BITS OF LAST XFER ADDRESS
6774 027054 001003 BNE 8$ ;BR IF NOT 0
6775 027056 020037 005546 CMP R0,XXDPAD ;CHECK LO BITS OF LAST XFER ADR
6776 027062 103740 BLO 4$ ;BR IF < XXDP ADR - DON'T MOVE XXDP
6777 027064 105237 003135 8$: INCB XDPSVD ;SET SAVE INDICATOR
6778 ;ATTEMPT TO FIND A SAVE AREA FOR LOADER
6779 027070 023727 005762 100000 CMP MA,#100000 ;SEE IF MA > OR = 16K
6780 027076 103410 BLO 12$ ;BR IF NOT
6781 027100 012737 072000 005550 MCV #72000,XXDPSAV ;SAVE ADR = 72000
6782 027106 005037 005552 CLR XXDPSAV+2
6783 027112 004737 027202 10$: JSR PC,SAVXDP ;SAVE THE LOADER
6784 027116 000722 BR 4$ ;BR TO RETURN
6785 027120 062700 000002 12$: ADD #2,R0 ;ADD 2 BYTES TO LAST ADR OF XFER
6786 027124 005501 ADC R1
6787 027126 010002 MOV R0,R2 ;GET A COPY
6788 027130 010103 MOV R1,R3
6789 027132 062702 005776 ADD #5776,R2 ;COMPUTE END OF SAVE AREA
6790 027136 005503 ADC R3
6791 027140 020337 006130 CMP R3,MAHILM+2 ;SEE IF THIS EXCEEDS MEMORY LIMIT
6792 027144 101011 BHI 16$ ;BR IF YES - CAN'T SAVE LOADER
6793 027146 001003 BNE 14$ ;BR IF < MEM LIMIT
6794 027150 020237 006126 CMP R2,MAHILM ;SEE IF LO BITS EXCEED MEM LIMIT
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6795 027154 101005
6796 027156 010037 005550
6797 027162 010137 005552
6798 027166 000751
6799 027170 017616 000000
6800 027174 105037 003135
6801 027200 000673

14\$: BHI 16\$;BR IF YES - CAN'T SAVE LOADER
MOV R0,XDPSAV ;SET SAVE ADR IN HI MEMORY
MOV R1,XDPSAV+2
BR 10\$;GO SAVE LOADER
16\$: MOV @ (SP), (SP) ;FIX UP NO-SAVE RETURN PC
CLRB XDPSVD ;CLEAR THE SAVE INDICATOR
BR 5\$;BR TO RETURN

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* SAVXDP - SAVE THE XXDP LOADER IN HI MEMORY
* THIS SUBROUTINE MOVES THE XXDP LOADER, WHICH RESIDES AT THE
* PHYSICAL ADDRESS STORED IN XXDPAD, TO THE PHYSICAL ADDRESS
* STORED IN XDPSAV AND XDPSAV+2. A TOTAL OF 1536(DEC) WORDS ARE MOVED.
*
* GETXDP - RESTORE THE XXDP LOADER TO ORIGINAL LOCATION
* THIS SUBROUTINE MOVES THE RELOCATED XXDP LOADER FROM THE ADDRESS
* STORED IN XDPSAV, XDPSAV+2, BACK TO LOCATION XXDPAD (ITS ORIGINAL
* ADDRESS).

6816 027202 104407
6817 027204 005004
6818 027206 000410
6819 027210 104407
6820 027212 105037 003134
6821 027216 105737 003135
6822 027222 001425
6823 027224 012704 000001
6824 027230 105737 000041
6825 027234 001420
6826 027236 012702 003000
6827 027242 013703 005546
6828 027246 013700 005550
6829 027252 013701 005552
6830 027256 005737 055662
6831 027262 100417
6832 027264 005704
6833 027266 001005
6834 027270 012320
6835 027272 005302
6836 027274 001375
6837 027276 104410
6838 027300 000207
6839 027302 062700 006000
6840 027306 062703 006000
6841 027312 014043
6842 027314 005302
6843 027316 001375
6844 027320 000766
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6846 027322 004737 026350
6847 027326 006100
6848 027330 006101
6849 027332 006100
6850 027334 006101

SAVXDP: SAVREG ;SAVE R0-R5
CLR R4 ;SET INDICATOR TO SAVE XXDP
BR XDP1
GETXDP: SAVREG ;SAVE R0-R5
CLRB XOVLAD ;CLEAR XXDP OVERLAID INDICATOR
TSTB XDPSVD ;SEE IF XXDP WAS SAVED
BEQ XDP2 ;BR IF NOT SAVED
MOV #1,R4 ;SET INDICATOR TO GET XXDP
XDP1: TSTB @#41 ;SEE IF LOADED BY XXDP
BEQ XDP2 ;BR IF NOT
MOV #1536.,R2 ;GET SET TO MOVE 1536(DEC) WORDS
MOV XXDPAD,R3 ;GET ORIG ADR OF START OF XXDP
MOV XDPSAV,R0 ;GET SAVE ADR LO BITS
MOV XDPSAV+2,R1 ;HI BITS
TST \$KT11 ;SEE IF MEM MGT PRESENT
BMI XDP3 ;BR IF PRESENT
TST R4 ;SEE IF WANT TO SAVE OR GET XXDP
BNE XDP4 ;BR IF WANT TO GET XXDP
6\$: MOV (R3)+, (R0)+ ;SAVE A WORD
DEC R2 ;SEE IF 1536(DEC) WORDS YET
BNE 6\$;BR IF NOT YET
XDP2: RESREG ;RESTORE R0-R5
RTS PC ;RETURN
XDP4: ADD #6000,R0 ;POINT TO END OF SAVE AREA
ADD #6000,R5 ;POINT TO END OF XXDP LOADER AREA
8\$: MOV -(R0), -(R3) ;GET A WORD
DEC R2 ;SEE IF 1536(DEC) WORDS YET
BNE 8\$;BR IF NOT YET
BR XDP2 ;GO EXIT
;COME HERE IF MEM MGT
XDP3: JSR PC,INITMM ;INIT MEM MGT REGISTERS
ROL R0 ;GET ADR BITS 13-21 INTO R1 BITS 7-15
ROL R1

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6851 027336 000301 SWAB R1
6852 027340 006100 ROL R0
6853 027342 106001 RORB R1
6854 027344 010137 172354 MOV R1,@#KIPAR6 ;SET UP PAR6
6855 027350 013701 005550 MOV XDPSAV,R1 ;GET LO ADRS BITS
6856 027354 042701 160000 BIC #160000,R1 ;FIX UP R1 TO REFERENCE PAR6
6857 027360 052701 140000 BIS #140000,R1
6858 027364 005704 16$: TST R4 ;SEE IF WANT TO SAVE OR GET XXDP
6859 027366 001007 BNE 18$ ;BR IF WANT TO GET XXDP
6860 027370 012305 MOV (R3)+,R5 ;SAVE A WORD
6861 027372 005237 177572 INC @#SRO ;TURN ON MEMORY MANAGEMENT
6862 027376 010521 MOV R5,(R1)+
6863 027400 005337 177572 DEC @#SRO ;TURN OFF MEMORY MANAGEMENT
6864 027404 000406 BR 20$
6865 027406 005237 177572 18$: INC @#SRO ;TURN ON MEMORY MANAGEMENT
6866 027412 012105 MOV (R1)+,R5 ;GET A WORD
6867 027414 005337 177572 DEC @#SRO ;TURN OFF MEMORY MANAGEMENT
6868 027420 010523 MOV R5,(R3)+
6869 027422 032701 020000 20$: BIT #BIT13,R1 ;SEE IF OVERFLOW 0 PAGE 7
6870 027426 001405 BEQ 22$ ;BR IF NOT
6871 027430 042701 020000 BIC #BIT13,R1 ;SET PAGE = 5 AG IN
6872 027434 062737 000200 172354 ADD #200,@#KIPAR6 ;UPDATE PAR BY 4K
6873 027442 005302 22$: DEC R2 ;SEE IF 1536 WORDS YET
6874 027444 001347 BNE 16$ ;BR IF NOT YET
6875 027446 000713 BR XDP2 ;BR TO RETURN
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6880 ;*****
6881 ;SBTTL INITSS - INITIALIZE SUBSYSTEM
6882 ;*
6883 ;*THIS SUBROUTINE INITIALIZES THE DRIVER AND ITS PARAMETERS
6884 ;*AND DOES A SUBSYSTEM CLEAR.
6885 ;* USES - R2,R5
6886 ;* CALL:
6887 ;* JSR PC,INITSS
6888 ;*****
6889 027450 012737 042130 003046 INITSS: MOV #ERRHDL,A.ABNL ;SET UP ABNORMAL ERROR RETURN ADDRESS
6890 027456 012737 040652 003044 MOV #ERRFRE,A.NORM ;SET UP NORMAL RETURN ADDRESS
6891 027464 013702 003036 MOV RKBAS,R2 ;GET ADDRESS OF RK611 REGISTERS
6892 027470 012705 002630 MOV #PARM0,R5 ;GET ADDRESS OF PARAMETER BLOCK
6893 027474 105037 003143 CLRB NORTRY ;CLEAR 'NO-RETRY' FLAG
6894 027500 004737 027556 JSR PC,CLRPRM ;CLEAR DRIVER INPUT PARAMETERS
6895 027504 112765 000177 00.0C' MOVB #SUBCLR,P.CMND(R5) ;SET SUBSYSTEM CLEAR COMMAND
6896 027512 004737 040252 JSR PC,DRVCAL ;DO SUBSYSTEM CLEAR
6897 027516 113765 005510 000000 MOVB DRIVE,P.DRVN(R5) ;SET CURRENT DRIVE NO.
6898 027524 113737 005510 002714 MOVB DRIVE,PARM1 ;SET DRIVE NO. IN ALTERNATE P.B.
6899 027532 113765 003125 000007 MOVB FORMAT,P.CS1H(R5) ;SET CURRENT DRIVE FORMAT
6900 027540 153765 003124 000007 BISB TYPFMT,P.CS1H(R5) ;SET DRV TYPE BIT
6901 027546 116537 000007 002723 MOVB P.CS1H(R5),PARM1+P.CS1H ;COPY DR. TYPE & FORMAT INTO PARM1
6902 027554 000207 RTS ;SUBROUTINE EXIT
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6913 027556 010046
6914 027560 010546
6915 027562 010500
6916 027564 062705 000016
6917 027570 005020
6918 027572 020005
6919 027574 001375
6920 027576 012605
6921 027600 012600
6922 027602 000207
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6942 027604 105037 003124
6943 027610 004737 027450
6944 027614 113765 005510 000000
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6946 027622 001012
6947 027624 122737 000013 000041
6948 027632 001006
6949 027634 105737 003116
6950 027640 001003
6951 027642 104401 010075
6952 027646 000517
6953 027650 042712 000100
6954 027654 113762 005510 000010
6955 027662 105737 003124
6956 027666 001003
6957 027670 012712 000001
6958 027674 000402
6959 027676 012712 002001
6960 027702 005037 005542
6961 027706 005237 005542
6962 027712 001375

```
.SBTTL CLRPRM - CLEAR DRIVER INPUT PARAMETERS
:*THIS SUBROUTINE ZEROS THE FIRST 14 BYTES IN THE DRIVER
:*PARAMETER BLOCK (WHOSE ADDRESS IS IN R5).
:* CALL - JSR PC,CLRPRM
:*****

CLRPRM: MOV R0,-(SP) ;SAVE R0
        MOV R5,-(SP) ;SAVE R5
        MOV R5,R0 ;GET PARAMETER BLOCK ADDRESS
1$: ADD #P.CS1,R5 ;COMPUTE LIMIT ADDRESS
    CLR (R0)+ ;CLEAR A WORD IN PARAMETER BLOCK
    CMP R0,R5 ;SEE IF DONE YET
    BNE 1$ ;BR IF NOT DONE YET
    MOV (SP)+,R5 ;RESTORE R5
    MOV (SP)+,R0 ;RESTORE R0
    RTS PC ;RETURN

:*****

.SBTTL SCNDRV - SCAN DRIVE FOR STATUS
:*THIS SUBROUTINE SCANS THE SELECTED DRIVE TO INSURE
:*THAT THE DRIVE IS ON-LINE, READY, NOT WRITE-LOCKED,
:*AND NOT LOADED WITH AN ALIGNMENT CARTRIDGE. IF ANY
:*OF THESE CONDITIONS ARE NOT MET, AN APPROPRIATE
:*MESSAGE IS TYPED, RK06 INTERRUPT IS DISABLED, AND
:*A RETURN IS MADE TO THE ADDRESS LISTED IMMEDIATELY
:*AFTER THE CALL TO SCNDRV. THE REQUESTED DRIVE NO.
:*MUST BE PASSED TO THE SUBROUTINE IN WORD 'DRIVE'.
:*ALSO, IF RUNNING FROM ADRS 200 AND RK06 IS LOAD MEDIUM,
:*DRIVE 0 WILL BE REJECTED FOR USE.
:* CALL - JSR PC,SCNDRV
:* <ERROR RETURN ADDRESS>
:*****

SCNDRV: CLRB TYPFMT ;ASSUME RK06 FOR FIRST TRY
        JSR PC,INITSS ;INITIALIZE DRIVER AND CLEAR SUBSYSTEM
        MOV# DRIVE,P.DRVN(R5) ;GET DRIVE NUMBER
;SEE IF DRIVE 0 IS XXDP LOAD MEDIUM
    BNE 3$ ;BR IF NOT DRIVE 0
    CMP# #13,@#41 ;SEE IF RK06 IS XXDP MEDIUM
    BNE 3$ ;BR IF NOT
    TSTB MDFLAG ;SEE IF 200 START
    BNE 3$ ;BR IF NOT
    TYPE ,DROXDP ;TYPE 'DRIVE 0 IS LOAD MEDIUM'
    BR 2$ ;TAKE ERROR EXIT
3$: BIC #IE,(R2) ;DISABLE RK06 INTERRUPT
    MOV# DRIVE,RKCS2(R2) ;SET DRIVE NO. IN RKCS2
    TSTB TYPFMT ;SEE IF RK07
    BNE 5$ ;BR IF YES
    MOV #GO,(R2) ;ELSE SET GO FOR RK06 IN CS1
    BR 7$
5$: MOV #<CDT!GO>,(R2) ;SET GO FOR RK07 IN CS1
7$: CLR SCRACH ;CLEAR STALL CTR
14$: INC SCRACH ;INCREMENT STALL COUNTER
    BNE 14$ ;STALL FOR SEVERAL MILLI-SEC
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6963 027714 032762 001000 000010 BIT #MDS,RKCS2(R2) ;SEE IF MDS ERROR
6964 027722 001417 BEQ 18$ ;BR IF NOT
6965 027724 112765 000101 000001 MOVB #SELDRV,P.CMND(R5) ;SET CMND FOR REPORT
6966 027732 011265 000016 MOV (R2),P.CS1(R5) ;GET RKCS1
6967 027736 004737 050156 JSR PC,I.CS1 ;GET OTHER RK611 REGISTERS
6968 027742 004737 041650 JSR PC,REPSUP ;SET UP ERROR REPORT
6969 027746 104052 ERROR 52 ;REPORT MDS ERROR
6970 027750 052737 000200 005504 BIS #ABORT,RECODE ;SET ABORT FLAG
6971 027756 000137 043724 JMP ALLTRM ;GO ABORT TESTING
6972 027762 032762 000040 000014 18$: BIT #DTYE,RKER(R2) ;SEE IF DRV TYPE ERROR
6973 027770 001403 BEQ 20$ ;BR IF NO, =RK06
6974 027772 152737 000004 003124 BISB #B.CDT,TYPFMT ;ELSE SET RK07 FLAG
6975 030000 004737 027450 20$: JSR PC,INITSS ;INIT THE SUBSYSTEM
6976 030004 112765 000141 000001 MOVB #RDSTAT,P.CMND(R5) ;SET READ DRIVE STATUS COMMAND
6977 030012 012737 025550 003046 MOV #NEDHDL,A.ABNL ;SET NED ABNORMAL RETURN ADDRESS
6978 030020 012737 000377 005540 MOV #377,NEWON ;INIT. ON-LINE INDICATOR
6979 030026 004737 040252 JSR PC,DRVCAL ;READ ALL DRIVE STATUS
6980 ;SEE IF THIS DRIVE EXISTS AND IS ON-LINE
6981 030032 012737 042130 003046 MOV #ERRHDL,A.ABNL ;RESTORE ABNL RETURN ADDRESS
6982 030040 022737 000377 005540 CMP #377,NEWON ;SEE IF NED INDICATION ON THIS DRIVE
6983 030046 001425 BEQ 4$ ;BR IF NED NOT SET
6984 030050 113737 005510 007617 MOVB DRIVE,BADDRV+6 ;GET DRIVE NO. INTO BUF
6985 030056 152737 000060 007617 BISB #'0,BADDRV+6 ;CONVERT TO ASCII
6986 030064 104401 007611 TYPE ,BADDRV ;TYPE 'DRIVE X'
6987 030070 104401 007622 TYPE ,NXDRIV ;TYPE 'NON-EXISTENT'
6988 030074 132737 000200 001341 BITB #BIT7,$ENVM ;SEE IF UNDER APT
6989 030102 001401 BEQ 2$ ;BR IF NO
6990 030104 104123 ERROR 123 ;NED UNDER APT SIZING
6991 ;SERVICE ERRORS HERE
6992 030106 042762 000100 000000 2$: BIC #IE,RKCS1(R2) ;DISABLE RK06 INTERRUPT
6993 030114 017616 000000 MOV @ (SP), (SP) ;SET UP ERROR RETURN ADDRESS
6994 030120 000207 RTS PC ;ERROR RETURN
6995 ;SEE IF DRIVE IS READY
6996 030122 032765 000200 000040 4$: BIT #S.DRY,P.A00(R5) ;TEST FOR DRIVE READY
6997 030130 001013 BNE 6$ ;BR IF DRIVE IS READY
6998 030132 113737 005510 007617 MOVB DRIVE,BADDRV+6 ;GET DRIVE NO. INTO BUF
6999 030140 152737 000060 007617 BISB #'0,BADDRV+6 ;CONVERT TO ASCII
7000 030146 104401 007611 TYPE ,BADDRV ;TYPE 'DRIVE X'
7001 030152 104401 007636 TYPE ,NTREDY ;TYPE 'NOT READY'
7002 030156 000753 BR 2$ ;TAKE ERROR EXIT
7003 ;SEE IF DRIVE IS WRITE ENABLED
7004 030160 032765 004000 000040 6$: BIT #S.WRL,P.A00(R5) ;SEE IF WRITE LOCK SET
7005 030166 001413 BEQ 8$ ;BR IF WRITE LOCK NOT SET
7006 030170 113737 005510 007617 MOVB DRIVE,BADDRV+6 ;GET DRIVE NO.
7007 030176 152737 000060 007617 BISB #'0,BADDRV+6 ;CONVERT TO ASCII
7008 030204 104401 007611 TYPE ,BADDRV ;TYPE 'DRIVE X'
7009 030210 104401 007650 TYPE ,WRTLOK ;TYPE 'WRITE-LOCKED'
7010 030214 000734 BR 2$ ;TAKE ERROR EXIT
7011 ;SEE IF DRIVE NOT LOADED WITH ALIGNMENT CARTRIDGE
7012 030216 112765 000103 000001 8$: MOVB #PACK,P.CMND(R5) ;SET PACK ACKNOWLEDGE COMMAND
7013 030224 004737 040252 JSR PC,DRVCAL ;SET VOLUME VALID
7014 030230 112765 000113 000001 MOVB #RECAL,P.CMND(R5) ;SET RECAL COMMAND
7015 030236 004737 040252 JSR PC,DRVCAL ;RECALIBRATE DRIVE
7016 030242 112765 000121 000001 MOVB #RDDATA,P.CMND(R5) ;SET READ COMMAND
7017 030250 105737 003124 TSTB TYPFMT ;SEE IF RK07
7018 030254 001004 BNE 25$ ;BR IF YES
```

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D 11

SEQ 0133

7019	030256	012765	000632	000002		MOV	#632,P.CYLN(R5)		
7020	030264	000403				BR	27\$		
7021	030266	012765	001456	000002	25\$:	MOV	#1456,P.CYLN(R5)		
7022	030274	112765	000002	000005	27\$:	MOVB	#LSTTRK,P.TRCK(R5)	;SET TRACK=2	
7023	030302	012765	063520	000010		MOV	#RWBUF,P.BALO(R5)	;BUS ADDRESS	
7024	030310	012765	177774	000012		MOV	#-4,P.WC(R5)	;READ 4 WORDS	
7025	030316	142765	000020	000007		BICB	#B.CFMT,P.GS1H(R5)	;SET 22 SECTOR FORMAT	
7026	030324	153765	003124	000007		BISB	TYPE,P.CS1H(R5)	;SET CORRECT DRV TYPE	
7027	030332	004737	040252			JSR	PC,DRVCL	;READ 4 WORDS OF BAD SECTOR FILE	
7028	030336	032737	100000	005504		BIT	#ANYDER,RECODE	;SEE IF DATA ERROR	
7029	030344	001402				BEQ	10\$;BR IF OK	
7030	030346	104401	012307			TYPE	,BAD632	;TYPE READ ERROR MESSAGE	
7031	030352	022737	177777	063526	10\$:	CMP	#177777,RWBUF+6	;SEE IF ALL 1'S IN I.D. WORD 3	
7032	030360	001013				BNE	12\$;BR IF NOT ALL 1'S	
7033	030362	113737	005510	007617		MOVB	DRIVE,BADDRV+6	;GET DRIVE NO.	
7034	030370	152737	000060	007617		BISB	#'0,BADDRV+6	;CONVERT TO ASCII	
7035	030376	104401	007611			TYPE	,BADDRV	;TYPE 'DRIVE X'	
7036	030402	104401	007663			TYPE	,ALNPAK	;TYPE 'LOADED WITH ALIGN PACK'	
7037	030406	000637				BR	2\$;TAKE ERROR EXIT	
7038								;ERROR FREE RETURN	
7039	030410	062716	000002		12\$:	ADD	#2,(SP)	;FIX UP RETURN PC	
7040	030414	000207				RTS	PC	;RETURN	

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```
*****  
;SETUP - SET UP FOR LOOP ON ERROR  
;THIS SUBROUTINE CANNOT BE CALLED BY ANY OTHER  
;SUBROUTINE --- ONLY MAIN-LINE CODE !!!!  
*****
```

7049	030416	011637	001076			SETUP: MOV	(SP),@#STACK-2	;MOVE RETURN PC ON STACK	
7050	030422	012706	001076			MOV	#STACK-2,SP	;RE-INIT THE STACK POINTER	
7051	030426	005037	005504			CLR	RECODE	;CLEAR ERROR RECOVERY FLAGS	
7052	030432	105037	003126			CLRB	ERRCNT	;CLEAR RETRY ERROR COUNT	
7053	030436	012705	002630			MOV	#PARMO,R5	;SET PARAM BLK ADRS	
7054	030442	112765	000177	000001		MOVB	#SUBCLR,P.CMND(R5)	;SET SUBSYSTEM CLEAR CMND	
7055	030450	004737	040252			JSR	PC,DRVCL	;CLEAR THE SUBSYSTEM	
7056	030454	012737	000000	177776		MOV	#PRO,@#PS	;RE-ESTABLISH PRIORITY 0	
7057	030462	000207				RTS	PC	;RETURN	

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*****  
;SBTTL CHKITR - CHECK CURRENT TEST ITERATION NUMBER  
;THIS SUBROUTINE LOADS THE ITERATION NUMBER FOR THE CURRENT  
;TEST INTO $TIMES, AND CHECKS IT TO DETERMINE IF THIS TEST  
;SHOULD BE RUN. IF ITERATION NO. = 0, THE SUBROUTINE RETURNS  
;TO THE STORED PC (AND THE TEST WILL BE SKIPPED). IF IT IS  
;NOT 0, THE RETURN IS MADE TO PC+2, AND THE TEST WILL  
;BE RUN. HOWEVER, ON THE FIRST PASS (QUICK VERIFY) OF A RUN  
;WHICH STARTED AT ADRS 200, ALL TESTS WILL BE RUN ONCE.
```

7071	030464	010146				CHKITR: MOV	R1,-(SP)	;SAVE R1	
7072	030466	112737	000144	001115		MOVB	#100,\$ERMAX	;SET MAX ERROR CNT TO 100 FOR \$SCOPE	
7073	030474	105737	003136			TSTB	DULACS	;SEE IF DUAL-ACCESS FLAG SET	
7074	030500	001006				BNE	3\$;BR IF YES	

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7075 030502 105737 003116          TSTB   MDFLAG          ;SEE IF 200 START
7076 030506 001007                   BNE    4$              ;BR IF NOT
7077 030510 005737 001326          TST    $PASS          ;SEE IF FIRST PASS
7078 030514 001004                   BNE    4$              ;BR IF NOT
7079 030516 012737 000001 001304 3$:  MOV    #1,$TIMES      ;SET UP FOR 1 ITERATION
7080 030524 010410                   BR     1$              ;GO RUN DUAL-ACCESS TEST
7081 030526 113701 001102          4$:  MOVB   $STNM,R1     ;GET CURRENT TEST NUMBER
7082 030532 005301                   DEC    R1              ;DECREMENT BY 1
7083 030534 006301                   ASL    R1              ;DOUBLE IT, TO GET TEST LIST INDEX
7084 030536 016137 005626 001304  MOV    TSTLST(R1),$TIMES ;LOAD ITERATION NUMBER
7085 030544 001403                   BEQ    2$              ;BR IF TEST SHOULD BE SKIPPED
7086 030546 062766 000004 000002 1$:  ADD    #4,2(SP)        ;ADJUST RETURN PC TO RUN THIS TEST
7087 030554 012601          2$:  MUV    (SP)+,R1      ;RESTORE R1
7088 030556 000207                   RTS     PC              ;RETURN
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*****
.SBTTL  RDCHRS - READ A STRING OF KBD INPUT CHARS
;THIS SUBROUTINE READS A STRING OF UP TO EIGHTY INPUT
;CHARACTERS AT THE KBD, TERMINATED BY <CR>, AND PLACES
;THEM IN BUFF0, TERMINATED BY A NULL (0) BYTE.
;RUB-OUT AND (^U) FEATURES ARE PROVIDED.
;IMMEDIATELY FOLLOWING THE SUBROUTINE CALL, THREE SPECIAL
;RETURN ADDRESSES MUST BE LISTED: THE FIRST RETURN IS
;TAKEN IF (^C) IS TYPED, THE SECOND IS TAKEN IF (^Z) IS
;TYPED, AND THE THIRD IS TAKEN IF (^U) OR INVALID INPUT IS TYPED.
;IF (^G) IS TYPED, THE SOFTWARE SWITCH REGISTER IS OPENED
;FOR MODIFICATION, IF SELECTED, AND THEN THE (^G) RETURN
;IS TAKEN.
;THE NUMBER OF INPUT CHARACTERS IN THE BUFFER IS RETURNED
;IN R0.
;
; CALL -   JSR    PC,RDCHRS
;          <CONTROL-C RETURN ADDRESS>
;          <CONTROL-Z RETURN ADDRESS>
;          <CONTROL-U OR ERROR RETURN ADDRESS>
;          RETURN
*****
```

```
RDCHRS:
      MOV    R1,-(SP)          ;SAVE R1
      MOV    R2,-(SP)          ;SAVE R2
      CLR    R0                ;INITIALIZE CHARACTER COUNT
      CLR    R1                ;INITIALIZE RUB-OUT INDICATOR
;READ A CHARACTER
2$:  RDCHR
      MOVB   (SP)+,R2          ;GET CHARACTER INTO R2
;CHECK FOR (^C)
      CMPB   #003,R2          ;SEE IF (^C) TYPED
      BNE    4$              ;BR IF NOT (^C)
      TYPE   ,CNTRLC          ;ECHO (^C)
3$:  MOV    @4(SP),4(SP)       ;PUT RETURN ADDRESS ON STACK
      BR     24$              ;BR TO TAKE EXIT
;CHECK FOR (^Z)
4$:  CMPB   #032,R2          ;SEE IF (^Z) TYPED
```



```
7131 030622 001006          BNE      6$          ;BR IF NOT (^Z)
7132 030624 104401 012360    TYPE    ,CNTRLZ     ;ECHO (^Z)
7133 030630 062766 000002    000004  ADD     #2,4(SP)    ;MAKE OLD PC POINT TO NEXT RETURN ADR.
7134 030636 000763          BR       3$          ;BR TO TAKE (^Z) EXIT
7135          ;CHECK FOR (^U)
7136 030640 122702 000025    6$:     CMPB    #025,R2 ;SEE IF (^U) TYPED
7137 030644 001006          BNE     8$          ;BR IF NOT (^U)
7138 030646 104401 012372    TYPE    ,CNTRLU     ;ECHO (^U)
7139 030652 062766 000004    000004  7$:     ADD     #4,4(SP)    ;MAKE OLD PC POINT TO NEXT RETURN ADDR.
7140 030660 000752          BR       3$          ;BR TO TAKE (^U) EXIT
7141          ;CHECK FOR (^G)
7142 030662 122702 000007    8$:     CMPB    #007,R2 ;SEE IF (^G) TYPED
7143 030666 001005          BNE     9$          ;BR IF NOT (^G)
7144 030670 104401 012377    TYPE    ,CNTRLG     ;ECHO (^G)
7145 030674 004737 026316    JSR     PC,GTSWRG   ;OPEN SOFTWARE SWITCH REG. FOR CHANGE
7146 030700 000764          BR       7$          ;TAKE (^U) RETURN
7147          ;CHECK FOR RUB-OUT (DELETE)
7148 030702 122702 000177    9$:     CMPB    #177,R2 ;SEE IF RUB-OUT (DEL) TYPED
7149 030706 001020          BNE     14$         ;BR IF NOT RUB-OUT
7150 030710 005700          TST     R0          ;CHECK THE CHARACTER COUNT
7151 030712 001726          BEQ     2$          ;BR IF COUNT = 0
7152 030714 005701          TST     R1          ;CHECK THE RUB-OUT INDICATOR
7153 030716 001003          BNE     11$         ;BR IF WE HAD A PREVIOUS RUB-OUT
7154 030720 005201          INC     R1          ;SET RUB-OUT INDICATOR
7155 030722 104401 012410    TYPE    ,BKSLSH     ;TYPE A BACK-SLASH (\)
7156 030726 005037 005542    11$:    CLR     SCRACH     ;USE SCRATCH WORD FOR TEMP. BUFFER
7157 030732 005300          DEC     R0          ;DECREMENT COUNT
7158 030734 116037 005272    005542  MOVB    BUFF0(R0),SCRACH ;GET LAST CHAR. INTO BUFFER
7159 030742 104401 005542    TYPE    ,SCRACH     ;ECHO CHARACTER TO BE DELETED
7160 030746 000710          BR       2$          ;GO READ ANOTHER CHARACTER
7161 030750 005701          14$:    TST     R1          ;CHECK THE RUB-OUT INDICATOR
7162 030752 001403          BEQ     16$         ;BR IF INDICATOR IS NOT SET
7163 030754 104401 012410    TYPE    ,BKSLSH     ;TYPE A BACK-SLASH
7164 030760 005001          CLR     R1          ;CLEAR THE RUB-OUT INDICATOR
7165          ;CHECK FOR CARRIAGE RETURN
7166 030762 122702 000015    16$:    CMPB    #015,R2   ;SEE IF <CR> TYPED
7167 030766 001426          BEQ     19$         ;BR IF <CR>
7168          ;HANDLE POSSIBLE DIGIT
7169 030770 005037 005542    CLR     SCRACH     ;USE SCRATCH WORD FOR TEMP. BUFFER
7170 030774 110237 005542    MOVB    R2,SCRACH  ;GET THIS CHARACTER INTO BUFFER
7171 031000 104401 005542    TYPE    ,SCRACH     ;ECHO THE CHARACTER TYPED
7172 031004 110260 005272    MOVB    R2,BUFF0(R0) ;PUT CHARACTER INTO BUFFER
7173 031010 005200          INC     R0          ;INCREMENT CHARACTER COUNTER
7174 031012 022700 000120    CMP     #80.,R0    ;SEE IF TOO MANY CHARACTERS TYPED
7175 031016 001264          BNE     2$          ;BR IF NOT TOO MANY
7176 031020 104401 001315    TYPE    ,$CRLF     ;TYPE <CR> AND <LF>
7177 031024 112760 000000    005272  MOVB    #0,BUFF0(R0) ;PUT TERMINATING NULL INTO BUFFER
7178 031032 104401 005272    TYPE    ,BUFF0     ;ECHO INPUT STRING
7179 031036 104401 001314    TYPE    ,$QUES     ;TYPE <?>,<CR>,<LF>
7180 031042 000703          BR       7$          ;TAKE ERROR EXIT FROM RDCHRS
7181 031044 104401 001315    19$:    TYPE    ,$CRLF     ;TYPE <CR>,<LF>
7182 031050 112760 000000    005272  MOVB    #0,BUFF0(R0) ;PUT TERMINATING NULL INTO BUFFER
7183 031056 062766 000006    000004  ADD     #6,4(SP)    ;FIX UP RETURN ADDRESS
7184 031064 012602          MOV     (SP)+,R2   ;RESTORE R2
7185 031066 012601          MOV     (SP)+,R1   ;RESTORE R1
7186 031070 000207          RTS     PC          ;SUBROUTINE EXIT
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7200 031072 104401 012421
7201 031076 010146
7202 031100 006216
7203 031102 005216
7204 031104 104403
7205 031106 002
7206 031107 000
7207 031110 104401 012414
7208 031114 016146 005626
7209 031120 104403
7210 031122 006
7211 031123 000
7212 031124 000207
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7226 031126 016137 006152 012424
7227 031134 104401 012424
7228 031140 016137 005732 005476
7229 031146 005037 005500
7230 031152 022761 040515 006152
7231 031160 001003
7232 031162 016137 005734 005500
7233 031170 012746 005476
7234 031174 004737 053366
7235 031200 004737 053702
7236 031204 000207
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*****  
:SBTTL TYPTST - TYPE CURRENT TEST AND ITERATION NUMBER  
:*THIS SUBROUTINE TYPES : 'XX YYYYYY', WITH NO <CR>  
:*OR <LF>. XX IS THE NUMBER OF THE CURRENT TEST AND  
:*YYYYYY IS THE NUMBER OF ITERATIONS FOR THAT TEST.  
:*R1 MUST CONTAIN THE INDEX INTO THE TEST LIST FOR  
:*THE CURRENT TEST.  
:* CALL - JSR PC, TYPTST  
:*****
```

```
TYPTST: TYPE ,SPACE1 ;TYPE A SPACE  
MOV R1,-(SP) ;PUT INDEX ONTO STACK  
ASR (SP) ;DIVIDE BY 2  
INC (SP) ;INCREMENT TO GET TEST NO.  
TYPOS ;TYPE TEST NO. IN OCTAL  
.BYTE 2 ;TYPE 2 DIGITS  
.BYTE 0 ;SUPPRESS LEADING ZEROS  
TYPE ,SPACE6 ;TYPE 6 SPACES  
MOV TSTLST(R1),-(SP) ;PUT CURRENT ITERATION NO. ONTO STACK  
TYPOS ;TYPE ITERATION NO. IN OCTAL  
.BYTE 6 ;TYPE 6 DIGITS  
.BYTE 0 ;SUPPRESS LEADING ZEROS  
RTS PC ;RETURN
```

```
*****  
:SBTTL TYPPRM - TYPE CURRENT PARAMETER  
:*THIS SUBROUTINE TYPES THE LINE : XX=YYYYYYYYY ,  
:*WHERE XX IS THE CURRENT PARAMETER MNEMONIC, AND  
:*YYYYYYYYY IS ITS VALUE IN OCTAL DIGITS, WITH LEADING  
:*ZEROS SUPPRESSED. NOTE: <CR> AND <LF> ARE NOT DONE.  
:*ON ENTRY, R1 MUST CONTAIN THE INDEX INTO THE  
:*PARAMETER TABLES, FOR THE CURRENT PARAMETER.  
:* CALL - JSR PC, TYPPRM  
:*****
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TYPPRM: MOV PRMNEM(R1),PRMBUF ;PUT MNEMONIC INTO OUTPUT BUFFER  
TYPE ,PRMBUF ;TYPE 'XX='  
MOV PRMLST(R1),LOWOCT ;PUT LOW BITS OF PARAM. INTO LOWOCT  
CLR HIGOCT ;CLEAR HIGH BINARY BITS  
CMP #'MA,PRMNEM(R1) ;SEE IF PARAMETER IS (MA)  
BNE 1$ ;BR IF NOT (MA)  
MOV PPMLST+2(R1),HIGOCT ;PUT HI BITS OF PARAM. INTO HIGOCT  
1$: MOV #LOWOCT,-(SP) ;PUT ADDRESS OF BINARY VALUE ON STACK  
JSR PC,@#SDB20 ;CONVERT BINARY TO OCTAL ASCII  
JSR PC,@#SSUPRS ;TYPE YYYYYYY, SUPPRESSING LEADING 0'S  
RTS PC ;RETURN
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*****  
:SBTTL TYPPAT - TYPE CURRENT WORD OF DATA PATTERN 15  
:*THIS SUBROUTINE TYPES THE LINE : WORD XX = YYYYYY,  
:*****
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7251 031206
7252 031206 104401 012430
7253 031212 010146
7254 031214 006216
7255 031216 104403
7256 031220 002
7257 031221 001
7258 031222 104401 012436
7259 031226 016146 007156
7260 031232 104403
7261 031234 006
7262 031235 001
7263 031236 000207
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7281 031240
7282 031240 010046
7283 031242 010146
7284 031244 010246
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7286 031246 022761 052120 006152
7287 031254 001127
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7289 031256 032737 100000 005760
7290 031264 001523
7291
7292 031266 104401 011130
7293 031272 004737 030560
7294 031276 031544
7295 031300 031554
7296 031302 031340
7297 031304 005700
7298 031306 001512

:*WHERE XX IS THE NUMBER OF THE CURRENT WORD OF
:*USER-DEFINED PATTERN 15, AND YYYYYY IS ITS 16-BIT
:*VALUE, IN OCTAL DIGITS. <CR> AND <LF> ARE NOT TYPED.
:*ON ENTRY, R1 MUST CONTAIN THE INDEX OF THE CURRENT
:*WORD IN THE PATTERN 15 TABLE.
:* CALL - JSR PC,TYPPAT
:*****

TYPPAT:
TYPE ,WORDSP ;TYPE 'WORD ''
MOV R1,-(SP) ;PUT INDEX ONTO STACK
ASR (SP) ;DIVIDE BY TWO FOR WORD NO.
TYPOS ;GO TYPE WORD NUMBER
.BYTE 2 ;DIGIT COUNT = 2 FOR TYPOC
.BYTE 1 ;TELL TYPOS TO TYPE LEADING ZEROS
TYPE ,EQUALS ;TYPE " = "
MOV PAT15(R1),-(SP) ;GET BINARY VALUE OF THIS WORD
TYPOS ;TYPE VALUE IN OCTAL
.BYTE 6 ;TYPE 6 DIGITS
.BYTE 1 ;TYPE LEADING ZEROS
RTS PC ;RETURN

:*****
:SBTTL MODP15 - MODIFY USER-DEFINED PATTERN 15
:*THIS SUBROUTINE DETERMINES WHETHER OR NOT USER-DEFINED
:*DATA PATTERN 15 SHOULD BE OPENED FOR MODIFICATION, AND
:*IF SO, IT REQUESTS THE NEW VALUES AND CONTROLS THEIR
:*INPUT ON THE TTY. IT CHECKS VALUES FOR VALIDITY, AND
:*LOADS THE 16 WORDS INTO THE PATTERN 15 TABLE (PAT15).
:*IF THE OPERATOR ANSWERS THE REQUEST FOR A PATTERN WORD
:*WITH EXCLAMATION POINT (!) (CR), THE SUBROUTINE
:*PROPAGATES THE LAST WORD TYPED INTO ALL REMAINING
:*WORDS OF THE PATTERN 15 TABLE.
:* CALL - JSR PC,MODP15
:*****

MODP15:
MOV R0,-(SP) ;SAVE R0
MOV R1,-(SP) ;SAVE R1
MOV R2,-(SP) ;SAVE R2
;SEE IF PARAMETER IS PT
CMP #'PT,PRMNM(R1) ;SEE IF CURRENT PARAMETER IS (PT)
BNE 22\$;BR IF NOT (PT)
;SEE IF PATTERN 15 IS SPECIFIED
BIT #BIT15,PT ;SEE IF PATTERN 15 SPECIFIED
BEQ 22\$;BR IF NOT SPECIFIED
;SEE IF PATTERN 15 SHOULD BE MODIFIED
4\$: TYPE ,MDFY15 ;ASK WHETHER PATTERN 15 SHOULD BE MODIFIED
JSR PC,RDCHRS ;READ RESPONSE
24\$;(^C) RETURN ADDRESS
26\$;(^Z) RETURN ADDRESS
8\$;(^U) OR ERROR RETURN ADDRESS
TST R0 ;SEE IF NULL INPUT
BEQ 22\$;BR IF MODIFICATION NOT REQUESTED

```

7299 031310 022737 000115 005272      CMP      #'M,BUFF0      :SEE IF (M) TYPED
7300 031316 001405                      BEQ      6$             :BR IF MODIFICATION REQUESTED
7301 031320 104401 005272                      TYPE     ,BUFF0        :ECHO BAD INPUT
7302 031324 104401 001314                      TYPE     ,SQUES
7303 031330 000756                      BR       4$             :GO ASK AGAIN
7304                      ;MODIFY PATTERN 15
7305 031332 104401 011106      6$:      TYPE     ,SELP15      :TYPE 'MODIFY PATTERN 15'
7306 031336 005001                      CLR      R1             :INITIALIZE WORD INDEX
7307 031340 004737 031206      8$:      JSR      PC,TYPPAT      :TYPE CURRENT WORD AND VALUE
7308 031344 104401 012421                      TYPE     ,SPACE1       :TYPE A SPACE
7309 031350 104401 012442                      TYPE     ,PROMPT       :TYPE ASTERISK AND SPACE
7310                      ;READ AND CHECK INPUT, IF ANY
7311 031354 004737 030560      JSR      PC,RDCHRS      :READ NEW DATA PATTERN WORD
7312 031360 031544                      24$      :(^C) RETURN ADDRESS FOR RDCHRS
7313 031362 031554                      26$      :(^Z) RETURN ADDRESS FOR RDCHRS
7314 031364 031340                      8$       :(^U) OR ERROR RETURN ADDR. FOR RDCHRS
7315 031366 005700                      TST     R0             :SEE IF ANY INPUT
7316 031370 001006                      BNE     12$           :BR IF ANY INPUT
7317 031372 062701 000002      10$:     ADD     #2,R1          :INCREMENT WORD INDEX
7318 031376 022701 000040                      CMP     #32.,R1        :SEE IF ALL DONE
7319 031402 001454                      BEQ     22$           :BR IF DONE, TO RETURN
7320 031404 000755                      BR      8$            :CONTINUE WITH NEXT WORD
7321 031406 022737 000041 005272      12$:     CMP     #'.,BUFF0      :SEE IF (!) TYPED
7322 031414 001431                      BEQ     16$           :BR TO PROPAGATE CURRENT VALUE
7323 031416 005002                      CLR     R2             :INIT. (!) INDICATOR
7324 031420 122760 000041 005271      CMPB    #'!,BUFF0-1(R0) :SEE IF LAST CHAR IN BUF IS (.)
7325 031426 001004                      BNE     14$           :BR IF NOT (!)
7326 031430 105060 005271      CLRB    BUFF0-1(R0)    :INSERT TERMINATOR BYTE
7327 031434 005300                      DEC     R0             :DECREMENT CHAR COUNT
7328 031436 005202                      INC     R2             :SET (!) INDICATOR
7329 031440 022700 000006      14$:     CMP     #6,R0         :SEE HOW MANY CHARS NOW
7330 031444 002426                      BLT     20$           :BR IF TOO MANY
7331 031446 012746 005272      MOV     #BUFF0,-(SP)   :GET BUF. ADDR. ON STACK FOR OCTBIN
7332 031452 004737 052430      JSR     PC,OCTBIN      :CHECK DIGITS AND CONVERT TO BINARY
7333 031456 031522                      20$      :ERROR RETURN ADDRESS FOR OCTBIN
7334 031460 012600                      MOV     (SP)+,R0       :GET BINARY VALUE
7335 031462 005737 052562      TST     $HIOCT         :SEE IF VALUE EXCEEDS 16 BITS
7336 031466 001015                      BNE     20$           :BR TO ECHO BAD INPUT
7337 031470 010061 007156      MOV     R0,PAT15(R1)   :PUT NEW WORD VALUE INTO TABLE
7338 031474 005702                      TST     R2             :SEE IF (!) WAS TYPED
7339 031476 001735                      BEQ     10$           :BR IF (!) WAS NOT TYPED
7340                      ;PROPAGATE CURRENT WORD TO END OF PATTERN 15 TABLE
7341 031500 022701 000036      16$:     CMP     #30.,R1        :SEE IF ALL DONE YET
7342 031504 001413                      BEQ     22$           :BR IF DONE
7343 031506 016161 007156 007160      MOV     PAT15(R1),PAT15+2(R1) :PROPAGATE TO NEXT WORD
7344 031514 062701 000002      ADD     #2,R1          :INCREMENT WORD INDEX
7345 031520 000767                      BR      16$           :LOOP UNTIL DONE
7346                      ;ECHO BAD INPUT
7347 031522 104401 005272      20$:     TYPE     ,BUFF0        :ECHO BAD INPUT
7348 031526 104401 001314                      TYPE     ,SQUES        :TYPE <?> AND <CR>,<LF>
7349 031532 000702                      BR      8$            :BR TO ASK AGAIN
7350                      ;NORMAL RETURN
7351 031534 012602      22$:     MOV     (SP)+,R2      :RESTORE R2
7352 031536 012601                      MOV     (SP)+,R1      :RESTORE R1
7353 031540 012600                      MOV     (SP)+,R0      :RESTORE R0
7354 031542 000207                      RTS     PC             :RETURN

```

```

7355      :(^C) RETURN
7356 031544 012766 013660 000006 24$: MOV #DRVST,6(SP) ;PC FOR (^C) RETURN
7357 031552 000770          BR 22$ ;BR TO RETURN
7358      :(^Z) RETURN
7359 031554 012766 015272 000006 26$: MOV #ASKMDE,6(SP) ;PC FOR (^Z) RETURN
7360 031562 000764          BR 22$ ;BR TO RETURN
7361
7362
7363
7364      :*****
7365      :SBTTL  CHKPRM - CHECK VALUE OF INPUT PARAMETER
7366      :*THIS SUBROUTINE CHECKS THE CONTENTS OF LOWOCT AND
7367      :*HIGOCT (IF 32-BIT VALUE) AGAINST LOWER AND UPPER
7368      :*LIMITS FOR THE CURRENT PARAMETER. THE PARAMETER TABLE
7369      :*INDEX FOR THE CURRENT PARAMETER MUST BE PASSED
7370      :*TO CHKPRM IN R1 ON ENTRY. IF THE PARAMETER VALUE
7371      :*IS LEGAL, IT IS ENTERED INTO THE PARAMETER TABLE
7372      :*(PRMLST). IF NOT, A RETURN IS MADE TO THE ADDR. FOLLOWING
7373      :*THE CALL TO CHKPRM. IF THE PARAMETER HAS A 2-WORD
7374      :*VALUE (32 BITS) R1 IS INCREMENTED BY 2 BEFORE RETURN.
7375      :* CALL -      JSR      PC,CHKPRM
7376      :*              <ERROR RETURN ADDRESS>
7377      :*              RETURN
7378      :*****
7379
7380 031564 104407      CHKPRM: SAVREG          ;SAVE R0-R5
7381 031566 010102      MOV      R1,R2          ;GET COPY OF INDEX
7382 031570 006302      ASL      R2              ;DOUBLE IT
7383 031572 022761 040515 006152  CMP      #'MA,PRMNEM(R1) ;SEE IF PARAMETER IS (MA)
7384 031600 001422      BEQ      20$          ;BR IF (MA)
7385 031602 005737 005500  TST      HIGOCT          ;SEE IF HIGH BITS = 0
7386 031606 001403      BEQ      18$          ;BR IF ZERO
7387 031610 017616 000000 16$: MOV      @2(SP),2(SP) ;GET ERROR RETURN PC
7388 031614 000475      BR      30$          ;GO TO RETURN
7389      :CHECK VALIDITY OF 16-BIT PARAMETER VALUE
7390 031616 023762 005476 006042 18$: CMP      LOWOCT,PRMLIM(R2) ;SEE IF INPUT VALUE IS TOO SMALL
7391 031624 103771      BLO      16$          ;BR IF INPUT VALUE TOO SMALL
7392 031626 023762 005476 006044  CMP      LOWOCT,PRMLIM+2(R2) ;SEE IF INPUT VALUE IS TOO LARGE
7393 031634 101365      BHI      16$          ;BR IF INPUT VALUE IS TOO LARGE
7394      :UPDATE 16-BIT PARAMETER VALUE IN LIST
7395 031636 013761 005476 005732  MOV      LOWOCT,PRMLST(R1) ;PUT NEW PARAMETER VALUE INTO LIST
7396 031644 000457      BR      28$          ;BR TO RETURN
7397      :CHECK VALIDITY OF 32-BIT PARAMETER VALUE
7398 031646 032737 000001 005476 20$: BIT      #BIT0,LOWOCT ;SEE IF MA IS ODD
7399 031654 001355      BNE      16$          ;BR IF MA IS ODD
7400 031656 023762 005500 006044  CMP      HIGOCT,PRMLIM+2(R2) ;SEE IF HIGH WORD TOO SMALL
7401 031664 103751      BLO      16$          ;BR IF HIGH WORD IS TOO SMALL
7402 031666 001004      BNE      24$          ;BR IF HIGH WORD NOT EQUAL TO LOW LIMIT
7403 031670 023762 005476 006042  CMP      LOWOCT,PRMLIM(R2) ;SEE IF LOW WORD IS TOO SMALL
7404 031676 103744      BLO      16$          ;BR IF LOW WORD IS TOO SMALL
7405 031700 023762 005500 006050 24$: CMP      HIGOCT,PRMLIM+6(R2) ;SEE IF HIGH WORD IS TOO BIG
7406 031706 101340      BHI      16$          ;BR IF HIGH WORD TOO BIG
7407 031710 001004      BNE      26$          ;BR IF HI WORD NOT EQUAL TO UPPER LIMIT
7408 031712 023762 005476 006046  CMP      LOWOCT,PRMLIM+4(R2) ;SEE IF LOW WORD IS TOO LARGE
7409 031720 101333      BHI      16$          ;BR IF LOW WORD IS TOO LARGE
7410      :UPDATE 32-BIT PARAMETER VALUE IN LIST

```

7411 031722 013761 005476 005732
7412 031730 013761 005500 005734
7413
7414 031736 004737 032014
7415 031742 104401 010651
7416 031746 010037 003176
7417 031752 010137 003200
7418 031756 012746 003176
7419 031762 004737 053366
7420 031766 004737 053702
7421 031772 104401 001315
7422 031776 062766 000002 000014
7423 032004 062716 000002
7424 032010 104410
7425 032012 000207
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7433 032014 013700 006126
7434 032020 013701 006130
7435 032024 163700 005762
7436 032030 005601
7437 032032 163701 005764
7438 032036 100413
7439 032040 000241
7440 032042 006001
7441 032044 006000
7442 032046 062700 000001
7443 032052 005501
7444 032054 005701
7445 032056 001403
7446 032060 005000
7447 032062 012701 000001
7448 032066 000207
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7457 032070 104407
7458 032072 004737 027450
7459 032076 112765 000141 000001
7460 032104 004737 040252
7461 032110 104401 011335
7462 032114 104401 011351
7463 032120 016501 000054
7464 032124 012704 053470
7465 032130 010446
7466 032132 012703 000003

```
26$:  MOV     LOWOCT,PRMLST(R1) ;PUT LOW WORD INTO LIST
      MOV     HIGOCT,PRMLST+2(R1) ;PUT HIGH WORD INTO LIST
      ; COMPUTE AND TYPE MAX ALLOWABLE WORD COUNT FOR THIS MA
      JSR     PC,MXWRDC           ;GET WORD COUNT IN R1-R0
      TYPE    ,MAWRDC           ;TYPE 'MAX WORD COUNT = ''
      MOV     R0,SUMLO1
      MOV     R1,SUMHI1
      MOV     #SUMLO1,-(SP)      ;PUT POINTER ON STACK
      JSR     PC,$DB20          ;CONVERT TO OCTAL
      JSR     PC,$SUPRS         ;TYPE IT
      TYPE    ,%CRLF           ;TYPE <CR>,<LF>
      ADD     #2,14(SP)         ;INCREMENT R1 INDEX
28$:  ADD     #2,(SP) ;GET NORMAL RETURN PC
30$:  RESREG  PC                ;RESTORE R0-R5
      RTS     PC                ;RETURN
```

```
*****
;*MXWRDC - GET MAX WORD COUNT FOR CURRENT MEM LIMITS AND CURRENT MA,
;*AND LEAVE THE WORD COUNT IN R1-R0. NO REGISTERS ARE SAVED.
*****
```

```
MXWRDC: MOV     MAHILM,R0        ;GET LO BITS OF MEM LIM
      MOV     MAHILM+2,R1      ;HI BITS OF MEM LIM
      SUB     MA,R0            ;SUBTRACT MA FROM MAHILM
      SBC     R1
      SUB     MA+2,R1
      BMI     -7$              ;IF NEGATIVE, RETURN
      CLC
      ROR     R1                ;DIVIDE BY 2 TO GET WORDS
      ROR     R0
      ADD     #1,R0            ;INCREMENT BY 1 WORD
      ADC     R1
      TST     R1
      BEQ     27$              ;BR IF WORD COUNT < 65,536 DEC
      CLR     R0                ;MAKE WORD COUNT = 65,536
      MOV     #1,R1
27$:  RTS     PC                ;RETURN
```

```
*****
.SBTTL  DRVSER - TYPE DRIVE SERIAL NUMBER (LOW 3 DIGITS)
;*THIS SUBROUTINE TYPES 'DRIVE SER. NO. XXX' (IN DECIMAL), WITH LEADING
;*ZEROS SUPPRESSED.
*****
```

```
DRVSER: SAVREG                    ;SAVE R0-R5
      JSR     PC,INITSS          ;CLEAR S.S. AND PARAMETERS
      MOVB   #RDSTAT,P.CMND(R5) ;SET READ STATUS COMMAND
      JSR     PC,DRVCAL         ;READ STATUS OF THIS DRIVE
      TYPE   ,DRIV              ;TYPE 'DRIVE'
      TYPE   ,SERNM            ;TYPE 'SER. NO. '
      MOV     P.A11(R5),R1      ;GET 'A' STATUS BYTE 11
      MOV     #%OCTVL,R4       ;GET ADDR OF CHAR BUFFER
      MOV     R4,-(SP)         ;STORE IT ON STACK FOR $SUPRS
      MOV     #3,R3            ;INIT CHAR COUNT
```

```
7467 032136 006101          ROL    R1          ;INITIALIZE BIT POSITIONS
7468 032140 006101          ROL    R1
7469 032142 006101 4$:    ROL    R1          ;GET NEXT 4 BITS
7470 032144 006101          ROL    R1
7471 032146 006101          ROL    R1
7472 032150 006101          ROL    R1
7473 032152 010100          MOV    R1,R0        ;GET A WORKING COPY
7474 032154 042700 177760    BIC    #177760,R0   ;CLEAR ALL BUT LOW 4 BITS
7475 032160 052700 000060    BIS    #'0,R0       ;CONVERT A DIGIT TO ASCII
7476 032164 110024          MOVVB  R0,(R4)+     ;PUT ASCII DIGIT INTO CHAR BUFFER
7477 032166 005303          DEC    R3          ;DECREMENT CHAR COUNT
7478 032170 001364          BNE    4$         ;BR IF NOT 3 CHARS YET
7479 032172 105014          CLRB  (R4)        ;INSERT NULL TERMINATOR
7480 032174 004737 053702    JSR    PC,@#$$SUPRS ;TYPE DRIVE SER. NUMBER
7481 032200 104401 001315    TYPE  ,%CRLF      ;TYPE <CR> AND <LF>
7482 032204 104410          RESREG ;RESTORE R0-R5
7483 032206 000207          RTS    PC         ;RETURN
```

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```
::*****
.SBTTL CRTSER - TYPE CARTRIDGE SERIAL NUMBER
;*THIS SUBROUTINE TYPES "CART. SER. NO. XXXXXXXXXXX" (IN OCTAL),
;*WITH LEADING ZEROS SUPPRESSED.
*****
```

```
7492 032210 004737 027450    CRTSER: JSR    PC,INITSS ;CLEAR S.S. AND PARAMETERS
7493 032214 142765 000020 000007    BICB  #B.CFMT,P.CS1H(R5) ;SET 22 SECTOR FORMAT
7494 032222 105737 003125          TSTB  FORMAT      ;CHECK THE ACTUAL FORMAT
7495 032226 001402          BEQ    4$         ;BR IF 22 SECTORS
7496 032230 105265 000004          INCB  P.SECT(R5)  ;IF 20 SECTORS, READ SECTOR 1
7497 032234 112765 000121 000001 4$:    MOVVB #RDDATA,P.CMND(R5) ;SET READ COMMAND
7498 032242 013765 017706 000002    MOV   LSTCYL,P.CYLN(R5) ;SET CYL = 632(OCT)/1456(OCT) FOR RK07
7499 032250 112765 000002 000005    MOVVB #LSTTRK,P.TRCK(R5) ;SET TRACK = 2
7500 032256 012765 063520 000010    MOV   #RWBUF,P.BALO(R5) ;SET READ BUFFER ADDRESS
7501 032264 012765 177776 000012    MOV   #-2,P.WC(R5)     ;SET WORD COUNT TO READ 2 WORDS
7502 032272 004737 040252          JSR    PC,DRVCAL    ;READ SERIAL NO. IN BSF
7503 032276 104401 011342          TYPE  ,CART        ;TYPE "CART."
7504 032302 104401 011351          TYPE  ,SERNM       ;TYPE "SER. NO. "
7505 032306 012746 063520          MOV   #RWBUF,-(SP)  ;GET POINTER FOR $DB20
7506 032312 004737 053366          JSR    PC,@#$DB20  ;CONVERT BINARY TO OCTAL
7507 032316 004737 053702          JSR    PC,@#$$SUPRS ;TYPE CART. SERIAL NO. IN OCTAL
7508 032322 104401 001315          TYPE  ,%CRLF      ;TYPE <CR> AND <LF>
7509 032326 000207          RTS    PC         ;RETURN
```

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```
::*****
.SBTTL SEEKER - PERFORM IMPLICIT OR EXPLICIT SEEK
;*THIS SUBROUTINE CHECKS SWR BIT 7, AND IF IT IS 0, AN IMPLICIT
;*SEEK VIA THE READ HEADER COMMAND IS PERFORMED. IF THE HEADER
;*DOES NOT CONTAIN THE CORRECT CYLINDER NO. AN ERROR IS REPORTED.
;*IF SWR BIT 7 = 1, AN EXPLICIT SEEK COMMAND IS PERFORMED.
;*ON ENTRY TO THE SUBROUTINE, THE DRIVE AND CYLINDER NUMBERS
;*MUST BE PRE-LOADED INTO THE PARAMETER BLOCK.
;* CALL - JSR PC,SEEKER
*****
```

```
7523  
7524 032330 010046 SEEKER: MOV RO,-(SP) ;SAVE RO  
7525 032332 112765 000117 000001 MOVB #SEEK,P.CMND(R5) ;SET SEEK COMMAND  
7526 032340 032777 000200 146572 BIT #BIT07,@SWR ;SEE IF EXPLICIT SEEKS REQUESTED  
7527 032346 001023 BNE 2$ ;BR IF EXPLICIT SEEKS  
7528 032350 112765 000125 000001 MOVB #RDHEAD,P.CMND(R5) ;SET READ HEADER COMMAND  
7529 032356 004737 040252 JSR PC,DRVCAL ;READ A HEADER  
7530 032362 016200 000024 MOV RKDB(R2),RO ;GET WORD 1 OF ACTUAL HEADER  
7531 032366 020065 000002 CMP RO,P.CYLN(R5) ;COMPARE TO EXPECTED CYLINDER  
7532 032372 001413 BEQ 4$ ;BR IF EQUAL  
7533 032374 004737 041650 JSR PC,REPSUP ;STORE PREV AND CURRENT CMNDS  
7534 032400 016537 000002 001174 MOV P.CYLN(R5),SREG5 ;GET GOOD CYL NO.  
7535 032406 010037 001176 MOV RO,SREG6 ;GET BAD CYL NO.  
7536 032412 104062 ERROR 62 ;CYLINDER MISCOMPARE  
7537 032414 000402 BR 4$  
7538 032416 004737 040252 2$: JSR PC,DRVCAL ;PERFORM COMMAND  
7539 032422 012600 4$: MOV (SP)+,RO ;RESTORE RO  
7540 032424 000207 RTS PC ;RETURN
```

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```
*****  
:SBTTL STALL - STALL FOR ST UNIT STALL TIMES  
:*IF SWR BIT 8 = 0, THIS SUBROUTINE STALLS FOR ST STALL TIMES.  
:*WHERE A STALL TIME = 40 US, FOR AN 'AVERAGE' CPU. IF BIT 8  
:*IS EQUAL TO 1, A RANDOM STALL IS APPLIED.  
:* CALL - JSR PC,STALL  
:*****
```

```
7550  
7551  
7552 032426 010046 STALL: MOV RO,-(SP) ;SAVE RO  
7553 032430 010146 MOV R1,-(SP) ;SAVE R1  
7554 032432 032777 000400 146500 BIT #BIT08,@SWR ;APPLY RANDOM STALL ?  
7555 032440 001407 BEQ 1$ ;BR IF NOT RANDOM  
7556 032442 004737 052564 JSR PC,$RAND ;GENERATE PSEUDO-RANDOM NUMBER  
7557 032446 013700 052664 MOV $LONUM,RO ;GET IT INTO RO  
7558 032452 006200 ASR RO ;SCALE IT DOWN  
7559 032454 006200 ASR RO  
7560 032456 000403 BR 2$  
7561 032460 013700 005512 1$: MOV STALLS,RO ;GO STALL WITH RANDOM NO.  
7562 032464 001406 REQ 6$ ;GET REQUESTED NO. OF STALLS  
7563 032466 012701 000016 2$: MOV #14.,R1 ;RETURN IF NO STALL REQUIRED  
7564 032472 005301 4$: DEC R1 ;SET CONSTANT FOR 40 US  
7565 032474 001376 BNE 4$ ;INNER LOOP COUNTER  
7566 032476 005300 DEC RO ;INNER LOOP BR  
7567 032500 001372 BNE 2$ ;OUTER LOOP COUNTER  
7568 032502 012601 6$: MOV (SP)+,R1 ;OUTER LOOP BR  
7569 032504 012600 MOV (SP)+,RO ;RESTORE R1  
7570 032506 000207 RTS PC ;RESTORE RO  
;RETURN
```

7571
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```
*****  
:SBTTL RNDADR - RANDOM CYLINDER ADDRESS GENERATOR  
:*THIS SUBROUTINE GENERATES A PSEUDO-RANDOM 9-BIT CYLINDER  
:*ADDRESS, AND LEAVES IT IN 'CYLNDR'. IT REQUIRES THE SYSMAC  
:*SUBROUTINE $RAND.
```

7575
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7578


```
7579      ;* CALL - JSR      PC,RNDADR
7580      ;*****
7581 032510 004737 052564      RNDADR: JSR      PC,$RAND      ;GENERATE 2 16-BIT RANDOM NUMBERS
7582 032514 013737 052664 005514      MOV      $LONUM,CYLNR      ;GET A RANDOM NUMBER
7583 032522 042737 177000 005514      BIC      #177000,CYLNR      ;SCALE IT TO 9 BITS
7584 032530 023737 017706 005514      CMP      LSTCYL,CYLNR      ;SEE IF CYL IS TOO BIG
7585 032536 002003      BGE      2$      ;BR IF CYL IS OK
7586 032540 043737 017712 005514      BIC      HOLD1,CYLNR      ;SCALE DOWN TO A VALID CYLINDER
7587 032546 000207      2$:      RTS      PC      ;RETURN
7588
7589
7590
7591      .SBTTL  ROUTINES TO HANDLE KW11-L OR P CLOCK
7592
7593      ;*****
7594      ;*CLKON - ENABLE KW11-L OR P CLOCK INTERRUPT
7595      ;*****
7596 032550 105737 003132      CLKON:  TSTB     PCLKF      ;SEE WHICH CLOCK WILL BE USED
7597 032554 001004      BNE      1$      ;BR IF P-CLOCK TO BE USED
7598 032556 012777 000100 150364      MOV      #100,@LKS      ;L-CLOCK, ENABLE INTERRUPT
7599 032564 000207      RTS      PC      ;RETURN
7600 032566 012777 174575 150360 1$:      MOV      #-1667.,@PKSB      ;SET 60 HZ. COUNT
7601 032574 005737 000166      TST      HZ      ;DETERMINE LINE FREQUENCY
7602 032600 001403      BEQ      3$      ;BR IF 60 HZ.
7603 032602 012777 174060 150344      MOV      #-2000.,@PKSB      ;SET 50 HZ. COUNT
7604 032610 012777 000131 150334 3$:      MOV      #131,@PKS      ;ENABLE INTERRUPT,CNT UP,REP. INT.
7605      ; 100 KHZ.,AND RUN
7606 032616 000207      RTS      PC      ;RETURN
7607
7608      ;*****
7609      ;*CLOCK - HANDLE KW11-L OR P CLOCK INTERRUPT
7610      ;*****
7611 032620 005200      CLOCK:  INC      RO      ;SIGNIFY A CLOCK TICK
7612 032622 000002      RTI      ;RETURN FROM INTR
7613
7614      ;*****
7615      ;*CLKOF - DISABLE KW11-L OR P CLOCK INTERRUPT
7616      ;*****
7617 032624 105737 003132      CLKOF:  TSTB     PCLKF      ;SEE WHICH CLOCK USED
7618 032630 001003      BNE      1$      ;BR IF P-CLOCK USED
7619 032632 005077 150312      CLR      @LKS      ;DISABLE KW11-L INTR
7620 032636 000207      RTS      PC      ;RETURN
7621 032640 005077 150306 1$:      CLR      @PKS      ;DISABLE KW11-P INTR
7622 032644 000207      RTS      PC      ;RETURN
7623
7624
7625
7626      ;*****
7627      .SBTTL  CALBRT - CALIBRATE THE SOFTWARE TIMER
7628      ;*THIS SUBROUTINE CALIBRATES THE SOFTWARE TIMER USED IN THE TIMING
7629      ;*TESTS. IT CALLS SUBROUTINE "TIMER" TO MEASURE THE TIME BETWEEN
7630      ;* 2 TICKS OF THE KW11-L OR P CLOCK. THIS INTERVAL EQUALS THE LINE
7631      ;*PERIOD. IN ALL, 128 MEASUREMENTS ARE MADE AND AVERAGED, AND THE
7632      ;*RESULTANT CALIBRATION CONSTANT IS LEFT IN TCONHI-TCONLO (IN
7633      ;* NANO-SEC). IF THE SUBROUTINE HAPPENS TO TIME OUT WAITING FOR
7634      ;*A TICK OF THE CLOCK, A RETURN IS MADE TO THE ADDRESS LISTED
```


7691 033062 001307
7692 033064 010337 003164
7693 033070 062716 000002
7694 033074 004737 032624
7695 033100 104410
7696 033102 000207
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7711 033104 062705 000001
7712 033110 005504
7713 033112 032704 000200
7714 033116 001005
7715 033120 005700
7716 033122 001770
7717
7718 033124 062716 000002
7719 033130 000207
7720 033132 017616 000000
7721 033136 000207
7722
7723
7724
7725
7726
7727
7728 033140 005037 003170
7729 033144 005037 003172
7730 033150 005037 003174
7731 033154 005037 003176
7732 033160 005037 003200
7733 033164 005037 003202
7734 033170 005037 003204
7735 033174 012737 177777 003212
7736 033202 012737 177777 003214
7737 033210 005037 003216
7738 033214 005037 003220
7739 033220 012737 177777 003222
7740 033226 012737 177777 003224
7741 033234 005037 003226
7742 033240 005037 003230
7743 033244 000207
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```
18$: BNE 6$ ;BR IF HI BITS NOT 0
      MOV R3, TCONLO ;GET LO BITS
      ADD #2, (SP) ;FIX UP ERROR-FREE RETURN PC
      JSR PC, CLKOF ;DISABLE KW11-L OR P CLOCK INTERRUPT
      RESREG ;RESTORE R0-R5
      RTS PC ;RETURN
```

```
*****
:SBTTL TIMER - SOFTWARE TIMER SUBROUTINE
:*THIS SUBROUTINE LOOPS THROUGH THE SOFTWARE TIMING LOOP,
:*INCREMENTING THE CYCLE COUNT IN R4-R5 EACH LOOP, AND CHECKS
:*THE INTERRUPT INDICATION IN R0, FOR COMPLETION. IF A COUNT
:*OF 4,194,304 IS REACHED IN R4-R5, A TIME-OUT ERROR RETURN
:*IS MADE TO THE ADDRESS LISTED AFTER THE CALL .
:* CALL - S PC, TIMER
:* <---> JR RETURN ADDRESS>
```

```
*****
:*** SPECIAL TIMING LOOP ***
TIMER: ADD #1, R5 ;INCREMENT LO CYCLE COUNT
        ADC R4 ;ADD CARRY TO HI CYCLE COUNT
        BIT #BIT07, R4 ;SEE IF TIMED-OUT WAITING FOR INTR
        BNE 4$ ;BR IF TIME-OUT
        TST R0 ;SEE IF INTERRUPT RECEIVED
        BEQ TIMER ;BR IF INTERRUPT NOT REC'D YET
```

```
*****
4$: ADD #2, (SP) ;FIX UP ERROR-FREE RETURN PC
     RTS PC ;ERROR-FREE RETURN
     MOV @ (SP), (SP) ;FIX UP ERROR RETURN PC
     RTS PC ;ERROR RETURN
```

```
*****
:* INIVRB - INITIALIZE VARIABLES USED IN TIMING TESTS
*****
INIVRB: CLR BLWMIN ;COUNT OF TIMES BELOW SPEC'D MIN
        CLR ABVMX1 ;COUNT OF FORWARD TIMES ABOVE SPEC'D MAX
        CLR ABVMX2 ;COUNT OF REVERSE TIMES ABOVE SPEC'D MAX
        CLR SUMLO1 ;FORWARD TIME SUM AND AVERAGE
        CLR SUMHI1
        CLR SUMLO2 ;REVERSE TIME SUM AND AVERAGE
        CLR SUMHI2
        MOV #-1, MINIL1 ;MIN MEAS'D FORWARD TIME
        MOV #-1, MINIH1
        CLR MAXIL1 ;MAX MEAS'D FORWARD TIME
        CLR MAXIH1
        MOV #-1, MINIL2 ;MIN MEAS'D REVERSE TIME
        MOV #-1, MINIH2
        CLR MAXIL2 ;MAX MEAS'D REVERSE TIME
        CLR MAXIH2
        RTS PC ;RETURN
```

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7757 033246 010246
7758 033250 010546
7759 033252 005000
7760 033254 005004
7761 033256 012777 033426 147554
7762 033264 112765 000117 000001
7763 033272 010537 033314
7764 033276 005005
7765 033300 062702 000000
7766 033304 004737 040542
7767 033310 004737 050534
7768 033314 000000 2\$:
7769 033316 004737 033104
7770 033322 033372
7771 033324 004737 034072
7772 033330 033372
7773 033332 010200
7774 033334 010301
7775 033336 062766 000002 000004
7776 033344 012777 045234 147466 4\$:
7777 033352 012605
7778 033354 012602
7779 033356 112765 000177 000001
7780 033364 004737 040252
7781 033370 000207
7782 033372 013702 003036 6\$:
7783 033376 042762 000100 000000
7784 033404 004737 041650
7785 033410 004737 044316
7786 033414 104106
7787 033416 017666 000004 000004
7788 033424 000747
7789
7790
7791
7792
7793 033426 011203
7794 033430 100002
7795 033432 000137 045234
7796 033436 032703 040000 2\$:
7797 033442 001401
7798 033444 005200
7799 033446 000002 4\$:
7800
7801
7802

```
*****  
:SBTTL TIMSEK - MEASURE AN EXPLICIT SEEK TIME  
:THIS SUBROUTINE MEASURES THE AMOUNT OF TIME REQUIRED TO PERFORM  
:A SEEK TO THE CYLINDER ADDRESS IN P.CYLN IN THE PARAM BLK. THIS  
:TIME IS RETURNED IN R0-R1, IN US. IF THE SEEK TIMES-OUT, OR IF  
:THE TIME CALCULATION OVERFLOWS, A MESSAGE IS TYPED, AND AN ERROR  
:RETURN IS MADE TO THE ADDRESS LISTED AFTER THE CALL.  
:CALL - JSR PC,TIMSEK  
: <ERROR RETURN ADDRESS>  
*****  
TIMSEK: MOV R2,-(SP) ;SAVE R2  
MOV R5,-(SP) ;SAVE R5  
CLR R0 ;INIT DRIVE INTR INDICATOR  
CLR R4 ;INIT HI CYCLE COUNT  
MOV #SEEKHD,@RKVEC ;SET SPECIAL RK06 SEEK TIMER HANDLER VECTOR  
MOVB #SEEK,P.CMND(R5) ;SET SEEK COMMAND  
MOV R5,2$ ;SET PARAM BLK ADDRESS FOR DRIVER  
CLR R5 ;INIT LO CYCLE COUNT  
ADD #RKCS1,R2 ;GET ADR OF RKCS1 IN R2 FOR SEEKHD  
JSR PC,STRCMD ;STORE PREV AND CURRENT COMMANDS  
JSR PC,C.INIT ;START THE SEEK  
2$: .WORD 0 ;PARAM BLK ADR GOES HERE  
JSR PC,TIMER ;MEASURE TIME TIL DRIVE ATT'N  
6$ ;ERROR RETURN ADDRESS FOR TIMER  
JSR PC,CNVTIM ;CONVERT MEAS'D CYCLES TO TIME  
6$ ;ERROR RETURN ADR FOR CNVTIM  
MOV R2,R0 ;PUT HI TIME BITS IN R0  
MOV R3,R1 ;PUT LO TIME BITS IN R1  
ADD #2,4(SP) ;FIX UP ERROR-FREE RETURN PC  
4$: MOV #1,INTR,@RKVEC ;RESTORE RK06 INTR HANDLER  
MOV (SP)+,R5 ;RESTORE R5  
MOV (SP)+,R2 ;RESTORE R2  
MOVB #SUBCLR,P.CMND(R5) ;SET SUBSYSTEM CLEAR COMMAND  
JSR PC,DRVCAL ;CLEAR THE SUBSYSTEM  
RTS PC ;RETURN  
6$: MOV RKBAS,R2 ;GET RK06 REG ADDR  
BIC #IE,RKCS1(R2) ;DISABLE RK06 INTERRUPT  
JSR PC,REPSUP ;GET PREV AND CURRENT CMNDS  
JSR PC,TOPROC ;GATHER STATUS  
ERROR 106 ;TIMED-OUT ON SEEK  
MOV @4(SP),4(SP) ;FIX ERROR RETURN PC  
BR 4$ ;BR TO EXIT  
*****  
:SPECIAL INTERRUPT HANDLER FOR SEEKS IN SEEK TIMING TESTS  
*****  
SEEKHD: MOV (R2),R3 ;GET BITS OF RKCS1  
BPL 2$ ;BR IF CERR NOT SET  
JMP 1,INTR ;LET DRIVER PROCESS THE ERRCR  
2$: BIT #DI,R3 ;SEE IF DRIVE INTR SET YET  
BEQ 4$ ;BR IF NOT YET  
INC R0 ;SET RK06 INTR INDICATOR  
4$: RTI ;RETURN FROM INTERRUPT  
*****
```

7803
7804
7805
7806 033450 104407
7807 033452 010002
7808 033454 010103
7809 033456 005004
7810 033460 013705 005542
7811 033464 006305
7812 033466 004737 053150
7813 033472 060337 003204
7814 033476 005502
7815 033500 060237 003202
7816 033504 005537 003200
7817 033510 104410
7818 033512 000207

```
*****  
;*FDTERM - COMPUTE A TERM OF THE FORWARD SEEK AVERAGE FORMULA,  
;*AND ADD IT TO THE FORWARD SUM IN SUMLO1-SUMHI1-SUMLO2-SUMHI2.  
*****  
FDTERM: SAVREG ;SAVE R0-R5  
MOV R0,R2 ;GET MEASUREMENT INTO R2-R3  
MOV R1,R3  
CLR R4  
MOV SCRACH,R5 ;GET COEFFICIENT  
ASL R5 ;MULTIPLY BY 2  
JSR PC,M.DPIM ;MULTIPLY COEFFICIENT  
ADD R3,SUMHI2 ;ADD TO FORWARD SUM  
ADC R2  
ADD R2,SUMLO2  
ADC SUMHI1  
RESREG ;RESTORE R0-R5  
RTS PC
```

7819
7820
7821
7822
7823
7824
7825
7826 033514 104407
7827 033516 010002
7828 033520 010103
7829 033522 005004
7830 033524 013705 005542
7831 033530 006305
7832 033532 004737 053150
7833 033536 060337 003230
7834 033542 005502
7835 033544 060237 003226
7836 033550 005537 003220
7837 033554 104410
7838 033556 000207

```
*****  
;*RVTERM - COMPUTE A TERM OF THE REVERSE AVERAGE FORMULA,  
;*AND ADD IT TO THE REVERSE SUM IN MAXIL1-MAXIH1-MAXIL2-MAXIH2.  
*****  
RVTERM: SAVREG ;SAVE R0-R5  
MOV R0,R2 ;GET MEASUREMENT INTO R2-R3  
MOV R1,R3  
CLR R4  
MOV SCRACH,R5 ;GET COEFFICIENT  
ASL R5 ;MULTIPLY BY 2  
JSR PC,M.DPIM ;MULTIPLY COEFFICIENT  
ADD R3,MAXIH2 ;ADD TO REVERSE SUM  
ADC R2  
ADD R2,MAXIL2  
ADC MAXIH1  
RESREG ;RESTORE R0-R5  
RTS PC
```

7839
7840
7841
7842
7843
7844
7845
7846
7847
7848 033560 020063 000002
7849 033564 101006
7850 033566 103402
7851 033570 020113
7852 033572 103003
7853 033574 010063 000002
7854 033600 010113
7855 033602 020063 000006
7856 033606 103410
7857 033610 101003
7858 033612 020163 000004

```
*****  
;*CMPTIM - COMPARE MEAS'D TIME TO MEAS'D MIN AND MAX  
;*AND REPLACE, IF NECESSARY. TIME IS IN R0-R1, POINTER  
;*TO LO MIN IS IN R3.  
*****  
CMPTIM: CMP R0,2(R3) ;COMPARE HI BITS TO HI MIN  
BHI 6$ ;BR IF > MIN  
BLO 4$ ;BR IF < MIN  
CMP R1,(R3) ;COMPARE LO BITS TO LO MIN  
BHI 6$ ;BR IF > OR = MIN  
4$: MOV R0,2(R3) ;SET NEW MIN HI BITS  
MOV R1,(R3) ;SET NEW MIN LO BITS  
6$: CMP R0,6(R3) ;COMPARE HI BITS TO HI MAX  
BLO 10$ ;BR IF < MAX  
BHI 8$ ;BR IF > MAX  
CMP R1,4(R3) ;COMPARE LO BITS TO LO MAX
```

7859 033616 101404
7860 033620 010063 000006
7861 033624 010163 000004
7862 033630 000207
7863
7864
7865
7866
7867
7868
7869
7870 033632 104407
7871 033634 010305
7872 033636 005000
7873 033640 005001
7874 033642 013702 003200
7875 033646 013703 003176
7876 033652 005004
7877 033654 004737 053210
7878 033660 010237 003200
7879 033664 010337 003176
7880 033670 005000
7881 033672 005001
7882 033674 013702 003204
7883 033700 013703 003202
7884 033704 004737 053210
7885 033710 010237 003204
7886 033714 010337 003202
7887 033720 104410
7888 033722 000207
7889
7890
7891
7892
7893
7894
7895
7896
7897
7898
7899
7900
7901
7902 033724
7903
7904 033724 104401 011663
7905 033730 012701 003212
7906 033734 004737 034144
7907 033740 104401 001315
7908 033744 012701 003216
7909 033750 004737 034170
7910 033754 013746 003172
7911 033760 104405
7912 033762 010337 033770
7913 033766 104401
7914 033770 000000

```

      BLOS      10$      ;BR IF < OR = MAX
8$:      MOV      R0,6(R3) ;SET NEW MAX HI BITS
      MOV      R1,4(R3) ;SET NEW MAX LO BITS
10$:     RTS      PC      ;RETURN

;*****
;*GETAVG - COMPUTE FORWARD AND REVERSE SEEK TIME AVERAGES BY
;*DIVIDING TIME SUMS BY NO. OF MEASUREMENTS (PASSED IN R3 ON ENTRY).
;*****
GETAVG:  SAVREG      ;SAVE R0-R5
      MOV      R3,R5   ;LO DIVISOR = MEASUREMENT COUNT
      CLR      R0      ;SET DIVIDEND = FORWARD TIME SUM
      CLR      R1
      MOV      SUMHI1,R2
      MOV      SUMLO1,R3
      CLR      R4      ;HI DIVISOR = 0
      JSR      PC,M.DPID ;PERFORM DIVISION
      MOV      R2,SUMHI1 ;STORE FORWARD TIME AVG
      MOV      R3,SUMLO1
      CLR      R0      ;SET DIVIDEND = REVERSE TIME SUM
      CLR      R1
      MOV      SUMHI2,R2
      MOV      SUMLO2,R3
      JSR      PC,M.DPID ;PERFORM DIVISION
      MOV      R2,SUMHI2 ;STORE REVERSE TIME AVG
      MOV      R3,SUMLO2
      RESREG
      RTS      PC      ;RESTORE R0-R5
      ;RETURN

;*****
;SBTTL TYPTMS - TYPE RESULTS OF SEEK TIMING MEASUREMENTS
;*THIS SUBROUTINE TYPES THE MIN,MAX, AND AVG SEEK TIMES
;*(IN DECIMAL) MEASURED IN ONE OF THE SEEK TIMING TESTS,
;*IN BOTH THE FORWARD AND REVERSE DIRECTIONS. ALSO TYPED ARE
;*THE MAX SPEC'D TIME AND THE NO. OF SEEKS EXCEEDING IT,
;*AND THE TOTAL NO. OF MEASUREMENTS. POINTER TO SPECIFIED
;*MAX MESSAGE MUST BE IN R3 ON ENTRY.
;*****
TYPTMS: ;TYPE FORWARD MEASUREMENTS
      TYPE     ,FORWRD ;TYPE "***FORWARD DIRECTION***"
      MOV      #MINI1,R1 ;POINTER TO LO MIN
      JSR      PC,TYPMIN ;TYPE MIN MEAS'D TIME
      TYPE     ,%CRLF ;TYPE <CR> AND <LF>
      MOV      #MAXI1,R1 ;POINTER TO LO MAX
      JSR      PC,TYPMAX ;TYPE MAX MEAS'D TIME
      MOV      ABVMX1,-(SP) ;GET COUNT OF TIMES ABOVE MAX
      TYPDS    ;CONVERT AND TYPE IT
      MOV      R3,4$ ;SET POINTER TO SPEC'D MAX MESSAGE
      TYPE     ;TYPE " OF XXX ABOVE MAX OF XXXX US "
4$:      .WORD   0 ;SPEC'D MAX MSG POINTER GOES HERE

```

7915 033772 012701 003176
7916 033776 004737 034214
7917 034002 104401 001315
7918
7919 034006 104401 011701
7920 034012 012701 003222
7921 034016 004737 034144
7922 034022 104401 001315
7923 034026 012701 003226
7924 034032 004737 034170
7925 034036 013746 003174
7926 034042 104405
7927 034044 010337 034052
7928 034050 104401
7929 034052 000000
7930 034054 012701 003202
7931 034060 004737 034214
7932 034064 104401 001315
7933 034070 000207

```
MOV #SUMLO1,R1 ;SET POINTER TO LO AVG  
JSR PC,TYPAVG ;TYPE AVG TIME  
TYPE ,SCLRF ;TYPE <CR> AND <LF>  
;TYPE REVERSE MEASUREMENTS  
TYPE ,REVRSE ;TYPE '***REVERSE DIRECTION***'  
MOV #MINIL2,R1 ;POINTER TO LO MIN  
JSR PC,TYPMIN ;TYPE MIN MEAS'D TIME  
TYPE ,SCLRF ;TYPE <CR> AND <LF>  
MOV #MAXIL2,R1 ;POINTER TO LO MAX  
JSR PC,TYPMAX ;TYPE MAX MEAS'D TIME  
MOV ABVMX2,-(SP) ;GET COUNT OF TIMES ABOVE MAX  
TYPDS ;CONVERT AND TYPE IT  
MOV R3,6$ ;GET POINTER TO SPEC'D MAX MESSAGE  
TYPE ;TYPE '' OF XXX ABOVE MAX OF XXXXX US ''  
6$: .WORD 0 ;SPEC'D MAX MSG POINTER GOES HERE  
MOV #SUMLO2,R1 ;SET POINTER TO LO AVG  
JSR PC,TYPAVG ;TYPE AVG TIME  
TYPE ,SCLRF ;TYPE <CR> AND <LF>  
RTS PC ;RETURN
```

7934
7935
7936
7937
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7939
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7942
7943
7944
7945
7946
7947

```
*****  
;* CNVTIM - CONVERT SOFTWARE TIMER CYCLES TO TIME  
;* THIS SUBROUTINE MULTIPLIES THE CYCLE COUNT IN R4-R5 BY THE  
;* TIME CONSTANT IN TCONHI-TCONLO, TO OBTAIN TIME IN N-SEC. THIS  
;* IS THEN DIVIDED BY 1000 (DEC) TO OBTAIN TIME IN MICRO-SEC.  
;* THIS TIME IS RETURNED IN R2-R3. IF THE MULTIPLICATION OVERFLOWS  
;* 32 BITS, AN ERROR RETURN IS MADE TO THE ADDRESS LISTED AFTER THE  
;* CALL TO CNVTIM.  
;* CALL - JSR PC,CNVTIM  
;* <ERROR RETURN ADDRESS>  
*****
```

7948 034072 013702 003166
7949 034076 013703 003164
7950 034102 004737 053150
7951 034106 005700
7952 034110 00100?
7953 034112 005701
7954 034114 001403
7955 034116 017616 000000
7956 034122 000207
7957 034124 005004
7958 034126 012705 001750
7959 034132 004737 053210
7960 034136 062716 000002
7961 034142 000207

```
CNVTIM: MOV TCONHI,R2 ;SET MULTIPLIER = TIME CONST  
MOV TCONLO,R3  
JSR PC,M.DPIM ;MULTIPLY  
TST R0 ;MAKE SURE HI 2 WORDS ARE 0  
BNE 4$ ;BR IF NOT 0  
TST R1  
BEQ 6$  
4$: MOV @ (SP), (SP) ;SET ERROR RETURN PC  
RTS PC ;ERROR RETURN  
6$: CLR R4 ;SET HI BITS = 0  
MOV #1000.,R5 ;SET LO DIVISOR = 1000 (DEC)  
JSR PC,M.DPID ;CONVERT TIME TO US  
ADD #2,(SP) ;FIX ERROR-FREE RETURN PC  
RTS PC ;ERROR-FREE RETURN
```

7962
7963
7964
7965
7966
7967
7968

```
*****  
;* TYPMIN - TYPE MIN MEAS'D TIME IN US. POINTER TO LO TIME IS IN R1  
;* ON ENTRY.  
*****
```

7969 034144 104401 011717
7970 034150 010146

```
TYPMIN: TYPE ,MINEQ ;TYPE 'MIN = '  
MOV R1,-(SP) ;GET POINTER TO LO TIME
```

7971 034152 004737 053506
7972 034156 004737 053702
7973 034162 104401 011744
7974 034166 000207

JSR PC,@#SDB2D ;CONVERT TO DEC
JSR PC,@#SSUPRS ;TYPE IT
TYPE ,MICROS ;TYPE 'US ''
RTS PC

7975
7976
7977
7978
7979

*TYPMAX - TYPE MAX MEAS'D TIME IN US. POINTER TO LO TIME IS IN R1
*ON ENTRY.

7981
7982 034170 104401 011726
7983 034174 010146
7984 034176 004737 053506
7985 034202 004737 053702
7986 034206 104401 011744
7987 034212 000207

TYPMAX: TYPE ,MAXEQ ;TYPE 'MAX = ''
MOV R1,-(SP) ;GET POINTER TO LO TIME
JSR PC,@#SDB2D ;CONVERT TO DEC
JSR PC,@#SSUPRS ;TYPE IT
TYPE ,MICROS ;TYPE 'US ''
RTS PC

7988
7989
7990
7991

*TYPAVG - TYPE AVERAGE MEAS'D TIME IN US. POINTER TO LO TIME IS IN R1
*ON ENTRY.

7992
7993
7994
7995 034214 104401 011735
7996 034220 010146
7997 034222 004737 053506
7998 034226 004737 053702
7999 034232 104401 011744
8000 034236 000207

TYPAVG: TYPE ,AVGEQ ;TYPE 'AVG = ''
MOV R1,-(SP) ;GET POINTER TO LO TIME
JSR PC,@#SDB2D ;CONVERT TO DEC
JSR PC,@#SSUPRS ;TYPE IT
TYPE ,MICROS ;TYPE 'US ''
RTS PC

8001
8002
8003
8004
8005

* LODSEC - THIS SUBROUTINE LOADS THE CONTENTS OF R0 INTO ALL
*256(DEC) WORDS OF THE DATA BUFFER (RWBUF).

8006
8007
8008
8009 034240 104407
8010 034242 012701 063520
8011 034246 010021
8012 034250 020127 064520
8013 034254 001374
8014 034256 104410
8015 034260 000207

LODSEC: SAVREG ;SAVE R0-R5
MOV #RWBUF,R1 ;GET ADDRESS OF DATA BUF INTO R1
2\$: MOV R0,(R1)+ ;PUT WORD INTO BUFFER
CMP R1,#RWBUF+512. ;SEE IF 256 WORDS WRITTEN YET
BNE 2\$;BR IF NOT DONE YET
RESREG ;RESTORE R0-R5
RTS PC ;RETURN

8016
8017
8018
8019

* TRKCHK - THIS SUBROUTINE DOES A WRITE-CHECK OF SECTOR FS ON CYL
*FC, FOR EACH OF TRACKS 0, 1, AND 2. THE DATA IS COMPARED TO THE
*TRACK NUMBER + 100(OCT) FOR THAT PARTICULAR BLOCK OF DATA WRITTEN.
*THE PARAMETER BLOCK MUST BE PRE-LOADED WITH THE PROPER PARAMETERS.

8020
8021
8022
8023
8024
8025 034262 104407
8026 034264 016546 000004

TRKCHK: SAVREG ;SAVE R0-R5
MOV P.SECT(R5),-(SP) ;SAVE TRACK AND SECTOR PARAMETERS

8027	034270	105065	000005	
8028				
8029	034274	116500	000005	
8030	034300	062700	000100	
8031	034304	004737	034240	
8032	034310	112765	000131	000001
8033	034316	004737	040252	
8034	034322	105265	000005	
8035	034326	122765	000003	000005
8036	034334	001357		
8037	034336	012665	000004	
8038	034342	104410		
8039	034344	000207		
8040				
8041				
8042				
8043				
8044				
8045				
8046				
8047	034346	104407		
8048	034350	012701	000010	
8049	034354	012700	007116	
8050	034360	004737	052564	
8051	034364	013720	052664	
8052	034370	013720	052662	
8053	034374	005301		
8054	034376	001370		
8055	034400	104410		
8056	034402	000207		
8057				
8058				
8059				
8060				
8061				
8062				
8063				
8064				
8065				
8066				
8067				
8068				
8069				
8070	034404	104407		
8071	034406	013746	005600	
8072	034412	005416		
8073	034414	005046		
8074	034416	116616	000003	
8075	034422	005066	000002	
8076	034426	116566	000004	000002
8077	034434	066616	000002	
8078	034440	005066	000002	
8079	034444	012700	000026	
8080	034450	105737	003125	
8081	034454	001402		
8082	034456	012700	000024	

```
CLR B P.TRCK(R5) ;CLEAR THE TRACK NO.
;LOAD THE DATA BUFFER WITH THE CURRENT TRACK NO.
2$: MOV B P.TRCK(R5),R0 ;GET TRACK NO. INTO R0
ADD #100,R0 ;ADD 100(OCT) TO TRACK NO.
JSR PC,LODSEC ;LOAD DATA BUF WITH TRACK NO. + 10J(OCT)
MOV B #WRTCHK,P.CMND(R5) ;SET WRITE CHECK COMMAND
JSR PC,DRVCAL ;PERFORM THE WRITE CHECK
INCB P.TRCK(R5) ;INCREMENT THE TRACK NO.
CMPB #3,P.TRCK(R5) ;SEE IF DONE WITH ALL TRACKS
BNE 2$ ;BR IF NOT DONE YET
MOV (SP)+,P.SECT(R5) ;RESTORE TRACK AND SECTOR PARAMETERS
RESREG ;RESTORE R0-R5
RTS PC ;RETURN
```

*LODP14 - GENERATE SIXTEEN PSEUDO-RANDOM NUMBERS AND LOAD THEM
*INTO THE PATTERN 14 TABLE.

```
LODP14: SAVREG ;SAVE R0-R5
MOV #8.,R1 ;INIT LOOP COUNTER TO 8.
MOV #PAT14,R0 ;GET ADDRESS OF PATTERN 14 BUFFER
4$: JSR PC,$RAND ;GENERATE 2 16-BIT RANDOM NUMBERS
MOV $LONUM,(R0)+ ;PUT ONE NUMBER INTO PATTERN
MOV $HINUM,(R0)+ ;PUT OTHER NO. INTO PATTERN
DEC R1 ;SEE IF 16 WORDS LOADED YET
BNE 4$ ;BR IF NOT YET
RESREG ;RESTORE R0-R5
RTS PC ;RETURN
```

*SBTTL FINADR - COMPUTE FINAL PACK ADDRESS
*THIS SUBROUTINE IS USED AFTER A DATA TRANSFER HAS COMPLETED, TO
*COMPUTE THE FINAL PACK ADDRESS AT TERMINATION. THE 2'S COMP. OF THE
*WORD NO. AT THE POINT OF INTEREST IS PASSED IN LASTWC. THIS IS USED WITH
*P.CYLN(R5), P.TRCK(R5), AND P.SECT(R5) TO COMPUTE THE CORRESPONDING
*PACK ADDRESS, WHICH IS RETURNED IN FINCYL, FINTRK, AND FINSEC.
*THE SUBROUTINE IS USED TO DETERMINE THE PACK ADDRESS OF A SOFTWARE
*DATA MISCOMPARE.

```
FINADR: SAVREG ;SAVE R0-R5
MOV (LASTWC,-(SP)) ;STORE WORD COUNT
NEG (SP) ;MAKE IT POSITIVE
18$: CLR -(SP) ;MAKE ROOM ON STACK
MOV B 3(SP),(SP) ;STORE NO. OF SECTORS TRANSFERRED
CLR 2(SP) ;CLEAR LOCATION ON STACK
MOV B P.SECT(R5),2(SP) ;STORE STARTING SECTOR
ADD 2(SP),(SP) ;DETERMINE FINAL SECTOR ADDRESS
CLR 2(SP) ;CLEAR NO. OF TRACKS TRANSFERRED
MOV #22.,R0 ;SET FOR 22 SECTORS
TSTB FORMAT ;DETERMINE THE FORMAT
BEQ 19$ ;BR IF 22 SECTORS
MOV #20.,R0 ;SET FOR 20 SECTORS
```

8083 034462 020016
8084 034464 101004
8085 034466 160016
8086 034470 005266 000002
8087 034474 000772
8088 034476 112637 005577
8089 034502 005046
8090 034504 116516 000005
8091 034510 066616 000002
8092 034514 005066 000002
8093 034520 122716 000003
8094 034524 101005
8095 034526 162716 000003
8096 034532 005266 000002
8097 034536 000770
8098 034540 112637 005576
8099 034544 066516 000002
8100 034550 011637 005574
8101 034554 005726
8102 034556 104410
8103 034560 000207
8104
8105
8106
8107
8108
8109
8110
8111
8112
8113
8114
8115
8116
8117
8118 034562 104407
8119 034564 013702 005572
8120 034570 005737 005544
8121 034574 001007
8122 034576 012700 006216
8123 034602 012746 000400
8124 034606 012701 063520
8125 034612 000463
8126 034614 005000
8127 034616 032701 000001
8128 034622 001003
8129 034624 005200
8130 034626 006201
8131 034630 000772
8132 034632 006300
8133 034634 006300
8134 034636 006300
8135 034640 006300
8136 034642 006300
8137 034644 062700 006216
8138 034650 012746 000020

```

19$:  CMP    R0,(SP)      ;CHECK FOR SECTOR OVERFLOW
      BHI    20$         ;NO, CHECK IF SECTOR CORRECT
      SUB    R0,(SP)     ;DECREMENT SECTOR COUNT BY 20 OR 22
      INC    2(SP)       ;INCREMENT TRACKS TRANSFERRED
      BR     19$         ;CHECK FOR SECTOR OVERFLOW

20$:  MOVB   (SP)+,FINSEC ;STORE FINAL SECTOR
      CLR   -(SP)       ;MAKE ROOM FOR TRACKS TRANSFERRED
      MOVB  P.TRCK(R5),(SP);STORE STARTING TRACK
      ADD   2(SP),(SP)  ;DETERMINE FINAL TRACK ADDRESS
      CLR   2(SP)       ;CLEAR FINAL CYLINDER

21$:  CMPB   #3,(SP)     ;CHECK FOR TRACK OVERFLOW
      BHI   22$         ;NO, CHECK FINAL TRACK
      SUB   #3,(SP)     ;DECREMENT TRACK COUNT BY 3
      INC   2(SP)       ;INCR CYL COUNT
      BR    21$        ;CHECK FOR TRACK OVERFLOW

22$:  MOVB   (SP)+,FINTRK ;STORE FINAL TRACK
      ADD   P.CYLN(R5),(SP);CALCULATE FINAL CYLINDER
      MOV   (SP),FINCYL ;STORE FINAL CYLINDER
      TST  (SP)+       ;CLEAN OFF STACK
      RESREG ;RESTORE R0-R5
      RTS   PC         ;RETURN
    
```

```

;*****
;SBTTL  LODBUF - LOAD THE READ/WRITE DATA BUFFER
;THIS SUBROUTINE LOADS THE READ/WRITE DATA BUFFER (POINTED TO BY
;* PMA AND PMA+2) WITH THE APPROPRIATE REPEATING DATA PATTERN. IF
;* PARAMETER PATRN = 0 ON ENTRY, THE DATA WHICH IS LOADED IS COMPRISED
;* OF ALL THE PATTERNS 00-15 (QUICK VERIFY DEFAULT DATA TEST).
;* IF PATRN IS NOT 0, THE NO. OF WORDS IN THE WORD WDSXFR
;* OF THE PATTERN IDENTIFIED BY THE NO. OF THE BIT SET IN R1 ON
;* ENTRY, ARE LOADED INTO THE BUFFER.
;*****
    
```

```

LODBUF: SAVREG          ;SAVE R0-R5
      MOV    WDSXFR,R2   ;GET NO. OF WORDS
      TST   PATRN       ;SEE IF QUICK VERIFY DATA TEST DESIRED
      BNE   3$         ;BR IF NOT QUICK VERIFY
      MOV   #PAT00,R0   ;SET DATA PATTERN STARTING ADDRESS
      MOV   #256,-(SP)  ;SET PATTERN WORD COUNT
      MOV   #RWBUF,R1  ;SET BUFFER ADDRESS
      BR    30$        ;PROCEED

3$:   CLR    R0         ;INIT PATTERN NUMBER
4$:   BIT    #BIT0,R1   ;SEE IF THIS BIT IS SET
      BNE   6$         ;BR IF THIS BIT IS SET
      INC   R0         ;INCREMENT PATTERN NO.
      ASR   R1         ;SHIFT TO EXAMINE NEXT BIT
      BR   4$         ;BR TO CHECK NEXT BIT

6$:   ASL   R0         ;MULTIPLY PATTERN NO. BY 32(DEC)
      ASL   R0
      ASL   R0
      ASL   R0
      ADD   #PAT00,R0   ;GET ADDRESS OF DESIRED PATTERN
      MOV   #16,-(SP)  ;SET PATTERN WORD COUNT
    
```

```
8139 034654 013701 005602      MOV     PMA,R1      ;SET BUFFER ADDRESS
8140 034660 005737 055662      TST     $K11      ;SEE IF MEM MGT PRESENT
8141 034664 100036                BPL     30$        ;BR IF NOT PRESENT
8142                ;BUFFER LOAD LOOP FOR MEM MGT STARTS HERE
8143 034666 013737 003206 172354  MOV     SAVPAR,@#KIPAR6 ;SET UP WORKING PAR
8144 034674 052737 000001 177572  BIS     #BIT0,@#SRO  ;TURN ON MEMORY MANAGEMENT
8145 034702 042701 160000        BIC     #160000,R1   ;FORCE RELOCATION THRU KIPAR6
8146 034706 052701 140000        BIS     #140000,R1
8147 034712 010003 22$:      MOV     R0,R3      ;GET A COPY OF PATTERN ADDRESS
8148 034714 011604                MOV     (SP),R4    ;INIT PATTERN WORD COUNT
8149 034716 012321 24$:      MOV     (R3)+,(R1)+ ;LOAD A DATA WORD INTO BUFFER
8150 034720 032701 020000        BIT     #BIT13,R1   ;SEE IF OVERFLOW TO NEXT PAGE
8151 034724 001405                BEQ     26$        ;BR IF NO OVERFLOW
8152 034726 062737 000200 172354  ADD     #200,@#KIPAR6 ;INCREMENT PAR BY 4K FOR NEW PAGE
8153 034734 042701 020000        BIC     #BIT13,R1   ;SET PAGE = 6 AGAIN
8154 034740 005302 26$:      DEC     R2         ;DECREMENT WORD COUNTER
8155 034742 001403                BEQ     28$        ;BR IF ALL DONE
8156 034744 005304                DEC     R4         ;DECREMENT PATTERN WORD COUNT
8157 034746 001363                BNE     24$        ;BR IF NOT DONE WITH PATTERN YET
8158 034750 000760                BR      22$        ;BR TO REPEAT THE PATTERN
8159 034752 042737 000001 177572 28$:      BIC     #BIT0,@#SRO ;DISABLE MEMCRY MANAGEMENT
8160 034760 000410                BR      44$        ;GO TO RETURN
8161                ;BUFFER LOAD LOOP FOR NO MEM MGT STARTS HERE
8162 034762 010003 30$:      MOV     R0,R3      ;GET A COPY OF PATTERN ADDRESS
8163 034764 011604                MOV     (SP),R4    ;INIT PATTERN WORD COUNT
8164 034766 012321 34$:      MOV     (R3)+,(R1)+ ;LOAD A DATA WORD INTO BUFFER
8165 034770 005302                DEC     R2         ;DECREMENT WORD COUNTER
8166 034772 001403                BEQ     44$        ;BR IF ALL DONE
8167 034774 005304                DEC     R4         ;DECREMENT PATTERN WORD COUNT
8168 034776 001373                BNE     34$        ;BR IF NOT DONE WITH PATTERN YET
8169 035000 000770                BR      30$        ;BR TO REPEAT THE PATTERN
8170 035002 005726 44$:      TST     (SP)+      ;POP THE STACK
8171 035004 104410                RESREG                ;RESTORE R0-R5
8172 035006 000207                RTS     PC         ;RETURN
```

```
8173
8174
8175
8176
8177      ;*****
8178      ;SBTTL CMPBUF - SOFTWARE COMPARE DATA
8179      ;*THIS SUBROUTINE PERFORMS A SOFTWARE COMPARE OF THE R/W DATA BUFFER (POINTED
8180      ;*TO BY PMA AND PMA+2) TO THE APPROPRIATE REPEATING DATA PATTERN. IF
8181      ;*PARAMETER PATRN = 0 ON ENTRY, THE DATA IS COMPRISED OF ALL THE PATTERNS
8182      ;*00-15 (QUICK VERIFY DEFAULT DATA TEST).
8183      ;*IF PATRN IS NOT 0, THE NO. OF THE BIT SET IN R1 ON ENTRY IDENTIFIES THE
8184      ;*REPEATING DATA PATTERN TO COMPARE AGAINST.
8185      ;*****
```

```
8185 035010 104407 062051  CMPBUF: SAVREG      ;SAVE R0-R5
8186 035012 112737 000040 063412  MOV     #40,DH701+38. ;RESTORE ERROR MSG PARAMS
8187 035020 012737 000007  MOV     #7,DF25+2
8188 035026 013702 005572  MOV     WDSXFR,R2    ;SET NO. OF WORDS
8189 035032 005037 005542  CLR     SCRACH       ;CLEAR COMPARE ERROR COUNT
8190 035036 004737 041650  JSR     PC,REPSUP    ;STORE PREV CMND FOR POSS. PRINT
8191 035042 005737 005544  TST     PATRN        ;SEE IF THIS IS QUICK VERIFY DEFAULT DATA TEST
8192 035046 001007                BNE     3$          ;BR IF NOT
8193 035050 012700 006216  MOV     #PAT00,R0    ;GET DATA PATTERN STARTING ADDR
8194 035054 012746 000400  MOV     #256.,-(SP) ;SET PATTERN WORD COUNT
```

```
8195 035060 012701 063520          MOV    #RWBUF,R1      ;SET BUFFER ADDRESS
8196 035064 000466          BR     30$           ;PROCEED
8197 035066 005000          3$:   CLR    R0       ;INIT PATTERN NO.
8198 035070 032701 000001          4$:   BIT    #BIT0,R1  ;SEE IF THIS BIT IS SET
8199 035074 001003          BNE   6$           ;BR IF THIS BIT IS SET
8200 035076 005200          INC   R0           ;INCREMENT PATTERN NUMBER
8201 035100 006201          ASR   R1           ;SHIFT TO EXAMINE NEXT BIT
8202 035102 000772          BR    4$           ;BR TO CHECK NEXT BIT
8203 035104 006300          6$:   ASL   R0       ;MULTIPLY PATTERN NO. BY 32(DEC)
8204 035106 006300          ASL   R0
8205 035110 006300          ASL   R0
8206 035112 006300          ASL   R0
8207 035114 006300          ASL   R0
8208 035116 062700 006216          ADD   #PAT00,R0     ;GET ADDRESS OF DESIRED PATTERN
8209 035122 012746 000020          MOV   #16,-(SP)    ;PATTERN WORD COUNT
8210 035126 013701 005602          MOV   PMA,R1       ;SET BUFFER ADDRESS
8211 035132 005737 055662          TST   $KT11        ;SEE IF MEM MGT PRESENT
8212 035136 100041          BPL   30$          ;BR IF NOT PRESENT
8213          ;COMPARE LOOP FOR MEM MGT STARTS HERE
8214 035140 013737 003206 172354          MOV   SAVPAR,@#KIPAR6 ;SET UP WORKING PAR
8215 035146 052737 000001 177572          BIS   #BIT0,@#SRO   ;TURN ON MEM MGT
8216 035154 042701 160000          BIC   #160000,R1    ;FORCE RELOCATION THRU KIPAR6
8217 035160 052701 140000          BIS   #140000,R1
8218 035164 010003          22$:  MOV   R0,R3        ;GET A COPY OF PATTERN ADDRESS
8219 035166 011604          MOV   (SP),R4       ;INIT PATTERN WORD COUNT
8220 035170 022321          24$:  CMP   (R3)+,(R1)+  ;COMPARE DATA WORD TO PATTERN WORD
8221 035172 001404          BEQ   25$           ;BR IF NO ERROR
8222 035174 013737 172354 001216          MOV   @#KIPAR6,$REG16 ;SET PAR FOR PRINTOUT
8223 035202 000432          BR    35$           ;GO HANDLE ERROR
8224 035204 032701 020000          25$:  BIT   #BIT13,R1    ;SEE IF OVERFLOW TO NEXT PAGE
8225 035210 001405          BEQ   26$           ;BR IF NO OVERFLOW
8226 035212 062737 000200 172354          ADD   #200,@#KIPAR6 ;INCR PAR BY 4K FOR NEW PAGE
8227 035220 042701 020000          BIC   #BIT13,R1    ;SET PAGE = 6 AGAIN
8228 035224 005302          26$:  DEC   R2           ;DECREMENT WORD COUNTER
8229 035226 001002          BNE   27$           ;BR IF NOT ALL DONE YET
8230 035230 000137 035674          JMP   54$           ;ALL DONE - GET OUT
8231 035234 005304          27$:  DEC   R4           ;DECREMENT PATTERN WORD COUNT
8232 035236 001354          BNE   24$           ;BR IF NOT DONE WITH PATTERN YET
8233 035240 000751          BR    22$          ;BR TO REPEAT THE PATTERN
8234          ;COMPARE LOOP FOR NO MEM MGT STARTS HERE
8235 035242 010003          30$:  MOV   R0,R3        ;GET COPY OF PATTERN ADDRESS
8236 035244 011604          MOV   (SP),R4       ;INIT PATTERN WORD COUNT
8237 035246 022321          34$:  CMP   (R3)+,(R1)+  ;COMPARE DATA WORD TO PATTERN WORD
8238 035250 001002          BNE   31$           ;BR I' COMPARE ERROR
8239 035252 000137 035654          JMP   40$           ;JUMP IF DATA COMPARES OK
8240 035256 105037 062051          31$:  CLRB  DH701+38.    ;ADJUST DATA HEADER FOR MSG
8241 035262 012737 000005 063412          MOV   #5,DF25+2    ;ADJUST ERROR DATA WORD COUNT
8242          ;COMMON COMPARE ERROR HANDLER
8243 035270 010237 001202          35$:  MOV   R2,$REG10    ;GET WORD NO.
8244 035274 163737 005572 001202          SUB   WDSXFR,$REG10
8245 035302 013737 001202 005600          MOV   $REG10,LASTWC ;GET 2'S COMP OF WORD NO.
8246 035310 004737 034404          JSR   PC,FINADR    ;COMPUTE ACTUAL PACK ADRS
8247 035314 104407          SAVREG             ;SAVE R0-R5
8248 035316 013700 005574          MOV   FINCYL,R0    ;GET CYL
8249 035322 113701 005576          MOVB  FINTRK,R1    ;GET TRACK
8250 035326 113702 005577          MOVB  FINSEC,R2    ;GET SECTOR
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8251	035332	004737	040146			JSR	PC,BDSRCK		:SEE IF THIS SECTOR LISTED BAD
8252	035336	104410				RESREG			:RESTORE R0-R5
8253	035340	032737	001000	005504		BIT	#BADSEC,RECODE		
8254	035346	001132				BNE	50\$:BR IF LISTED- DON'T REPORT ERROR
8255	035350	005737	005542			TST	SCRACH		:CHECK THE ERROR COUNT
8256	035354	001024				BNE	36\$:BR IF THIS IS NOT FIRST ERROR
8257	035356	105737	003144			TSTB	UBMPRS		:SEE IF UNIBUS MAP PRESENT
8258	035362	001111				BEQ	46\$:BR IF NOT
8259	035364	013737	005270	001174		MOV	CRMPHO,\$REG5		:GET CURRENT MAP REG 0
8260	035372	013737	005266	001176		MCV	CRMPLO,\$REG6		
8261	035400	104116				ERROR	116		:DATA MISCOMPARE (11/70)
8262	035402	104117				ERROR	117		
8263	035404	000401				BR	48\$		
8264	035406	104034			46\$:	ERROR	34		:TYPE HEADING FOR ERROR MSG
8265	035410	012737	177777	001174	48\$:	MOV	#-1,\$REG5		:INIT CYL NO.
8266	035416	005037	001176			CLR	\$REG6		
8267	035422	005037	001200			CLR	\$REG7		
8268	035426	005237	005542		36\$:	INC	SCRACH		:INCREMENT THE ERROR COUNT
8269	035432	032777	000001	143500		BIT	#BIT0,@JWR		:SEE IF ALL ERRORS SHOULD BE REPORTED
8270	035440	001004				BNE	38\$:BR TO REPORT ALL ERRORS
8271	035442	022737	000012	005542		CMP	#10.,SCRACH		:SEE IF 10(DFC) ERRORS YET
8272	035450	002511				BLT	54\$:BR IF ERROR LIMIT EXCEEDED
8273	035452	023737	001174	005574	38\$:	CMP	\$REG5,FINCYL		:SEE IF DIFFERENT CYL
8274	035460	001010				BNE	42\$:BR IF YES
8275	035462	123737	001176	005576		CMPB	\$REG6,FINTRK		:SEE IF DIFFERENT TRACK
8276	035470	001004				BNE	42\$:BR IF YES
8277	035472	123737	001200	005577		CMPB	\$REG7,FINSEC		:SEE IF DIFFERENT SECTOR
8278	035500	001412				BEQ	44\$:BR IF SAME PACK ADDRESS
8279	035502	013737	005574	001174	42\$:	MOV	FINCYL,\$REG5		:SET NEW PACK ADRS FOR PRINTOUT
8280	035510	113737	005576	001176		MOVB	FINTRK,\$REG6		
8281	035516	113737	005577	001200		MOVB	FINSEC,\$REG7		
8282	035524	104115				ERROR	115		:TYPE NEW PACK ADDRESS
8283	035526	005437	001202		44\$:	NEG	\$REG10		:GET WORD NO.
8284	035532	016337	177776	001204		MOV	-2(R3),\$REG11		:GET GOOD DATA
8285	035540	016137	177776	001206		MOV	-2(R1),\$REG12		:GET BAD DATA
8286	035546	013737	001202	001212		MOV	\$REG10,\$REG14		:COMPUTE PHYSICAL ADDRESS
8287	035554	005037	001210			CLR	\$REG13		
8288	035560	006137	001212			ROL	\$REG14		
8289	035564	006137	001210			ROL	\$REG13		
8290	035570	063737	005602	001212		ADD	PMA,\$REG14		
8291	035576	005537	001210			ADC	\$REG13		
8292	035602	063737	005604	001210		ADD	PMA+2,\$REG13		
8293	035610	010137	001214			MOV	R1,\$REG15		:GET VIRT. ADRS FOR PRINTOUT
8294	035614	162737	000002	001214		SUB	#2,\$REG15		
8295	035622	104063				ERROR	63		:TYPE GOOD AND BAD DATA, MEM. ADRS.
8296	035624	032777	000100	143306		BIT	#BIT6,@SWR		:SEE IF JUST 1 ERROR SHOULD BE REPORTED
8297	035632	001020				BNE	54\$:BR IF JUST 1 ERROR SHOULD BE REPORTED
8298	035634	005737	005544		50\$:	TST	PATRN		:SEE IF DEFAULT DATA TEST
8299	035640	001405				BEQ	40\$:BR IF YES
8300	035642	005737	055662			TST	\$KT11		:SEE IF MEM MGT PRESENT
8301	035646	100002				BPL	40\$:BR IF NOT PRESENT
8302	035650	000137	035204			JMP	25\$:GO HANDLE MEM MGT
8303	035654	005302			40\$:	DEC	R2		:DECREMENT WORD COUNTER
8304	035656	001406				BEQ	54\$:BR IF ALL DONE
8305	035660	005304				DEC	R4		:DECREMENT PATTERN WORD COUNT
8306	035662	001002				BNE	53\$:BR IF NOT DONE WITH PATTERN YET

8307 035664 000137 035242
8308 035670 000137 035246
8309 035674 005737 055662
8310 035700 100003
8311 035702 042737 000001 177572
8312 035710 005726
8313 035712 104410
8314 035714 000207
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8330 035716
8331 035716 026537 000002 017702
8332 035724 002551
8333 035726 104407
8334 035730 005037 003176
8335 035734 005037 003200
8336 035740 023765 017706 000002
8337 035746 001004
8338 035750 122765 000002 000005
8339 035756 001537
8340 035760 105737 003125
8341 035764 001411
8342
8343 035766 012746 024000
8344 035772 012746 036000
8345 035776 012746 012000
8346 036002 012746 000024
8347 036006 000410
8348
8349 036010 012746 026000
8350 036014 012746 041000
8351 036020 012746 013000
8352 036024 012746 000026
8353
8354 036030 012603
8355 036032 116502 000004
8356 036036 160203
8357 036040 005002
8358 036042 012705 000400
8359 036046 005004
8360 036050 004737 053150
8361 036054 010337 003176
8362 036060 016605 000012

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      JMP      30$      ;JUMP TO REPEAT THE PATTERN
53$:  JMP      34$
54$:  TST      $KT11    ;SEE IF MEM MGT PRESENT
      BPL      56$      ;BR IF NOT PRESENT
      BIC      #BIT0,#SRO ;DISABLE MEM MGT
56$:  TST      (SP)+    ;POP THE STACK
      RESREG    ;RESTORE R0-R5
      RTS      PC      ;RETURN

;*****
;* CHKLIM - CHECK CURRENT DATA TRANSFER LIMITS
;* THIS SUBROUTINE DETERMINES IF THE NEXT DATA TRANSFER SHOULD BE
;* ALLOWED WITH THE CURRENT PACK ADDRESS AND WORD COUNT. IF THE
;* TRANSFER WOULD CAUSE OVERFLOW BEYOND CYL 632, TRACK 1, THE
;* WORD COUNT IN P.WC(R5) MUST BE SCALED DOWN TO A VALID NUMBER.
;* IF NO POSSIBLE TRANSFER CAN BE ALLOWED, RETURN IS MADE TO
;* ADDRESS FOLLOWING THE CALL TO CHKLIM.
;* CYL 1456 USED FOR RK07
;* CALL - JSR PC,CHKLIM
;* <'NO TRANSFER' RETURN ADDRESS>
;*****
CHKLIM:  ;SAVE R0-R5
        CMP      P.CYLN(R5),LCMS ;SEE IF SHOULD CHECK ADDR. LIMITS YET
        BLT      34$          ;BR TO RETURN IF NOT
        SAVREG    ;SAVE R0-R5
        CLR      SUMLO1
        CLR      SUMHI1
        CMP      LSTCYL,P.CYLN(R5) ;SEE IF ON LAST CYL
        BNE      8$          ;BR IF NOT
        (MPB     #2,P.TRCK(R5) ;SEE IF ON TRACK 2, CYL 632/1456
        BEQ      36$          ;BR IF CAN'T DO TRANSFER
        TSTB     FORMAT      ;DETERMINE THE FORMAT
        BEQ      10$         ;BR IF 22(DEC) SECTORS
;STORE CONSTANTS FOR 20(DEC) SECTORS
        MOV      #10240,-(SP) ;NO. OF WORDS ON 2 TRACKS
        MOV      #15360,-(SP) ;NO. OF WORDS PER CYL
        MOV      #5120,-(SP)  ;NO. OF WORDS PER TRACK
        MOV      #20,-(SP)    ;NO. OF SECTORS
        BR       12$
;STORE CONSTANTS FOR 22(DEC) SECTORS
10$:   MOV      #11264,-(SP) ;NO. OF WORDS ON 2 TRACKS
        MOV      #16896,-(SP) ;NO. OF WORDS PER CYL
        MOV      #5632,-(SP)  ;NO. OF WORDS PER TRACK
        MOV      #22,-(SP)    ;NO. OF SECTORS
;COMPUTE NO. OF WORDS LEFT ON THIS TRACK
12$:  MOV      (SP)+,R3      ;GET NO. OF SECTORS
        MOVB     P.SECT(R5),R2 ;GET CURRENT SECTOR NO.
        SUB      R2,R3      ;NO. OF SECTORS LEFT
        CLR      R2        ;GET NO. OF WDS IN THESE SECTORS
        MOV      #256.,R5
        CLR      R4
        JSR      PC,M.DPIM
        MOV      R3,SUMLO1 ;STORE THIS NO.
        MOV      12(SP),R5 ;RESTORE PARAM BLK ADDR
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8363 ; COMPUTE NO. OF WORDS LEFT ON THIS CYLINDER
8364 036064 012703 000002 MOV #2,R3 ; TRACK LIMIT = 2
8365 036070 023765 017706 000002 CMP LSTCYL,P.CYLN(R5) ; SEE IF ON CYL 632/1456
8366 036076 001001 BNE 14$ ; BR IF NOT
8367 036100 005303 DEC R3 ; DECREMENT TRACK LIMIT TO 1 FOR CYL 632/1456
8368 036102 116502 000005 14$: MOV#B P.TRCK(R5),R2 ; GET CURRENT TRACK NO.
8369 036106 160203 SUB R2,R3 ; GET NO. OF TRACKS LEFT
8370 036110 005002 CLR R2 ; GET NO. OF WORDS IN THESE TRACKS
8371 036112 012605 MOV (SP)+,R5 ; NO. OF WDS PER TRACK
8372 036114 004737 053150 JSR PC,M.DPIM
8373 036120 060337 003176 ADD R3,SUML01 ; ADD WORDS TO TOTAL
8374 036124 016605 000010 MOV 10(SP),R5 ; RESTORE PARAM BLK ADR
8375 ; COMPUTE NO. OF WORDS ON WHOLE CYLINDERS REMAINING
8376 036130 013703 017704 MOV LCM1,R3 ; CYL LIMIT = 631/1455
8377 036134 166503 000002 SUB P.CYLN(R5),R3 ; GET NO. OF WHOLE CYLS LEFT
8378 036140 100001 BPL 16$
8379 036142 005003 CLR R3
8380 036144 005002 16$: CLR R2
8381 036146 012605 MOV (SP)+,R5 ; GET NO. OF WDS PER CYL
8382 036150 004737 053150 JSR PC,M.DPIM ; COMPUTE NO. OF WDS IN THESE WHOLE CYLS
8383 036154 063703 003176 ADD SUMLO1,R3 ; ADD THESE WDS TO TOTAL
8384 036160 005502 ADC R2
8385 036162 063702 003200 ADD SUMH11,R2
8386 036166 016605 000006 MOV 6(SP),R5 ; RESTORE PARAM BLK ADDR
8387 ; ADD THE NO. OF WDS ON TRACKS 0,1, CYL 632/1456
8388 036172 023765 017706 000002 CMP LSTCYL,P.CYLN(R5) ; SEE IF CYL = 632/1456
8389 036200 001002 BNE 18$ ; BR IF NOT 632/1456
8390 036202 005726 TST (SP)+ ; POP STACK
8391 036204 000402 BR 20$
8392 036206 062603 18$: ADD (SP)+,R3 ; ADD WDS ON TRKS 0,1, CYL 632/1456
8393 036210 005502 ADC R2
8394 ; GET DESIRED WORD COUNT
8395 036212 005000 20$: CLR R0 ; WORD COUNT HI BITS
8396 036214 016501 000012 MOV P.WC(R5),R1 ; GET THE DESIRED WORD COUNT
8397 036220 005401 NEG R1
8398 036222 001001 BNE 22$ ; BR IF WORD COUNT NOT 65,536(DEC)
8399 036224 005200 INC R0 ; SET HI WORD COUNT = 1
8400 ; SUBTRACT WORD COUNT FROM NO. OF WORDS LEFT TO DETERMINE IF OVERFLOW.
8401 ; NUMBER OF WORDS LEFT IS IN R2-R3, WORD COUNT IS IN R0-R1.
8402 036226 160103 22$: SUB R1,R3 ; LD PARTS
8403 036230 005602 SBC R2
8404 036232 160002 SUB R0,R2 ; HI PARTS
8405 036234 100004 BPL 30$ ; BR IF WORD COUNT NOT > NO. OF WDS LEFT
8406 ; SCALE DOWN WORD COUNT TO AVOID OVERFLOW (SUBTRACT THE EXCESS)
8407 036236 060301 ADD R3,R1 ; GET NEW WORD COUNT IN R1
8408 036240 005401 NEG R1
8409 036242 010165 000012 MOV R1,P.WC(R5) ; SET NEW WORD COUNT IN PARAM BLOCK
8410 ; RETURN
8411 036246 104410 30$: RESREG ; RESTORE R0-R5
8412 036250 062716 000002 34$: ADD #2,(SP) ; FIX NORMAL RETURN PC
8413 036254 000207 RTS PC ; EXIT
8414 036256 017616 000000 36$: MOV @ (SP),(SP) ; FIX 'NO TRANSFER' RETURN PC
8415 036262 104410 RESREG ; RESTORE R0-R5
8416 036264 000207 RTS PC ; RETURN
8417
8418
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8430 036266 122737 000001 003120 CTLOUT: CMPB #1, TSTING ;SEE IF CURRENTLY RUNNING TESTS
8431 036274 001077 BNE 10$ ;BR IF NOT RUNNING TESTS
8432 036276 005737 005532 TST INTCHR ;SEE IF ANY TTY INPUT
8433 036302 001474 BEQ 10$ ;BR IF NO INPUT
8434 036304 105737 003116 TSTB MDFLAG ;SEE IF DEFAULT MODE RUN
8435 036310 001446 BEQ 12$ ;BR IF YES
8436 036312 122737 000003 005532 CMPB #003, INTCHR ;SEE IF (^C) TYPED
8437 036320 001033 BNE 4$ ;BR IF NOT (^C)
8438 036322 012700 013660 MOV #DRVST, R0 ;SET RETURN ADDR = DRVST
8439 036326 105737 003134 2$: TSTB XOVLD ;SEE IF XXDP CURRENTLY OVERLAID
8440 036332 001402 BEQ 6$ ;BR IF NOT
8441 036334 004737 027210 JSR PC, GETXDP ;RESTORE SAVED XXDP, IF NECESSARY
8442 036340 000005 6$: RESET ;RESET ALL DEVICES
8443 036342 005037 005532 CLR INTCHR ;CLEAR TTY CHAR BUFFER WORD
8444 036346 005037 001304 CLR $TIMES ;CLEAR THE ITER. COUNT
8445 036352 105037 003135 CLRB XDPSVD ;CLEAR THE XXDP SAVED FLAG
8446 036356 012737 042130 003046 MOV #ERRHDL, A.ABNL ;RESTORE ERROR HANDLER ADDRESS
8447 036364 012706 001100 MOV #STACK, SP ;RESET THE STACK
8448 036370 112765 000113 000001 MOVB #RECAL, P.CMND(R5) ;SET RECAL COMMAND
8449 036376 004737 040252 JSR PC, DRVCAL ;DO CLEANUP RECALIBRATE
8450 036402 005037 001102 CLR $TSTNM ;CLEAR THE TEST NO.
8451 036406 000110 JMP @R0 ;EXIT FROM TESTS
8452 036410 122737 000032 005532 4$: CMPB #032, INTCHR ;SEE IF (^Z) TYPED
8453 036416 001016 BNE 7$ ;BR IF NOT (^Z)
8454 036420 012700 015266 MOV #INPUTP, R0 ;SET RETURN ADDR = INPUTP
8455 036424 000740 BR 2$ ;TAKE EXIT
8456 036426 122737 000003 005532 12$: CMPB #003, INTCHR ;SEE IF (^C) TYPED
8457 036434 001007 BNE 7$ ;BR IF NOT
8458 036436 104401 010062 TYPE ,HLTRQD ;TYPE 'HALT REQUESTED'
8459 036442 104401 010032 TYPE ,CNTRDY ;TYPE 'PRESS CONT WHEN RDY'
8460 036446 012700 044100 MOV #HLTPRG, R0 ;SET HALT ADDRESS
8461 036452 000725 BR 2$ ;TAKE EXIT
8462 036454 122737 000007 005532 7$: CMPB #007, INTCHR ;SEE IF (^G) TYPED
8463 036462 001002 BNE 8$ ;BR IF NOT (^G)
8464 036464 004737 026316 JSR PC, GTSWRG ;OPEN SOFTWARE SWR FOR MODIFICATION
8465 036470 004737 025766 8$: JSR PC, PREPKB ;ENABLE KBD INPUT AGAIN
8466 036474 000207 10$: RTS PC ;RETURN
8467
8468
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```

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*****
* CTLOUT - THIS SUBROUTINE CHECKS FOR (^C) OR (^Z) TTY INPUT.
* IF (^C) WAS TYPED, THE SUBROUTINE RETURNS TO DRVST. IF (^Z)
* WAS TYPED, THE SUBROUTINE RETURNS TO INPUTP. IN EITHER CASE,
* A RESET INSTRUCTION IS EXECUTED, AND THE STACK IS RE-INIT-
* IALIZED. IF THERE IS NO INPUT, OR INPUT OTHER THAN (^C) OR (^Z),
* NO ACTION IS TAKEN.
* CALL - JSR PC, CTLOUT
* OR - CKEXIT
*****
```

```
*****
* WRTSEC - WRITE A BLOCK OF 400(OCT) WORDS ONTO DISK AND PERFORM
* A WRITE CHECK. PARAMETER BLOCK MUST BE PRE-LOADED WITH PACK
* ADDRESS. DATA WORD IS PASSED IN R3 ON ENTRY. RWBUF IS THE BUFFER
* USED.
*****
```



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8475  
8476 036476 104407  
8477  
8478 036500 012700 063520  
8479 036504 012701 000400  
8480 036510 010320  
8481 036512 005301  
8482 036514 001375  
8483  
8484 036516 012765 063520 000010  
8485 036524 012765 177400 000012  
8486  
8487 036532 112765 000123 000001  
8488 036540 004737 040252  
8489  
8490 036544 112765 000131 000001  
8491 036552 004737 040252  
8492 036556 104410  
8493 036560 000207  
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8500 036562 104407  
8501 036564 005001  
8502 036566 016500 000012  
8503 036572 001002  
8504 036574 005201  
8505 036576 000401  
8506 036600 005400  
8507 036602 000241  
8508 036604 006100  
8509 036606 006101  
8510 036610 060037 005762  
8511 036614 005537 005764  
8512 036620 060137 005764  
8513 036624 104410  
8514 036626 000207  
8515  
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8521  
8522 036630  
8523 036630 004737 027450  
8524 036634 142765 000020 000007  
8525 036642 112765 000121 000001  
8526 036650 013765 017706 000002  
8527 036656 112765 000002 000005  
8528 036664 012703 000012  
8529 036670 105737 003125  
8530 036674 001403  
*****  
WRTSEC: SAVREG ;SAVE R0-R5  
;LOAD THE R/W BUFFER WITH DATA  
MOV #RWBUF,R0 ;BUFFER ADDRESS  
MOV #400,R1 ;400(OCT) WORDS  
4$: MOV R3,(R0)+ ;LOAD A BUFFER WORD  
DEC R1 ;DECR COUNTER  
BNE 4$ ;BR IF NOT DONE YET  
;SET UP PARAMETERS  
MOV #RWBUF,P.BALO(R5) ;SET BUS ADDRESS  
MOV #-400,P.WC(R5) ;SET WORD COUNT  
;WRITE THE DATA  
MOVB #WRDATA,P.CMND(R5) ;SET WRITE COMMAND  
JSR PC,DRVCAL ;WRITE THE DATA  
;PERFORM WRITE CHECK  
6$: MOVB #WRTCHK,P.CMND(R5) ;SET WRITE CHECK COMMAND  
JSR PC,DRVCAL ;PERFORM WRITE CHECK  
RESREG ;RESTORE R0-R5  
RTS PC ;RETURN  
*****  
;*INCRMA - INCREMENT MA BY WORD COUNT IN P.WC(R5).  
*****  
INCRMA: SAVREG ;SAVE R0-R5  
CLR R1  
MOV P.WC(R5),R0 ;GET WORD COUNT  
BNE 4$ ;BR IF NOT 65,536(DEC)  
INC R1 ;SET HI BIT  
BR 6$ ;CONTINUE  
4$: NEG R0 ;GET TRUE WORD COUNT  
6$: CLC ;DOUBLE THE WORD COUNT TO GET BYTES  
ROL R0  
ROL R1  
ADD R0,MA ;ADD IT TO COMPUTE NEW MA  
ADC MA+2  
ADD R1,MA+2  
RESREG ;RESTORE R0-R5  
RTS PC ;RETURN  
*****  
;*REDBSF - READ FACTORY BSF INTO BSFACT, AND READ SOFTWARE BSF  
;*INTO BSSOFT.  
*****  
REDBSF:  
JSR PC,INITSS ;INIT THE S.S.  
BICB #B.CFMT,P.CS1H(R5) ;SET FOR 22 SECTOR FORMAT  
MOVB #RDDATA,P.CMND(R5) ;SET READ DATA COMMAND  
MOV LSTCYL,P.CYLN(R5) ;SET CYL = 632/1456  
MOVB #2,P.TRCK(R5) ;SET TRACK = 2  
MOV #10.,R3 ;SET FACTORY BSF SECTOR LIMIT  
TSTB FORMAT  
BEQ 6$ ;BR IF 22 SECTORS
```

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8531 036676 105265 000004          INCB  P.SECT(R5)      ;SET STARTING FACTORY BSF SECTOR NO.
8532 036702 005203          INC    R3
8533 036704 012765 177400 000012 6$:  MOV    #-400,P.WC(R5) ;SET WORD COUNT FOR 1 SECTOR
8534 036712 012765 004232 000010  MOV    #BSFACT,P.BALO(R5) ;SET BA FOR FACTORY BSF
8535 036720 012737 037074 003046 8$:  MOV    #BDSCHD,A.ABNL ;SET ERROR HANDLER FOR BSF READ
8536 036726 105037 003141          CLRB  WCEFLG        ;INIT THE ERROR FLAG
8537 036732 004737 040252          JSR   PC,DRVCAL     ;READ THE FACTORY BSF
8538 036736 105737 003141          TSTB  WCEFLG        ;SEE IF ANY ERRORS
8539 036742 001413          BEQ   12$           ;BR IF NOT
8540 036744 062765 000002 000004  ADD    #2,P.SECT(R5) ;CHECK NEXT SECTOR
8541 036752 126503 000004          CMPB  P.SECT(R5),R3 ;SEE IF LIMIT EXCEEDED YET
8542 036756 001360          BNE   8$            ;BR IF NOT YET
8543 036760 004737 041650          JSR   PC,REPSUP     ;GATHER STATUS FOR PRINTOUT
8544 036764 104120          ERROR 120           ;ABORTING- BAD BSF READ
8545 036766 000137 044100          JMP   HLTPRG        ;HALT- CAN'T PROCEED
8546 036772 012765 003232 000010 12$: MOV    #BSOFT,P.BALO(R5) ;SET BA FOR SOFTWARE RECORD
8547 037000 110365 000004          MOVB  R3,P.SECT(R5) ;SET STARTING SECTOR NO.
8548 037004 012703 000026          MOV   #22.,R3       ;SET 22 SECTOR LIMIT
8549 037010 105737 003125          TSTB  FORMAT        ;SEE IF 22 SECTOR FORMAT
8550 037014 001402          BEQ   14$           ;BR IF YES
8551 037016 012703 000023          MOV   #19.,R3       ;SET 20 SECTOR LIMIT
8552 037022 012737 037074 003046 14$: MOV    #BDSCHD,A.ABNL ;SET ERROR HANDLER FOR BSF READ
8553 037030 105037 003141          CLRB  WCEFLG        ;INIT THE ERROR FLAG
8554 037034 004737 040252          JSR   PC,DRVCAL     ;READ THE SOFTWARE BSF
8555 037040 105737 003141          TSTB  WCEFLG        ;SEE IF ANY ERRORS
8556 037044 001407          BEQ   16$           ;BR IF NOT
8557 037046 062765 000002 000004  ADD    #2,P.SECT(R5) ;CHECK NEXT SECTOR
8558 037054 126503 000004          CMPB  P.SECT(R5),R3 ;SEE IF LIMIT EXCEEDED YET
8559 037060 003760          BLE  14$           ;BR IF NOT YET
8560 037062 000736          BR    10$          ;REPORT ERROR AND ABORT
8561 037064 012737 042130 003046 16$: MOV    #ERRHDL,A.ABNL ;RESTORE ERROR HANDLER ADRS
8562 037072 000207          RTS   PC            ;RETURN
8563
8564          ;*THIS IS THE ABNORMAL DRIVER RETURN WHICH IS USED TO HANDLE
8565          ;*POSSIBLE ERRORS IN READING BAD SECTORS.
8566 037074 032765 130600 000034 BDSCHD: BIT    #BSE!HVRC!DTE!OPI!DCK,P.ER(R5) ;SEE IF ANY READ ERRORS
8567 037102 001002          BNE   4$            ;BR IF A READ ERROR OCCURRED
8568 037104 000137 042130          JMP   ERRHDL        ;GO HANDLE OTHER ERROR
8569 037110 105237 003141          4$:  INCB  WCEFLG        ;SET ERROR FLAG
8570 037114 000137 044306          JMP   RETNML        ;TAKE NORMAL DRIVER RETURN
8571
8572
8573
8574          ;*****
8575          ;*TYPBSF - TYPE FACTORY AND SOFTWARE BAD SECTOR FILES (BSF'S)
8576          ;*WITH THE CYLINDER, TRACK, AND SECTOR NO. OF EACH BAD SECTOR
8577          ;*LISTED SEPARATELY, IN A TABLE OF OCTAL VALUES, FOR EACH OF THE
8578          ;*2 BSF'S.
8579          ;*****
8580 037120 104407          TYPBSF: SAVREG      ;SAVE R0-R5
8581 037122 005004          CLR    R4           ;INIT BSF FILE INDICATOR
8582 037124 012700 004242          MOV   #BSFACT+10,R0 ;ADRS OF DATA IN FACTORY BSF
8583 037130 104401 011364          TYPE  ,FACTBS       ;TYPE 'FACTORY'
8584 037134 104401 011411          TYPE  ,BDSECT       ;TYPE 'BAD SECTORS'
8585 037140 104401 061616          3$:  TYPE  ,DH6041    ;TYPE 'CYLNR TRACK SECTOR'
8586 037144 104401 001315          TYPE  ,$CRLF
```

```
8587 037150 005001          CLR      R1          ;INIT LIMIT COUNTER
8588 037152 005710          TST      (R0)        ;SEE IF A SECTOR LISTED HERE
8589 037154 100007          BPL      8$          ;BR IF YES
8590 037156 005701          TST      R1          ;SEE IF FILE IS EMPTY
8591 037160 001032          BNE      14$         ;BR IF NOT EMPTY
8592 037162 104401 011431   TYPE     ,NOFALS     ;TYPE 'NONE'
8593 037166 104401 001315   TYPE     ,$CRLF      ;TYPE <CR>,<LF>
8594 037172 000425          BR       14$         ;
8595 037174 012046          8$:     MOV      (R0)+,-(SP) ;GET A CYL NO.
8596 037176 104402          TYP0C    ;TYPE IT
8597 037200 104401 012420   TYPE     ,SPACE2    ;TYPE SEPARATORS
8598 037204 011003          MOV      (R0),R3    ;GET TRACK AND SECTOR
8599 037206 105003          CLRB    R3          ;
8600 037210 000303          SWAB    R3          ;GET THE TRACK NO.
8601 037212 010346          MOV      R3,-(SP)   ;
8602 037214 104402          TYP0C    ;TYPE IT
8603 037216 104401 012420   TYPE     ,SPACE2    ;TYPE SEPARATORS
8604 037222 111003          MOV      (R0),R3    ;GET SECTOR NO.
8605 037224 010346          MOV      R3,-(SP)   ;
8606 037226 104402          TYP0C    ;TYPE IT
8607 037230 104401 001315   TYPE     , $CRLF     ;
8608 037234 005720          TST      (R0)+      ;INCR THE POINTER
8609 037236 005201          INC     R1          ;INCR THE COUNTER
8610 037240 020127 000176   CMP     R1,#126.    ;SEE IF LIMIT REACHED IN THIS FILE
8611 037244 002742          BLT     4$          ;BR IF NOT YET
8612 037246 005704          14$:    TST      R4          ;SEE IF BOTH FILES TYPED YET
8613 037250 001006          BNE     18$         ;BR IF YES
8614 037252 005204          INC     R4          ;SLT INDICATOR FOR SOFT. FILE
8615 037254 012700 003242   MOV     #BSSOFT+10,R0 ;ADRS OF DATA IN SOFTWARE BSF
8616 037260 104401 011377   TYPE     ,SOFTBS    ;TYPE 'SOFTWARE BAD SECTORS :''
8617 037264 000725          BR      3$          ;GO TYPE SOFT. BSF
8618 037266 104410          18$:    RESREG   ;RESTORE R0-R5
8619 037270 000207          RTS     PC          ;RETURN
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8627 037272 104407          ;*****
8628 037274 016500 000002   ;*FINMEM - COMPUTE NO. OF WORDS XFERRED ON PARTIAL XFER
8629 037300 116501 000005   ;*(TERMINATED BY BSE ERROR), AND STORE IT IN WDSXFR.
8630 037304 116502 000004   ;*****
8631 037310 005003          INMEM:  SAVREG      ;SAVE R0-R5
8632 037312 026500 000030   MOV     P.CYLN(R5),R0 ;GET ORIG. CYL NO.
8633 037316 001006          MOV     P.TRCK(R5),R1 ;GET TRACK NO.
8634 037320 126501 000027   MOV     P.SECT(R5),R2 ;SECTOR NO.
8635 037324 001003          CLR     R3          ;INIT SECTOR COUNT TO 0
8636 037326 126502 000026   6$:     CMP     P.DCYL(R5),R0 ;COMPARE ORIG. AND CURRENT CYLS
8637 037332 001404          BNE     8$          ;BR IF NOT EQUAL
8638 037334 005203          CMP     P.DTS+1(R5),R1 ;COMPARE TRACKS
8639 037336 004737 037356   BNE     8$          ;BR IF NOT EQUAL
8640 037342 000763          CMP     P.DTS(R5),R2  ;COMPARE SECTORS
8641 037344 000303          BEQ     12$         ;BR IF ADRS ARE EQUAL
8642 037346 010337 000572   8$:     INC     R3          ;INCR SECTOR COUNT
          JSR     PC,INCRSC ;INCR PACK ADRS BY 1 SECTOR
          BR      6$          ;GO COMPARE ADDRESSES
          12$:    SWAB    R3          ;COMPUTE NO. OF WORDS XFERRED
          MOV     R3,WDSXFR ;STORE IT
```

8643 037352 104410
8644 037354 000207
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8653 037356 012704 000025
8654 037362 105737 003125
8655 037366 001402
8656 037370 012704 000023
8657 037374 005202
8658 037376 020204
8659 037400 003407
8660 037402 005002
8661 037404 005201
8662 037406 020127 000002
8663 037412 003402
8664 037414 005001
8665 037416 005200
8666 037420 000207
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8676 037422 104407
8677 037424 004737 037272
8678 037430 016500 000030
8679 037434 116501 000027
8680 037440 116502 000026
8681 037444 004737 037356
8682 037450 010065 000002
8683 037454 110165 000005
8684 037460 110265 000004
8685 037464 013703 005572
8686 037470 062703 000400
8687 037474 060365 000012
8688 037500 005004
8689 037502 006103
8690 037504 006104
8691 037506 060337 005602
8692 037512 005537 005604
8693 037516 060437 005604
8694 037522 013765 005602 000010
8695 037530 013700 005604
8696 037534 042700 177774
8697 037540 150065 000007
8698 037544 005737 055662

```
RESREG ;RESTORE R0-R5
RTS PC ;RETURN

*****
*INCRSC - INCREMENT PACK ADDRESS TO NEXT SECTOR
*THE CYLINDER IS PASSED AND RETURNED IN R0, TRACK IN R1, AND SECTOR
*IN R2.
*****
INCRSC: MOV #21.,R4 ;SET SECTOR LIMIT
TSTB FORMAT ;SEE IF 22 SECTOR FORMAT
BEQ 4$ ;BR IF YES
MOV #19.,R4 ;SET SECTOR LIMIT
4$: INC R2 ;INCR. SECTOR NO.
CMP R2,R4 ;SEE IF SECTOR LIMIT EXCEEDED
BLE 6$ ;BR IF NOT
CLR R2 ;SET SECTOR = 0
INC R1 ;INCR. TRACK NO.
CMP R1,#2 ;SEE IF TRACK LIMIT EXCEEDED
BLE 6$ ;BR IF NOT
CLR R1 ;SET TRACK = 0
INC R0 ;INCR. CYLINDER NO.
6$: RTS PC ;RETURN
```

```
*****
*MIDXFR - MID-TRANSFER PARAMETER UPDATE SUBROUTINE
*THIS SUBROUTINE UPDATES PMA,PMA+2, MEM MGT REGS, UNIBUS MAP, P.BALO(R5),
*P.BAHI(R5), AND P.WC(R5), IN PREPARATION TO RESUME A TRANSFER
*TERMINATED BY A BAD SECTOR ERROR (BSE).
*****
MIDXFR: SAVREG ;SAVE R0-R5
JSR PC,FINMEM ;COMPUTE NO. OF WORDS XFERRERD
MOV P.DCYL(R5),R0 ;GET CYL NO.
MOVB P.DTS+1(R5),R1 ;GET TRACK NO.
MOVB P.DTS(R5),R2 ;GET SECTOR NO.
JSR PC,INCRSC ;INCREMENT PACK ADRS TO NEXT SECTOR
MOV R0,P.CYLN(R5) ;UPDATE CYLINDER
MOVB R1,P.TRCK(R5) ;UPDATE TRACK
MOVB R2,P.SECT(R5) ;UPDATE SECTOR
MOV WDSXFR,R3 ;GET NO. OF WORDS XFERRERD
ADD #400,R3 ;SKIP BAD SECTOR
ADD R3,P.WC(R5) ;UPDATE P.WC(R5)
CLR R4 ;GET BYTES XFERRERD
ROL R3
ROL R4
ADD R3,PMA ;UPDATE PMA,PMA+2
ADC PMA+2
ADD R4,PMA+2
MOV PMA,P.BALO(R5) ;UPDATE P.BALO(R5)
MOV PMA+2,R0
BIC #177774,R0
BISB R0,P.BAHI(R5) ;UPDATE P.BAHI(R5)
TST $K11 ;SEE IF MEM MGT
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8699 037550 100002
8700 037552 004737 026532
8701 037556 104410
8702 037560 000207
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BPL 16\$
JSR PC,PREPAR ;UPDATE MEM MGT AND UNIBUS MAP
16\$: RESREG ;RESTORE RO-R5
RTS PC ;RETURN

*SVPRMS - SAVE INITIAL PARAMETERS FOR TRANSFER
*GTPRMS - RESTORE SAVED TRANSFER PARAMETERS

8710 037562 104407
8711 037564 012700 002632
8712 037570 012701 005560
8713 037574 016537 000012 005572
8714 037602 0054J7 005572
8715 037606 012737 063520 005602
8716 037614 005037 005604
8717 037620 005737 005544
8718 037624 001414
8719 037626 013737 005762 005602
8720 037634 013737 005764 005604
8721 037642 000405
8722 037644 104407
8723 037646 012700 005560
8724 037652 012701 002632
8725 037656 012702 000005
8726 037662 012021
8727 037664 005302
8728 037666 001375
8729 037670 104410
8730 037672 000207

SVPRMS: SAVREG ;SAVE RO-R5
MOV #PARMO+2,RO ;ADRS OF PARAMS
MOV #SAVPRS,R1 ;ADRS OF SAVE AREA
MOV P.WC(R5),WDSXFR ;GET WORD COUNT
NEG WDSXFR ;MAKE IT POSITIVE
MOV #RWBUF,PMA ;INIT PMA TO RWBUF
CLR PMA+2 ;INIT PMA+2 TO 0
TST PATRN ;SEE IF DEFAULT DATA TEST
BEQ GTO ;BR IF YES
MOV MA,PMA ;SET PMA=MA
MOV MA+2,PMA+2 ;SET PMA+2=MA+2
BR GTO

GTPRMS: SAVREG ;SAVE RO-R5
MOV #SAVPRS,RO ;ADRS OF SAVE AREA
MOV #PARMO+2,R1 ;ADRS OF PARAMS
GTO: MOV #5,R2 ;SET FOR 5 WORDS
6\$: MOV (R0)+,(R1)+ ;MOVE A WORD
DEC R2 ;SEE IF DONE YET
BNE 6\$;BR IF NOT YET
RESREG ;RESTORE RO-R5
RTS PC ;RETURN

*TRANSFR - PERFORM A DATA TRANSFER, AND HANDLE BAD SECTORS, IF
*ENCOUNTERED. THE COMMAND MUST BE SET IN THE PARAM BLK. THIS SUB-
*ROUTINE IS SUBSTITUTED FOR DRVCAL WHEN BAD SECTORS MUST BE HANDLED.

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8739 037674 010446
8740 037676 005004
8741 037700 105037 003141
8742 037704 004737 040252
8743 037710 032737 000100 005504
8744 037716 001403
8745 037720 112737 000001 003141
8746 037726 032737 000002 005504
8747 037734 001406
8748 037736 005204
8749 037740 004737 037422
8750 037744 005765 000012
8751 037750 001355
8752 037752 005704
8753 037754 001411
8754 037756 004737 037644

TRANSFR: MOV R4,-(SP) ;SAVE R4 ON STACK
CLR R4 ;CLEAR BSE ERROR INDICATOR
CLRB WCEFLG ;INIT WCE ERROR FLAG TO 0
4\$: JSR PC,DRVCAL ;PERFORM THE S.S. FUNCTION
BIT #WCERR,RECODE ;SEE IF WCE ERROR OCCURRED
BEQ 5\$;BR IF NOT
MOVB #1,WCEFLG ;SET WCE ERROR FLAG
5\$: BIT #BSERR,RECODE ;SEE IF BSE ERROR OCCURRED
BEQ 6\$;BR IF NOT
INC R4 ;INCR BSE ERROR INDICATOR
JSR PC,MIDXFR ;UPDATE PARAMS TO RESUME XFER
TST P.WC(R5) ;SEE IF ENTIRE XFER IS COMPLETED
BNE 4\$;BR IF NOT
6\$: TST R4 ;SEE IF ANY BSE ERRORS OCCURRED
BEQ 8\$;BR IF NOT
JSR PC,GTPRMS ;RESTORE ORIGINAL PARAMS OF XFER

8755 037762 004737 037562
8756 037766 005737 055662
8757 037772 100002
8758 037774 004737 026532
8759 040000 012604
8760 040002 000207
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8775 040004 010237 001266
8776 040010 010337 001270
8777 040014 012637 001272
8778 040020 011402
8779 040022 012437 001264
8780 040026 062737 001000 001264
8781 040034 005003
8782 040036 062702 000010
8783 040042 005712
8784 040044 100430
8785 040046 020012
8786 040050 001406
8787 040052 062702 000004
8788 040056 020237 001264
8739 040062 002021
8790 040064 000766
8791 040066 005722
8792 040070 011237 001302
8793 040074 042737 174377 001302
8794 040102 123701 001303
8795 040106 001402
8796 040110 005722
8797 040112 000753
8798 040114 005203
8799 040116 012246
8800 040120 042716 177700
8801 040124 000746
8802 040126 010346
8803 040130 013702 001266
8804 040134 013703 001270
8805 040140 013746 001272
8806 040144 000204
8807
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8810

```
JSR PC,SVPRMS ;RESTORE WDSXFR,PMA,PMA+2
TST $K111 ;SEE IF MEM MGT PRESENT
BPL $S ;BR IF NOT
JSR PC,PREPAR ;PREPARE MEM MGT AND U.M.
8$: MOV (SP)+,R4 ;RESTORE R4
RTS PC ;RETURN

:*****
:SBTTL SEARCH BAD SECTOR TABLES ROUTINE
:*THIS ROUTINE RETURNS A LIST OF BAD SECTORS FOUND IN
:*THE SPECIFIC TABLE (BAD SECTOR SOFTWARE OR BAD SECTOR FACTORY)
:*FOR THE SPECIFIC CYLINDER AND TRACK DESIRED. R0 AND R1 MUST
:*CONTAIN THE CYLINDER AND TRACK, RESPECTIVELY.
:*
:*THE LIST OF BAD SECTORS IS FOUND ON THE STACK WHEN THE ROUTINE
:*RETURNS TO THE CALLER. THE FIRST ENTRY ON THE STACK WILL BE THE
:*NUMBER OF BAD SECTORS FOUND FOR THIS CYLINDER AND TRACK.
:*****
SRHTBS: MOV R2,$TMP2
MOV R3,$TMP3
MOV (SP)+,$TMP4 ;STORE RETURN CONTENTS OF R4
MOV (R4),R2 ;GET ADDRESS OF BAD SECTOR TABLE TO SEARCH
MOV (R4)+,$TMP1 ;SETUP FOR LENGTH OF BAD SECTOR TABLE
ADD #1000,$TMP1
CLR R3 ;CLEAR R3 FOR COUNT
ADD #10,R2 ;SET R2 FOR POINTER TO CYLINDER ENTRY
1$: TST (R2) ;TEST IF ALL ONES
BMI $S ;YES-DONE
CMP R0,(R2) ;TEST IF BAD SECTOR IN PRESENT CYL
BEQ $S ;YES-GO CHECK TRACK
ADD #4,R2 ;ELSE BUMP POINTER
CMP R2,$TMP1 ;TEST IF OUT OF TABLE
BGE $S ;YES-EXIT
BR 1$ ;LOOP
3$: TST (R2)+ ;TRACK CHECK - PUT POINTER AT TRK/SEC BYTE
MOV (R2),$TMP10 ;GET TRK/SEC WORD
BIC #174377,$TMP10 ;CLEAR ALL BUT TRACK
CMPB $TMP10+1,R1 ;CHECK IF BAD SECTOR IN THIS TRACK
BEQ 4$ ;YES - GO PUT SECTOR NUMBER ON STACK
TST (R2)+ ;ELSE BUMP POINTER TO NEXT CYL WORD
BR 1$ ;GO TEST NEXT CYL
4$: INC R3 ;BUMP BAD SECTOR COUNT
MOV (R2)+,-(SP) ;PUT TRK/SEC WORD ON STACK
BIC #177700,-(SP) ;CLEAR ALL BUT SECTOR NUMBER
BR 1$ ;GO CHECK REST OF FILE
5$: MOV R3,-(SP) ;EXIT - PUT NUMBER OF BAD
MOV $TMP2,R2 ;SECTORS ON STACK-RESTORE
MOV $TMP3,R3 ;REGISTERS
MOV $TMP4,-(SP) ;PUT RETURN ON STACK
RTS R4 ;RETURN

:*****
:SBTTL BAD SECTOR CHECK ROUTINE
:*THIS ROUTINE WILL SEARCH BOTH TABLES TO DETERMINE IF A
:*SPECIFIC SECTOR IS LISTED AS BAD. IF THE SECTOR IS LISTED IN THE
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```
8811 ;*TABLES THE ROUTINE SETS THE 'BADSEC' FLAG AND RETURNS. IF THE SECTOR
8812 ;*IS NOT LISTED THE FLAG IS RESET.
8813 ;*****
8814 040146 104407 BDSRCK: SAVREG
8815 040150 012737 004232 040162 MOV #BSFACT,1$ ;SET TABLE TO SEARCH
8816 040156 004437 040004 2$: JSR R4,SRHTBS ;GO SEARCH IT
8817 ^40162 000000 1$: .WORD ;TABLE ADDRESS GOES HERE
8818 0.0164 012603 MOV (SP)+,R3 ;GET NUMBER OF BAD SECTORS, IF ANY
8819 040166 001015 BNE 6$ ;IF ANY, GO TEST WHICH ONES
8820 040170 023727 040162 003232 7$: CMP 1$,#BSOFT ;ELSE TEST IF OTHER TABLE ALREADY SEARCHED
8821 040176 001404 BEQ 3$ ;IF YES-EXIT
8822 040200 012737 003232 040162 MOV #BSOFT,1$ ;SET OTHER TABLE FOR SEARCH
8823 040206 000763 BR 2$ ;GO SEARCH IT
8824 040210 042737 001000 005504 3$: BIC #BADSEC,RECODE ;CLEAR BAD SECTOR BIT
8825 040216 104410 4$: RESREG
8826 040220 000207 RTS PC ;RETURN
8827 040222 022602 6$: CMP (SP)+,R2 ;THIS SECTOR IN TABLE?
8828 040224 001403 BEQ 8$ ;YES-GO SET BIT & EXIT
8829 040226 005303 DEC R3 ;DECREMENT BAD SECTOR COUNT
8830 040230 001374 BNE 6$ ;IF NOT ZERO-CHECK NEXT ENTRY
8831 040232 000756 BR 7$ ;ELSE GO SEARCH OTHER TABLE
8832 040234 052737 001000 005504 8$: BIS #BADSEC,RECODE ;SET BAD SECTOR BIT
8833 040242 005303 9$: DEC R3 ;CLEAR STACK OF OTHER BAD SECTOR
8834 040244 001764 BEQ 4$ ;NUMBER
8835 040246 005726 TST (SP)+
8836 040250 000774 BR 9$ ;EXIT
```

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8837
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8840 ;*****
8841 .SBTTL CALL DRIVER ROUTINE
8842 ;*ENTRY JSR PC,DRVCAL
8843 ;* WITH R5 POINTING TO PARAMETER BLOCK
8844 ;*RETURN RTS PC
8845 ;*
8846 ;*THIS ROUTINE IS USED TO INITIATE A SUBSYSTEM OPERATION BY
8847 ;*CALLING THE DRIVER. THE PARAMETER BLOCK MUST BE SET UP
8848 ;*BY THE CALLER AND R5 MUST POINT TO THE PARAMETER
8849 ;*BLOCK WHEN THE ROUTINE IS CALLED.
8850 ;*
8851 ;*THIS ROUTINE WAITS FOR THE OPERATION TO BE COMPLETED.
8852 ;*WHILE WAITING THE WATCHDOG TIMER IS CALLED TO PREVENT
8853 ;*SILENT DEATH IN CASE THE SUBSYSTEM DOES NOT PROVIDE AN
8854 ;*INTERRUPT. THE TERMINATION HANDLER ROUTINES WILL SET THE DONE
8855 ;*FLAG WHICH KEYS THE ROUTINE TO RETURN TO THE CALLER.
8856 ;*****
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8857 040252 DRVCAL: SAVREG ;SAVE R0-R5
8858 040252 104407 JSR PC,STALL ;PERFORM A STALL IF REQUIRED
8859 040254 004737 032426 JSR PC,CTLOUT ;CHECK FOR (^C) OR (^Z) KBD INPUT
8860 040260 004737 036266 CLR B DONE ;CLEAR DONE FLAG
8861 040264 105037 003123 CLR B DRNAFG ;CLEAR DRIVE SIEZED BY OTHER PORT FLAG
8862 040270 105037 003137 JSR PC,STRCMD ;STORE PREV AND CURRENT COMMANDS
8863 040274 004737 040542 TST B DULACS ;SEE IF DUAL-ACCESS TEST
8864 040300 105737 00,136 BEQ 26$ ;BR IF NOT
8865 040304 001502 CLR B $TKS ;DISABLE KBD INPUT
8866 040306 005077 140632
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8867 040312 113762 005510 000010      MOVB  DRIVE,RKCS2(R2) ;SET DRIVE NO. IN RKCS2
8868 040320 012712 000001              MOV   #BIT0,(R2)      ;SELECT THE DRIVE
8869 040324 032712 000200      20$: BIT   #BIT7,(R2)      ;WAIT FOR C. READY
8870 040330 001775              BEQ   20$
8871 040332 032762 010000 000010      BIT   #BIT12,RKCS2(R2) ;SEE IF NED ERROR SET
8872 040340 001060              BNE  28$             ;BR IF YES
8873 040342 032762 000001 000012      BIT   #BIT0,RKDS(R2) ;SEE IF DRIVE IS AVAILABLE
8874 040350 001044              BNE  30$             ;BR IF YES
8875 040352 012712 100000      MOV   #100000,(R2)   ;ISSUE CONTROLLER CLEAR
8876 040356 113700 005510      MOVB  DRIVE,R0      ;GET DRIVE NO.
8877 040362 012703 000100      MOV   #100,R3       ;SET OUTER COUNTER
8878 040366 005001      72$: CLR   R1         ;INIT INNER COUNTER
8879 040370 005201      33$: INC   R1         ;INCR INNER COUNTER
8880 040372 001027              BNE  22$
8881 040374 005303              DEC   R3             ;DECR OUTER COUNTER
8882 040376 001373              BNE  32$             ;BR IF NO TIME-OUT YET
8883 040400 052737 000200 005504      BIS   #ABORT,RECODE ;SET ABORT FLAG
8884 040406 112737 000101 005233      MOVB  #SELDRV,PRVCM+1 ;SET PREV CMND = SELECT DRIVE
8885 040414 112765 000176 000001      MOVB  #CONCLR,P.CMND(R5) ;SET CURRENT CMND = CONTR CLEAR
8886 040422 113737 005510 005232      MOVB  DRIVE,PRVCM   ;SET DRIVE NO. FOR PREV. CMND
8887 040430 011265 000016      MOV   (R2),P.CS1(R5) ;GET CURRENT CONTENTS OF RKCS1
8888 040434 004737 050156      JSR   PC,I.CST1     ;GO READ CONTROLLER REGS
8889 040440 004737 041650      JSR   PC,REPSUP     ;SET UP REGS FOR PRINTOUT
8890 040444 104107      ERROR 107          ;TYPE 'DRIVE SIEZED BY OTHER PORT'
8891 040446 000137 043724      JMP   ALLTRM        ;REPORT DRIVE SIEZED AND HALT
8892 040452 136062 003102 000017 22$: BITB  I.DRV(R0),RKASOF+1(R2) ;WAIT FOR DRIVE ATTENTION
8893 040460 001743              BEQ   33$            ;BR IF NO ATT'N YET
8894 040462 113762 005510 000010 30$: MOVB  DRIVE,RKCS2(R2) ;SET DRIVE NO. IN RKCS2
8895 040470 012712 000005      MOV   #5,(R2)       ;ISSUE DRIVE CLEAR
8896 040474 032712 000200      24$: BIT   #BIT7,(R2)      ;WAIT FOR CONTROLLER READY
8897 040500 001775              BEQ   24$
8898 040502 012712 100000      28$: MOV   #100000,(R2)   ;ISSUE CONTROLLER CLEAR
8899 040506 004737 025766      JSR   PC,PREPKB     ;ENABLE KBD INPUT
8900 040512 010537 040522      26$: MOV   R5,4$       ;GET PARAM BLOCK ADDRESS
8901 040516 004737 050534      JSR   PC,C.INIT    ;CALL DRIVER
8902 040522 000000      4$:  .WORD          ;P.B. ADDRESS GOES HERE
8903 040524 004737 045104      6$:  JSR   PC,W.WTCH  ;CALL WATCH DOG
8904 040530 105737 003123      TSTB  DONE         ;DONE SET?
8905 040534 001773              BEQ   6$            ;NO-LOOP
8906 040536 104410      12$: RESREG          ;RESTORE R0-R5
8907 040540 000207      RTS   PC           ;YES-RETURN
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8914 040542 104407      STRCMD: SAVREG      ;SAVE R0-R5
8915 040544 012701 005232      MOV   #PRVCM,R1     ;ADDR OF PREV COMMAND STORAGE
8916 040550 012700 005246      MOV   #COMSTR,R0    ;ADDR OF CURRENT COMMAND STORAGE
8917
8918 040554 012021      ;STORE PREVIOUS COMMAND
8919 040556 012021      MOV   (R0)+,(R1)+
8920 040560 012021      MOV   (R0)+,(R1)+
8921 040562 012021      MOV   (R0)+,(R1)+
8922 040564 012021      MOV   (R0)+,(R1)+
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8923 040566 012021      MOV      (R0)+,(R1)+
8924 040570 105737 003144  TSTB    UBMPRS      ;SEE IF UNIBUS MAP PRESENT
8925 040574 001414      BEQ     4$          ;BR IF NOT
8926 040576 013737 005270 005264  MOV     CRMPHO,PRMPHO ;STORE PREV U.B. MAP REG 0
8927 040604 013737 005266 005262  MOV     CRMPLO,PRMPLO
8928 040612 013737 170202 005270  MOV     @#MAPH00,CRMPHO ;STORF CURRENT U.B. MAP REG 0
8929 040620 013737 170200 005266  MOV     @#MAPL00,CRMPI0
8930                                ;STORE CURRENT COMMAND
8931 040626 012701 005246 4$:     MOV     #COMSTR,R1
8932 040632 012521      MOV     (R5)+,(R1)+ ;R5 POINTS TO DRIVER PARAM BLK
8933 040634 012521      MOV     (R5)+,(R1)+
8934 040636 012521      MOV     (R5)+,(R1)+
8935 040640 012521      MOV     (R5)+,(R1)+
8936 040642 012521      MOV     (R5)+,(R1)+
8937 040644 012521      MOV     (R5)+,(R1)+
8938 040646 104410      RESREG  ;RESTORE R0-R5
8939 040650 000207      RTS     PC         ;RETURN
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8950 040652 152737 000377 003123  ERRFRE: BISB    #377,DONE ;SET THE DONE FLAG
8951 040660 032737 100000 005504  BIT     #ANYDER,RECODE ;TEST IF ANY DATA ERROR
8952 040666 001403      BEQ     2$          ;IF NO - DO ERROR RECOVERY PRINT TEST
8953 040670 105037 003126      CLRB   ERRCNT      ;CLEAR ERROR COUNT
8954 040674 000413      BR     1$          ;EXIT
8955 040676 105737 003126 2$:     TSTB   ERRCNT      ;CHECK IF ANY ERRORS HAVE OCCURRED
8956 040702 001410      BEQ     1$          ;NO - SKIP TO EXIT
8957 040704 005037 001174      CLR    $REG5
8958 040710 113737 003126 001174  MOVB   ERRCNT,$REG5 ;GET RETRY COUNT
8959 040716 104101      ERROR  101         ;PRINT RETRY SUCCESSFUL MESSAGE
8960 040720 105037 003126      CLRB   ERRCNT      ;CLEAR ERROR COUNT
8961 040724 005037 005504 1$:     CLR    RECODE     ;CLEAR RECOVERY FLAGS
8962 040730 000207      RTS     PC         ;RETURN
8963
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8973 040732 104407      TYPERR: SAVREG
8974 040734 105237 003130      INCB   DRVERS      ;INCR ERROR COUNT FOR THIS DRIVE
8975 040740 032737 000040 005770  BIT     #BIT5,CS    ;SEE IF DRIVE SHOULD BE DROPPED
8976                                ; IF ERROR LIMIT EXCEEDED
8977 040746 001412      BEQ     9$          ;BR IF NOT
8978 040750 123727 003130 000024  CMPB   DRVERS,#20. ;SEE IF 20(DEC) ERRORS EXCEEDD
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8979 040756 002406          BLT      9$          :BR IF NOT
8980 040760 105037 003130    CLRB     DRVERS     :CLEAR DRIVE ERROR COUNT
8981 040764 104401 011251    TYPE    ,DROPR     :TYPE 'DROPPING DRIVE'
8982 040770 000137 016754    JMP     NEWDRV     :PROCEED TO TEST NEXT DRIVE
8983 040774 032777 020000 140136 9$: BIT     #SW13,@SWR  :INHIBIT ERROR TYPEOUTS?
8984 041002 001402          BEQ     6$          :BR IF NO
8985 041004 000137 041410    JMP     20$
8986 041010 005000          6$: CLR     R0          :CLR R0 FOR ERROR NUMBER
8987 041012 005005          CLR     R5          :INIT INDENT INDICATOR
8988 041014 005105          COM     R5
8989 041016 113700 001114    MOVB    $ITEMB,R0  :ENTER ERROR NUMBER
8990 041022 005300          DEC     R0          :FORM INDEX FOR ERROR TABLE
8991 041024 006300          ASL     R0
8992 041026 006300          ASL     R0
8993 041030 006300          ASL     R0
8994 041032 062700 001400 1$: ADD     #SERRTB,R0 :FORM ADDRESS OF ERROR ENTRY
8995 041036 012037 041056    MOV     (R0)+,2$   :GET EM POINTER
8996 041042 001406          BEQ     3$          :BRANCH IF THERE ISN'T ONE
8997 041044 104401 012404    TYPE    ,CR2LF
8998 041050 104401 056320    TYPE    ,AS2SP2   :TYPE '* * '
8999 041054 104401          TYPE    :TYPE ERROR MESSAGE (EM)
9000 041056 000000          .WORD   0          :EM POINTER GOES HERE
9001 041060 012037 041234 3$: MOV     (R0)+,4$   :GET DH POINTER
9002 041064 001467          BEQ     5$          :BR IF THERE ISN'T ONE
9003 041066 104401 001315    TYPE    ,SCLF
9004 041072 104401 056306    TYPE    ,TSTMSG   :TYPE ' TEST '
9005 041076 013746 001102    MOV     $TSTNM,-(SP) :GET TEST NO. ON STACK
9006 041102 104403          TYPOS
9007 041104 002          .BYTE   2          :2 DIGITS
9008 041105 000          .BYTE   0          :SUPPRESS LEADING ZEROS
9009 041106 104401 012404    TYPE    ,CR2LF
9010 041112 032777 010000 140020 BIT     #BIT12,@SWR :REPORT DESCRIPTION ONLY ?
9011 041120 001133          BNE     20$
9012 041122 104401 060767    TYPE    ,DH105
9013 041126 104401 001315    TYPE    ,SCLF
9014 041132 104401 061132    TYPE    ,DH101+10 :TYPE PREV COMMAND HEADER
9015 041136 104401 001315    TYPE    ,SCLF
9016 041142 012701 000006    MOV     #6,R1      :SIX COMMAND VALUES
9017 041146 012702 001236    MOV     #SREG26,R2 :STARTING ADDR OF PREV CMND VALUES
9018 041152 012246          30$: MOV     (R2)+,-(SP) :PUT A WORD ON STACK
9019 041154 104402          TYPOC
9020 041156 104401 012420    TYPE    ,SPACE2   :TYPE SEPARATORS
9021 041162 005301          DEC     R1          :SEE IF 7 VALUES TYPED YET
9022 041164 001372          BNE     30$
9023 041166 104401 001315    TYPE    ,SCLF
9024 041172 104401 012420    TYPE    ,SPACE2   :INDENT
9025 041176 104401 061211    TYPE    ,DH102
9026 041202 104401 001315    TYPE    ,SCLF
9027 041206 104401 012420    TYPE    ,SPACE2   :INDENT
9028 041212 012246          MOV     (R2)+,-(SP)
9029 041214 104402          TYPOC
9030 041216 104401 012420    TYPE    ,SPACE2
9031 041222 011246          MOV     (R2)+,-(SP)
9032 041224 104402          TYPOC
9033 041226 104401 012404    TYPE    ,CR2LF
9034 041232 104401          TYPE
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```
9035 041234 000000          4$: .WORD 0 ;DH POINTER GOES HERE
9036 041236 104401 001315  TYPE ,SCLF
9037 041242 005005          CLR R5 ;INIT INDENT INDICATOR
9038 041244 032777 010000 137666 5$: BIT #BIT12,@SWR ;REPORT DESCRIPTION ONLY ?
9039 041252 001056          BNE 20$ ;BR IF YES
9040 041254 012001          MOV (R0)+,R1 ;GET DT POINTER
9041 041256 001454          BEQ 20$ ;BRANCH IF THERE ARE NONE
9042 041260 012000          MOV (R0)+,R0 ;GET DF POINTER
9043 041262 012002          MOV (R0)+,R2 ;STORE NUMBER OF DH'S
9044 041264 112003          10$: MOVB (R0)+,P3 ;GET & STORE NUMBER OF DATA WORDS
9045 041266 105720          TSTB (R0)+ ;BUMP PAST FORMAT WORD
9046 041270 005703          TST R3 ;TEST IF ANY DATA FOR THIS HEADER
9047 041272 001417          BEQ 14$ ;NO - SKIP DATA PRINT
9048 041274 005705          TST R5 ;SEE IF SHOULD INDENT
9049 041276 001002          BNE 11$ ;BR IF NOT
9050 041300 104401 012420  TYPE ,SPACE2 ;INDENT
9051 041304 013146          11$: MOV @ (R1)+,-(SP) ;PUT FIRST DATA WORD ON STACK
9052 041306 104402          TYPOC ;TYPE IT
9053 041310 005303          DEC R3 ;MORE DATA WORDS
9054 041312 001403          BEQ 12$ ;NO-BRANCH
9055 041314 104401 012420  TYPE ,SPACE2 ;TYPE SEPARATORS
9056 041320 000771          BR 11$ ;LOOP
9057 041322 005702          12$: TST R2 ;SEE IF <CR>,<LF> NEEDED
9058 041324 001402          BEQ 14$ ;BR IF NOT
9059 041326 104401 001315  TYPE ,SCLF ;TYPE IT
9060 041332 005302          14$: DEC R2 ;MORE DH'S?
9061 041334 003425          BLE 20$ ;NO-BRANCH
9062 041336 012037 041370  15$: MOV (R0)+,16$ ;GET NEXT DH POINTER
9063 041342 105710          TSTB (R0) ;SEE IF ANY DATA FOR HDR
9064 041344 001004          BNE 34$ ;BR IF YES
9065 041346 104401 001315  TYPE ,SCLF ;SKIP EXTRA LINE
9066 041352 005005          CLR R5 ;RE-INIT INDENT INDICATOR
9067 041354 000404          BR 36$
9068 041356 005105          34$: COM R5 ;COMPLEMENT INDENT INDICATOR
9069 041360 001002          BNE 36$ ;BR IF NO INDENT REQUIRED
9070 041362 104401 012420  TYPE ,SPACE2 ;INDENT
9071 041366 104401          36$: TYPE ;TYPE DH
9072 041370 000000          16$: .WORD 0 ;DH POINTER GOES HERE
9073 041372 104401 001315  TYPE ,SCLF
9074 041376 105710          TSTB (R0) ;TYPE A DT?
9075 041400 001331          BNE 10$ ;YES-BRANCH
9076 041402 062700 000002  ADD #2,R0 ;INCREMENT DF POINTER
9077 041406 000751          BR 14$ ;SEE IF END OF DF BLOCK
9078 041410 104410          20$: RESREG
9079 041412 000207          RTS PC
9080
9081 :*****
9082 :SBTTL CONTROLLER ERROR REPORTER ROUTINE
9083 :*ENTRY: JSR PC, CONERR
9084 :*RETURN: RTS PC
9085 :*
9086 :*THIS ROUTINE DECODES THE CONTROLLER ERROR WORD AND
9087 :*REPORTS THE APPROPRIATE MESSAGE. THE RK611 REGISTERS ARE
9088 :*RETRIEVED FROM THE RK611 AND PLACED IN THE PARAMETER
9089 :*BLOCK. THIS IS DONE BECAUSE PARM 0 MAY NOT BE VALID
9090 :*AT THIS TIME.
9090 :*****
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9091 041414 104407          CONERR: SAVREG          ;SAVE R0-R5
9092 041416 152737 000377 003123  BISB   #377,DONE        ;SET DONE FLAG
9093 041424 105237 003126          INCB   ERRCNT          ;INCREMENT ERROR COUNT
9094 041430 004737 044316          JSR    PC,TOPROC       ;LOAD RK REGS INTO $REGS
9095 041434 032737 000001 003052  BIT    #BIT0,E.CONT     ;ERROR 0?
9096 041442 001402          BEQ    1$              ;NO-BRANCH
9097 041444 104064          ERROR  64              ;CLEAR CONT DID NOT CLEAR ERROR
9098 041446 000474          BR     7$              ;
9099 041450 032737 000002 003052  1$:  BIT    #BIT1,E.CONT     ;ERROR 1?
9100 041456 001402          BEQ    2$              ;NO-BRANCH
9101 041460 104065          ERROR  65              ;NO ATTENTION IN ATTENTION SUM REG
9102 041462 000466          BR     7$              ;
9103 041464 032737 000004 003052  2$:  BIT    #BIT2,E.CONT     ;ERROR 2?
9104 041472 001407          BEQ    3$              ;NO-BRANCH
9105 041474 105737 003137          TSTB  DRNAFG           ;SEE IF DRIVE WAS SIEZED BY OTHER PORT
9106 041500 001402          BEQ    15$             ;BR IF NOT
9107 041502 105237 003140          INCB  REISSU           ;SET FLAG TO RE-ISSUE COMMAND
9108 041506 104066          15$:  ERROR  66              ;UNSOLICITED ATTENTION
9109 041510 000453          BR     7$              ;
9110 041512 032737 000010 003052  3$:  BIT    #BIT3,E.CONT     ;ERROR 3?
9111 041520 001402          BEQ    4$              ;NO-BRANCH
9112 041522 104067          ERROR  67              ;UNEXPECTED DATA TYPE ERROR
9113 041524 000445          BR     7$              ;
9114 041526 032737 000020 003052  4$:  BIT    #BIT4,E.CONT     ;ERROR 4?
9115 041534 001402          BEQ    5$              ;NO-BRANCH
9116 041536 104070          ERROR  70              ;ATTENTION DID NOT RESET WITH CLEAR
9117 041540 000437          BR     7$              ;
9118 041542 032737 000040 003052  5$:  BIT    #BIT5,E.CONT     ;ERROR 5?
9119 041550 001402          BEQ    6$              ;NO-BRANCH
9120 041552 104071          ERROR  71              ;SUBSYS CLEAR DIDN'T CLEAR DRIVE ATTENTION
9121 041554 000431          BR     7$              ;
9122 041556 032737 000400 003052  6$:  BIT    #BIT8,E.CONT     ;
9123 041564 001402          BEQ    8$              ;
9124 041566 104072          ERROR  72              ;DATA LATE WHEN UNLOADING HEADER
9125 041570 000423          BR     7$              ;
9126 041572 032737 001000 003052  8$:  BIT    #BIT9,E.CONT     ;
9127 041600 001402          BEQ    9$              ;
9128 041602 104073          ERROR  73              ;CONTROLLER ERROR DURING DRIVER SERVICE
9129 041604 000415          BR     7$              ;
9130 041606 032737 002000 003052  9$:  BIT    #BIT10,E.CONT    ;
9131 041614 001402          BEQ   10$              ;
9132 041616 104074          ERROR  74              ;DRIVE DETECTED PARITY ERROR
9133 041620 000407          BR     7$              ;
9134 041622 032737 100000 003052 10$:  BIT    #BIT15,E.CONT    ;
9135 041630 001402          BEQ   11$              ;
9136 041632 104052          ERROR  52              ;MULTIPLE DRIVE SELECT
9137 041634 000401          BR     7$              ;
9138 041636 104075          11$:  ERROR  75              ;UNDEFINED ERROR
9139 041640 005037 003052  7$:  CLR    E.CONT          ;CLEAR CONTROLLER ERROR WORD
9140 041644 000137 044116          JMP    BGNRTY         ;GO DO RETRY
```

```
*****
.SBTTL  REPORT SUPPORT ROUTINE
;*THIS ROUTINE LOADS ALL THE PARAMETER BLOCK DATA TO BE REPORTED
;*INTO THE PROPER TEMPORARY REGISTERS FOR REPORTING.  ALL THESE MAY
;*NOT BE INCLUDED IN THE REPORT, BUT THEY ARE LOADED ANYWAY.
REPSUP:
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9146 041650

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9147 041650 104407 SAVREG
9148 041652 005037 005506 CLR ERRCOM ;CLEAR ERROR COMMAND STORE
9149 041656 116537 000001 005506 MOV P.CMND(R5),ERRCOM ;STORE COMMAND START VALUES
9150 041664 012700 001162 MOV #SREG0,R0 ;FOR REPORTING
9151 041670 016520 000002 MOV P.CYLN(R5),(R0)+
9152 041674 116520 000005 MOV P.TRCK(R5),(R0)+
9153 041700 105020 CLR (R0)+
9154 041702 116520 000004 MOV P.SECT(R5),(R0)+
9155 041706 105020 CLR (R0)+
9156 041710 016520 000012 MOV P.WC(R5),(R0)+
9157 041714 012700 001174 MOV #SREG5,R0
9158 041720 116503 000007 MOV P.BAHI(R5),R3
9159 041724 042703 177774 BIC #177774,R3
9160 041730 010337 001256 MOV R3,SREG36
9161 041734 016537 000010 001260 MOV P.BALO(R5),SREG37 ;HI BA BITS
9162 041742 016520 000016 MOV P.CS1(R5),(R0)+ ;LO BA BITS
9163 041746 016520 000020 MOV P.CS2(R5),(R0)+ ;GET ALL THE VALUES FROM THE
9164 041752 016520 000030 MOV P.DCYL(R5),(R0)+ ;PARAMETER BLOCK AND LOAD
9165 041756 016520 000026 MOV P.DTS(R5),(R0)+ ;THE TEMPORARY REGISTERS
9166 041762 016520 000022 MOV P.WCR(R5),(R0)+ ;FOR REPORTING. ALL THIS
9167 ;DATA MAY NOT BE VALID
9168 041766 016520 000024 MOV P.BAR(R5),(R0)+ ;FOR ALL REPORTS (TO BE
9169 041772 016520 000032 MOV P.ASOF(R5),(R0)+ ;DETERMINED LATER) BUT IT IS
9170 041776 016520 000036 MOV P.DS(R5),(R0)+ ;STORED ANY WAY.
9171 042002 016520 000034 MOV P.ER(R5),(R0)+
9172 042006 016520 000040 MOV P.A00(R5),(R0)+
9173 042012 016520 000042 MOV P.B00(R5),(R0)+
9174 042016 016520 000044 MOV P.A01(R5),(R0)+
9175 042022 016520 000046 MOV P.B01(R5),(R0)+
9176 042026 016520 000050 MOV P.A10(R5),(R0)+
9177 042032 016520 000052 MOV P.B10(R5),(R0)+
9178 042036 016520 000054 MOV P.A11(R5),(R0)+
9179 042042 016520 000056 MOV P.B11(R5),(R0)+
9180 ;STORE PREVIOUS COMMAND FOR PRINTOUT
9181 042046 012701 001236 MOV #SREG26,R1 ;ADRS OF PRINT BUF AREA
9182 042052 012700 005232 MOV #PRVCMND,R0 ;ADRS OF PREV CMND STORAGE
9183 042056 112021 MOV (R0)+,(R1)+ ;DRIVE NO.
9184 042060 105021 CLR (R1)+
9185 042062 112021 MOV (R0)+,(R1)+ ;COMMAND
9186 042064 105021 CLR (R1)+
9187 042066 012021 MOV (R0)+,(R1)+ ;CYL ADDRESS
9188 042070 116021 000001 MOV 1(R0),(R1)+ ;TRACK
9189 042074 105021 CLR (R1)+
9190 042076 111021 MOV (R0),(R1)+ ;SECTOR
9191 042100 105021 CLR (R1)+
9192 042102 016021 000006 MOV 6(R0),(R1)+ ;WORD COUNT
9193 042106 116003 000003 MOV 3(R0),R3 ;HI BA BITS
9194 042112 042703 177774 BIC #177774,R3
9195 042116 010321 MOV R3,(R1)+
9196 042120 016011 000004 MOV 4(R0),(R1)+ ;LO BA BITS
9197 042124 104410 RESREG
9198 042126 000207 RTS PC
9199 ;*****
9200 .SBTTL REPORT ERROR ROUTINE
9201 ;* ENTRY JSR PC,ERRHDL
9202 ;*RETURN RTS PC
```

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9209 042130 104407
9210 042132 152737 000377 003123
9211 042140 105237 003126
9212 042144 005037 005504
9213 042150 032737 000400 005504
9214 042156 001402
9215 042160 012705 002714
9216 042164 012737 044270 003046
9217 042172 012737 044306 003044
9218 042200 004737 041650
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9236 042204 016504 000034
9237 042210 032765 020000 000020
9238 042216 001406
9239 042220 104001
9240 042222 052737 000200 005504
9241 042230 000137 043624
9242 042234 032765 004000 000020 1$:
9243 042242 001406
9244 042244 104002
9245 042246 052737 000200 005504
9246 042254 000137 043624
9247 042260 032765 010000 000020 2$:
9248 042266 001412
9249 042270 032765 000400 000020
9250 042276 001403
9251 042300 104035
9252 042302 000137 043624
9253 042306 104003 38$:
9254 042310 000137 043624
9255 042314 032765 000400 000020 3$:
9256 042322 001412
9257 042324 032765 010000 000020
9258 042332 001403
```

: * THIS ROUTINE IS CALLED BY THE DRIVER WHEN AN ERROR IS DETECTED
: * IN THE OPERATION. THE ROUTINE DETERMINES WHICH COMMAND WAS
: * BEING EXECUTED AND GENERATES THE PROPER ERROR MESSAGE.
: *****

ERRHDL: SAVREG
BISB #377,DONE ;SET DONE FLAG
INCB ERRCNT ;INCREMENT ERROR COUNT
CLR RECODE ;CLEAR RECOVERY CODE WORD
ER2ENT: BIT #LEV2ER,RECODE ;TEST IF 2ND LEVEL ERROR
BEQ 52\$;NO - SKIP PARAM BLOCK CHANGE
MOV #PARM1,R5 ;ELSE SET R5 TO PARAMETER BLOCK 1
52\$: MOV #RETANL,A.ABNL ;SET NOW ABNORMAL AND NORMAL RETURN FOR
MOV #RETNML,A.NORM ;DRIVER OPERATIONS IN ERROR PROCESSING
JSR PC,REPSUP ;GO SET UP REGISTERS FOR REPOPI
;NOW BEGIN TESTING THE ERROR
;BITS. THE SEQUENCE IN
;WHICH THEY ARE TESTED IS
;CONSIDERED SIGNIFICANT IN
;THAT ERRORS OF A MORE
;CATASTROPHIC NATURE ARE FIRST
;TESTED.
;IF AN ERROR IS FOUND SET,
;THAT ERROR IS REPORTED AND
;THE REPORTING IS TERMINATED.
;IF ADDITIONAL ERRORS ARE SET,
;THE RK611 REGISTER PRINTOUTS
;WILL SHOW THIS BUT THE
;REGISTER CONTENTS MUST BE
;MANUALLY DECODED TO LOCATE THE
;SECOND ERROR
MOV P.ER(R5),R4 ;SET R4 TO ERROR REGISTER
BIT #UPE,P.CS2(R5) ;TEST UPE. IF UES-SET
BEQ 1\$;REPORT ERROR
ERROR 1
BIS #ABORT,RECODE
JMP 37\$
1\$: BIT #NEM,P.CS2(R5) ;TEST NON-EXISTANT MEMORY
BEQ 2\$
ERROR 2
BIS #ABORT,RECODE
JMP 37\$
2\$: BIT #NED,P.CS2(R5) ;TEST NON-EXISTANT DRIVE
BEQ 3\$
BIT #UFE,P.CS2(R5) ;TEST IF NED & UFE BOTH SET
BEQ 38\$
ERROR 35 ;NED & UFE BOTH SET ERROR
JMP 37\$
38\$: ERROR 3 ;NED ONLY
JMP 37\$
3\$: BIT #UFE,P.CS2(R5) ;TEST UNIT FIELD ERROR
BEQ 4\$
BIT #NED,P.CS2(R5) ;TEST IF UFE & NED BOTH SET
BEQ 39\$;NO-SKIP

9259	042334	104035				ERROR	35		:REPORT NED & UFE BOTH SET
9260	042336	000137	043624			JMP	37\$		
9261	042342	104004			39\$:	ERROR	4		:REPORT UFE ONLY
9262	042344	000137	043624			JMP	37\$		
9263	042350	032765	000100	000014	4\$:	BIT	#CMDTO,P.PRST(R5)		:
9264	042356	001423				BEQ	5\$		
9265	042360	004737	044316			JSR	PC, TOPROC		:GO PROCESS TIMEOUT
9266	042364	032737	002000	005504		BIT	#TWOTOS, RECODE		:2ND TIMEOUT IN TIMEOUT PROC?
9267	042372	001007				BNE	40\$:YES - SKIP TO ERROR 56
9268	042374	032737	000400	005504		BIT	#LEV2ER, RECODE		:TEST IF LEVEL 2 ERROR
9269	042402	001006				BNE	41\$:YES - SKIP TO ERROR 57
9270	042404	104005				ERROR	5		:ELSE MAKE FULL TIMEOUT REPORT
9271	042406	000137	043624			JMP	37\$		
9272	042412	104056			40\$:	ERROR	56		:TWO TIMEOUTS ERROR REPORT
9273	042414	000137	043624			JMP	37\$		
9274	042420	104057			41\$:	ERROR	57		:2ND LEVEL ERROR REPORT
9275	042422	000137	042150			JMP	ER2ENT		:GO BUILD AND MAKE 2ND REPORT
9276	042426	032765	010000	000014	5\$:	BIT	#DRVSZD,P.PRST(R5)		:SEE IF DRIVE SIEZED BY OTHER PORT
9277	042434	001403				BEQ	65\$:BR IF DRIVE IS AVAILABLE
9278	042436	104107				ERROR	107		:DRIVE SIEZED BY OTHER PORT
9279	042440	000137	043624			JMP	37\$		
9280	042444	032765	020000	000016	65\$:	BIT	#SPAR,P.CS1(R5)		:TEST D TO C PARITY ERROR
9281	042452	001406				BEQ	6\$		
9282	042454	104006				ERROR	6		
9283	042456	052737	004000	005504		BIS	#RCLREQ, RECODE		
9284	042464	000137	043624			JMP	37\$		
9285	042470	032704	000010		6\$:	BIT	#DRPAR,R4		:TEST DRIVE DETECTED PARITY ERROR
9286	042474	001406				BEQ	7\$		
9287	042476	104007				ERROR	7		
9288	042500	052737	004000	005504		BIS	#RCLREQ, RECODE		
9289	042506	000137	043624			JMP	37\$		
9290	042512	032765	000010	000036	7\$:	BIT	#ACLO,P.DS(R5)		:TEST AC LOW
9291	042520	001403				BEQ	8\$		
9292	042522	104010				ERROR	10		
9293	042524	000137	043624			JMP	37\$		
9294	042530	032765	000020	000036	8\$:	BIT	#SPDLSS,P.DS(R5)		:TEST SPEED LOSS
9295	042536	001403				BEQ	24\$		
9296	042540	104011				ERROR	11		
9297	042542	000137	043624			JMP	37\$		
9298	042546	032765	004000	000016	24\$:	BIT	#CTO,P.CS1(R5)		:TEST FOR CONTROLLER TIMEOUT
9299	042554	001401				BEQ	25\$		
9300	042556	104027				ERROR	27		
9301	042560	032704	000001		25\$:	BIT	#ILC,R4		:TEST ILLEGAL FUNCTION CODE
9302	042564	001403				BEQ	10\$		
9303	042566	104012				ERROR	12		
9304	042570	000137	043624			JMP	37\$		
9305	042574	032765	002000	000020	10\$:	BIT	#PGE,P.CS2(R5)		:TEST PROGRAMMING ERROR
9306	042602	001403				BEQ	11\$		
9307	042604	104013				ERROR	13		
9308	042606	000137	043624			JMP	37\$		
9309	042612	032704	000004		11\$:	BIT	#ILF,R4		:TEST ILLEGAL DRIVE FUNCTION
9310	042616	001403				BEQ	12\$		
9311	042620	104014				ERROR	14		
9312	042622	000137	043624			JMP	37\$		
9313	042626	032704	000040		12\$:	BIT	#DTYE,R4		:TEST DRIVE TYPE ERROR
9314	042632	001403				BEQ	13\$		

9315	042634	104015				ERROR	15		
9316	042636	000137	043624			JMP	37\$		
9317	042642	032704	000020		13\$:	BIT	#FMTE,R4		;TEST FORMAT ERROR
9318	042646	001403				BEQ	14\$		
9319	042650	104016				ERROR	16		
9320	042652	000137	043624			JMP	37\$		
9321	042656	032704	004000		14\$:	BIT	#WLE,R4		;TEST WRITE LOCK ERROR
9322	042662	001403				BEQ	15\$		
9323	042664	104017				ERROR	17		
9324	042666	000137	043624			JMP	37\$		
9325	042672	032704	040000		15\$:	BIT	#UNS,R4		;TEST DRIVE UNSAFE
9326	042676	001406				BEQ	16\$		
9327	042700	104020				ERROR	20		
9328	042702	052737	000200	005504		BIS	#ABORT,RECODE		
9329	042710	000137	043724			JMP	ALLTRM		
9330	042714	032704	000002		16\$:	BIT	#SKI,R4		;TEST SEEK INCOMPLETE
9331	042720	001406				BEQ	17\$		
9332	042722	104021				ERROR	21		
9333	042724	052737	004000	005504		BIS	#RCLREQ,RECODE		
9334	042732	000137	043624			JMP	37\$		
9335	042736	032704	001000		17\$:	BIT	#COE,R4		;TEST CYLINDER OVERFLOW
9336	042742	001406				BEQ	18\$		
9337	042744	104022				ERROR	22		
9338	042746	052737	004000	005504		BIS	#RCLREQ,RECODE		
9339	042754	000137	043624			JMP	37\$		
9340	042760	032704	002000		18\$:	BIT	#IDAE,R4		;TEST ILLEGAL CYLINDER
9341	042764	001406				BEQ	19\$		
9342	042766	104023				ERROR	23		
9343	042770	052737	004000	005504		BIS	#RCLREQ,RECODE		
9344	042776	000137	043624			JMP	37\$		
9345	043002	032765	000040	000036	19\$:	BIT	#DROT,P.DS(R5)		;TEST DRIVE OFF TRACK
9346	043010	001406				BEQ	20\$		
9347	043012	104024				ERROR	24		
9348	043014	052737	004000	005504		BIS	#RCLREQ,RECODE		
9349	043022	000137	043624			JMP	37\$		
9350	043026	032704	010000		20\$:	BIT	#DTE,R4		;TEST DRIVE TIMING ERROR
9351	043032	001406				BEQ	21\$		
9352	043034	104025				ERROR	25		
9353	043036	052737	000200	005504		BIS	#ABORT,RECODE		
9354	043044	000137	043624			JMP	37\$		
9355	043050	032765	100000	000020	21\$:	BIT	#DLT,P.CS2(R5)		;TEST DATA LATE
9356	043056	001403				BEQ	22\$		
9357	043060	104026				ERROR	26		
9358	043062	000137	043624			JMP	37\$		
9359	043066	032704	020000		22\$:	BIT	#OPI,R4		;TEST IF OPI ERROR
9360	043072	001457				BEQ	29\$		
9361	043074	052737	000010	005504		BIS	#OPIERR,RECODE		
9362	043102	105737	003121			TSTB	DERCNT		;TEST IF FIRST ERROR
9363	043106	001402				BEQ	50\$		
9364	043110	000137	043624			JMP	37\$;NO - SKIP REPORT
9365	043114	032737	000400	005504	50\$:	BIT	#LEV2ER,RECODE		;TEST IF A SECOND LEVEL 2 ERROR
9366	043122	001403				BEQ	26\$;HAS ALREADY OCCURRED
9367	043124	104054			27\$:	ERROR	54		
9368	043126	000137	043624			JMP	37\$;GET OUT OF ERROR REPORT
9369	043132	004737	044756		26\$:	JSR	PC,BLDEXH		;GO BUILD EXPECTED HEADER
9370	043136	004737	044466			JSR	PC,RDHD0		;GET HEADER OF SECTOR 0

9371	043142	032737	000400	005504	BIT	#LEV2ER,RECODE	;TEST IF ERROR GETTING HDR
9372	043150	001025			BNE	28\$;IF YES-GO MAKE ABBREVIATED REPORT
9373	043152	013702	003036		MOV	RKBAS,R2	;STORE HEADER 0 INTO REGISTERS
9374	043156	042762	000100	000000	BIC	#IE,RKCS1(R2)	;RESET INTERRUPT ENABLE
9375	043164	016237	000024	001202	MOV	RKDB(R2),\$REG10	;FOR REPORTING
9376	043172	016237	000024	001204	MOV	RKDB(R2),\$REG11	
9377	043200	016237	000024	001206	MOV	RKDB(R2),\$REG12	
9378	043206	032762	100000	000000	BIT	#CERR,RKCS1(R2)	;TEST IF ERROR DURING STORAGE
9379	043214	001343			BNE	27\$	
9380	043216	104030			ERROR	30	;MAKE OPI REPORT
9381	043220	000137	043624		JMP	37\$	
9382	043224	104054		29\$:	ERROR	54	
9383	043226	000137	042150		JMP	ER2ENT	;GO MAKE 2ND LEVEL REPORT
9384	043232	032704	000400	29\$:	BIT	#HVRC,R4	;TEST IF HVRC ERROR
9385	043236	001457			BEQ	23\$	
9386	043240	052737	000004	005504	BIS	#HVRCER,RECODE	
9387	043246	105737	003121		TSTB	DERCNT	;TEST IF FIRST ERROR
9388	043252	001402			BEQ	30\$;YES - REPORT
9389	043254	000137	043624		JMP	37\$;JUMP TO RETURN
9390	043260	032737	000400	005504	30\$:	BIT	#LEV2ER,RECODE ;TEST IF A 2ND LEVEL ERROR HAS ALREADY
9391	043266	001403			BEQ	31\$;OCCURRED. NO-SKIP EXIT
9392	043270	104055		51\$:	ERROR	55	
9393	043272	000137	043624		JMP	37\$;GET OUT OF ERROR REPORT
9394	043276	004737	044756	31\$:	JSR	PC,BLDEXH	;GO BUILD EXPECTED HEADER
9395	043302	004737	044466		JSR	PC,RDHDO	;GO GET HDR 0
9396	043306	032737	000400	005504	BIT	#LEV2ER,RECODE	;TEST IF ERROR IN GETTING HDR
9397	043314	001025			BNE	32\$;IF YES-GO MAKE ABBREVIATED REPORT
9398	043316	013702	003036		MOV	RKBAS,R2	;GET RK611 BASE ADDRESS
9399	043322	042762	000100	000000	BIC	#IE,RKCS1(R2)	;CLEAR INTERRUPT ENABLE
9400	043330	016237	000024	001202	MOV	RKDB(R2),\$REG10	;STORE OFF HEADER
9401	043336	016237	000024	001204	MOV	RKDB(R2),\$REG11	
9402	043344	016237	000024	001206	MOV	RKDB(R2),\$REG12	
9403	043352	032762	100000	000000	BIT	#CERR,RKCS1(R2)	;TEST IN ANY ERROR IN UNLOAD
9404	043360	001343			BNE	51\$;IF YES - GO MAKE SHORT REPORT
9405	043362	104031			ERROR	31	;MAKE FULL REPORT
9406	043364	000137	043624		JMP	37\$	
9407	043370	104055		32\$:	ERROR	55	;ABBREVIATED HVRC ERROR REPORT
9408	043372	000137	042150		JMP	ER2ENT	;GO REPORT 2ND LEVEL ERROR
9409	043376	032704	000200	23\$:	BIT	#BSE,R4	;TEST FOR BAD SECTOR ERROR
9410	043402	001430			BEQ	33\$;NO - SKIP
9411	043404	052737	000002	005504	BIS	#BSERR,RECODE	;SET ERROR FLAG
9412	043412	016500	000030		MOV	P.DCYL(R5),R0	;GET CYL NO.
9413	043416	116501	000027		MOVB	P.DTS+1(R5),R1	;GET TRACK NO.
9414	043422	042701	177774		BIC	#177774,R1	;CLEAR ALL BITS EXCEPT TRACK
9415	043426	016502	000026		MOV	P.DTS(R5),R2	;GET SECTOR IN ERROR
9416	043432	042702	177740		BIC	#177740,R2	;CLEAR ALL BITS EXCEPT SECTOR
9417	043436	004737	040146		JSR	PC,BDSRCK	;GO SEE IF THIS SECTOR LISTED
9418	043442	032737	001000	005504	BIT	#BADSEC,RECODE	;TEST RESULT
9419	043450	001065			BNE	37\$;YES - EXIT, NO ERROR OR REPORT
9420	043452	104104			ERROR	104	;ELSE REPORT BAD BSE
9421	043454	042737	000002	005504	BIC	#BSERR,RECODE	;RESET BSE ERROR FLAG
9422	043462	000460			BR	37\$;GO EXIT
9423	043464	032704	100000	33\$:	BIT	#DCK,R4	;TEST IF DATA CHECK
9424	043470	001435			BEQ	36\$	
9425	043472	052737	000020	005504	BIS	#DCKERR,RECODE	;SET DATA CHECK ERROR IN RECOVERY CODE
9426	043500	032704	000100		BIT	#ECH,R4	;TEST IF ECC IS HARD. IF

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9427 043504 001406          BEQ      34$          ;YES SET UNCORRECTABLE IN
9428 043506 052737 000040 005504  BIS      #ECCNC,RECODE ;RECOVERY FLAG AND A 0 IN
9429 043514 005037 001206          CLR      $REG12      ;REG12 TO INDICATE UNCORRECTABLE,
9430 043520 000403          BR       35$          ;A 1 IN REG 12 FOR CORRECTABLE
9431 043522 012737 000001 001205 34$:  MOV     #1,$REG12
9432 043530 105737 003121 35$:  TSTB   DERCNT      ;TEST IF FIRST ERROR
9433 043534 001033          BNE     37$          ;NO SKIP REPORT
9434 043536 004737 044632          JSR     PC,GTPKAD    ;GO GET PACK ADDRESS OF ERROR
9435 043542 016537 000060 001202  MOV     P.EPOS(R5),$REG10 ;STORE ECC POSITION &
9436 043550 016537 000062 001204  MOV     P.EPAT(R5),$REG11 ;PATTERN
9437 043556 104032          ERROR   32          ;REPORT DCK ERROR
9438 043560 000137 043624          JMP     37$
9439 043564 032765 040000 000020 36$:  BIT     #WCE,P.CS2(R5) ;TEST WRITE CHECK ERROR
9440 043572 001414          BEQ     37$
9441 043574 042737 000200 005504  BIC     #ABORT,RECODE ;CLEAR ABORT & SET WRITE
9442 043602 052737 000100 005504  BIS     #WCERR,RECODE ;CHECK ERROR IN RECODE
9443 043610 105737 003121          TSTB   DERCNT      ;TEST IF FIRST ERROR
9444 043614 001003          BNE     37$          ;NO - SKIP
9445 043616 004737 044632          JSR     PC,GTPKAD    ;GO GET ADDRESS OF ERROR
9446 043622 104033          ERROR   33          ;REPORT WCE
9447
9448 043624 032765 000020 000014 37$:  BIT     #DRVHRD,P.PRST(R5) ;TEST HARD ERROR
9449 043632 001404          BEQ     43$
9450 043634 104036          ERROR   36
9451 043636 052737 000200 005504  BIS     #ABORT,RECODE
9452
9453 043644 032765 000040 000014 43$:  BIT     #DRVDSC,P.PRST(R5) ;TEST STATUS CHANGE NOT CLEARED
9454 043652 001404          BEQ     44$
9455 043654 104037          ERROR   37
9456 043656 052737 000200 005504  BIS     #ABORT,RECODE
9457
9458 043664 032765 004000 000014 44$:  BIT     #NODSC,P.PRST(R5) ;IFST ATTENTION BUT NO FAULT OR DSC
9459 043672 001404          BEQ     46$
9460 043674 104040          ERROR   40
9461 043676 052737 000200 005504  BIS     #ABORT,RECODE
9462
9463 043704 032765 000010 000014 46$:  BIT     #UEXATT,P.PRST(R5) ;TEST UNEXPECTED ATTENTION
9464 043712 001404          BEQ     ALLTRM
9465 043714 104042          ERROR   42
9466 043716 052737 000200 005504  BIS     #ABORT,RECODE
9467
9468
9469          ;ALL ERRORS MUST EXIT THROUGH THIS POINT
9470
9471 043724 012705 002630          ALLTRM: MOV     #PARMO,R5 ;RESTORE PARAMETER BLOCK SELECTION
9472 043730 012737 042130 003046  MOV     #ERRHDL,A.ABNL ;RESTORE INTERRUPT VECTORS FOR RETRY
9473 043736 012737 040652 003044  MOV     #ERRFRE,A.NORM ;DRIVER OPERATIONS, IF ANY
9474 043744 032737 000200 005504  BIT     #ABORT,RECODE ;IF ABORT IS NOT SET AND
9475 043752 001043          BNE     48$          ;THE DRIVE IS READY (HAS NOT
9476          ;CYCLED DOWN)
9477 043754 013702 003036          MOV     RKBAS,R2    ;GET BASE ADDRESS
9478 043760 032762 000200 000012  BIT     #RDY,RKDS(R2) ;TEST IF DRIVE READY SET
9479 043766 001004          BNE     47$          ;RECALIBRATE REQUIRED BIT IS SET
9480 043770 052737 000200 005504  BIS     #ABORT,RECODE ;ELSE ABORT WITH ABORT MESSAGE
9481 043776 000431          BR      48$
9482 044000 032737 004000 005504 47$:  BIT     #RCLREQ,RECODE ;IF RECALIBRATE IS REQUIRED

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9483 044006 001443          BEQ      BGNRTY          ;FOR RETRY SET UP PARAM
9484 044010 112737 000113 000001  MOVB     #RECAL,P.CMND  ;BLOCK TO DO IT.
9485 044016 012737 044270 003046  MOV      #RETANL,A.ABNL
9486 044024 004737 040252  JSR      PC,DRVCAL
9487 044030 012737 042130 003046  MOV      #ERRHDL,A.ABNL ;RESTORE ERROR RETURN
9488 044036 032737 000400 005504  BIT      #LEVZER,RECODE ;IF AN ERROR OCCURRED IN THE
9489 044044 001424          BEQ      BGNRTY          ;RECAL ATTEMP SET ABORT,
9490 044046 052737 000200 005504  BIS      #ABORT,RECODE  ;PRINT THE RECAL ERROR MESSAGE, AND
9491 044054 104060          ERROR    60             ;GO REPORT DETAILS
9492 044056 000137 042150          JMP      ERZENT
9493 044062 104061          48$:  ERROR    61             ;REPORT ABORT-RETRY FAILED
9494 044064 105037 003130          CLRB    DRVERS        ;DROP DRIVE & GO TO NEW DRIVE
9495 044070 105037 003126          CLRB    ERRCNT
9496 044074 000137 016754          JMP      NEWDRV
9497
9498          ;THE PROGRAM WILL HALT HERE IF THE ABORT FLAG HAS BEEN SET OR
9499          ;IF THE DRIVE READY BIT IS RESET.
9500
9501 044100 C00005          HLTPRG: RESET          ;DISABLE ALL DEVICES
9502 044102 000000          HALT              ;HALT THE CPU
9503 044104 105037 003126          CLRB    ERRCNT        ;CLEAR ERROR COUNT
9504 044110 000005          RESET            ;RESET ALL DEVICES
9505 044112 000137 012534          JMP      CMSTRT
9506
9507
9508          ;THE FOLLOWING CODE WILL DETERMINE IF AND DATA TYPE ERROR
9509          ;HAS OCCURRED. IF YES, THE RETRY IS NOT DONE HERE BUT RETURNS TO
9510          ;THE INITIATING ROUTINE FOR RETRY. ANY OTHER ERROR IS TO BE
9511          ;RETRIED HERE. IF RETRY IS UNSUCCESSFUL AFTER 4 ATTEMPTS, THE ABORT
9512          ;FLAG IS SET AND PROGRAM HALTS.
9513
9514 044116 032737 000136 005504  BGNRTY: BIT      #BSERR!HVR CER!OPIERR!DCKERR!WCERR,RECODE ;TEST IF ANY DATA ERROR
9515 044124 001404          BEQ      3$
9516 044126 052737 100000 005504  9$:  BIS      #ANYDER,RECODE
9517 044134 000453          BR       2$
9518 044136          3$:
9519 044136 105737 003143          TSTB    NORTRY        ;SEE IF 'NO-RETRY' FLAG SET
9520 044142 001371          BNE     9$           ;BR IF YES
9521 044144 032737 001000 005504  BIT      #BADSEC,RECODE ;TEST IF BAD SECTOR FLAG SET
9522 044152 001044          BNE     2$           ;IF YES-EXIT TO CALLER
9523 044154 123737 003126 003127  CMPB    ERRCNT,ERRLMT ;BEGIN RETRY IF ERROR COUNT HAS
9524 044162 001012          BNE     1$           ;NOT BEEN EXCEEDED
9525 044164 005037 001174          CLR     $REGS
9526 044170 113737 003126 001174  MOVB    ERRCNT,$REGS  ;GET ERROR RETRY COUNT
9527 044176 104102          ERROR   102          ;REPORT RETRY UNSUCCESSFUL
9528 044200 052737 000200 005504  BIS      #ABORT,RECODE ;SET ABORT & QUIT
9529 044206 000734          BR      HLTPRG
9530 044210 013702 003036          1$:  MOV      RKBAS,R2      ;GET RK BASE ADDRESS
9531 044214 112765 000177 000001  MOVB    #SUBCLR,P.CMND(R5) ;SET UP TO CLEAR SUBSYSTEM
9532 044222 004737 040252          JSR     PC,DRVCAL     ;GO DO IT
9533 044226 012700 005246          MOV     #COMSTR,R0    ;GO AND REESTABLISH THE COMMAND
9534 044232 012025          MOV     (R0)+,(R5)+  ;AS IT WAS ENTERED INTO THE
9535 044234 012025          MOV     (R0)+,(R5)+  ;PARAMETER BLOCK
9536 044236 012025          MOV     (R0)+,(R5)+
9537 044240 012025          MOV     (R0)+,(R5)+
9538 044242 012025          MOV     (R0)+,(R5)+
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9539 044244 012025          MOV      (R0)+,(R5)+
9540 044246 012705 002630    MOV      #PARMO,R5
9541 044252 012737 000000 177776    MOV      #PRO,@#PSW      ;LOWER PRIORITY TO ALLOW INTERRUPT
9542 044260 004737 040252    JSR      PC,DRVCAL      ;CALL DRIVER
9543 044264 104410          2$:     RESREG      ;IF RETURN GETS HERE, NO ERROR
9544 044266 000207          RTS      PC              ;OCCURRED, RECOVERY WAS SUCCESSFUL
9545
9546
9547 044270 152737 000377 003123 RETANL: BISB      #377,DONE      ;SET DONE
9548 044276 052737 000400 005504    BIS      #LEV2ER,RECODE ;SET LEVEL TWO ERROR
9549 044304 000207          RTS      PC
9550 044306 152737 000377 003123 FETNML: BISB      #377,DONE      ;SET DONE
9551 044314 000207          RTS      PC
9552
9553
9554      ;*****
9555      ;SBTTL TIME OUT PROCESSOR ROUTINE
9556      ;*THIS ROUTINE SUPPORTS THE ERROR HANDLER BY PROCCSSING TIME OUT STATUS
9557      ;*GATHERING DUTIES.
9558      ;*****
9559      ;TOPROC:
9560      SAVREG
9561 044316 104407          MOV      RKBAS,R2
9562 044320 013702 003036    MOV      #SREG5,R1      ;SET UP R1 FOR RK REGISTER STORAGE
9563 044324 012701 001174    MOV      RKCS1(R2),(R1)+ ;STORE ALL VALID RK611
9564 044330 016221 000000    MOV      RKCS2(R2),(R1)+ ;REGISTERS
9565 044334 016221 000010    MOV      RKDC(R2),(R1)+
9566 044340 016221 000020    MOV      RKDA(R2),(R1)+
9567 044344 016221 000006    MOV      RKWC(R2),(R1)+
9568 044350 016221 000002    MOV      RKBA(R2),(R1)+
9569 044354 016221 000004    MOV      RKASOF(R2),(R1)+
9570 044360 016221 000016    MOV      RKDS(R2),(R1)+
9571 044364 016221 000012    MOV      RKER(R2),(R1)+
9572 044374 005000          CLR      R0              ;THIS CODE WILL ATTEMPT TO
9573                          ;RETRIEVE THE STATUS FROM THE
9574                          ;DRIVE.
9575 044376 012705 002714    MOV      #PARM1,R5      ;SET UP TO USE PARM1
9576 044402 112765 000141 000001    MOVNB   #RDSTAT,P.CMND(R5) ;DO READ DRIVE STATUS COMMAND
9577 044410 004737 040252    JSR      PC,DRVCAL      ;CALL DRIVER
9578 044414 032765 000100 000014    BIT      #CMDTO,P.PRST(R5) ;TEST FOR TIMEOUT
9579 044422 001403          BEQ      1$              ;NO - SKIP
9580 044424 052737 002000 005504    BIS      #TWOTOS,RECODE ;SET TWO TIMEOUTS FLAG
9581 044432 062705 000040 1$:     ADD      #P.AOO,R5      ;BUMP R5 TO POINT TO DRIVE STATUS
9582 044436 012521          MOV      (R5)+,(R1)+    ;MOVE ALL THE DRIVE STATUS INTO THE
9583 044440 012521          MOV      (R5)+,(R1)+    ;TEMP REGS FOR REPORTING.
9584 044442 012521          MOV      (R5)+,(R1)+
9585 044444 012521          MOV      (R5)+,(R1)+
9586 044446 012521          MOV      (R5)+,(R1)+
9587 044450 012521          MOV      (R5)+,(R1)+
9588 044452 012521          MOV      (R5)+,(R1)+
9589 044454 012521          MOV      (R5)+,(R1)+
9590
9591 044456 012705 002630    MOV      #PARMO,R5      ;RESTORE PARM 0
9592 044462 104410          RESREG
9593 044464 000207          RTS      PC
9594
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9600 044466
9601 044466 104407
9602 044470 016501 000026
9603 044474 016500 000052
9604 044500 042700 160017
9605 044504 006200
9606 044506 006200
9607 044510 006200
9608 044512 006200
9609 044514 012705 002714
9610 044520 010165 000004
9611 044524 010065 000002
9612 044530 112765 000177 000001
9613 044536 012737 000000 177776
9614 044544 004737 040252
9615 044550 112765 000125 000001
9616 044556 004737 040252
9617 044562 013712 017716
9618 044566 032712 000200 4$:
9619 044572 001775
9620 044574 013712 017720
9621 044600 032712 000200 6$:
9622 044604 001775
9623 044606 142765 000020 000007
9624 044614 153765 003125 000007
9625 044622 012705 002630
9626 044626 104410
9627 044630 000207
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9634 044632 016537 000030 001174
9635 044640 005037 001176
9636 044644 005037 001200
9637 044650 116537 000026 001200
9638 044656 116537 000027 001176
9639 044664 005737 001200
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9641 044670 001403
9642 044672 005337 001200
9643 044676 000426
9644 044700 032765 010000 000016 1$:
9645 044706 001404
9646 044710 012737 000023 001200
9647 044716 000403
9648 044720 012737 000025 001200 2$:
9649 044726 005737 001176 3$:
9650 044732 001403

:*****
:SBTTL READ HEADER 0 ROUTINE
:*****
RDHDO:
SAVREG
MOV P.DTS(R5),R1 ;STORE TRACK AND SECTOR
MOV P.B10(R5),R0 ;GET THE CYLINDER ADRS
BIC #160017,R0 ;FROM THE DRIVE STATUS. CLEAR
ASR R0 ;OFF UNUSED BITS AND POSITION
ASR R0 ;FOR USE AS THE DESIRED
ASR R0 ;CYLINDER IN THE READ
ASR R0 ;HEADER COMMAND.
MOV #PARM1,R5 ;SET UP TO USE P.B 1
MOV R1,P.SECT(R5) ;INSERT TRK,SECT FOR READ HDR
MOV R0,P.CYLN(R5) ;SET CYL NO.
MOVB #SUBCLR,P.CMND(R5) ;SET S.S. CLEAR CMND
MOV #PRO,#PSW ;ALLOW INTERRUPTS
JSR PC,DRVCAL ;CLEAR THE S.S.
MOVB #RDHEAD,P.CMND(R5) ;SET READ HEADER COMMAND
JSR PC,DRVCAL ;DO READ HEADER
MOV HOLD3,(R2) ;ISSUE RD HDR WITH FMT BIT = 0
4$: BIT #BIT7,(R2) ;CHECK FOR C. READY IN CS1
BEQ 4$ ;BR IF NOT RDY YET
MOV HOLD4,(R2) ;ISSUE RD HDR WITH FMT BIT = 1
6$: BIT #BIT7,(R2) ;CHECK FOR C. READY IN CS1
BEQ 6$ ;BR IF NOT RDY YET
BICB #B.CFMT,P.CS1H(R5) ;CLEAR THE FORMAT BIT
BISB FORMAT,P.CS1H(R5) ;RESTORE TYPE AND FMT IN USE
MOV #PARM0,R5 ;RESTORE P.B. 0 ADDRESS
RESREG ;RESTORE R0-R5
RTS PC ;RETURN

:*****
:SBTTL GET PACK ADDRESS ROUTINE
:*****
GTPKAD: MOV P.DCYL(R5),$REG5 ;GET CYLINDER NUMBER
CLR $REG6 ;CLEAR REGISTERS FOR
CLR $REG7 ;TRACK & SECTOR STORAGE
MOVB P.DTS(R5),$REG7 ;STORE THE TRACK AND SECTOR
MOVB P.DTS+1(R5),$REG6
TST $REG7 ;ADJUST THE ADDRESS CONTAINED IN
;THE RK REGISTERS FOR THE AUTOMATIC
;INCREMENT
BEQ 1$
DEC $REG7
BR 5$
1$: BIT #CFMT,P.CS1(R5)
BEQ 2$
MOV #19,$REG7
BR 3$
2$: MOV #21,$REG7
3$: TST $REG6
BEQ 4$
```

9651 044734 005337 001176
9652 044740 000405
9653 044742 012737 000002 001176 4\$:
9654 044750 005337 001174
9655 044754 000207 5\$:
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9665 044756 104407
9666 044760 016537 000030 001174
9667 044766 016501 000026
9668 044772 042701 174377
9669 044776 006201
9670 045000 006201
9671 045002 006201
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9673 045004 016537 000026 001176
9674 045012 042737 177740 001176
9675 045020 060137 001176
9676 045024 052737 140000 001176
9677 045032 032765 010000 000016
9678 045040 001403
9679 045042 052737 001000 001176
9680 045050 013737 001174 001200 23\$:
9681 045056 013701 001176
9682 045062 043737 001176 001200
9683 045070 043701 001174
9684 045074 050137 001200
9685 045100 104410
9686 045102 000207
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```
DEC $REG6  
BR 5$  
MOV #2,$REG6  
DEC $REG5  
RTS PC  
  
:*****  
:SBTTL BUILD EXPECTED HEADER  
:*USES DESIRED CYLINDER, TRACK AND SECTOR REGISTERS TO DETERMINE  
:*WHICH HEADER WAS EXPECTED. LOADS EXPECTED VALUES IN $REG5, 6, AND  
:*7 FOR REPORTING.  
:*****  
BLDEXH: SAVREG  
MOV P.DCYL(R5),$REG5 ;CONSTRUCT EXPECTED HDR  
MOV P.DTS(R5),R1 ;DESIRED CYLINDER & DESIRED TRACK  
BIC #174377,R1 ;CLEAR ALL BUT TRACK BITS  
ASR R1 ;AND SECTOR. SHIFT THE TRACK  
ASR R1 ;OVER TO CONFORM TO HEADER FORMAT  
ASR R1 ;CHECK THE FORMAT BIT AND  
;IF SET, SET THE HEADER FORMAT  
MOV P.DTS(R5),$REG6 ;BIT.  
BIC #177740,$REG6 ;CLEAR ALL BUT SECTOR  
ADD R1,$REG6 ;ADD TRACK AND SECTOR TOGETHER  
BIS #140000,$REG6 ;INSERT BSE BITS  
BIT #CFMT,P.CS1(R5)  
BEQ 23$  
BIS #1000,$REG6  
MOV $REG5,$REG7 ;COMPUTE THE HEADER VRC  
MOV $REG6,R1  
BIC $REG6,$REG7  
BIC $REG5,R1  
BIS R1,$REG7  
RESREG  
RTS PC
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.SBTTL RK611/RK06-RK07 UNIBUS DRIVER FOR SEQUENTIAL OPERATIONS (REV. 0.10)

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:*MAYNARD, MA. 01754
:*AUTHOR: ROY SPITZER

.SBTTL *WATCH-DOG TIMER

* THE WATCH-DOG TIMER DOES A PSEUDO-TIMING OF RK06-RK07 UNIBUS
* SUBSYSTEM COMMAND. SINCE ONE CAN NOT GUARANTEE THAT A
* REAL-TIME CLOCK (KW11-P OR KW11-L) IS ON THE SYSTEM
* THE RK06-RK07 DRIVER WILL USE THE LOCATION W.MTIM FOR
* MILLI-SECOND TIMING. WHEN W.MTIM REACHES ZERO THE
* WATCH-DOG TIMER WILL SCAN THE DRIVES IN USE AS
* DETERMINED BY THE LOCATION W.TIME. THE TIMER COUNTS
* (ONE FOR EACH DRIVE) ARE KEPT IN THE TABLE W.DRV.
* IF ANY COUNT IN THE TABLE W.DRV REACHES ZERO A COMMAND
* TIME-OUT WILL BE DESIGNATED IN THE PROGRAM DEVICE STATUS
* REGISTER OF THAT DRIVE'S PARAMETER BLOCK.

* THE DRIVER WILL USE THE LOCATION W.MIN AS THE NUMBER
* OF MILLISECONDS FOR AN UNLOAD OR START SPINDLE COMMAND.
* THE DRIVER WILL USE THE LOCATION W.SEC AS THE TIME
* LIMIT FOR ALL OTHER COMMANDS.

* FOR QUEUED OPERATIONS THE WATCH-DOG TIMER WILL
* WATCH UP TO 8 OPERATIONS SIMULATEOUSLY. FOR SEQUENTIAL
* OPERATIONS ONLY ONE OPERATION WILL BE WATCHED.

*CALL JSR PC,W.WTCH
*RETURN IF NO DRIVE ORDER EXCEEDED ITS TIME LIMIT

* OTHERWISE AN ABNORMAL RETURN TO THE ROUTINE ADDRESS
* BY LOCATION A.ABNL WILL OCCUR AND THE CMDTO FLAG
* IN THE PROGRAM DEVICE STATUS REGISTER OF THE
* APPROPRIATE PARAMETER BLOCK WILL BE SET.

W.WTCH: MOV R5,-(SP) ;SAVE R5 ON THE STACK
MOV R4,-(SP) ;SAVE R4 ON THE STACK
MOV R3,-(SP) ;SAVE R3 ON THE STACK
MOV R2,-(SP) ;SAVE R2 ON STACK
MOV PS,-(SP) ;SAVE PROGRAM STATUS WORD ON STACK
DEC W.MTIM ;DECREMENT MILLISECOND TIMER
BNE 20\$;IF NOT ZERO RETURN
MOV W.MILI,W.MTIM ;REINITIALIZE MILLISECOND TIMER
TSTB W.TIME ;CHECK IF DRIVE IS BEING TIMED
BEQ 20\$;NO, RETURN
MOV RKPRI,PS ;LOCK OUT RK06-RK07 INTERRUPTS
MOV RKBAS,R2 ;LOAD BASE OF RK06-RK07 REGISTERS
DEC W.DRV ;DECREMENT COMMAND TIMER
BNE 20\$;RETURN IF NO TIME OUT

045104 010546
045106 010446
045110 010346
045112 010246
045114 013746 177776
045120 005337 003056
045124 001034
045126 013737 003060 003056
045134 105737 003100
045140 001426
045142 013737 003042 177776
045150 013702 003036
045154 005337 003114
045160 001016

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*WATCH-DOG TIMER

SEQ 0182

9744	045162	105037	003100		CLRB	W.TIME	:RESET TIMING INDICATOR
9745	045166	013705	003112		MOV	PBLKT,R5	:LOAD ADDRESS OF PARAMETER BLOCK
9746							:TABLE FOR INDEXING
9747	045172	052765	000100	000014	BIS	#CMDTO,P.PRST(R5)	:SET COMMAND TIME OUT
9748	045200	020537	003054		CMP	R5,O.WAIT	:CHECK IF DRIVER IS WAITING FOR
9749							:COMMAND COMPLETION
9750	045204	001002			BNE	5\$:NO, DO NOT ALTER WAITING FOR
9751							:COMMAND COMPLETION
9752	045206	005037	003054		CLR	O.WAIT	:CLEAR WAIT FOR COMMAND COMPLETION
9753	045212	004737	050466	5\$:	JSR	PC,R.ABNL	:BRANCH TO ERROR ROUTINE
9754	045216	012637	177776	20\$:	MOV	(SP)+,PS	:RESTORE PSW
9755	045222	012602			MOV	(SP)+,R2	:RESTORE R2
9756	045224	012603			MOV	(SP)+,R3	:RESTORE R3
9757	045226	012604			MOV	(SP)+,R4	:RESTORE R4
9758	045230	012605			MOV	(SP)+,R5	:RESTORE R5
9759	045232	000207			RTS	PC	:RETURN

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.SBTTL *RK06 INTERRUPT SERVICE ROUTINE

THIS ROUTINE WILL SERVICE ALL RK06 INTERRUPTS.
UPON RECEIVING AN INTERRUPT, THIS ROUTINE WILL PERFORM ONE OF THE FOLLOWING SERVICES:
1.) SERVICE PORT WAS SEIZED BY OTHER PORT
2.) SERVICE DRIVER IS WAIT FOR COMMAND COMPLETION
3.) SERVICE POSITIONING COMPLETION
4.) REQUEUE COMMAND IF DRIVE WAS RELEASED FOR THE QUEUED RK06 DRIVER.
5.) IF NO SERVICE IS REQUIRED, THE COMMAND WILL BE ISSUED FOR THE QUEUED RK06 DRIVER.

THREE LINKS ARE PROVIDED TO THE DRIVING PROGRAM. THEY ARE:

- 1.) A.NORM ADDRESS OF NORMAL RETURN (SUCESSFUL COMPLETION OF COMMAND)
- 2.) A.ABNL ADDRESS OF ABNORMAL RETURN (UNSUCESSFUL COMPLETION OF COMMAND)
- 3.) A.CONT ADDRESS OF CONTROL ERROR RETURN

FOR NORMAL AND ADNORMAL RETURNS, THE ADDRESS OF THE APPROPRIATE PARAMETER BLOCK WILL BE IN R5.

FOR THE CONTROLLER ERROR RETURN, THE LOCATION E.CONT CONTAINS THE REASON FOR THE CONTROLLER ERROR.

- ROUTINES USED:
- C.OPT (QUEUED ONLY)
 - Q.PUSH (QUEUED ONLY)
 - Q.RMOV (QUEUED ONLY)
 - R.CONT (SEQUENTIAL ONLY)
 - R.NORM (SEQUENTIAL ONLY)
 - R.ABNL (SEQUENTIAL ONLY)
 - I.CSTS
 - I.STAT
 - I.ISSU
 - I.CCLR

```
I.INTR: MOV R5,-(SP) ;STORE R5 ON THE STACK
        MOV R4,-(SP) ;STORE R4 ON THE STACK
        MOV R3,-(SP) ;STORE R3 ON THE STACK
        MOV R2,-(SP) ;STORE R2 ON THE STACK
        MOV R1,-(SP) ;STORE R1 ON THE STACK
        MOV R0,-(SP) ;STORE R0 ON THE STACK
        MOV RKBAS,R2 ;LOAD R2 TO ADDRESS RK06 REGISTER
        MOV RKCS2(R2),T.CS2 ;STORE CS2
        BIT #MDS,T.CS2 ;CHECK IF MULTIPLE DRIVE SELECT
        BEQ 1$ ;NO, CONTINUE PROCESSING
        BIS #E.MDS,E.CONT ;SET MULTIPLE DRIVE SELECT
        JSR PC,R.CONT ;REPORT ERROR
```

045234 010546
045236 010446
045240 010346
045242 010246
045244 010146
045246 010046
045250 013702 003036
045254 016237 000010 003002
045262 032737 001000 003002
045270 001407
045272 052737 100000 003052
045300 704737 050512

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9816 045304 000137 047464          JMP      I.RTRN          ;RETURN
9817
9818 045310 105737 003074          1$:    TSTB     I.ISRL    ;CHECK IF INTERRUPT OR RELEASE
9819 045314 001410                    BEQ      6$             ;NO, CHECK IF DRIVE AVAILABLE
9820 045316 100403                    BMI      5$             ;CHECK IF RELEASE COMMAND
9821 045320 105037 003074          CLR      I.ISRL    ;YES, CLEAR FLAG
9822 045324 000473                    BR       I.I00         ;CONTINUE PROCESSING INTERRUPT
9823
9824 045326 105037 003074          5$:    CLR      I.ISRL    ;CLEAR FLAG
9825 045332 000137 046446          JMP      I.ATTN        ;GO PROCESS DRIVE ATTENTIONS
9826
9827 045336 032737 010400 003002 6$:    BIT      #NED!UFE,T.CS2 ;CHECK FOR NON-EXISTENT DRIVE OR
9828                                ; UNIT FIELD ERROR
9829 045344 001413                    BEQ      7$             ;NO, WAIT FOR DUAL ACCESS INTERRUPT
9830 045346 013704 003002          MOV      T.CS2,R4      ;LOAD R4 FOR DRIVE NUMBER
9831 045352 042704 177770          BIC      #^C<DRV ISK>,R4 ;KEEP DRIVE BITS
9832 045356 013705 003112          MOV      PBLKT,R5      ;STORE PARAMETER BLOCK ADDRESS
9833 045362 016237 000000 003000  MOV      RKCS1(R2),T.CS1 ;LOAD TEMPORARY CS1 FOR STATUS REPORT
9834 045370 000137 045656          JMP      I.ERRC        ;REPORT ERROR
9835
9836 045374 016237 000012 003020 7$:    MOV      RKDS(R2),T.DS  ;STORE STATUS REGISTER FOR COMPARISON
9837 045402 032737 000001 003020  BIT      #DRA,T.DS     ;CHECK IF DRIVE SEIZED BY OTHER
9838                                ; PORT
9839 045410 001041                    BNE      I.I00         ;NO, CONTINUE PROCESSING INTERRUPT
9840
9841                                ;CHECK IF ANY DATA TRANSFER ERROR EXISTS
9842 045412 032737 164000 003002  BIT      #DLT!WCE!UPE!NEM,T.CS2
9843
9844 045420 001007                    BNE      10$           ;INDICATE ERROR
9845 045422 016237 000014 003016  MOV      RKER(R2),T.ER  ;STORE ERROR REGISTER
9846
9847                                ;CHECK FOR DATA TRANSFER ERROR TYPE ERROR
9848 045430 032737 125700 003016  BIT      #DCK!OPI!WLE!COE!HVRC!BSE!ECH,T.ER
9849
9850 045436 001407                    BEQ      11$           ;NO, WAIT FOR RELEASE OF RK06 DRIVE
9851
9852 045440 052737 000010 003052 10$:   BIS      #E.UDAT,E.CONT ;SET UNEXPECTED DATA TYPE ERROR
9853 045446 004737 050512                    JSR      PC,R.CONT     ;REPORT ERROR
9854 045452 000137 047464                    JMP      I.RTRN        ;RESTORE REGISTERS
9855
9856 045456 105037 003100          11$:   CLR      W.TIME       ;RESET TIMING ON THIS DRIVE
9857 045462 005037 003114          CLR      W.DRV        ;CLEAR TIMING COUNT FOR THIS DRIVE
9858 045466 013705 003112          MOV      PBLKT,R5      ;LOAD R5 WITH PARAMETER BLOCK
9859                                ; ADDRESS
9860 045472 052765 010000 000014  BIS      #DRVSZD,P.PRST(R5) ;SET DRIVE SEIZED IN THE
9861                                ; PROGRAM DRIVE STATUS REGISTER
9862 045500 005037 003054          CLR      O.WAIT       ;CLEAR WAIT FOR COMMAND COMPLETION
9863 045504 004737 050466          JSR      PC,R.ABNL     ;INDICATE ABNORMAL TERMINATION
9864 045510 000137 047464          JMP      I.RTRN        ;GO RESTORE REGISTERS
9865
9866 045514 013705 003054          I.I00: MOV      O.WAIT,R5  ;LOAD PARAMETER BLOCK ADDRESS INTO R5
9867 045520 001002                    BNE      2$             ;IS COMMAND WAITING PROCESSING
9868                                ; YES, DO PROCESSING
9869 045522 000137 046446          JMP      I.ATTN        ;NO, PROCESS ATTENTION
9870
9871 045526 013704 003002          2$:    MOV      T.CS2,R4  ;STORE RKCS2 FOR DRIVE NUMBER
  
```

```
9872 045532 042704 177770          BIC      #^C<DRVMSK>,R4  ;MASK OUT UNNECESSARY BITS
9873
9874
9875 045536 126504 000000          CMPB     P.DRVN(R5),R4  ;CHECK IF DRIVE NUMBER IS EXPECTED
9876 045542 001401                    BEQ      3$              ;YES, CONTINUE
9877 045544 000000                    HALT     ;NO, DRIVER ERROR
9878 045546 122765 000164 000001 3$:  CMPB     #RDALHD,P.CMND(R5) ;CHECK IF READ ALL HEADERS
9879 045554 001002                    BNE     10$             ;NO, EXECUTE NORMAL DATA TRANSFER
9880 045556 000137 046114          JMP      I.HDAL         ;GO EXECUTE SPECIAL HEADER SEQUENCE
9881
9882 045562 005037 003054          10$:    CLR      O.WAIT        ;CLEAR WAIT FOR COMMAND COMPLETION
9883 045566 005037 003114          CLR      W.DRV         ;CLEAR WATCH-DOG TIME
9884 045572 105037 003100          CLRB    W.TIME         ;RESET TIMING ON THIS DRIVE
9885 045576 016237 000000 003000  MOV      RKCS1(R2),T.CS1 ;STORE COMMAND AND STATUS REGISTER 1
9886 045604 032737 100000 003000  BIT      #CERR,T.CS1    ;CHECK IF CONTROLLER ERROR
9887 045612 001021                    BNE     I.ERRC         ;YES, PROCESS ERROR
9888 045614 016237 000016 003014  MOV      RKASOF(R2),T.ASOF ;STORE ATTENTION SUMMARY
9889 045622 133737 003101 003015  BITB    INTMSK,T.ASOF+1 ;CHECK IF DRIVE ATTENTION SET
9890 045630 001004                    BNE     15$             ;YES, REPORT ERROR
9891 045632 004737 050500          JSR     PC,R.NORM      ;INDICATE NORMAL RETURN
9892 045636 000137 047464          JMP     I.RTRN         ;RESTORE REGISTERS
9893
9894 045642 052765 000010 000014 15$:    BIS      #UEXATT,P.PRST(R5) ;SET UNEXPECTED ATTENTION
9895
9896 045650 004737 050134          I.ERRA: JSR     PC,I.CSTS   ;STORE CONTROLLER STATUS
9897 045654 000405                    BR      I.ERR          ;STORE PATTERN AND POSITION INFORMATION
9898
9899 045656 013765 003000 000016  I.ERRC: MOV      T.CS1,P.CS1(R5) ;GET ERROR RKCS1
9900 045664 004737 050156          JSR     PC,I.CST1     ;GET REST OF CONTROLLER STATUS
9901 045670 016265 000032 000062  I.ERR:  MOV      RKECPT(R2),P.EPAT(R5) ;STORE ECC PATTERN
9902 045676 016265 000030 000060  MOV      RKECPC(R2),P.EPOS(R5) ;STORE ECC POSITION
9903 045704 004037 047502          JSR     RO,I.CCLR     ;CLEAR CONTROLLER
9904 045710 047464          I.RTRN ;ERROR RETURN
9905 045712 032765 010400 000020  BIT      #NED!UFE,P.CS2(R5) ;CHECK IF IT WAS NON-EXISTENT DRIVE OR
9906                                     ; UNIT FIELD ERROR
9907 045720 001046                    BNE     5$              ;YES, REPORT ERROR
9908 045722 004037 050240          JSR     RO,I.STAT     ;GATHER DRIVE STATUS
9909 045726 047464          I.RTRN ;ERROR RETURN
9910 045730 112737 000005 003000  MOVB    #DR.CLR,T.CS1  ;LOAD COMMAND
9911 045736 004037 047564          JSR     RO,I.ISSU     ;ISSUE DRIVE CLEAR
9912 045742 047464          I.RTRN ;ERROR RETURN
9913 045744 133737 003101 003015  BITB    INTMSK,T.ASOF+1 ;CHECK IF ATTENTION RESET
9914 045752 001407                    BEQ     2$              ;NO, INDICATE DRIVE ERROR
9915 045754 052737 000020 003052  BIS      #E.CLAT,E.CONT ;SET ATTENTION DID NOT RESET
9916                                     ; WITH CLEAR
9917 045762 004737 050512          JSR     PC,R.CONT     ;REPORT CONTROLLER ERROR
9918 045766 000137 047464          JMP     I.RTRN         ;GO RESTORE REGISTERS
9919
9920 045772 032737 040000 003024 2$:    BIT      #S.DSC,T.MR2   ;CHECK IF DRIVE STATUS CHANGE CLEARED
9921 046000 001403                    BEQ     3$              ;YES, CHECK FAULT
9922 046002 052765 000040 000014  BIS      #DRVDSC,P.PRST(R5) ;SET DSC DID NOT CLEAR
9923 046010 032737 001000 003026 3$:    BIT      #S.PAR,T.MR3   ;CHECK IF DRIVE PARITY ERROR
9924 046016 001407                    BEQ     5$              ;NO, INDICATE ABNORMAL TERMINATION
9925 046020 052737 002000 003052  BIS      #E.DPAR,E.CONT ;SET DRIVE PARITY ERROR
9926 046026 004737 050512          JSR     PC,R.CONT     ;INDICATE CONTROLLER ERROR
9927 046032 000137 047464          JMP     I.RTRN         ;RETURN
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9928
9929      ,036 032765 000020 000014 5$: BIT #DRVHRD,P.PRST(R5) ;CHECK IF HARD DRIVE ERROR
9930      046044 001017 BNE 10$ ;YES, GO REPORT ERROR
9931      046046 032737 020000 003024 BIT #S.PIP,T.MR2 ;CHECK IF DRIVE IS CYCLING DOWN
9932      046054 001413 BEQ 10$ ;NO, REPORT ERROR
9933      046056 052765 020000 000014 BIS #E.UNLD,P.PRST(R5) ;SET DRIVE UNLOADING
9934      046064 113737 003101 003100 MOVB INTMSK,W.TIME ;SET UP 8 SECONDS FOR DRIVE TO CYCLE UP
9935      046072 013737 003064 003114 MOV W.8SEC,W.DRV
9936      046100 000137 047464 JMP I.RTRN ;GO RESTORE REGISTERS
9937
9938      046104 004737 050466 10$: JSR PC,R.ABNL ;GO REPORT ERROR
9939      046110 000137 047464 JMP I.RTRN ;GO RESTORE REGISTERS
9940
9941      .SBTTL *READ ALL HEADERS INTERRUPT SEQUENCE
9942
9943      046114 016237 000000 003000 I.HDAL: MOV RKCS1(R2),T.CS1 ;STORE CS1 TO CHECK CONTROLLEP
9944                                     ; ERROR
9945      046122 032737 100000 003000 BIT #CERR,I.CS1 ;CHECK IF CONTROLLER ERROR
9946      046130 001422 BEQ 5$ ;NO, CHECK FOR ATTENTION
9947
9948      046132 005037 003054 CLR O.WAIT ;CLEAR WAITING FOR COMMAND COMPLETE
9949      046136 105037 003100 CLRB W.TIME ;RESET TIMING ON DRIVE
9950      046142 005037 003114 CLR W.DRV ;CLEAR TIME OUT COUNT
9951      046146 013765 003000 000016 MOV T.CS1,P.CS1(R5) ;STORE ERROR RKCS1
9952      046154 004737 050156 JSR PC,I.CST1 ;STORE CONTROLLER REGISTERS
9953      046160 004037 047502 JSR RO,I.CCLR ;CLEAR CONTROLLER
9954      046164 047464 I.RTRN ;ERROR RETURN
9955      046166 004737 050466 JSR PC,R.ABNL ;INDICATE ERROR RETURN
9956      046172 000137 047464 JMP I.RTRN ;RESTORE REGISTERS
9957
9958      046176 016237 000016 003014 5$: MOV RKASOF(R2),T.ASOF ;STORE ATTENTION SUMMARY
9959      046204 133737 003101 003015 BITB INTMSK,T.ASOF+1 ;CHECK IF DRIVE ATTENTION IS SET
9960      046212 001410 BEQ 7$ ;NO, CHECK IF READ ALL HEADERS
9961      046214 005037 003054 CLR O.WAIT ;CLEAR WAITING FOR COMMAND COMPLETION
9962      046220 105037 003100 CLRB W.TIME ;RESET TIMING ON DRIVE
9963      046224 005037 003114 CLR W.DRV ;CLEAR TIME OUT COUNT
9964      046230 000137 045650 JMP I.ERRA ;GO REPORT ERROR
9965
9966      046234 013701 003070 7$: MOV HDR.AD,R1 ;GET MAIN MEMORY ADDRESS
9967      046240 016221 000024 MOV RKDB(R2),(R1)+ ;GET FIRST WORD OF HEADER
9968      046244 016221 000024 MOV RKDB(R2),(R1)+ ;GET SECOND WORD OF HEADER
9969      046250 016221 000024 MOV RKDB(R2),(R1)+ ;GET THIRD WORD OF HEADER
9970      046254 010137 003070 MOV R1,HDR.AD ;STORE ADDRESS FOR NEXT HEADER
9971      046260 016237 000010 003002 MOV RKCS2(R2),T.CS2 ;STORE CS2 TO CHECK FOR DATA LATE
9972      046266 032737 100000 003002 BIT #DLT,T.CS2 ;CHECK FOR DATA LATE
9973      046274 001055 BNE 35$ ;YES, REPORT ERROR
9974      046276 005337 003072 DEC HDR.CT ;DECREMENT NUMBER OF HEADER YET TO READ
9975      046302 001026 BNE 25$ ;IF NON-ZERO, GO ISSUE NEXT READ HEADER
9976      046304 005037 003054 CLR O.WAIT ;CLEAR DRIVER WAITING FOR COMMAND COMPLETION
9977      046310 005037 003114 CLR W.DRV ;CLEAR TIME OUT COUNT FOR THIS DR'VE
9978      046314 105037 003100 CLRB W.TIME ;CLEAR WATCH DOG TIME ON THIS DRIVE
9979      046320 012762 000003 000026 MOV #3,RKMR1(R2) ;LOAD MAINTENANCE REGISTER FOR SECTOR COUNT
9980      046326 112737 000001 003000 MOVB #DR.SEL,T.CS1 ;LOAD SELECT COMMAND
9981      046334 004037 047564 JSR RO,I.ISSU ;GET SECTOR COUNT
9982      046340 047464 I.RTRN ;ERROR RETURN
9983      046342 013765 003026 000056 MOV T.MR3,P.B11(R5) ;LOAD SECTOR COUNT

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*READ ALL HEADERS INTERRUPT SEQUENCE

F 15

SEQ 0187

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9984 046350 004737 050500      JSR   PC,R.NORM      ;INDICATE NORMAL TERMINATION
9985 046354 000137 047464      JMP   I.RTRN        ;RESTORE REGISTERS
9986
9987 046350 016562 000002 000020 25$:  MOV   P.CYLN(R5),RKDCYL(R2) ;LOAD CYLINDER ADDRESS REGISTER
9988 046366 016562 000004 000006      MOV   P.SECT(R5),RKDA(R2) ;LOAD SECTOR AND TRACK
9989 046374 116565 000007 000017      MOVSB P.CS1H(R5),P.CS1+1(R5) ;STORE BITS 8-15 OF CS1
9990 046402 042765 165777 000016      BIC   #^C<CDT!CFMT>,P.CS1(R5) ;CLEAR ALL BITS EXCEPT FORMAT AND
9991                                     ; DRIVE TYPE
9992 046410 112765 000125 000016      MOVSB #RDHEAD,P.CS1(R5) ;STORE COMMAND ISSUED
9993 046416 016562 000016 000000      MOV   P.CS1(R5),RKCS1(R2) ;ISSUE READ HEADER
9994 046424 000137 047464      JMP   I.RTRN        ;RESTORE REGISTERS
9995
9996 046430 052737 000400 003052 35$:  BIS   #E.DLT,E.CONT    ;SET DATA LATE WHILE UNLOADING HEADER
9997 046436 004737 050512      JSR   PC,R.CONT     ;REPORT ERROR
9998 046442 000137 047464      JMP   I.RTRN        ;RESTORE REGISTERS
9999
10000 .SBTTL *DRIVE ATTENTION SCANNER
10001
10002 046446 016237 000000 003000 1.ATTN: MOV  RKCS1(R2),T.CS1 ;STORE COMMAND AND STATUS
10003                                     ; REGISTER 1 FOR COMPARISON
10004 046454 032737 100000 003000      BIT   #CERR,T.CS1   ;CHECK IF CONTROLLER ERROR OCCURRED
10005 046462 001441      BEQ   5$           ;NO, CHECK IF ATTENTION
10006
10007                                     ;CHECK IF ANY DATA TRANSFER TYPE ERROR EXISTS
10008 046464 032737 164000 003002      BIT   #DLT!WCE!UPE!NEM,T.CS2
10009
10010 046472 001007      BNE   1$           ;INDICATE ERROR
10011 046474 016237 000014 003016      MOV   RKER(R2),T.ER ;STORE ERROR REGISTER
10012
10013                                     ; CHECK FOR DATA TRANSFER ERROR TYPE
10014 046502 032737 125700 003016      BIT   #DCK!OPI!WLE!COE!HVRC!BSE!ECH,T.ER
10015
10016 046510 001407      BEQ   2$           ;NO DATA TRANSFER ERROR
10017
10018 046512 052737 000010 003052 1$:  BIS   #E.UDAT,E.CONT ;SET UNEXPECTED DATA TYPE ERROR
10019 046520 004737 050512      JSR   PC,R.CONT     ;REPORT ERROR
10020 046524 000137 047464      JMP   I.RTRN        ;RESTORE REGISTERS
10021
10022 046530 013704 003002 2$:  MOV   T.CS2,R4      ;SAVE CS2 FOR REGISTER NUMBER
10023 046534 042704 177770      BIC   #^C<DRVMSK>,R4 ;STRIP OFF JUNK
10024 046540 105037 003100      CLR   W.TIME       ;CLEAR WATCH DOG TIMER
10025 046544 005037 003114      CLR   W.DRV        ;RESET TIMER VALUE
10026 046550 013705 003112      MOV   PBLKT,R5     ;STORE PARAMETER BLOCK ADDRESS IN R5
10027
10028                                     ; CLEAR DRIVE POSITIONING AND DRIVE POSITIONED FOR DATA TRANSFER
10029                                     ; IN PROGRAM DEVICE STATUS REGISTER
10030 046554 042765 000006 000014      BIC   #DRVPOS!DRVPDT,P.PRST(R5)
10031
10032 046562 000137 045656      JMP   I.ERRC       ;GO REPORT ERROR
10033
10034 046566 032737 040000 003000 5$:  BIT   #DI,T.CS1    ;CHECK IF ANY DRIVE ATTENTION
10035 046574 001002      BNE   6$           ;YES, PROCESS INTERRUPT
10036 046576 000137 047464      JMP   I.RTRN        ;RESTORE REGISTERS
10037
10038 046602 016237 000016 003014 6$:  MOV   RKASOF(R2),T.ASOF ;STORE ATTENTION SUMMARY
10039 046610 105737 003015      TSTB  T.ASOF+1     ;CHECK IF ANY ATTENTIONS SET
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10040 046614 001007          BNE 7$ ;YES GO PROCESS INTERRUPT
10041 046616 052737 000002 003052 BIS #E.NOAT,E.CONT ;SET NO ATTENTION IN ATTENTION SUMMARY
10042 046624 004737 050512 JSR PC,R.CONT ;GO REPORT ERROR
10043 046630 000137 047464 JMP I.RTRN ;GO RESTORE REGISTERS
10044
10045 046634 133737 003101 003015 7$: BITB INTMSK,T.ASOF+1 ;CHECK IF DESIRED INTERRUPT
10046 046642 001007          BNE 8$ ;YES, GO PROCESS IT
10047 046644 052737 000004 003052 BIS #E.UATT,E.CONT ;SET UNSOLICATED ATTENTION
10048 046652 004737 050512 JSR PC,R.CONT ;GO REPORT ERROR
10049 046656 000137 047464 JMP I.RTRN ;GO RESTORE REGISTERS
10050
10051 046662 013705 003112          8$: MOV PBLKT,R5 ;STORE PARAMETER BLOCK TABLE
10052 046666 116504 000000          MOVB P.DRVN(R5),R4 ;STORE DRIVE NUMBER
10053 046672 032765 020000 000014 BIT #E.UNLD,P.PRST(R5) ;CHECK IF DRIVE UNLOADING
10054 046700 001402          BEQ 11$ ;NO, CONTINUE
10055 046702 000137 047364 JMP I.UNLD ;SERVICE DRIVE IN POSITION AFTER ERROR
10056
10057 046706 042765 000002 000014 11$: BIC #DRVPOS,P.PRST(R5) ;RESET DRIVE POSITIONING
10058 046714 005062 000026 CLR RKMRI(R2) ;CLEAR MAINTENANCE REGISTER 1
10059 046720 112737 000001 003000 MOVB #DR.SEL,T.CS1 ;LOAD COMMAND
10060 046726 004037 047564 JSR RO,I.ISSU ;SELECT DRIVE WITH ATTENTION HIGH
10061 046732 047464          I.RTRN ;ERROR RETURN
10062 046734 013765 003026 000042 MOV T.MR3,P.B00(R5) ;STORE STATUS BYTE 00 MESS B
10063 046742 032765 000200 000042 BIT #S.FLT,P.B00(R5) ;CHECK IF DRIVE FAULT
10064 046750 001401          BEQ 12$ ;NO, CHECK FOR DRIVE STATUS CHANGE
10065 046752 000461          BR I.AERR ;PROCESS ERROR
10066
10067 046754 013765 003024 000040 12$: MOV T.MR2,P.A00(R5) ;STORE MAINTENANCE REGISTER 2
10068 046762 032765 040000 000040 BIT #S.DSC,P.A00(R5) ;CHECK FOR DRIVE STATUS CHANGE
10069 046770 001004          BNE 13$ ;YES, PROCESS DRIVE STATUS CHANGE
10070 046772 052765 004000 000014 BIS #NODSC,P.PRST(R5) ;SET NO DRIVE STATUS CHANGE
10071 047000 000446          BR I.AERR ;PROCESS ERROR
10072
10073 047002 112737 000005 003000 13$: MOVB #DR.CLR,T.CS1 ;LOAD COMMAND
10074 047010 004037 047564 JSR RO,I.ISSU ;CLEAR DRIVE STATUS CHANGE
10075 047014 047464          I.RTRN ;ERROR RETURN
10076 047016 013765 003014 000032 MOV T.ASOF,P.ASOF(R5) ;STORE ATTENTION SUMMARY
10077 047024 133765 003101 000033 BITB INTMSK,P.ASOF+1(P5) ;CHECK IF ATTENTION RESET
10078 047032 001407          BEQ 15$ ;YES, CONTINUE INTERRUPT PROCESSING
10079 047034 052737 000020 003052 BIS #E.CLAT,E.CONT ;SET ATTENTION DID NOT RESET
10080 ; WITH DRIVE CLEAR
10081 047042 004737 050512 JSR PC,R.CONT ;FLAG ERROR
10082 047046 000137 047464 JMP I.RTRN ;RESTORE REGISTERS
10083
10084 047052 013765 003024 000040 15$: MOV T.MR2,P.A00(R5) ;STORE MAINTENANCE REGISTER 2
10085 047060 032765 040000 000040 BIT #S.DSC,P.A00(R5) ;CHECK IF DRIVE STATUS CHANGE
10086 ; RESET
10087 047066 001404          BEQ 16$ ;YES, CONTINUE INTERRUPT PROCESSING
10088 047070 052765 000040 000014 BIS #DRVDC,P.PRST(P5) ;SET DRIVE STATUS CHANGE DID NOT CLEAR
10089 047076 000407          BR I.AERR ;GO PROCESS ERROR
10090
10091 047100 105037 003100          16$: CLRB W.TIME ;RESET TIMING ON THIS DRIVE
10092 047104 005037 003114 CLR W.DRV ;CLEAR DRIVE TIMING COUNT
10093 047110 004737 050500 JSR PC,R.NORM ;REPORT SUCCESSFUL COMMAND COMPLETION
10094 047114 000563          BR I.RTRN ;RESTORE REGISTERS
10095

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```
10096 .SBTTL *ATTENTION ERROR HANDLER
10097
10098 047116 042765 000004 000014 I.AERR: BIC #DRV PDT,P.PRST(R5) ;RESET POSITIONING IN PROGRESS BECAUSE
10099 ; OF DATA TRANSFER
10100 047124 105037 003100 CLR W.TIME ;CLEAR TIMING FOR THIS DRIVE
10101 047130 005037 003114 CLR W.DRV ;RESET WATCH-DOG TIME
10102 047134 042765 177741 000016 BIC #177741,P.CS1(R5) ;KEEP COMMAND ISSUED
10103 047142 042737 000036 003000 BIC #36,T.CS1 ;KEEP CURRENT CONTROLLER STATUS
10104 047150 053765 003000 000016 BIS T.CS1,P.CS1(R5) ;MAKE GOOD MESSAGE
10105 047156 013765 003002 000020 MOV T.CS2,P.CS2(R5) ;STORE CONTROLLER REGISTERS
10106 047164 013765 003004 000022 MOV T.WCR,P.WCR(R5)
10107 047172 013765 003006 000024 MOV T.BA,P.BAR(R5)
10108 047200 013765 003010 000026 MOV T.DA,P.DTS(R5)
10109 047206 013765 003012 000030 MOV T.DC,P.DCYL(R5)
10110 047214 013765 003014 000032 MOV T.ASOF,P.ASOF(R5)
10111 047222 013765 003016 000034 MOV T.ER,P.ER(R5)
10112 047230 013765 003020 000036 MOV T.DS,P.DS(R5)
10113 047236 004037 050240 JSR RO,I.STAT ;GATHER DRIVE STATUS
10114 047242 047464 I.RTRN ;ERROR RETURN
10115 047244 112737 000005 003000 MOV B #DR.CLR,T.CS1 ;LOAD COMMAND
10116 047252 004037 047564 JSR RO,I.ISSU ;CLEAR DRIVE ERRORS
10117 047256 047464 I.RTRN ;ERROR RETURN
10118 047260 133737 003101 003015 BITB INTMSK,T.ASOF+1 ;CHECK IF ATTENTION RESET
10119 047266 001407 BEQ 2$ ;YES, FLAG DRIVE ERROR
10120 047270 052737 000020 003052 BIS #E.CLAT,E.CONT ;SET ATTENTION DID NOT RESET
10121 047276 004737 050512 JSR PC,R.CONT ;REPORT ERROR
10122 047302 000137 047464 JMP I.RTRN ;RESTORE REGISTERS
10123
10124 047306 032765 000020 000014 2$: BIT #DRVHRD,P.PRST(R5) ;CHECK IF A HARD DRIVE ERROR
10125 047314 001017 BNE 10$ ;YES, REPORT ERROR
10126 047316 032737 020000 003024 BIT #S.PIP,T.MR2 ;CHECK IF DRIVE IS UNLOADING
10127 047324 001413 BEQ 10$ ;NO, REPORT ERROR
10128 047326 052765 020000 000014 BIS #E.UNLD,P.PRST(R5) ;SET DRIVE UNLOADING DUE TO ERROR
10129 047334 113737 003101 003100 MOV B INTMSK,W.TIME ;SET TIMING ON THIS DRIVE
10130 047342 013737 003064 003114 MOV W.8SEC,W.DRV ;LOAD 8 SECONDS FOR CYCLE UP TIME
10131 047350 000137 047464 JMP I.RTRN ;RESTORE REGISTERS
10132
10133 047354 004737 050466 10$: JSR PC,R.ABNL ;REPORT ERROR
10134 047360 000137 047464 JMP I.RTRN ;RESTORE REGISTERS
10135
10136 .SBTTL *ERROR CAUSING DRIVE TO UNLOAD
10137
10138 047364 052765 020000 000014 I.UNLD: BIS #E.UNLD,P.PRST(R5) ;CLEAR DRIVE UNLOADING BECAUSE OF ERROR
10139 047372 112737 000005 003000 MOV B #DR.CLR,T.CS1 ;LOAD IN DRIVE CLEAR
10140 047400 004037 047564 JSR RO,I.ISSU ;GO ISSUE DRIVE CLEAR
10141 047404 047464 I.RTRN ;ERROR RETURN
10142 047406 136437 003101 003015 BITB INTMSK(R4),T.ASOF+1 ;CHECK IF ATTENTION CLEARED
10143 047414 001406 BEQ 15$ ;YES, CONTINUE
10144 047416 012737 000020 003052 MOV #E.CLAT,E.CONT ;SET ATTENTION DID NOT RESEI
10145 047424 004737 050512 JSR PC,R.CONT ;REPORT ERROR
10146 047430 000415 BR I.RTRN ;RESTORE REGISTERS
10147
10148 047432 032737 040000 003024 15$: BIT #S.DSC,T.MR2 ;CHECK IF DRIVE STAUUS CHANGE RESET
10149 047440 001403 BEQ 20$ ;YES, CONTINUE
10150 047442 052765 000040 000014 BIS #DRV DSC,P.PRST(R5) ;SET DRIVE STAUUS CHANGE DID NOT CLEAR
10151 047450 105037 003100 20$: CLR B W.TIME ;RESET TIMING ON THIS DRIVE
```

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*ERROR CAUSING DRIVE TO UNLOAD

SEQ 0190

10152	047454	005037	003114	CLR	W.DRV	;CLEAR TIME COUNT
10153	047460	004737	050466	JSR	PC,R.ABNL	;REPORT ERROR
10154						
10155	047464	012600		I.RTRN: MOV	(SP)+,R0	;RESTORE R0
10156	047466	012601		MOV	(SP)+,R1	;RESTORE R1
10157	047470	012602		MOV	(SP)+,R2	;RESTORE R2
10158	047472	012603		MOV	(SP)+,R3	;RESTORE R3
10159	047474	012604		MOV	(SP)+,R4	;RESTORE R4
10160	047476	012605		MOV	(SP)+,R5	;RESTORE R5
10161	04750^	000002		RTI		;RETURN
10162						

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.SBTTL *CONTROLLER CLEAR ROUTINE

* THIS ROUTINE WILL BE USED BY THE DRIVER TO CLEAR THE CONTROLLER
* AND CHECK IF THE CONTROLLER ERRORS ARE RESET. IF THE ERROR IS NOT
* CLEARED, THE ROUTINE AS SPECIFIED IN A.CONT WILL BE CALLED WITH
* E.CCLR SET IN E.CONT.

REGISTER USE
----- ---

R2 ADDRESS OF RK06 REGISTERS
R5 ADDRESS OF PARAMETER BLOCK

*CALL JSR R0,I.CCLR
* <ADDRESS OF ERROR RETURN>
* RETURN

I.CCLR: MOV #CCLR,RKCS1(R2) ;CLEAR CONTROLLER
 MCV RKCS1(R2),T.CS1 ;STORE COMMAND AND STATUS REGISTER 1
 BIT #CERR,T.CS1 ;CHECK IF CONTROLLER CLEAR DID
 ; CLEAR ERROR
 BEQ \$\$;YES, RETURN TO DRIVER PROCESSING
 BIS #E.CCLR,E.CONT ;SET CLEAR CONTROLLER DID NOT CLEAR ERROR
 JSR PC,R.CONT ;REPORT CONTROLLER ERROR
 MOV (R0),R0 ;SET UP ERROR RETURN
 RTS R0 ;RETURN

\$\$: MOV #IE,RKCS1(R2) ;SET INTERRUPT ENABLE
 MOVB #-1,I.ISRL ;SET INTERRUPT ENABLE ISSUED
 TST (R0)+ ;ADJUST FOR NORMAL RETURN
 RTS R0 ;RETURN

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.SBTTL *COMMAND ISSUED BY DRIVER SERVICE ROUTINE

```
*****
*
* THIS ROUTINE WILL ISSUE THE COMMAND AS SPECIFIED IN T.CS1
* AND CHECK IF A CONTROLLER ERROR OCCURRED. IF A CONTROLLER
* ERROR OCCURRED, E.CERR WILL BE SET IN E.CONT AND
* CONTROL WILL BE TURN OVER TO THE ROUTINE SPECIFIED BY THE
* ADDRESS IN A.CONT.
*
* REGISTER      USE
* -----      ---
*
* R2             ADDRESS OF RK06 REGISTERS
* R5             ADDRESS OF PARAMETER BLOCK
*
*CALL JSR      RO,I.ISSU
*      <ADDRESS OF ERROR RETURN>
*      RETURN
*
* ROUTINES USED:
* -----
*
*          I.CCLR
*          I.STOR
*****
```

```
I.ISSU: MOV      T.CS1,-(SP)      ;STORE COMMAND ISSUED
        CLR      T.CS2          ;CLEAR TEMPORARY CS2
        MOV      P.DRVN(R5),T.CS2 ;LOAD IN DRIVE NUMBER
        MOV      T.CS2,RKCS2(R2) ;LOAD DRIVE NUMBER FOR COMMAND
        MOV      P.CS1H(R5),T.CS1+1 ;STORE BITS 8-15 OF CS1
        BICB    #^C<B.CDT!B.CFMT>,T.CS1+1 ;CLEAR ALL BITS EXCEPT
        ;      FORMAT AND DRIVE TYPE
1$:     MOV      T.CS1,RKCS1(R2) ;ISSUE COMMAND
        TSTB    RKCS1(R2)      ;WAIT FOR READY
        BPL     1$
        JSR     PC,I.STOR      ;GO STORE REGISTERS
        BIT     #CERR,T.CS1    ;CHECK IF CONTROLLER ERROR OCCURED
        BEQ     5$            ;NO, RETURN
        BIT     #MDS,T.CS2    ;CHECK IF MULTIPLE DRIVE SELECT
        BEQ     2$            ;NO, CHECK FOR OTHER CONTROLLER ERRORS
        BIS     #E.MDS,E.CONT  ;SET MULTIPLE DRIVE SELECT FLAG
        JSR     PC,R.CONT      ;REPORT CONTROLLER ERROR
        BR      10$          ;RETURN
;CHECK IF ANY CONTROLLER ERROR IS SET
2$:     BIT     #CTO!SPAR,T.CS1
        BNE     7$
        BIT     #UFE!PGE!NEM!NED!UPE!WCE!DLT,T.CS2
        BNE     7$
        BIT     #ILC!DTYE!FMTE!ECH!BSE!HVRC!COE!DTE!OPI!DCK,T.ER
        BNE     7$
        CMPB    #DR.CLR,(SP)   ;CHECK IF CLEAR DRIVE
```

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*COMMAND ISSUED BY DRIVER SERVICE ROUTINE

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SEQ 0193

10254	047734	001003				BNE	3\$:NO, DO NOT SET DRIVE HARD ERROR
10255	047736	052765	000020	000014		BIS	#DRVHRD,P.PRST(R5)	;SET HARD DRIVE ERROR
10256	047744	004037	047502		3\$:	JSR	RO,I.CCLR	;GO ISSUE A CONTROLLER CLEAR
10257	047750	050000				10\$;ERROR RETURN
10258	047752	012762	000100	000000	5\$:	MOV	#IE,RKCS1(R2)	;SET INTERRUPT ENABLE
10259	047760	005726				TST	(SP)+	;ADJUST STACK
10260	047762	005720				TST	(RO)+	;ADJUST RO FOR NORMAL RETURN
10261	047764	000200				RTS	RO	;RETURN
10262								
10263	047766	052737	001000	003052	7\$:	BIS	#E.CERR,E.CONT	;SET CONTROLLER ERROR DURING
10264								; DRIVER SERVICING
10265	047774	004737	050512			JSR	PC,R.CONT	;REPORT ERROR
10266	050000	005726			10\$:	TST	(SP)+	;ADJUST STACK
10267	050002	011000				MOV	(RO),RO	;ADJUST RO FOR ERROR RETURN
10268	050004	000200				RTS	RO	;RETURN

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*STORE RK611 UNIBUS REGISTERS

SEQ 0194

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10286 050006 016237 000000 003000
10287 050014 016237 000010 003002
10288 050022 016237 000002 003004
10289 050030 016237 000004 003006
10290 050036 016237 000006 003010
10291 050044 016237 000012 003020
10292 050052 016237 000014 003016
10293 050060 016237 000016 003014
10294 050066 016237 000020 003012
10295 050074 016237 000026 003022
10296 050102 016237 000034 003024
10297 050110 016237 000036 003026
10298 050116 016237 000030 003030
10299 050124 016237 000032 003032
10300 050132 000207

.SBTTL *STORE RK611 UNIBUS REGISTERS

```
*****  
* THIS SUBROUTINE IS CALLED BY THE RK06 DRIVER TO STORE ALL  
* RK611 REGISTER IN TEMPORARY LOCATIONS.  
*CALL JSR PC,I.STOR  
* RETURN  
*  
* REGISTER USE  
* -----  
* R2 ADDRESS OF RK611 REGISTERS  
*****
```

```
I.STOR: MOV RKCS1(R2),T.CS1 ;STORE ALL CONTROLLER REGISTERS  
MOV RKCS2(R2),T.CS2 ; EXCEPT DATA BUFFER  
MOV RKWC(R2),T.WCR  
MOV RKBA(R2),T.BA  
MOV RKDA(R2),T.DA  
MOV RKDS(R2),T.DS  
MOV RKER(R2),T.ER  
MOV RKASOF(R2),T.ASOF  
MOV RKDCYL(R2),T.DC  
MOV RKMR1(R2),T.MR1  
MOV RKMR2(R2),T.MR2  
MOV RKMR3(R2),T.MR3  
MOV RKECPS(R2),T.POS  
MOV RKECPT(R2),T.PAT  
RTS PC ;RETURN
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.SBTTL *STORE CONTROLLER STATUS

THIS SUBROUTINE IS CALLED BY THE RK06 DRIVER AT PRIORITY 7.
 THE FOLLOWING REGISTERS WILL BE STORED:

- COMMAND AND STATUS REGISTER 2
- WORD COUNT REGISTER
- BUS ADDRESS REGISTER
- DESIRED TRACK AND SECTOR
- STATUS REGISTER
- ERROR REGISTER
- ATTENTION SUMMARY/OFFSET REGISTER
- CYLINDER ADDRESS REGISTER

*CALL JSR PC,I.CSTS
 RETURN

THIS ROUTINE ASSUMES THE FOLLOWING REGISTERS CONTAIN:

REGISTER	CONTENTS
-----	-----
R2	RK06 BASE ADDRESS
R5	ADDRESS OF PARAMETER BLOCK

```

10331 050134 042765 177741 000016 I.CSTS: BIC #177741,P.CS1(R5) ;CLEAR ALL BITS EXCEPT FUNCTION
10332                                     ;OF LAST COMMAND ISSUED
10333 050142 042737 000036 003000 BIC #36,T.CS1 ;CLEAR FUNCTION OF CS1 STATUS
10334 050150 053765 003000 000016 BIS T.CS1,P.CS1(R5) ;GENERATE CS1 STATUS INFORMATION
10335 050156 016265 000010 000020 I.CST1: MOV RKCS2(R2),P.CS2(R5) ;STORE COMMAND AND STATUS REGISTER 2
10336 050164 016265 000002 000022 MOV RKWC(R2),P.WCR(R5) ;STORE WORD COUNT REGISTER
10337 050172 016265 000004 000024 MOV RKBA(R2),P.BAR(R5) ;STORE BUS ADDRESS REGISTER
10338 050200 016265 000006 000026 MOV RKDA(R2),P.DTS(R5) ;STORE DESIRED TRACK AND SECTOR
10339 050206 016265 000012 000036 MOV RKDS(R2),P.DS(R5) ;STORE DRIVE STATUS REGISTER
10340 050214 016265 000014 000034 MOV RKER(R2),P.ER(R5) ;STORE ERROR REGISTER
10341 050222 016265 000016 000032 MOV RKASOF(R2),P.ASOF(R5) ;STORE ATTENTION SUMMARY AND
10342                                     ; OFFSET
10343 050230 016265 000020 000030 MOV RKDCYL(R2),P.DCYL(R5) ;STORE CYLINDER ADDRESS
10344 050236 000207 RTS PC ;RETURN
10345
  
```

.SBTTL *GATHER DRIVE STATUS

THIS SUBROUTINE WILL BE USED TO GATHER DRIVE STATUS
BYTE 01, 10, AND 11. IT IS ASSUMED THAT THE DRIVE
HAS PREVIOUSLY BEEN SEIZED. IT RUNS AT PRIORITY 7.

*CALL JSR RO,I.STAT
<ADDRESS OF ERROR RETURN>
RETURN

THIS ROUTINE ASSUMES THE FOLLOWING REGISTERS CONTAIN:

REGISTER	CONTENTS
-----	-----
R2	RK06 BASE ADDRESS
R5	ADDRESS OF PARAMETER BLOCK

ROUTINES USED:
I.ISSU

10372	050240	012762	000001	000026	I.STAT: MOV	#1,RKMR1(R2)	:LOAD MAINTENANCE REGISTER 1
10373							: FOR STATUS BYTE 01
10374	050246	112737	000001	003000	MOVB	#DR.SEL,T.CS1	:LOAD COMMAND
10375	050254	004037	047564		JSR	RO,I.ISSU	:GET STATUS BYTES 01
10376	050260	050450			3\$:ERROR RETURN
10377	050262	013765	003024	000044	MOV	T.MR2,P.A01(R5)	:STORE STATUS BYTE 01 MESS A
10378	050270	013765	003026	000046	MOV	T.MR3,P.B01(R5)	:STORE STATUS BYTE 01 MESS B
10379	050276	012762	000002	000026	MOV	#2,RKMR1(R2)	:LOAD MAINTENANCE REGISTER 1
10380							: FOR STATUS BYTE 10
10381	050304	112737	000001	003000	MOVB	#DR.SEL,T.CS1	:LOAD COMMAND
10382	050312	004037	047564		JSR	RO,I.ISSU	:GET STATUS BYTES 10
10383	050316	050450			3\$:ERROR RETURN
10384	050320	013765	003024	000050	MOV	T.MR2,P.A10(R5)	:STORE STATUS BYTE 10 MESS A
10385	050326	013765	003026	000052	MOV	T.MR3,P.B10(R5)	:STORE STATUS BYTE 10 MESS B
10386	050334	012762	000003	000026	MOV	#3,RKMR1(R2)	:LOAD MAINTENANCE REGISTER
10387							: FOR STATUS BYTE 11
10388	050342	112737	000001	003000	MOVB	#DR.SEL,T.CS1	:LOAD COMMAND
10389	050350	004037	047564		JSR	RO,I.ISSU	:GET STATUS BYTES 11
10390	050354	050450			3\$:ERROR RETURN
10391	050356	013765	003024	000054	MOV	T.MR2,P.A11(R5)	:STORE STATUS BYTE 11 MESS A
10392	050364	013765	003026	000056	MOV	T.MR3,P.B11(R5)	:STORE STATUS BYTE 11 MESS B
10393	050372	005062	000026		CLR	RKMR1(R2)	:LOAD MAINTENANCE REGISTER 1
10394							: FOR STATUS BYTE 00
10395	050376	112737	000001	003000	MOVB	#DR.SEL,T.CS1	:LOAD COMMAND
10396	050404	004037	047564		JSR	RO,I.ISSU	:GET STATUS BYTES 00
10397	050410	050450			3\$:ERROR RETURN
10398	050412	013765	003024	000040	MOV	T.MR2,P.A00(R5)	:STORE STATUS BYTE 00 MESS A
10399	050420	013765	003026	000042	MOV	T.MR3,P.B00(R5)	:STORE STATUS BYTE 00 MESS B
10400	050426	032737	001000	003026	BIT	#S.PAR,T.MR3	:CHECK IF BAD PARITY DETECTED BY DRIVE
10401	050434	001407			BEQ	5\$:NO, RETURN NORMALLY

10402	050436	052737	002000	003052		BIS	#E.DPAR,E.CON'	:INDICATE BAD PARITY DETECTED BY DRIVE
10403	050444	004737	050512			JSR	PC,R.CONT	:REPCRT ERROR
10404	050450	011000			3\$:	MOV	(R0),R0	:LOAD R0 FOR ERROR RETURN
10405	050452	000200				RTS	R0	:RETURN
10406								
10407	050454	052765	001000	000014	5\$:	BIS	#PBSVAL,P.PRST(R5)	:SET PARAMETER BLOCK STATUS VALID
10408	050462	005720				TST	(R0)+	:ADJUST R0 FOR NORMAL RETURN
10409	050464	000200				RTS	R0	:RETURN
10410								

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*COMMON DRIVER RETURNS

SEQ 0198

.SBTTL *COMMON DRIVER RETURNS

10411									
10412									
10413	050466	105037	003101	R.ABNL:	CLRB	INTMSK		:	INHIBIT FUTURE DRIVE INTERRUPT REPORTING
10414	050472	004777	132350		JSR	PC,@A.ABNL		:	INDICATE ABNORMAL RETURN
10415	050476	000207			RTS	PC		:	RETURN
10416									
10417	050500	105037	003101	R.NORM:	CLRB	INTMSK		:	INHIBIT FUTURE DRIVE INTERRUPT REPORTING
10418	050504	004777	132334		JSR	PC,@A.NORM		:	INDICATE NORMAL RETURN
10419	050510	000207			RTS	PC		:	RETURN
10420									
10421	050512	105037	003101	R.CONT:	CLRB	INTMSK		:	INHIBIT FUTURE DRIVE INTERRUPT REPORTING
10422	050516	105037	003100		CLRB	W.TIME		:	RESET WATCH DOG TIMING ON THIS DRIVE
10423	050522	005037	003114		CLR	W.DRV		:	CLEAR TIMING COUNT FOR THIS DRIVE
10424	050526	004777	132316		JSR	PC,@A.CONT		:	INDICATE CONTROLLER ERROR RETURN
10425	050532	000207			RTS	PC		:	RETURN

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.SBTTL *COMMAND INITATOR

```
*****  
* THIS SUBROUTINE WILL INITIATE ALL COMMANDS AS SPECIFIED  
* BY THE COMMAND FIELD OF THE PARAMETER BLOCK. THE FOLLOWING  
* SPECIAL COMMAND ARE ALSO EXECUTED:  
*  
* RELEASE  
* CONROLLER CLEAR  
* SUBSYSTEM CLEAR  
* READ ALL DRIVE STATUS  
* READ SPECIFIED HEADER  
*  
* THE ABOVE COMMANDS ARE TRANSLATED INTO A SEQUENCE OF COMMANDS  
*  
*CALL JSR PC,C.INIT  
* <ADDRESS OF PARAMETER BLOCK>  
* RETURN  
*  
* FOR THE SEQUENTIAL OPERATIONS, THE DRIVER WILL LOAD THE  
* LOCATIONS, PBLKT AND INTMSK.  
*  
* ROUTINES USED:  
* W.WTCH  
* I.CSTS  
* I.STAT  
* I.CCLR  
*****
```

```
C.INIT: MOV R5,-(SP) ;STORE R5 ON STACK  
MOV R4,-(SP) ;STORE R4 ON STACK  
MOV R3,-(SP) ;STORE R3 ON STACK  
MOV R2,-(SP) ;STORE R2 ON STACK  
MOV R1,-(SP) ;STORE R1 ON STACK  
MOV R0,-(SP) ;STORE R0 ON STACK  
MOV PS,-(SP) ;STORE PSW ON STACK  
MOV RKPRI,PS ;LOCK OUT RK06 INTERRUPTS  
MOV @16(SP),R5 ;STORE PARAMETER BLOCK ADDRESS  
ADD #2,16(SP) ;ADJUST RETURN  
MOV P.DRVN(R5),R4 ;STORE DRIVE NUMBER  
BIC #^C<DRVMSK>,R4 ;MASK OUT JUNK  
MOV R5,PBLKT ;LOAD PARAMETER BLOCK TABLE  
MOVB I.DRV(R4),INTMSK ;LOAD INTERRUPT MASK  
MOVB I.DRV(R4),W.TIME ;SET WATCH-DOG TIMER FLAG  
MOV W.SEC,W.DRV ;LOAD WATCH-DOG TIME  
  
MOV RKBAS,R2 ;LOAD R2 WITH RK06 ADDRESS BASE  
  
; RESET ALL BITS IN PROGRAM DEVICE STATUS REGISTER EXCEPT  
; DRIVE IN USE  
; WRITE FOR WRITE CHECK  
; NO CHECK  
; DROP DRIVE FROM TEST SEQUENCE  
; INHIBIT BUS ADDRESS INCREMENT
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10482 050636 042765 075176 000014      BIC      #*C<DRVUSE!W.WCK!NOCHK!DRPDRV!DTBAII>,P.PRST(R5)
10483
10484 050644 010500      MOV      R5,R0          ;STORE PARAMETER BLOCK ADDRESS
10485 050646 062700 000016      ADD      #P.CS1,R0      ;CALCULATE FIRST LOCATION TO BE CLEARED
10486 050652 010501      MOV      R5,R1          ;STORE PARAMETER BLOCK ADDRESS
10487 050654 062701 000062      ADD      #P.EPAT,R1     ;CALCULATE LAST LOCATION TO BE CLEARED
10488
10489 050660 005020      1$:     CLR      (R0)+          ;CLEAR RETURN PARAMETER
10490 050662 020001      CMP      R0,R1          ;CHECK IF FINISHED
10491 050664 101775      BLOS     1$             ;NO, CLEAR NEXT RETURN PARAMETER
10492 050666 105037 003074      CLRB    I.ISRL         ;CLEAR RELEASE OR INTERRUPT ISSUED
10493 050672 010465 000020      MOV      R4,P.CS2(R5)   ;STORE DRIVE NUMBER
10494 050676 005062 000026      CLR      RKMRI(R2)     ;CLEAR RK06 MAINTENANCE REGISTER 1
10495 050702 132765 000040 000001      BITB    #BITS,P.CMND(R5);CHECK IF SPECIAL COMMAND
10496 050710 001402      BEQ      3$             ;NO, PROCESS
10497 050712 000137 051426      JMP      C.SPEC         ;JUMP TO SPECIAL COMMAND PROCESSOR
10498
10499 050716 122765 000107 000001 3$:     CMPB    #UNLOAD,P.CMND(R5);CHECK IF POSITIONING COMMAND
10500                                     ; START SPINDLE
10501                                     ; RECALIBRATE
10502                                     ; OFFSET
10503                                     ; SEEK
10504                                     ; UNLOAD
10505
10506 050724 101174      BHI      25$           ;NO, DRIVE COMMAND
10507                                     ; SELECT DRIVE
10508                                     ; PACK ACKNOWLEDGE
10509                                     ; CLEAR
10510
10511 050726 122765 000117 000001      CMPB    #SEEK,P.CMND(R5);CHECK IF DATA TRANSFER
10512 050734 103540      BLO      20$           ;YES, DATA TRANSFER COMMAND
10513                                     ; READ DATA
10514                                     ; WRITE DATA
10515                                     ; READ HEADER
10516                                     ; WRITE HEADER
10517                                     ; WRITE CHECK
10518 050736 016562 000020 000010      MOV      P.CS2(R5),RKCS2(R2);LOAD DRIVE NUMBER
10519 050744 052765 000002 000014      BIS     #DRVPOS,P.PRST(R5);SET DRIVE POSITIONING
10520 050752 005037 003054      CLR      O.WAIT        ;CLEAR WAIT FOR COMMAND
10521 050756 122765 000117 000001      CMPB    #SEEK,P.CMND(R5);CHECK IF SEEK
10522 050764 001007      BNE      5$             ;NO, CHECK FOR OFFSET
10523 050766 016562 000002 000020      MOV      P.CYLN(R5),RKDCYL(R2);LOAD CYLINDER ADDRESS
10524 050774 016562 000004 000006      MOV      P.SECT(R5),RKDA(R2);LOAD SECTOR AND TRACK
10525 051002 000431      BR       8$             ;GO ISSUE COMMAND
10526
10527 051004 122765 000115 000001 5$:     CMPB    #OFFSET,P.CMND(R5);CHECK IF OFFSET
10528 051012 001007      BNE      6$             ;NO, CHECK FOR UNLOAD
10529 051014 116565 000006 000032      MOVB    P.OFST(R5),P.ASOF(R5);STORE OFFSET
10530 051022 016562 000032 000016      MOV      P.ASOF(R5),RKASOF(R2);LOAD OFFSET REGISTER
10531 051030 000416      BR       8$             ;GO ISSUE COMMAND
10532
10533 051032 122765 000111 000001 6$:     CMPB    #SRTSPL,P.CMND(R5);CHECK IF START SPINDLE
10534 051040 001003      BNE      7$             ;NO, CHECK IF RECAL
10535 051042 013737 003066 003114      MOV      W.MIN,W.DRV    ;LOAD WATCH DOG TIME FOR 1 MINUTE
10536 051050 122765 000113 000001 7$:     CMPB    #RECAL,P.CMND(R5);CHECK IF RECAL
10537 051056 001003      BNE      8$             ;NO, CONTINUE

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10538 051060 013737 003064 003114      MOV      W.BSEC,W.DRV      ;LOAD RECAL TIME FOR 8 SECONDS
10539 051066 116565 000007 000017 8$:      MOVVB   P.CS1H(R5),P.CS1+1(R5) ;STORE BITS 8-15 OF CS1
10540 051074 042765 165777 000016      BIC     #^C<CFMT!CDT>,P.CS1(R5) ;CLEAR ALL BITS EXCEPT FORMAT
10541                                     ; AND DRIVE TYPE
10542 051102 116565 000001 000016      MOVVB   P.CMND(R5),P.CS1(R5) ;MOVE COMMAND INTO CS1
10543 051110 042765 000200 000014      BIC     #W.WCK,P.PRST(R5) ;RESET WRITE FOR WRITE CHECK
10544 051116 032765 000400 000014      BIT     #NOCHK,P.PRST(R5) ;CHECK IN NO CHECK MODE
10545 051124 001533                                     BEQ     30$ ;NO, SKIP CLEAR OF INTERRUPT ENABLE
10546 051126 042765 000100 000016      BIC     #IE,P.CS1(R5) ;CLEAR INTERRUPT ENABLE
10547 051134 016562 000016 000000      MOV     P.CS1(R5),RKCS1(R2) ;ISSUE COMMAND
10548 051142 004737 045104 10$:      JSR     PC,W.WTCH ;CALL WATCH DOG TIMER
10549 051146 016237 000000 003000      MOV     RKCS1(R2),T.CS1 ;STORE COMMAND AND STATUS REGISTER 1
10550 051154 032737 000200 003000      BIT     #RDY,T.CS1 ;WAIT FOR READY
10551 051162 001767                                     BEQ     10$
10552 051164 032737 100000 003000      BIT     #CERR,T.CS1 ;CHECK FOR ERROR
10553 051172 001011                                     BNE     15$ ;YES, GIVE NORMAL RETURN
10554 051174 004737 045104 11$:      JSR     PC,W.WTCH ;CALL WATCH DOG TIMER
10555 051200 016237 000016 003014      MOV     RKASOF(R2),T.ASOF ;STORE ATTENTION SUMMARY
10556 051206 133737 003101 003015      BITB   INTMSK,T.ASOF+1 ;CHECK IF INTERRUPT HAS OCCURRED
10557 051214 001767                                     BEQ     11$ ;WAIT FOR DRIVE INTERRUPT
10558 051216 105037 003100 15$:      CLRB   W.TIME ;RESET TIMING ON THIS DRIVE
10559 051222 005037 003114      CLR     W.DRV ;CLEAR DRIVE TIMING COUNT
10560 051226 004737 050500      JSR     PC,R.NORM ;INDICATE COMMAND IS FINISHED
10561 051232 000137 052406      JMP     C.RTRN ;RESTORE REGISTERS
10562
10563 051236 016562 000010 000004 20$:      MOV     P.BALO(R5),RKBA(R2) ;LOAD BUS ADDRESS REGISTER
10564 051244 016562 000012 000002      MOV     P.WC(R5),RKWC(R2) ;LOAD WORD COUNT REGISTER
10565 051252 016562 000002 000020      MOV     P.CYLN(R5),RKDCYL(R2) ;LOAD CYLINDER ADDRESS REGISTER
10566 051260 016562 000004 000006      MOV     P.SECT(R5),RKDA(R2) ;LOAD SECTOR AND TRACK NUMBER
10567 051266 122765 000131 000001      CMPB   #WRTCHK,P.CMND(R5) ;CHECK IF WRITE CHECK COMMAND
10568 051274 001010                                     BNE     25$ ;NO, GO ISSUE THE COMMAND
10569 051276 032765 000200 000014      BIT     #W.WCK,P.PRST(R5) ;CHECK IF WRITE COMMAND SHOULD BE ISSUED
10570 051304 001404                                     BEQ     25$ ;NO, GO ISSUE THE COMMAND
10571 051306 012765 000123 000016      MOV     #WRDATA,P.CS1(R5) ;ISSUE WRITE COMMAND
10572 051314 000406      BR     26$ ;GO ISSUE COMMAND
10573
10574 051316 116565 000001 000016 25$:      MOVVB   P.CMND(R5),P.CS1(R5) ;MOVE COMMAND INTO CS1
10575 051324 042765 000200 000014      BIC     #W.WCK,P.PRST(R5) ;RESET WRITE FOR WRITE CHECK
10576 051332 116565 000007 000017 26$:      MOVVB   P.CS1H(R5),P.CS1+1(R5) ;STORE BITS 8-15 OF CS1
10577 051340 142765 177750 000017      BICB   #^C<B.CFMT!B.CDT!B.BA16!B.BA17>,P.CS1+1(R5) ;CLEAR ALL BITS EXCEPT
10578                                     ; FORMAT, DRIVE TYPE, AND BUS ADDRESS
10579                                     ; BITS 16-17
10580 051346 010537 003054      MOV     R5,O.WAIT ;LOAD WAITING FOR COMMAND
10581 051352 032765 100000 000014      BIT     #DTBA11,P.PRST(R5) ;CHECK IF INHIBIT BUS ADDRESS INCREMENT
10582 051360 001403                                     BEQ     27$ ;NO, LOAD CS2
10583 051362 052765 000020 000020      BIS     #BA1,P.CS2(R5) ;SET INHIBIT BUS ADDRESS INCREMENT
10584 051370 016562 000020 000010 27$:      MOV     P.CS2(R5),RKCS2(R2) ;LOAD CS2
10585 051376 032765 000400 000014      BIT     #NOCHK,P.PRST(R5) ;CHECK IN NO CHECK MODE
10586 051404 001403                                     BEQ     30$ ;NO, SKIP CLEAR OF INTERRUPT ENABLE
10587 051406 042765 000100 000016      BIC     #IE,P.CS1(R5) ;CLEAR INTERRUPT ENABLE
10588 051414 016562 000016 000000 30$:      MOV     P.CS1(R5),RKCS1(R2) ;ISSUE COMMAND
10589 051422 000137 052406      JMP     C.RTRN ;RESTORE REGISTERS
10590
10591                                     .SBTTL *SPECIAL COMMAND PROCESSING
10592
10593 051426 122765 000141 000001 C.SPEC: CMPB #RDSTAT,P.CMND(R5) ;CHECK IF READ DRIVE STATUS

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10594	051434	001132			BNE	10\$;NO, PROCESS OTHER COMMANDS	
10595	051436	016562	000020	000010	MOV	P.CS2(R5),RKCS2(R2)	;LOAD CS2 FOR COMMAND	
10596	051444	116565	000007	0C0017	MOVB	P.CS1H(R5),P.CS1+1(R5)	;STORE BITS 8-15 OF CS1	
10597	051452	042765	165777	000016	BIC	#*C<CFMT!CDT>,P.CS1(R5)	;CLEAR ALL BITS EXCEPT FORMAT	
10598							; AND DRIVE TYPE	
10599	051460	112765	000001	000016	MOVB	#DR.SEL,P.CS1(R5)	;STORE COMMAND	
10600	051466	016562	000016	000000	MOV	P.CS1(R5),RKCS1(R2)	;ISSUE COMMAND	
10601	051474	004737	045104		JSR	PC,W.WTCH	;CALL WATCH-DOG TIMER	
10602	051500	016265	000000	000C16	MOV	RKCS1(R2),P.CS1(R5)	;STORE COMMAND AND STATUS REG. 1	
10603	051506	032765	000200	000016	BIT	#RDY,P.CS1(R5)	;WAIT FOR READY	
10604	051514	001767			BEQ	2\$		
10605	051516	004737	050156		JSR	PC,I.CST1	;STORE CONTROLLER REGISTERS	
10606	051522	016265	000034	000040	MOV	RKMR2(R2),P.A00(R5)	;STORE STATUS PYTE 00 MESSAGE A	
10607	051530	016265	000036	000042	MOV	RKMR3(R2),P.B00(R5)	;STORE STATUS BYTE 00 MESSAGE B	
10608	051536	032765	100000	000016	BIT	#CERR,P.CS1(R5)	;CHECK IF CONTROLLER ERROR	
10609	051544	001436			BEQ	6\$;NO, GATHER DRIVE STATUS	
10610	051546	105037	003100		CLRB	W.TIME	;RESET WATCH DOG TIMING ON THIS DRIVE	
10611	051552	005037	003114		CLR	W.DRV	;CLEAR WATCH DOG COUNT	
10612	051556	032765	000400	000014	BIT	#NOCHK,P.PRST(R5)	;CHECK IF NO CHECK MODE	
10613	051564	001043			BNE	8\$;YES, INDICATE NORMAL RETURN	
10614	051566	032765	001000	000020	BIT	#MDS,P.CS2(R5)	;CHECK IF MULTIPLE DRIVE SELECT	
10615	051574	001043			BNE	9\$;YES, INDICATE CONTROLLER ERROR	
10616	051576	004037	047502		JSR	RO,I.CCLR	;CLEAR ERROR	
10617	051602	052406			C.RTRN		;ERROR RETURN	
10618	051604	032765	010400	0C0020	BIT	#NED!UFE,P.CS2(R5)	;CHECK IF NON-EXISTENT DRIVE OR UNIT FIELD ERROR	
10619	051612	001007			BNE	5\$;REPORT ERROR	
10620	051614	032765	000001	000036	BIT	#DRA,P.DS(R5)	;CHECK IF DRIVE AVAILIABLE	
10621	051622	001003			BNE	5\$;YES, REPORT ERROR	
10622	051624	052765	010000	000014	BIS	#DRVSZD,P.PRST(R5)	;INDICATE DRIVE IS SEIZED BY OTHER PORT	
10623	051632	004737	050466		JSR	PC,R.ABNL	;INDICATE ABNORMAL RETURN	
10624	051636	000137	052406		JMP	C.RTRN	;RESTORE REGISTERS	
10625								
10626	051642	004037	050240		JSR	RO,I.STAT	;GATHER DRIVE STATUS	
10627	051646	052406			C.RTRN		;ERROR RETURN	
10628	051650	105037	003100		CLRB	W.TIME	;STOP WATCH-DOG TIMING ON DRIVE	
10629	051654	005037	003114		CLR	W.DRV	;RESET WATCH-DOG TIME	
10630	051660	032765	000400	000014	BIT	#NOCHK,P.PRST(R5)	;CHECK IF NO CHECK MODE	
10631	051666	001402			BEQ	8\$;NO, REPORT ERROR	
10632	051670	005062	000000		CLR	RKCS1(R2)	;CLEAR INTERRUPT ENABLE	
10633	051674	004737	050500		JSR	PC,R.NORM	;REPORT COMMAND COMPLETE	
10634	051700	000137	052406		JMP	C.RTRN	;RESTORE REGISTERS	
10635								
10636	051704	052737	100090	003052	BIS	#E.MDS,E.CONT	;SET MULTIPLE DRIVE SELECT	
10637	051712	004737	050512		JSR	PC,R.CONT	;INDICATE CONTROLLER ERROR	
10638	051716	000137	052406		JMP	C.RTRN		
10639								
10640	051722	122765	000140	000001	10\$:	CMPB	#RELEAS,P.CMND(R5)	;CHECK IF RELEASE COMMAND
10641	051730	001040			BNE	13\$;NO, CHECK IF READ ALL HEADERS	
10642	051732	010537	003054		MOV	R5,O.WAIT	;STORE PARAMETER BLOCK ADDRESS IN	
10643							; WAIT FOR COMMAND	
10644	051736	052765	000010	000020	BIS	#RLS,P.CS2(R5)	;SET RELEASE BIT	
10645	051744	016562	000020	000010	MOV	P.CS2(R5),RKCS2(R2)	;LOAD CS2 FOR DESELECT	
10646	051752	112737	000001	003074	MOVB	#1,I.ISRL	;SET FLAG FOR RELEASE COMMAND	
10647	051760	116565	000007	000017	MOVB	P.CS1H(R5),P.CS1+1(R5)	;STORE BITS 8-15 OF CS1	
10648	051766	042765	165777	000016	BIC	#*C<CFMT!CDT>,P.CS1(R5)	;CLEAR ALL BITS EXCEPT FORMAT	
10649							; AND DRIVE TYPE	

10650	051774	112765	000101	000016		MOVB	#SELDRV,P.CS1(R5) ;STORE COMMAND
10651	052002	032765	000400	000014		BIT	#NOCHK,P.PRST(R5) ;CHECK IF NO CHECK MODE
10652	052010	001403				BEQ	11\$;NO, DO NOT RESET INTERRUPT ENABLE
10653	052012	042765	000100	000016		BIC	#IE,P.CS1(R5) ;RESET INTERRUPT ENABLE
10654	052020	016562	000016	000000	11\$:	MOV	P.CS1(R5),RKCS1(R2) ;ISSUE COMMAND
10655	052026	000137	052406			JMP	C.RTRN ;RESTORE REGISTERS
10656							
10657	052032	122765	000164	000001	13\$:	CMPB	#RDALHD,P.CMND(R5) ;CHECK IF READ ALL HEADERS
10658	052040	001053				BNE	30\$;NO, CHECK IF CONTROLLER CLEAR
10659	052042	010537	003054			MOV	R5,0.WAIT ;SET WAITING FOR COMMAND COMPLETION
10660	052046	016537	000010	003070		MOV	P.BALO(R5),HDR.AD ;LOAD HEADER ADDRESS
10661	052054	132765	000020	000007		BITB	#B.CFMT,P.CS1H(R5) ;CHECK IF 22 SECTOR FORMANT
10662	052062	001404				BEQ	14\$;YES, LOAD 22 IN HEADER COUNT
10663	052064	012737	000024	003072		MOV	#20.,HDR.CT ;LOAD 20 IN SECTOR COUNT
10664	052072	000403				BR	22\$;GO ISSUE READ HEADER COMMAND
10665							
10666	052074	012737	000026	003072	14\$:	MOV	#22.,HDR.CT ;LOAD 22 IN SECTOR COUNT
10667	052102	016562	000002	000020	22\$:	MOV	P.CYLN(R5),RKDCYL(R2) ;LOAD CYLINDER ADDRESS
10668	052110	016562	000004	000006		MOV	P.SECT(R5),RKDA(R2) ;LOAD TRACK NUMBER
10669	052116	016562	000020	000010		MOV	P.CS2(R5),RKCS2(R2) ;LOAD DRIVE NUMBER
10670	052124	116565	000007	000017		MOVB	P.CS1H(R5),P.CS1+1(R5) ;STORE BITS 8-15 OF CS1
10671	052132	042765	165777	000016		BIC	#*C<CFMT!CDT>,P.CS1(R5) ;CLEAR ALL BITS EXCEPT DRIVE TYPE
10672							; AND FORMAT
10673	052140	112765	000125	000016		MOVB	#RDHEAD,P.CS1(R5) ;STORE READ HEADER COMMAND
10674	052146	032765	000400	000014		BIT	#NOCHK,P.PRST(R5) ;CHECK IF NO CHECK MODE
10675	052154	001027				BNE	34\$;YES, INDICATE ILLEGAL DRIVER COMMAND
10676	052156	016562	000016	000000		MOV	P.CS1(R5),RKCS1(R2) ;ISSUE READ HEADER
10677	052164	000137	052406			JMP	C.RTRN ;RESTORE REGISTERS
10678							
10679	052170	122765	000176	000001	30\$:	CMPB	#CONCLR,P.CMND(R5) ;CHECK IF CONTROLLER CLEAR
10680	052176	001012				BNE	32\$;NO, CHECK IF SUBSYSTEM CLEAR
10681	052200	004037	047502			JSR	RO,I.CCLR ;CLEAR CONTROLLER
10682	052204	052406				C.RTRN	;ERROR RETURN
10683	052206	032765	000400	000014		BIT	#NOCHK,P.PRST(R5) ;CHECK IF NO CHECK MODE
10684	052214	001472				BEQ	40\$;NO, INDICATE NORMAL RETURN
10685	052216	005062	000000			CLR	RKCS1(R2) ;RESET INTERRUPT ENABLE
10686	052222	000467				BR	40\$;INDICATE NORMAL RETURN
10687							
10688	052224	122765	000177	000001	32\$:	CMPB	#SUBCLR,P.CMND(R5) ;CHECK IF SUBSYSTEM CLEAR
10689	052232	001406				BEQ	36\$;YES, CLEAR SUBSYSTEM
10690	052234	052737	000100	003052	34\$:	BIS	#E.ILLD,E.CONT ;SET ILLEGAL DRIVER COMMAND
10691	052242	004737	050512			JSR	PC,R.CONT ;REPORT ERROR
10692	052246	000457				BR	C.RTRN ;RESTORE REGISTERS
10693							
10694	052250	012762	000040	000010	36\$:	MOV	#SCLR,RKCS2(R2) ;ISSUE SUBSYSTEM CLEAR
10695	052256	016265	000000	000016		MOV	RKCS1(R2),P.CS1(R5) ;STORE COMMAND AND STATUS REGISTER 1
10696	052264	032765	100000	000016		BIT	#CERR,P.CS1(R5) ;CLEAR IF CONTROLLER ERROR RESET
10697	052272	001406				BEQ	37\$;NO, FINISH COMMAND
10698	052274	052737	000001	003052		BIS	#BITO,E.CONT ;SET CLEAR SUBSYSTEM DID NOT CLEAR
10699							; CONTROLLER ERROR
10700	052302	004737	050512			JSR	PC,R.CONT ;REPORT ERROR
10701	052306	000437				BR	C.RTRN ;RESTORE REGISTERS
10702							
10703	052310	013746	003060		37\$:	MOV	W.MILI,-(SP) ;LOAD 16 MILI-SECOND COUNT FOR ATTENTION
10704							; TO DISAPPEAR
10705	052314	016265	000000	000016	38\$:	MOV	RKCS1(R2),P.CS1(R5) ;STORE CS1

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10706 052322 032765 040000 000016 BIT #DI,P.CS1(R5) ;CHECK IF ATTENTIONS CLEARED
10707 052330 001411 BEQ 39$ ;YES, FINISH COMMAND
10708 052332 005316 DEC (SP) ;DECREMENT 16 MILISECOND COUNT
10709 052334 001367 BNE 38$ ;CHECK DRIVE INTERRUPT AGAIN
10710 052336 005726 TST (SP)+ ;ADJUST STACK
10711 052340 052737 000040 003052 BIS #E.SCLR,E.CONT ;SET SUBSYSTEM CLEAR DID NCT CLEAR
10712 ; DRIVE ATTENTIONS
10713 052346 004737 050512 JSR PC,R.CONT ;REPORT ERROR
10714 052352 000415 BR C.RTRN ;RESTORE REGISTER
10715
10716 052354 005726 39$: TST (SP)+ ;ADJUST STACK
10717 052356 032765 000400 000014 BIT #NOCWK,P.PRST(R5) ;CHECK IF NO CHECK MODE
10718 052364 001010 BNE C.RTRN ;YES, RESTORE REGISTERS
10719 052366 112737 177777 003074 MOVB #-1,I.ISRL ;SET INTERRUPT ENABLE SET
10720 052374 012762 000100 000000 MOV #IE,RKCS1(R2) ;SET INTERRUPT ENABLE
10721 052402 004737 050500 40$: JSR PC,R.NCRM ;INDICATE NORMAL TERMINATION
10722
10723 052406 012637 177776 C.RTRN: MOV (SP)+,PS ;RESTORE PSW
10724 052412 012600 MOV (SP)+,R0 ;RESTORE R0
10725 052414 012601 MOV (SP)+,R1 ;RESTORE R1
10726 052416 012602 MOV (SP)+,R2 ;RESTORE R2
10727 052420 012603 MOV (SP)+,R3 ;RESTORE R3
10728 052422 012604 MOV (SP)+,R4 ;RESTORE R4
10729 052424 012605 MOV (SP)+,R5 ;RESTORE R5
10730 052426 000207 RTS PC ;RETURN
10731
10732 .SBTTL OCTAL TO BINARY CONVERSION ROUTINE
10733
10734 ;*****
10735 ;* THIS ROUTINE WILL CHECK A STRING OF ASCII CHARACTERS TERMINATED
10736 ;* WITH A NULL <000> OR COMMA. IF THE CHARACTERS ARE LEGAL
10737 ;* IT WILL GENERATE TWO BINARY WORDS PLACING THE LOW 16 BITS
10738 ;* ON THE STACK AND THE HIGH 16 BITS IN LOCATION $HI0CT.
10739 ;*
10740 ;*CALL
10741 ;* MOV <ADDRESS OF ASCII STRING>,-(SP)
10742 ;* JSR PC,OCTBIN
10743 ;* <ADDRESS OF ERROR RETURN>
10744 ;* RETURN
10745 ;*
10746 ;*****
10747
10748 052430 010046 OCTBIN: MOV R0,-(SP) ;SAVE R0
10749 052432 010146 MOV R1,-(SP) ;SAVE R1
10750 052434 010246 MOV R2,-(SP) ;SAVE R2
10751 052436 016600 000010 MOV 10(SP),R0 ;GET ADDRESS OF ASCII STRING
10752 052442 005001 CLR R1 ;CLEAR DATA WORDS
10753 052444 005002 CLR R2
10754 052446 112046 2$: MOVB (R0)+,-(SP) ;PICK THIS CHARACTER
10755 052450 001423 BEQ 3$ ;IF ZERO GET OUT
10756 052452 121627 000054 CMPB (SP),#', ;CHECK IF COMMA
10757 052456 001420 BEQ 3$ ;IF COMMA GET OUT
10758 052460 122716 000060 CMPB #'0,(SP) ;MAKE SURE THIS CHARACTER IS
10759 052464 003030 BGT 4$ ; AN OCTAL DIGIT
10760 052466 122716 000067 CMPB #'7,(SP)
10761 052472 002425 BLT 4$
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10762 052474 006301      ASL    R1      ; *2
10763 052476 006102      ROL    R2
10764 052500 006301      ASL    R1      ; *4
10765 052502 006102      ROL    R2
10766 052504 006301      ASL    R1      ; *8
10767 052506 006102      ROL    R2
10768 052510 042716 177770  BIC    #^C7,(SP) ;STRIP THE ASCII JUNK
10769 052514 062601      ADD    (SP)+,R1 ;ADD THIS DIGIT
10770 052516 000753      BR     2$      ;LOOP
10771 052520 005726      3$:    TST    (SP)+ ;CLEAN PARTIAL FROM STACK
10772 052522 010166 000010  MOV    R1,10(SP) ;SAVE RESULT
10773 052526 010237 052562  MOV    R2,$HIOCT
10774 052532 012602      MOV    (SP)+,R2 ;RESTORE R2
10775 052534 012601      MOV    (SP)+,R1 ;RESTORE R1
10776 052536 012600      MOV    (SP)+,R0 ;RESTORE R0
10777 052540 062716 000002  ADD    #2,(SP) ;ADJUST RETURN
10778 052544 000207      RTS    PC      ;RETURN
10779
10780 052546 005726      4$:    TST    (SP)+ ;CLEAN UP PARTIAL FROM STACK
10781 052550 012602      MOV    (SP)+,R2 ;RESTORE R2
10782 052552 012601      MOV    (SP)+,R1 ;RESTORE R1
10783 052554 012600      MOV    (SP)+,R0 ;RESTORE R0
10784 052556 013616      MOV    @ (SP)+,(SP) ;PUT ADDRESS OF ERROR ROUTINE ON STACK
10785 052560 000207      RTS    PC      ;GO PROCESS ERROR
10786 052562 000000  $HIOCT: .WORD 0 ;HIGH ORDER BITS GO HERE
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10798 052564
10799 052564 010046
10800 052566 010146
10801 052570 010246
10802 052572 013700 052664
10803 052576 013701 052662
10804 052602 012702 177771
10805 052606 006300
10806 052610 006101
10807 052612 005202
10808 052614 001374
10809 052616 063700 052664
10810 052622 005501
10811 052624 063701 052662
10812 052630 062700 001057
10813 052634 005501
10814 052636 062701 047401
10815 052642 010037 052664
10816 052646 010137 052662
10817 052652 012602

*****
;THIS ROUTINE IS A DOUBLE PRECISION PSEUDO RANDOM NUMBER GENERATOR
;WITH A RANGE OF 0 TO 2(+33)-1.
;CALL:
;* JSR    PC,$RAND ;:CALL THE ROUTINE
;* RETURN ;:RETURN HERE THE RANDOM
;* ;:NUMBER WILL BE IN
;* ;:$HINUM,$LONUM

$RAND:
MOV    R0,-(SP) ;:PUSH R0 ON STACK
MOV    R1,-(SP) ;:PUSH R1 ON STACK
MOV    R2,-(SP) ;:PUSH R2 ON STACK
MOV    $LONUM,R0 ;:SET R0 WITH LOW
MOV    $HINUM,R1 ;:SET R1 WITH HIGH
MOV    #-7,+2 ;:SET SHIFT COUNT
1$:    ASL    R0 ;:SHIFT R0 LEFT AND
ROL    R1 ;:ROTATE CARRY INTO R1 AND
INC    R2 ;:CHECK FOR DONE
BNE    1$ ;:CONTINUE SHIFT LOOP
ADD    $LONUM,R0 ;:ADD NUMBER TO MAKE X 129
ADC    R1 ;:PROPOGATE CARRY
ADD    $HINUM,R1 ;:ADD NUMBER TO MAKE X 129
ADD    #1057,R0 ;:ADD LOW CONSTANT
ADC    R1 ;:PROPOGATE CARRY
ADD    #47401,R1 ;:ADD HIGH CONSTANT
MOV    R0,$LONUM ;:SAVE R0
MOV    R1,$HINUM ;:SAVE R1
MOV    (SP)+,R2 ;:POP STACK INTO R2
```


10930 053206 000207
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10945 053210 012746 000040
10946 053214 010446
10947 053216 010546
10948 053220 005466 000002
10949 053224 005416
10950 053226 005666 000002
10951 053232 061601
10952 053234 005500
10953 053236 066600 000002
10954 053242 103445
10955 053244 005046
10956 053246 006103
10957 053250 006102
10958 053252 006101
10959 053254 006100
10960 053256 005716
10961 053260 001410
10962 053262 005016
10963 053264 066601 000002
10964 053270 005500
10965 053272 005516
10966 053274 066600 000004
10967 053300 000404
10968 053302 060501
10969 053304 005500
10970 053306 005516
10971 053310 060400
10972 053312 005516
10973 053314 005716
10974 053316 001401
10975 053320 005203
10976 053322 005366 000006
10977 053326 003347
10978 053330 006003
10979 053332 103404
10980 053334 060501
10981 053336 005500
10982 053340 060400
10983 053342 000241
10984 053344 006103
10985 053346 062706 000010

RTS PC

```
*****  
:SBTTL DOUBLE-PRECISION DIVIDE SUBROUTINE  
:SUBROUTINE TO PERFORM DOUBLE-PRECISION INTEGER DIVISION  
:USES ALL REGISTERS (R0-R5)  
:  
:ENTER WITH JSR PC,M.DPID  
:DIVIDEND IN R0-R1-R2-R3  
:DIVISOR IN R4-R5  
:REMAINDER RETURNED IN R0-R1  
:QUOTIENT RETURNED IN R2-R3  
:*****
```

```
M.DPID: MOV #40,-(SP) ;COUNTER FOR DIVISION CYCLES  
MOV R4,-(SP) ;HI ORDER  
MOV R5,-(SP) ;LO ORDER DIVISOR TO THE STACK  
NEG 2(SP) ;FORM NEGATIVE  
NEG @SP ;VERSION OF THE DIVISOR  
SBC 2(SP)  
ADD @SP,R1  
ADC R0 ;PERFORM THE INITIAL SUBTRACTION  
ADD 2(SP),R0  
BCS M.DP50 ;IF CARRY THEN OVERFLOW HAS OCCURRED  
CLR -(SP) ;THIS IS A LONGER LASTING CARRY BIT  
M.DP40: ROL R3  
ROL R2  
ROL R1  
ROL R0  
TST @SP ;TEST 'CARRY INDICATOR'  
BEQ M.DP41 ;IF NO 'CARRY' THEN ADD ELSE SUBTRACT  
CLR @SP ;CLEAR UP FOR NEXT TIME  
ADD 2(SP),R1  
ADC R0 ;ADD -(DIVISOR)  
ADC @SP ;SET 'CARRY'  
ADD 4(SP),R0 ;<  
BR M.DP42  
M.DP41: ADD R5,R1  
ADC R0 ;ADD +(DIVISOR)  
ADC @SP ;SET 'CARRY'  
ADD R4,R0 ;<  
M.DP42: ADC @SP ;SET 'CARRY'  
TST @SP ;TEST THE UPDATE INDICATOR  
BEQ .+4 ;> ;IF ZERO FORGET IT  
INC R3 ;> ;NO CARRY POSSIBLE HERE  
DEC 6(SP) ;< ;DECREMENT COUNTER  
BGT M.DP40 ;BR IF MORE TO DO  
ROR R3  
BCS M.DP44  
ADD R5,R1  
ADC R0  
ADD R4,R0  
CLC  
M.DP44: ROL R3  
ADD #10,SP ;ADJUST STACK BY 4 WORDS
```



```
11154 054016 113704 054167      MOVB   $OMODE+1,R4      ;;GET THE NUMBER OF DIGITS TO TYPE
11155 054022 005404              NEG    R4
11156 054024 062704 000006      ADD    #6,R4           ;;SUBTRACT IT FOR MAX. ALLOWED
11157 054030 110437 054166      MOVB   R4,$OMODE      ;;SAVE IT FOR JSE
11158 054034 113704 054165      MOVB   $OFILL,R4      ;;GET THE ZERO FILL SWITCH
11159 054040 016605 000012      MOV    12(SP),R5      ;;PICKUP THE INPUT NUMBER
11160 054044 005003              CLR    R3             ;;CLEAR THE OUTPUT WORD
11161 054046 006105      1$:   ROL    R5         ;;ROTATE MSB INTO 'C'
11162 054050 000404              BR     3$            ;;GO DO MSB
11163 054052 006105      2$:   ROL    R5         ;;FORM THIS DIGIT
11164 054054 006105
11165 054056 006105
11166 054060 010503
11167 054062 006103      3$:   ROL    R3         ;;GET LSB OF THIS DIGIT
11168 054064 105337 054166      DECB   $OMODE        ;;TYPE THIS DIGIT?
11169 054070 100016              BPL    7$            ;;BR IF NO
11170 054072 042703 177770      BIC    #177770,R3    ;;GET RID OF JUNK
11171 054076 001002              BNE    4$            ;;TEST FOR 0
11172 054100 005704              TST    R4            ;;SUPPRESS THIS 0?
11173 054102 001403              BEQ    5$            ;;BR IF YES
11174 054104 005204      4$:   INC    R4         ;;DON'T SUPPRESS ANYMORE 0'S
11175 054106 052703 000060      BIS    #'0,R3        ;;MAKE THIS DIGIT ASCII
11176 054112 052703 000040      5$:   BIS    #' ,R3    ;;MAKE ASCII IF NOT ALREADY
11177 054116 110337 054162      MOVB   R3,8$         ;;SAVE FOR TYPING
11178 054122 104401 054162      TYPE   ,8$          ;;GO TYPE THIS DIGIT
11179 054126 105337 054164      7$:   DECB   $OCNT    ;;COUNT BY 1
11180 054132 003347              BGT    2$            ;;BR IF MORE TO DO
11181 054134 002402              BLT    6$            ;;BR IF DONE
11182 054136 005204              INC    R4            ;;INSURE LAST DIGIT ISN'T A BLANK
11183 054140 000744              BR     2$            ;;GO DO THE LAST DIGIT
11184 054142 012605      6$:   MOV    (SP)+,R5    ;;RESTORE R5
11185 054144 012604              MOV    (SP)+,R4      ;;RESTORE R4
11186 054146 012603              MOV    (SP)+,R3      ;;RESTORE R3
11187 054150 016666 000002 000004  MOV    2(SP),4(SP)    ;;SET THE STACK FOR RETURNING
11188 054156 012616              MOV    (SP)+,(SP)
11189 054160 000002              RTI                    ;;RETURN
11190 054162 000          8$:   .BYTE  0           ;;STORAGE FOR ASCII DIGIT
11191 054163 000              .BYTE  0           ;;TERMINATOR FOR TYPE ROUTINE
11192 054164 000      $OCNT: .BYTE  0           ;;OCTAL DIGIT COUNTER
11193 054165 000      $OFILL: .BYTE  0           ;;ZERO FILL SWITCH
11194 054166 000000      $OMODE: .WORD   0           ;;NUMBER OF DIGITS TO TYPE
11195
11196      .SBTTL CONVERT BINARY TO DECIMAL AND TYPE ROUTINE
11197
11198      ;*****
11199      ;*THIS ROUTINE IS USED TO CHANGE A 16-BIT BINARY NUMBER TO A 5-DIGIT
11200      ;*SIGNED DECIMAL (ASCII) NUMBER AND TYPE IT. DEPENDING ON WHETHER THE
11201      ;*NUMBER IS POSITIVE OR NEGATIVE A SPACE OR A MINUS SIGN WILL BE TYPED
11202      ;*BEFORE THE FIRST DIGIT OF THE NUMBER. LEADING ZEROS WILL ALWAYS BE
11203      ;*REPLACED WITH SPACES.
11204      ;*CALL:
11205      ;*      MOV    NUM,-(SP)      ;;PUT THE BINARY NUMBER ON THE STACK
11206      ;*      TYPDS                    ;;GO TO THE ROUTINE
11207      $TYPDS:
11208      MOV    R0,-(SP)      ;;PUSH R0 ON STACK
11209      MOV    R1,-(SP)      ;;PUSH R1 ON STACK
```



```

11266                ;*SAVE THE ERROR ITEM NUMBER AND THE ADDRESS OF THE ERROR CALL
11267                ;*AND GO TO TYPERR ON ERROR
11268                ;*THE SWITCH OPTIONS PROVIDED BY THIS ROUTINE ARE:
11269                ;*SW15=1      HALT ON ERROR
11270                ;*SW13=1      INHIBIT ERROR TYPEOUTS
11271                ;*SW10=1      BELL ON ERROR
11272                ;*SW09=1      LOOP ON ERROR
11273                ;*CALL
11274                ;*      ERROR      N      ;;ERROR=EMT AND N=ERROR ITEM NUMBER
11275
11276 054414           $ERROR:
11277 054414 105237 001103 7$:      INCB      $ERFLG      ;;SET THE ERROR FLAG
11278 054420 001775      BEQ      7$      ;;DON'T LET THE FLAG GO TO ZERO
11279 054422 013777 001102 124512  MOV      $TSTNM,@DISPLAY ;;DISPLAY TEST NUMBER AND ERROR FLAG
11280 054430 032777 002000 124502  BIT      #BIT10,@SWR      ;;BELL ON ERROR?
11281 054436 001402      BEQ      1$      ;;NO - SKIP
11282 054440 104401 001310      TYPE      ,SBELL      ;;RING BELL
11283 054444 005237 001112      1$:      INC      $ERTTL      ;;COUNT THE NUMBER OF ERRORS
11284 054450 011637 001116      MOV      (SP),$ERRPC      ;;GET ADDRESS OF ERROR INSTRUCTION
11285 054454 162737 000002 001116  SUB      #2,$ERRPC
11286 054462 117737 124430 001114  MOVB     @ $ERRPC,$ITEMB ;;STRIP AND SAVE THE ERROR ITEM CODE
11287 054470 032777 020000 124442  BIT      #BIT13,@SWR      ;;SKIP TYPEOUT IF SET
11288 054476 001004      BNE      20$      ;;SKIP TYPEOUTS
11289 054500 004737 040732      JSR      PC,TYPERR      ;;GO TO USER ERROR ROUTINE
11290 054504 104401 001315      TYPE      ,$CRLF
11291 054510
11292 054510 122737 000001 001340 20$:      CMPB     #APTENV,$ENV      ;;RUNNING IN APT MODE
11293 054516 001007      BNE      2$      ;;NO,SKIP APT ERROR REPORT
11294 054520 113737 001114 054532  MOVB     $ITEMB,21$      ;;SET ITEM NUMBER AS ERROR NUMBER
11295 054526 004737 055374      JSR      PC,$ATY4      ;;REPORT FATAL ERROR TO APT
11296 054532 000      21$:      .BYTE    0
11297 054533 000      .BYTE    0
11298 054534 000777      22$:      BR      22$      ;;APT ERROR LOOP
11299 054536 005777 124376 2$:      TST     @SWR      ;;HALT ON ERROR
11300 054542 100001      BPL     3$      ;;SKIP IF CONTINUE
11301 054544 000000      HALT      ;;HALT ON ERROR!
11302 054546 032777 001000 124364 3$:      BIT     #BIT09,@SWR      ;;LOOP ON ERROR SWITCH SET?
11303 054554 001402      BEQ     4$      ;;BR IF NO
11304 054556 013716 001110      MOV     $LPERR,(SP)      ;;FUDGE RETURN FOR LOOPING
11305 054562 005737 001306 4$:      TST     $ESCAPE      ;;CHECK FOR AN ESCAPE ADDRESS
11306 054566 001402      BEQ     5$      ;;BR IF NONE
11307 054570 013716 001306      MOV     $ESCAPE,(SP)      ;;FUDGE RETURN ADDRESS FOR ESCAPE
11308 054574
11309 054574 022737 025462 000042 5$:      CMP     #SENDAD,@#42      ;;ACT-11 AUTO-ACCEPT?
11310 054602 001001      BNE     6$      ;;BRANCH IF NO
11311 054604 000000      HALT      ;;YES
11312 054606
11313 054606 000002      6$:      RTI      ;;RETURN
11314                .SBTTL  TTY INPUT ROUTINE
11315
11316                ;*****
11317                .ENABL  LSB
11318
11319                .DSABL  LSB
11320
11321

```



```
11322 ::*****  
11323 :*THIS ROUTINE WILL INPUT A SINGLE CHARACTER FROM THE TTY  
11324 :*CALL:  
11325 :* RDCHR ;:INPUT A SINGLE CHARACTER FROM THE TTY  
11326 :* RETURN HERE ;:CHARACTER IS ON THE STACK  
11327 :* ;:WITH PARITY BIT STRIPPED OFF  
11328 :  
11329 :  
11330 054610 011646 $RDCHR: MOV (SP),-(SP) ;:PUSH DOWN THE PC  
11331 054612 016666 000004 000002 MOV 4(SP),2(SP) ;:SAVE THE PS  
11332 054620 105777 124320 1$: TSTB @TKS ;:WAIT FOR  
11333 054624 100375 BPL 1$ ;:A CHARACTER  
11334 054626 117766 124314 000004 MOVB @TKB,4(SP) ;:READ THE TTY  
11335 054634 042766 177600 000004 BIC #^C<177>,4(SP) ;:GET RID OF JUNK IF ANY  
11336 054642 026627 000004 000023 CMP 4(SP),#23 ;:IS IT A CONTROL-S?  
11337 054650 001013 BNE 3$ ;:BRANCH IF NO  
11338 054652 105777 124266 2$: TSTB @TKS ;:WAIT FOR A CHARACTER  
11339 054656 100375 BPL 2$ ;:LOOP UNTIL ITS THERE  
11340 054660 117746 124262 MOVB @TKB,-(SP) ;:GET CHARACTER  
11341 054664 042716 177600 BIC #^C<177>,(SP) ;:MAKE IT 7-BIT ASCII  
11342 054670 022627 000021 CMP (SP)+,#21 ;:IS IT A CONTROL-Q?  
11343 054674 001366 BNE 2$ ;:IF NOT DISCARD IT  
11344 054676 000750 BR 1$ ;:YES, RESUME  
11345 054700 026627 000004 000140 3$: CMP 4(SP),#140 ;:IS IT UPPER CASE?  
11346 054706 002407 BLT 4$ ;:BRANCH IF YES  
11347 054710 026627 000004 000175 CMP 4(SP),#175 ;:IS IT A SPECIAL CHAR?  
11348 054716 003003 BGT 4$ ;:BRANCH IF YES  
11349 054720 042766 000040 000004 BIC #40,4(SP) ;:MAKE IT UPPER CASE  
11350 054726 000002 4$: RTI ;:GO BACK TO USER  
11351 054730 052536 005015 000 $CNTLU: .ASCIZ /^U/<15><12> ;:CONTROL 'U'  
11352 054735 136 006507 000012 $CNTLG: .ASCIZ /^G/<15><12> ;:CONTROL 'G'  
11353 054742 005015 053523 020122 $MSWR: .ASCIZ <15><12>/SWR = /  
11354 054750 020075 000  
11355 054753 040 047040 053505 $MNEW: .ASCIZ / NEW = /  
11356 054760 036440 000040  
11357  
11358  
11359  
11360 .SBTTL POWER DOWN AND UP ROUTINES  
11361  
11362 ;POWER DOWN ROUTINE  
11363 054764 012737 054776 000024 $PWRDN: MOV #SPWRUP,PWRVEC ;:SET VECTOR FOR POWER UP  
11364 054772 000000 HALT ;:HANG UP  
11365 054774 000776 BR -2  
11366  
11367 ;POWER UP ROUTINE  
11368 054776 005037 055050 $PWRUP: CLR $PWRCT ;:WAIT LOOP FOR TTY TO COME UP  
11369 055002 005237 055050 4$: INC $PWRCT  
11370 055006 001375 BNE 4$  
11371 055010 012737 054764 000024 MOV #SPWRDN,PWRVEC ;:SET VECTOR FOR POWER DOWN  
11372 055016 012737 000340 000026 MOV #PR7,PWRVEC+2 ;:RE-ESTABLISH POWER AND TRAP PRIORITIES  
11373 055024 012737 000340 000036 MOV #PR7,TRAPVEC+2  
11374 055032 012706 001100 MOV #STACK,SP ;:RE-INITIALIZE THE STACK  
11375 055036 104401 055052 TYPE ,PWRMSG ;:TYPE 'POWER FAILED'  
11376 055042 000005 RESET ;:CLEAR THE UNIBUS  
11377 055044 000177 124036 JMP @SLPADR ;:RESTART THE CURRENT TEST
```



```
11434 055220 001404        BEQ      4$           ;;BR IF NO
11435 055222 013737 001110 001106 7$: MOV      $LPERR,$LPADR ;;SET LOOP ADDRESS TO LAST SCOPE
11436 055230 000443        BR           $OVER
11437 055232 105037 001103      4$: CLR     $ERFLG       ;;ZERO THE ERROR FLAG
11438 055236 005037 001304      CLR     $TIMES        ;;CLEAR THE NUMBER OF ITERATIONS TO MAKE
11439 055242 000412        BR           1$       ;;ESCAPE TO THE NEXT TEST
11440 055244 032777 004000 123666 3$: BIT     #BIT11,@SWR   ;;INHIBIT ITERATIONS?
11441 055252 001006        BNE     1$           ;;BR IF YES
11442 055254 005237 001104      INC     $ICNT         ;;INCREMENT ITERATION COUNT
11443 055260 023737 001304 001104      CMP     $TIMES,$ICNT  ;;CHECK THE NUMBER OF ITERATIONS MADE
11444 055266 002024        BGE     $OVER        ;;BR IF MORE ITERATION REQUIRED
11445 055270 012737 000001 001104 1$: MOV     #1,$ICNT    ;;REINITIALIZE THE ITERATION COUNTER
11446 055276 013737 055354 001304      MOV     $MXCNT,$TIMES ;;SET NUMBER OF ITERATIONS TO DO
11447 055304 105237 001102      $SVLAD: INCB     $TSTNM   ;;COUNT TEST NUMBERS
11448 055310 113737 001102 001324      MOV     $TSTNM,$TESTN ;;SET TEST NUMBER IN APT MAILBOX
11449 055316 011637 001106      MOV     (SP),$LPADR   ;;SAVE SCOPE LOOP ADDRESS
11450 055322 011637 001110      MOV     (SP),$LPERR   ;;SAVE ERROR LOOP ADDRESS
11451 055326 005037 001306      CLR     $ESCAPE      ;;CLEAR THE ESCAPE FROM ERROR ADDRESS
11452 055332 112737 000001 001115      MOV     #1,$ERMAX    ;;ONLY ALLOW ONE(1) ERROR ON NEXT TEST
11453 055340 013777 001102 123574 $OVER: MOV     $TSTNM,@DISPLAY ;;DISPLAY TEST NUMBER
11454 055346 013716 001106      MOV     $LPADR,(SP)  ;;FUDGE RETURN ADDRESS
11455 055352 000002        RTI           ;;FIXES PS
11456 055354 003720      $MXCNT: 2000.     ;;MAX. NUMBER OF ITERATIONS
11457                .SBTTL  APT COMMUNICATIONS ROUTINE
11458
11459                ;*****
11460 055356 112737 000001 055622 $ATY1: MOV     #1,$FFLG  ;;TO REPORT FATAL ERROR
11461 055364 112737 000001 055620 $ATY3: MOV     #1,$MFLG  ;;TO TYPE A MESSAGE
11462 055372 000403        BR           $ATYC
11463 055374 112737 000001 055622 $ATY4: MOV     #1,$FFLG  ;;TO ONLY REPORT FATAL ERROR
11464 055402  $ATYC:
11465 055402 010046      MOV     R0,-(SP)     ;;PUSH R0 ON STACK
11466 055404 010146      MOV     R1,-(SP)     ;;PUSH R1 ON STACK
11467 055406 105737 055620      TSTB   $MFLG        ;;SHOULD TYPE A MESSAGE?
11468 055412 001450      BEQ     5$           ;;IF NOT: BR
11469 055414 122737 000001 001340      CMPB   #APTENV,$ENV  ;;OPERATING UNDER APT?
11470 055422 001031      BNE     3$           ;;IF NOT: BR
11471 055424 132737 000100 001341      BITB   #APTSPOOL,$ENVM ;;SHOULD SPOOL MESSAGES?
11472 055432 001425      BEQ     3$           ;;IF NOT: BR
11473 055434 017600 000004      MOV     @4(SP),R0    ;;GET MESSAGE ADDR.
11474 055440 062766 000002 000004      ADD     #2,4(SP)     ;;BUMP RETURN ADDR.
11475 055446 005737 001320      1$: TST     $MSGTYPE    ;;SEE IF DONE W/ LAST XMISSION?
11476 055452 001375      BNE     1$           ;;IF NOT: WAIT
11477 055454 010037 001334      MOV     R0,$MSGAD   ;;PUT ADDR IN MAILBOX
11478 055460 105720      2$: TSTB   (R0)+       ;;FIND END OF MESSAGE
11479 055462 001376      BNE     2$
11480 055464 163700 001334      SUB     $MSGAD,R0    ;;SUB START OF MESSAGE
11481 055470 006200      ASR     R0           ;;GET MESSAGE LGTH IN WORDS
11482 055472 010037 001336      MOV     R0,$MSGGLT  ;;PUT LENGTH IN MAILBOX
11483 055476 012737 000004 001320      MOV     #4,$MSGTYPE  ;;TELL APT TO TAKE MSG.
11484 055504 000413      BR           5$
11485 055506 017637 000004 055532 3$: MOV     @4(SP),4$    ;;PUT MSG ADDR IN JSR LINKAGE
11486 055514 062766 000002 000004      ADD     #2,4(SP)     ;;BUMP RETURN ADDRESS
11487 055522 013746 177776      MOV     177776,-(SP) ;;PUSH 177776 ON STACK
11488 055526 004737 052666      JSR    PC,$TYPE     ;;CALL TYPE MACRO
11489 055532 000000      4$: .WORD   0
```

11490 055534
11491 055534 105737 055622
11492 055540 001416
11493 055542 005737 001340
11494 055546 001413
11495 055550 005737 001320
11496 055554 001375
11497 055556 017637 000004 001322
11498 055564 062766 000002 000004
11499 055572 005237 001320
11500 055576 105037 055622
11501 055602 105037 055621
11502 055606 105037 055620
11503 055612 012601
11504 055614 012600
11505 055616 000207
11506 055620 000
11507 055621 000
11508 055622 000
11509 055624
11510 000200
11511 000001
11512 000100
11513 000040
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11530 055624 010046
11531 055626 010146
11532 055630 010246
11533 055632 010346
11534 055634 013746 000004
11535 055640 013746 000006
11536 055644 010600
11537
11538 055646 104400
11539 055650 012637 000006
11540 055654 012701 003776
11541 055660 105727
11542 055662 000200
11543 055664 100062
11544 055666 012737 056024 000004
11545 055674 005737 177572

```
5$:  
10$: TSTB $FFLG ;; SHOULD REPORT FATAL ERROR?  
      BEQ 12$ ;; IF NOT: BR  
      TST $ENV ;; RUNNING UNDER APT?  
      BEQ 12$ ;; IF NOT: BR  
11$: TST $MSGTYPE ;; FINISHED LAST MESSAGE?  
      BNE 11$ ;; IF NOT: WAIT  
      MOV @4(SP), $FATAL ;; GET ERROR #  
      ADD #2, 4(SP) ;; BUMP RETURN ADDR.  
      INC $MSGTYPE ;; TELL APT TO TAKE ERROR  
12$: CLRB $FFLG ;; CLEAR FATAL FLAG  
      CLRB $LFLG ;; CLEAR LOG FLAG  
      CLRB $MFLG ;; CLEAR MESSAGE FLAG  
      MOV (SP)+, R1 ;; POP STACK INTO R1  
      MOV (SP)+, R0 ;; POP STACK INTO R0  
      RTS PC ;; RETURN  
$MFLG: .BYTE 0 ;; MESSG. FLAG  
$LFLG: .BYTE 0 ;; LOG FLAG  
$FFLG: .BYTE 0 ;; FATAL FLAG  
      .EVEN
```

```
APTSIZE=200  
APTENV=001  
APTSPOOL=100  
APTCSUP=040  
.SBTTL ROUTINE TO SIZE MEMORY
```

```
*****  
*CALL:  
* JSR PC, $SIZE  
* RETURN  
* $LSTAD WILL CONTAIN:  
* WITH KT11 OPTION -- LAST VIRTUAL ADDRESS OF THE LAST BANK  
* WITHOUT KT11 OPTION -- LAST ABSOLUTE ADDRESS OF AVAILABLE MEMORY  
* $LSTBK WILL CONTAIN THE LAST BANK AS A SAF  
* $KT11 IS THE MEMORY MANAGEMENT KEY  
* $BIT07 = 0 DON'T USE MEMORY MANAGEMENT  
* MUST BE SETUP BEFORE THE CALL  
* $BIT15 = 0 DON'T HAVE MEMORY MANAGEMENT OPTION  
* DETERMINED BY ROUTINE
```

```
$SIZE: MOV R0, -(SP) ;; SAVE R0 ON THE STACK  
      MOV R1, -(SP) ;; SAVE R1 ON THE STACK  
      MOV R2, -(SP) ;; SAVE R2 ON THE STACK  
      MOV R3, -(SP) ;; SAVE R3 ON THE STACK  
      MOV @#ERRVEC, -(SP) ;; SAVE PRESENT ERROR VECTOR PS & PC  
      MOV @#ERRVEC+2, -(SP)  
      MOV SP, R0 ;; SAVE THE STACK POINTER  
;; SET THE ERRVEC PS TO THE PRESENT PS  
TRAP ;; PUSH OLD PSW AND PC ON STACK  
      MOV (SP)+, @#ERRVEC+2 ;; SAVE THE PSW IN @#ERRVEC+2  
      MOV #3776, R1 ;; SETUP ADDRESS  
      TSTB (PC)+ ;; USE MEMORY MANAGEMENT?  
$KT11: .WORD 200 ;; SET TO USE MEMORY MANAGEMENT  
      BPL $SCORE ;; BR IF NO  
      MOV # $SKTNEX, @#ERRVEC ;; SET FOR TIMEOUT  
      TST @#SRO ;; KT11 ARE YOU THERE?
```


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TRAP TABLE

G 2

SEQ 0225

11882	060344	020122	047117	051440		
11883	060352	041505	020124	047516		
11884	060360	020124	044514	052123		
11885	060366	042105	041040	042101		
11886	060374	000				
11887	060375	124	046511	042105	EM103:	.ASCIZ /TIMED-OUT ON READ HDR/
11888	060402	047455	052125	047440		
11889	060410	020116	042522	042101		
11890	060416	044040	051104	000		
11891	060423	124	046511	042105	EM104:	.ASCIZ /TIMED-OUT ON SEEK/
11892	060430	047455	052125	047440		
11893	060436	020116	042523	045505		
11894	060444	000				
11895	060445	104	053122	051440	EM105:	.ASCIZ /DRV SIEZED BY OTHER PORT/
11896	060452	042511	042532	020104		
11897	060460	054502	047440	044124		
11898	060466	051105	050040	051117		
11899	060474	000124				
11900	060476	040504	040524	046440	EM106:	.ASCIZ /DATA MISCMPR WHILE BAI SET/
11901	060504	051511	046503	051120		
11902	060512	053440	044510	042514		
11903	060520	041040	044501	051440		
11904	060526	052105	000			
11905	060531	116	020117	042516	EM107:	.ASCIZ /NO NEM ERR WHEN REF'ING LOC 760000/
11906	060536	020115	051105	020122		
11907	060544	044127	047105	051040		
11908	060552	043105	044447	043516		
11909	060560	046040	041517	033440		
11910	060566	030066	030060	000060		
11911	060574	047111	051124	052120	EM110:	.ASCIZ /INTRPT WHEN CNTRLR NOT RDY/
11912	060602	053440	042510	020116		
11913	060610	047103	051124	051114		
11914	060616	047040	052117	051040		
11915	060624	054504	000			
11916	060627	116	020117	052101	EM111:	.ASCIZ /NO ATT'N ON SEEK/
11917	060634	023524	020116	047117		
11918	060642	051440	042505	000113		
11919	060650	051104	020126	054503	EM112:	.ASCIZ /DRV CYL INCORRECT/
11920	060656	020114	047111	047503		
11921	060664	051122	041505	000124		
11922	060672	041101	051117	026524	EM113:	.ASCIZ /ABORT- CAN'T READ BSF/
11923	060700	041440	047101	052047		
11924	060706	051040	040505	020104		
11925	060714	051502	000106			
11926	060720	052113	030461	043040	EM114:	.ASCIZ /KT11 FAILURE/
11927	060726	044501	052514	042522		
11928	060734	000				
11929	060735	115	046505	050040	EM115:	.ASCIZ /MEM PAR ERR/
11930	060742	051101	042440	051122		
11931	060750	000				
11932	060751	103	051125	042522	DH100:	.ASCIZ /CURRENT CMD :/
11933	060756	052116	041440	042115		
11934	060764	035040	000			
11935	060767	120	042522	020126	DH105:	.ASCIZ /PREV CMD :/
11936	060774	046503	020104	000072		
11937	061002	045522	030466	020061	DH200:	.ASCIZ /RK611 REGS :/

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Line	Code	Address	Count	Hex	Label	Description
12218	063102	007	000	.BYTE	7,0	
12219	063104	062263		.WORD	DH204	
12220	063106	004	000	.BYTE	4,0	
12221						
12222	063110	000006		DF13: .WORD	6	:FORMAT FOR 2ND LEVEL ERROR
12223	063112	000	000	.BYTE	0,0	:IN HEADER COMPARE ERROR
12224	063114	061122		.WORD	DH101	:AND 2ND LEVEL HEADER
12225	063116	007	000	.BYTE	7,0	:VRC ERROR
12226	063120	061211		.WORD	DH102	
12227	063122	002	000	.BYTE	2,0	
12228	063124	061514		.WORD	DH601	
12229	063126	000	000	.BYTE	0,0	
12230	063130	061735		.WORD	DH606	
12231	063132	003	000	.BYTE	3,0	
12232	063134	062321		.WORD	DH502	
12233	063136	000	000	.BYTE	0,0	
12234						
12235	063140	000006		DF14: .WORD	6	:FORMAT FOR 2ND LEVEL ERROR
12236	063142	000	000	.BYTE	0,0	:IN OPERATION INCOMPLETE ERROR
12237	063144	061122		.WORD	DH101	
12238	063146	007	000	.BYTE	7,0	
12239	063150	061211		.WORD	DH102	
12240	063152	002	000	.BYTE	2,0	
12241	063154	061514		.WORD	DH601	
12242	063156	000	000	.BYTE	0,0	
12243	063160	061735		.WORD	DH606	
12244	063162	003	000	.BYTE	3,0	
12245	063164	062321		.WORD	DH502	
12246	063166	000	000	.BYTE	0,0	
12247						
12248	063170	000011		DF15: .WORD	11	
12249	063172	000	000	.BYTE	0,0	
12250	063174	061122		.WORD	DH101	
12251	063176	007	000	.BYTE	7,0	
12252	063200	061211		.WORD	DH102	
12253	063202	002	000	.BYTE	2,0	
12254	063204	061002		.WORD	DH200	
12255	063206	000	000	.BYTE	0,0	
12256	063210	061314		.WORD	DH201	
12257	063212	007	000	.BYTE	7,0	
12258	063214	061403		.WORD	DH202	
12259	063216	002	000	.BYTE	2,0	
12260	063220	062360		.WORD	DH503	
12261	063222	000	000	.BYTE	0,0	
12262	063224	061050		.WORD	DH501	
12263	063226	000	000	.BYTE	0,0	
12264	063230	061420		.WORD	DH203	
12265	063232	010	000	.BYTE	10,0	
12266						
12267	063234	000011		DF16: .WORD	11	
12268	063236	000	000	.BYTE	0,0	
12269	063240	061122		.WORD	DH101	
12270	063242	007	000	.BYTE	7,0	
12271	063244	061211		.WORD	DH102	
12272	063246	002	000	.BYTE	2,0	
12273	063250	061002		.WORD	DH200	

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12274	063252	000	000		.BYTE	0,0
12275	063254	061314			.WORD	DH201
12276	063256	007	000		.BYTE	7,0
12277	063260	061403			.WORD	DH202
12278	063262	002	000		.BYTE	2,0
12279	063264	062321			.WORD	DH502
12280	063266	000	000		.BYTE	0,0
12281	063270	061050			.WORD	DH501
12282	063272	000	000		.BYTE	0,0
12283	063274	061420			.WORD	DH203
12284	063276	010	000		.BYTE	10,0
12285						
12286	063300	000005		DF17:	.WORD	5
12287	063302	000	000		.BYTE	0,0
12288	063304	061122			.WORD	DH101
12289	063306	007	000		.BYTE	7,0
12290	063310	061211			.WORD	DH102
12291	063312	002	000		.BYTE	2,0
12292	063314	062247			.WORD	DH711
12293	063316	000	000		.BYTE	0,0
12294	063320	062072			.WORD	DH702
12295	063322	002	000		.BYTE	2,0
12296						
12297	063324	000002		DF20:	.WORD	2
12298	063326	000	000		.BYTE	0,0
12299	063330	061252			.WORD	DH104
12300	063332	002	000		.BYTE	2,0
12301						
12302	063334	000002		DF21:	.WORD	2
12303	063336	000	000		.BYTE	0,0
12304	063340	061700			.WORD	DH6051
12305	063342	003	000		.BYTE	3,0
12306						
12307	063344	000006		DF22:	.WORD	6
12308	063346	000	000		.BYTE	0,0
12309	063350	060751			.WORD	DH100
12310	063352	000	000		.BYTE	0,0
12311	063354	061122			.WORD	DH101
12312	063356	007	000		.BYTE	7,0
12313	063360	061211			.WORD	DH102
12314	063362	002	000		.BYTE	2,0
12315	063364	061272			.WORD	DH106
12316	063366	000	000		.BYTE	0,0
12317	063370	061252			.WORD	DH104
12318	063372	002	000		.BYTE	2,0
12319						
12320	063374	000002		DF23:	.WORD	2
12321	063376	000	000		.BYTE	0,0
12322	063400	062431			.WORD	DH800
12323	063402	001	000		.BYTE	1,0
12324						
12325	063404	000001		DF24:	.WORD	1
12326	063406	002	000		.BYTE	2,0
12327						
12328	063410	000000		DF25:	.WORD	0
12329	063412	007	000		.BYTE	7,0

SW0	=	000001	1889#				
SW00	=	000001	1879#	1889			
SW01	=	000002	1878#	1888			
SW02	=	000004	1877#	1887			
SW03	=	000010	1876#	1886			
SW04	=	000020	1875#	1885			
SW05	=	000040	1874#	1884			
SW06	=	000100	1873#	1883			
SW07	=	000200	1872#	1882			
SW08	=	000400	1871#	1881			
SW09	=	001000	1870#	1880			
SW1	=	000002	1888#				
SW10	=	002000	1869#				
SW11	=	004000	1868#				
SW12	=	010000	1867#				
SW13	=	020000	1866#	8983			
SW14	=	040000	1865#				
SW15	=	100000	1864#				
SW2	=	000004	1887#				
SW3	=	000010	1886#				
SW4	=	000020	1885#				
SW5	=	000040	1884#				
SW6	=	000100	1883#				
SW7	=	000200	1882#				
SW8	=	000400	1881#				
SW9	=	001000	1880#				
S.ACLO	=	000100	3119#				
S.BRHM	=	000100	3134#				
S.BRKE	=	040000	3156#				
S.CART	=	000400	3136#				
S.DCLO	=	010000	3126#				
S.DIB	=	002000	3152#				
S.DOOR	=	000200	3135#				
S.DRA	=	000040	3105#				
S.DROT	=	020000	3127#				
S.DRY	=	000200	3107#	6996			
S.DSC	=	040000	3114#	9920	10068	10085	10148
S.FLT	=	000200	3120#	10063			
S.FORM	=	001000	3109#				
S.FWD	=	002000	3138#				
S.HDFL	=	000200	3149#				
S.HDHM	=	000040	3133#				
S.ICYL	=	000040	3118#				
S.ILF	=	000400	3121#				
S.LIMD	=	020000	3155#				
S.LOAD	=	010000	3140#				
S.MHD	=	000400	3150#				
S.NMOV	=	010000	3154#				
S.OFF	=	002000	3110#				
S.PAR	=	001000	3122#	9923	10400		
S.PIP	=	020000	3113#	9931	10126		
S.PLO	=	004000	3153#				
S.REV	=	004000	3139#				
S.RTZ	=	020000	3141#				
S.SECT	=	000020	3146#				
S.SKI	=	002000	3123#				

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CROSS REFERENCE TABLE -- USER SYMBOLS

SEQ 0257

SCM1 = 000040	2067#	2068#	2069#	2070#	2071#	2072#	2073#	2074#	2075#	2076#	2077#	2078#	2079#
	2080#	2081#	2082#	2083#	2084#	2085#	2086#	2087#	2088#	2089#	2090#	2091#	2092#
SCM2 = 000100	2067#	2068#	2069#	2070#	2071#	2072#	2073#	2074#	2075#	2076#	2077#	2078#	2079#
	2080#	2081#	2082#	2083#	2084#	2085#	2086#	2087#	2088#	2089#	2090#	2091#	2092#
	2093#	2094#	2095#	2096#	2097#	2098#	2099#						
SCM3 = 000040	2065#	2067#											
SCM4 = 000011	2099#	2100#	2101#	2102#	2103#	2104#	2105#	2106#	2107#	2108#			
SCNTLG 054735	11352#												
SCNTLU 054730	11351#												
SCORE 056032	11543	11570#											
SCPUOP 001346	2133#												
SCRLF 001315	2112#	4603	4672	4814	4998	5035	5847	6490	6571	6572	7176	7181	7421
	7481	7508	7907	7917	7922	7932	8586	8593	8607	9003	9013	9015	9023
	9026	9036	9059	9065	9073	10867	10902	11290	11314				
SCROUT 056062	11570	11577#											
SDBLK 054404	11219	11253	11261#										
SDB2D 053506	7971	7984	7997	11046#									
SDB2O 053366	6569	7234	7419	7506	11005#								
SDECVL 053666	11048	11094#											
SDEVCT 001330	2124#	5223*	6395*										
SDEVN 001376	2160#	4740											
SDOAGN 025472	6405	6414	6420#										
SDTBL 054374	11222	11257#											
SENDAD 025462	1998	6416#	11309										
SENCCT 025430	4462	6407#											
SENDMG 025501	6409	6424#											
SENULL 025476	6412	6423#											
SENV 001340	2129#	6427	10846	11292	11469	11493							
SEVM 001341	2130#	4485	4559	4741	6988	10848	10853	11471					
SEOP 025356	6264	6393#											
SEOPCT 025422	4462*	5073*	5217*	6404#	6408								
SERFLG 001103	2038#	11277*	11314	11390	11404	11429	11431	11437*	11457				
SERMAX 001115	2044#	4465*	4492*	7072*	11431	11452*	11457						
SERROR 054414	4456	11276#											
SERRPC 001116	2045#	11284*	11285*	11286	11314	12089							
SERRTB 001400	2177#	8994											
SERTTL 001112	2042#	11283*	11314										
SESCAP 001306	2109#	4464*	11305	11307	11314	11451*							
SETABL 001340	2128#												
SETEND 001400	2024	2161#											
SFATAL 001322	2121#	11497*											
SFFLG 055622	11460*	11463*	11491	11500*	11508#								
SFILLC 001156	2063#	10871	10902										
SFILLS 001155	2062#	10902											
\$GDADR 001120	2046#												
\$GDDAT 001124	2048#												
\$GET42 025452	6413#												
\$GTSWR= ***** U	11673												
\$HD = 000000	1794												
\$HIBTS 001000	2019#												
\$HINUM 052662	4523*	8052	10803	10811	10816*	10821#							
\$HIOCT 052562	5098	5178	6606	7335	10773*	10786#							
\$ICNT 001104	2039#	11442*	11443	11445*	11456								
\$INTAG 001135	2053#												
\$ITEMB 001114	2043#	8989	11286*	11294	11314								

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CROSS REFERENCE TABLE -- MACRO NAMES

SEQ 0263

.\$SUPR	1#	1775#	11095
.\$STRAP	1#	1775#	11636
.\$STYPB	1#		
.\$STYPD	1#	1775#	11195
.\$STYPE	1#	1775#	10823
.\$STYPO	1#	1775#	11118
.\$40CA	1#		
.\$1170	1#		

. ABS. 064424 000

ERRORS DETECTED: 0

DSKZ:CZR6ME.BIN,DSKZ:CZR6ME.LST/CRF/SOL/NL:TOC/EQ:QNEWSW=DSKZ:CZR6ME.SML,DRIV10.P11,CZR6ME.P11
RUN-TIME: 91 132 11 SECONDS
RUN-TIME RATIO: 494/236=2.0
CORE USED: 55K (109 PAGES)