

RK611
RK06, RK07

UNIBUS RK6 DR PT1
CZR6HF0

AH-9118F-MC
FICHE 1 OF 2

APR 1982
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CZR6HF0

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FICHE 2 OF 2

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IDENTIFICATION

PRODUCT CODE:	AC-9116F-MC
PRODUCT NAME:	CZR6HF0 UNIBUS RK6 DR PT1
DATE:	JANUARY 1982
MAINTAINER:	STORAGE SYSTEMS SOFTWARE TEST APPLICATIONS
AUTHOR:	B. T. LEBLANC

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REVISION HISTORY

REVISION	FIXES	DATE
CZR6HF0	IMPLEMENTED XXDP LOAD MEDIA OPTION	JAN 82

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AD SECTORS

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1.0 ABSTRACT

THIS PROGRAM PERFORMS PART 1 OF THE DRIVE DIAGNOSTICS TO INSURE THAT THE RK06 & RK07 DISK DRIVE IS CAPABLE OF PERFORMING ALL STATIC & CYCLE UP TESTS. IT INSURES THAT THE DRIVE CAN WRITE AND READ HEADERS IN BOTH 20 & 22 SECTOR FORMATS. FINALLY, IT INSURES THAT THE DISK CAN PERFORM SEEK OPERATIONS BY DOING SEVERAL SEEK PATTERNS. ERROR DETECTION LOGIC IS CHECKED BY SOFTWARE ERROR FORCING.

AFTER A SUCCESSFUL RUN (WITH NO ERRORS) OF PART 1, THE DRIVE IS READY FOR PART 2 OF THE DRIVE DIAGNOSTICS.

TESTING IS BASED ON A HIERARCHY APPROACH STARTING WITH BASIC LOGIC TESTS AND PROCEEDING THRU DYNAMIC TESTING. THE TESTS WILL BE KEPT SMALL TO FACILITATE SCOPING LOOPS.

*****CAUTION*****

HALTING THIS PROGRAM ANYWHERE BUT AT THE END OF A PASS, MAY LEAVE THE HEADERS IN THE DISK CARTRIDGE IN AN UNDETERMINED STATE.

2.0 REQUIREMENTS

2.1 HARDWARE

THE FOLLOWING HARDWARE IS REQUIRED TO RUN THE DISK DIAGNOSTIC:

PDP-11
CONSOLE TELETYPE
16K MEMORY
KW11-L OR KW11-P CLOCK
RK06 UNIBUS CONTROLLER (RK611)
1 TO 8 (TOTAL) RK06/RK07 DRIVES

NOTES: 1. IF NEITHER KW11-L OR P CLOCK IS USED, ALL TIMING TESTS WILL BE BYPASSED. A MSG AT THE BEGINNING OF THE TESTS WILL CONFIRM THIS.

2. THE PROGRAM CAN WORK OFF EITHER FORMATTED OR NON-FORMATTED PACKS.

2.2 PRELIMINARY TESTING & PROGRAMS

THE RK611 DISKLESS CONTROLLER DIAGNOSTICS (ALL PARTS) SHOULD FIRST RUN SUCCESSFU

3.0 PROGRAM CONSIDERATIONS

3.1 PDP-11 FAMILY COMPATIBILITY

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THIS PROGRAM CAN BE USED BY THE PDP-11/04,05,10,20,
34,35,40,45,50, & 70.

IT IS COMPATABLE WITH THE LSI-11 INSTRUCTION SET AND CAN TEST
THE RK06 ONLY IF THE DRIVE CONTROLLER FOR THE LSI-11 IS
DESIGNED TO BE DIAGNOSTICALLY COMPATABLE WITH THE RK611.

3.2 XXDP

THIS PROGRAM CAN BE CHAINED BY XXDP & WILL NOT OVERLAY THE
LOADER.

CHAIN MODE OPERATION (MONITOR)

1. THE INPUT DIALOGUE IS BYPASSED.
2. THE BUSS ADDRESS & CONTROLLER INTERRUPT VECTOR IS
DEFAULTED.
3. DRIVE 0 WILL NOT BE TESTED.
4. ALL OTHER DRIVES IN THE 'DRIVE PRESENT' CONDITION WILL
BE TESTED.

NOTE: THE DRIVE PRESENT CONDITION IS:

- A. HEADS MANUALLY LOADED
- B. CORRECT PORT SELECTED
- C. WRITE LOCK DISABLED
- D. DRIVE READY INDICATOR ON

DUMP MODE OPERATION (MANUAL)

1. INPUT DIALOGUE IF STARTED FROM 220.
2. DRIVE 0 CAN BE TESTED, BUT THE OPERATOR IS FIRST GIVEN
A MSG TO REPLACE THE PACK IN DRO WITH A SCRATCH
PACK & TYPE <CR> WHEN DONE.

3.3 ACT/APT

THIS PROGRAM IS ACT COMPATIBLE. IT IS APT
COMPATIBLE TO THE EXTENT THAT APT HOOKS WILL BE IN THE
PROGRAM & WILL WORK THRU THE 'UPTON INTERFACE'.

FOR OTHER INTERFACES, APT MAY ONLY LOAD & START THE PROGRAM.
I.E. LOAD & DUMP MODE.

AUTOMATIC MODE (MONITOR)

1. THE INPUT DIALOGUE IS BYPASSED.
2. THE BUSS ADDRESS & CONTROLLER INTERRUPT VECTOR IS
DEFAULTED.
3. ALL DRIVES IN THE 'DRIVE PRESENT' CONDITION WILL BE
TESTED.

NOTE: THE DRIVE PRESENT CONDITION IS:

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- A. HEADS MANUALLY LOADED
- B. CORRECT PORT SELECTED
- C. WRITE LOCK DISABLED
- D. DRIVE READY INDICATOR ON

DUMP MODE (MANUAL): INPUT DIALOGUE IF STARTED FROM 220.

3.3.1 APT ETABLE DEFINITIONS

THE FOLLOWING DEFINITIONS ARE VALID FOR SPECIFYING APT ENVIRONMENTAL TABLE (ETABLE) ENTRIES, VIA RUNNING THE APT UTILITY PROGRAM 'TSP':

1. SOFTWARE ENVIRONMENT:
 - =1 IF APT SCRIPT MODE
 - =0 IF STANDALONE MODE
2. ENVIRONMENT MODE:
 - BIT 7 = 1 ETABLE DOES SIZING
 - = 0 PROGRAM DOES SIZING
 - BIT 6 = 1 SPOOL MSGS TO APT IF SCRIPT MODE
 - = 0 DON'T SPOOL TO APT
 - BIT 5 = 1 SUPPRESS CONSOLE OUTPUT
 - = 0 ALLOW CONSOLE OUTPUT
 - BITS 4-0 NOT USED
3. SWITCH 1 (SOFTWARE SWITCH REGISTER)
IF ENVIRONMENT MODE BIT 7 (SIZING BIT) IS SET TO 1, THE SOFTWARE SWITCH REGISTER WILL BE USED, INSTEAD OF THE HARDWARE CONSOLE SWITCH REGISTER. REGARDLESS OF WHICH ONE IS USED, ALL BITS DEFINED IN SECTIONS 4.3 & 4.4 (SWITCH REGISTER OPTIONS) MAY USED WHEN RUNNING IN STANDALONE MODE. IN APT SCRIPT MODE, HOWEVER, BIT 14 (LOOP ON TEST) MUST ALWAYS BE SET TO 0.
4. SWITCH 2 (USER SWITCH REGISTER)
NOT USED
5. CPU OPTIONS:
NOT USED
6. MEMORY TYPES 1-4 AND MAX MEMORY ADDRESSES
NOT USED
7. INTERRUPT VECTOR 1:
USED WHEN ENVIRONMENT MODE BIT 7 = 1. DEFAULT = 210
8. BUS PRIORITY 1:
USED WHEN ENVIRONMENT MODE BIT 7 = 1. DEFAULT = 5
9. INTERRUPT VECTOR 2:
NOT USED
10. BUS PRIORITY 2:
NOT USED

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11. BASE ADDRESS:
USED WHEN ENVIRONMENT MODE BIT 7 = 1. DEFAULT = 177440

12. DEVICE MAP:
USED WHEN ENVIRONMENT MODE BIT 7 = 1. EACH BIT
SET TO 1 IN BITS 0-7 WILL SELECT THE CORRESPONDING
DRIVE TO BE TESTED. BITS 8-15 ARE NOT USED.

13. CONTROLLER DESCRIPTOR WORDS:
NOT USED

14. DEVICE DESCRIPTOR CODES (IN WORDS):
NOT USED

3.4 DUAL ACCESS

THIS PROGRAM WILL NOT TEST OR SUPPORT DUAL-ACCESS. A DRIVE
EQUIPED WITH DUAL ACCESS MUST BE SWITCHED TO THE PORT UNDER
TEST TO PREVENT CONTENTION WITH THE OTHER PORT.

DUAL ACCESS TESTS WILL BE INCORPORATED IN A SEPARATE PROGRAM
AT A LATER DATE.

3.5 MEMORY MANAGEMENT

MEMORY MANAGEMENT NOT USED

3.6 PARITY CHECK ENABLED

IF THE MEMORY PARITY CHECK OPTION IS AVAILABLE ON THE SYSTEM,
THE PROGRAM WILL RUN WITH MEMORY CHECK ENABLED.

3.7 BAD SECTOR

THE PROGRAM WILL COMPARE DATA ERRORS WITH THE BAD SECTOR
INFORMATION CONTAINED ON CYL 410, HEAD 2. PRINTOUTS
OF DATA ERRORS DUE TO BAD SECTORS/TRACKS WILL BE MASKED OUT.

3.8 EXECUTION TIME

THE EXECUTION TIMES SHOWN BELOW ARE BASED ON THE PDP 11/50.

TOTAL TIME: 5 MIN, 30 SEC

A BREAKDOWN OF THE MORE LENGTHY TESTS ARE SHOWN BELOW:

TEST 16 STATIC CYL ADDRESS & DIFF REGS-PART 2: 2 MIN, 15 SEC
TEST 34 FORMAT PACK : 1 MIN
TEST 37 SEEK FROM CYL 0 TO ALL CYLS : 40 SEC
TEST 40 SEEK FROM CYL 410 TO ALL CYLS : 40 SEC

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THE ABOVE TIMES ARE APPROX. DOUBLED FOR THE RK07.

3.9 FAULT ISOLATION

TO BE DETERMINED.

3.10 ERROR CORRECTION AND FAILURE RATE ANALYSIS

THIS PROGRAM WILL NOT DO ERROR CORRECTION OR FAILURE RATE ANALYSIS.

3.11 DEFAULT UNIBUS ADDRESSES & VECTORS

THE FOLLOWING IS A LIST OF ALL DEFAULT ADDRESSES & VECTORS OF ALL HARDWARE TO BE USED & THEIR MEMORY ADDRESSES WHERE THEY CAN BE CHANGED.

	LOCATION	DEFAULT CONTENTS
RK06 BUSS ADDRESS	1264	177440
CONTROLLER INTERRUPT VECTOR	1314	210
CONTROLLER PRIORITY	1316	240
P-CLOCK STATUS REG	1320	172540
P-CLOCK SET BUFFER	1322	172542
P-CLOCK READ BUFFER	1324	172544
L-CLOCK STATUS REG	1326	177546
L-CLOCK INTERRUPT VECTOR	1330	100
P-CLOCK INTERRUPT VECTOR	1332	104
TTY KB STATUS REG	1144	177560
TTY KB BUFFER	1146	177562
TTY PRINTER STATUS REG	1150	177564
TTY PRINTER BUFFER	1152	177566

4.0 OPERATING PROCEDURE & CONTROL FUNCTIONS

4.1 PROGRAM LOADING

THE PROGRAM CAN BE LOADED FROM PAPER TAPE USING STANDARD PROCEDURE FOR ABSOLUTE LOADER TAPES; OR FROM ANY MEDIA SUPPORTED BY XXDP.

4.1.1 LOAD THE STARTING ADDRESS (SEE SEC 4.2).

4.1.2 SET SWITCH REGISTERS AS DESIRED (SEE SEC 4.3).

4.1.3 SET DRIVES TO BE TESTED IN THE 'LOAD' CONDITION & WITH THE APPROPRIATE PORT SELECTED & WRITE LOCK DISABLED. DRIVES NOT TO BE TESTED MUST HAVE BOTH PORTS DESELECTED.

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NOTE: THE DRIVE WILL NOT RESPOND TO THE 'START SPINDLE'
CMD IF THE RUN/STOP SWITCH IS IN THE 'STOP'
POSITION.

4.1.4 PRESS 'START'

THE PROGRAM WILL IDENTIFY ITSELF AND WILL BEGIN A
DIALOGUE WITH THE OPERATOR TO DETERMINE DRIVES TO BE TESTED
(SEE SEC 4.5).

THE PROGRAM BEGINS TESTING ONLY THOSE DRIVES SPECIFIED BY
THE INPUT DIALOGUE. IF A SPECIFIED DRIVE CANNOT BE FOUND BY
THE PROGRAM IT WILL BE FLAGGED AS AN ERROR THAT THE DRIVE
WAS NOT AVAILABLE. THEN BEGINNING WITH THE LOWEST NUMERICAL
DRIVE AND PROCEEDING IN SEQUENTIAL ORDER, ALL VALID DRIVES
WILL BE TESTED. ONE PASS THROUGH THE TEST SEQUENCE WILL BE
PERFORMED ON EACH DRIVE BEFORE MOVING TO THE NEXT DRIVE
IN SEQUENCE. THE DRIVE TO BE TESTED WILL BE TYPED AT THE
BEGINNING OF EACH PASS. 'END OF PASS' WILL BE TYPED AFTER
TESTING ALL DRIVES.

4.2 STARTING LOCATIONS

LOCATION 200 - STARTING ADDRESS TO DEFAULT THE BUSS
ADDRESS & THE CONTROLLER INTERRUPT VECTOR
& TEST ALL DRIVES IN THE 'DRIVE PRESENT'
CONDITION.

NOTE: THE DRIVE PRESENT CONDITION IS:

- A. HEADS MANUALLY LOADED
- B. CORRECT PORT SELECTED
- C. WRITE LOCK DISABLED
- D. DRIVE READY INDICATOR ON

LOCATION 204 - SAME AS 200 START BUT BYPASS TEST 16 (N SQUARE)

LOCATION 220 - STARTING ADDRESS TO INPUT TESTING PARAMETERS
VIA THE INPUT DIALOGUE. BUSS ADDRESS &
CONT. INTERRUPT VECTOR INPUTTED ONLY ON
1ST PASS.

LOCATION 230 - SAME AS 220 START BUT BYPASS TEST 16 (N SQUARE)

LOCATION 260 - RUN MODULE TEST ...DEFAULT MODE ONLY.
THIS SKIPS OVER THE FOLLOWING TESTS:

- 1. TEST 35 FORMAT PACK
- 2. TEST 36 DECREMENT FROM CYL 410 TO 0 & READ HEADERS
- 3. TEST 40 SEEK FROM CYL 0 TO ALL
- 4. TEST 41 SEEK FROM CYL 410 TO ALL

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THE PURPOSE OF BYPASSING IS TO PROVIDE
A QUICK MODULE TEST

LOCATION 270 - SAME AS 260 START BUT BYPASS TEST 16 ALSO.

IMPORTANT: FOR VARIATIONS OF THE ABOVE, SEE XXDP, ACT/APT
CONSIDERATIONS IN SECTIONS 3.2 & 3.3.

ALSO, THE PROGRAM WILL DETERMINE WHETHER
THE DRIVE TO BE TESTED IS AN RK06 OR RK07.
OPERATOR INPUTS ARE NOT REG'D.

4.3 SWITCH REGISTER

THE SWITCHES ARE USED TO PROVIDE CONTROL FUNCTIONS.

SWITCH	FUNCTION
-----	-----
15	HALT ON ERROR
14	LOOP ON TEST
13	INHIBIT ERROR TYPEOUT
12	BYPASS DRIVE AFTER 20 ERRORS
11	INHIBIT ITERATION
10	BELL ON ERROR
9	LOOP ON ERROR
8	LOOP ON TEST IN SW<07:00>

4.3.1 SW<15>

THE PROGRAM HALTS ON ENCOUNTERING AN ERROR, AFTER TYPING OUT
THE ERROR MSG AND PERTINENT INFORMATION.
PRESSING "CONTINUE" CONTINUES OPERATION OF THE PROGRAM.

4.3.2 SW<14>

THE PROGRAM LOOPS ON THE TEST THAT IS BEING EXECUTED WHEN
THE SWITCH IS PUT ON. THIS SWITCH IS NORMALLY USED ALONG
WITH SW15.

4.3.3 SW<13>

THIS SWITCH INHIBITS ALL ERROR MSGS. NORMALLY USED WHEN
LOOPING ON TEST (SW14) OR LOOPING ON ERROR (SW9).
WITH SWITCH <13> SET, SWITCH <15> SHOULD NOT BE SET.

4.3.4 SW<12>

THIS SWITCH BYPASSES A GIVEN DRIVE AFTER 20 ERRORS HAVE

489 BEEN DETECTED.
490
491
492 4.3.5 SW<11>
493
494 EACH TEST WILL BE EXECUTED ONLY ONCE. NORMALLY AFTER THE
495 FIRST PASS, EACH SUBTEST IS ITERATED A NUMBER OF TIMES
496 (USUALLY 50, 5 IN SOME CASES). SETTING THIS SWITCH INHIBITS
497 ITERATIONS, SO THAT QUICK PASSES CAN BE MADE.
498
499
500 4.3.6 SW<10>
501
502 RINGS A BELL ON ERROR. USEFUL WHEN ERROR TYPEOUT IS INHIBITED.
503
504
505 4.3.7 SW<09>
506
507 THIS SWITCH PROVIDES THE TIGHTEST POSSIBLE SCOPE LOOP FOR
508 ERRORS. IF THE PROGRAM DETECTS AN ERROR, IT WILL LOOP BACK
509 TO THE BEGINNING OF TEST.
510
511
512 4.3.8 SW<08>
513
514 THIS SWITCH IS USED TO SELECT A PARTICULAR TEST (AS PER
515 SW<00-7>) FOR EXECUTION AND SUBSEQUENT LOOPING. THUS IF
516 TEST 15 IS TO BE SELECTED THE SWITCH SETTING WOULD BE
517 000415. IT SHOULD BE NOTED THAT BEFORE SELECTING & LOOPING TEST 15,
518 ALL THE PREVIOUS TESTS (1-14) WILL BE EXECUTED.
519
520
521 4.4 'SOFTWARE' SWITCH REGISTER
522
523 IF THE PROGRAM IS BEING RUN ON A SWITCHLESS PROCESSOR (I.E. AN 11/04 OR 11/34)
524 THE PROGRAM WILL DETERMINE THAT THE HARDWARE SWITCH REGISTER IS
525 NOT PRESENT AND WILL USE A 'SOFTWARE' SWITCH REGISTER. THE
526 'SOFTWARE' SWITCH REGISTER IS LOCATED AT LOCATION 176 (8). THE
527 SETTINGS OF THE "SOFTWARE" SWITCHES ARE CONTROLLED THROUGH
528 A KEYBOARD ROUTINE WHICH IS CALLED BY TYPING A 'CONTROL G'.
529 THE PROGRAM WILL RECOGNIZE THE 'CONTROL G' AT ANY TIME EXCEPT
530 WHEN THE PROGRAM IS AT A HIGHER PRIORITY PROCESSING AN RK06 INTERRUPT.
531 THE 'SOFTWARE' SWITCH VALUES ARE ENTERED AS AN OCTAL NUMBER
532 IN RESPONSE TO THE PROMPT FROM THE SWITCH ENTRY ROUTINE:
533
534 SWR = NNNNNN NEW =
535
536 EACH TIME SWITCH SETTING ARE ENTERED, THE ENTIRE SWITCH
537 REGISTER IMAGE MUST BE ENTERED. LEADING ZEROS ARE NOT
538 REQUIRED., 'RUBOUT' AND 'CONTROL U' FUNCTIONS MAY BE USED TO
539 CORRECT TYPING ERRORS DURING SWITCH ENTRY.
540
541 ON PROCESSORS WITH HARDWARE SWITCH REGISTERS, THE 'SOFTWARE'
542 SWITCH REGISTER MAY BE USED. IF THE PROGRAM FINDS ALL 16
543 SWITCHES IN THE 'UP' POSITION, ALL SWITCH REGISTER REFERENCES
544 WILL BE TO THE 'SOFTWARE' REGISTER AND THE PROCEDURES DESCRIBED

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ABOVE MUST BE FOLLOWED.

4.5 INPUT DIALOGUE

THE DIALOGUE WILL BE DONE INTERACTIVELY. THE PROGRAM WILL REQUEST A PARAMETER BY CONSOLE TYPEOUT. THE PARAMETER MAY THEN BE ENTERED AS SPECIFIED BELOW OR ALLOWED TO DEFAULT BY A CARRIAGE RETURN. UNRECOGNIZED OR ILLEGAL RESPONSES WILL BE ECHOED BACK FOLLOWED BY "?". THE PROPER RESPONSE MAY THEN BE ENTERED.

IMPORTANT: FOR VARIATIONS OF THE ABOVE, SEE XXDP, ACT/APT CONSIDERATIONS IN SECTIONS 3.2 & 3.3.

4.5.1 DRIVE SELECTION

THE REQUEST WILL BE:

DRIVES TO BE TESTED:

THE DEFAULT RESPONSE IS CARRIAGE RETURN TO TEST ALL DRIVES IN THE 'DRIVE PRESENT' CONDITION.

THE OPERATOR CAN ALSO TYPE IN THE SPECIFIC DRIVE NUMBERS TO BE TESTED, SEPARATED BY COMMAS & TERMINATED BY A CARRIAGE RETURN.

E.G. DRIVES TO BE TESTED: 1,2,4,6

IMPORTANT: FOR VARIATIONS OF THE ABOVE, SEE XXDP, ACT/APT CONSIDERATIONS IN SECTIONS 3.2 & 3.3.

4.5.2 BUS ADDRESS

THE REQUEST WILL BE:

TYPE IN BUSS ADDRESS IF NOT 177440

THE DEFAULT IS A CARRIAGE RETURN

4.5.3 CONTROLLER INTERRUPT VECTOR

THE REQUEST WILL BE:

TYPE IN CONTROLLER INTERRUPT VECTOR IF NOT 210

THE DEFAULT IS A CARRIAGE RETURN.

4.5.4 EXAMPLE OF PROGRAM DIALOGUE

THE EXAMPLE SHOWN IS FOR A PROGRAM STARTED AT ADDRESS 220.

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ALL OPERATOR RESPONSES ARE UNDERLINED.

UNIBUS RK06-RK07 DRIVE DIAGNOSTIC
PART 1
CZR6HFO

DRIVES TO BE TESTED: 1,3<CR>

TYPE IN BUSS ADDRESS IF NOT 177440 <CR>

TYPE IN CONTROLLER INTERRUPT VECTOR IF NOT 210 <CR>

WILL TEST DRIVES:

1
3

DRIVE 1

(THE REST IS IDENTICAL TO THE EXAMPLE SHOWN IN 4.6 BELOW)

4.6 PROGRAM EXAMPLE

THE FOLLOWING IS AN EXAMPLE OF A PROGRAM STARTED AT THE
DEFAULT ADDRESS (200) & WITH 2 DRIVES ON THE LINE.

UNIBUS RK06-RK07 DRIVE DIAGNOSTIC
PART 1
CZR6HFO

WILL TEST DRIVES:

0
1

DRIVE 0

DRIVE SERIAL NO. AAA
CARTRIDGE SERIAL NO. BBB

DRIVE 1

DRIVE SERIAL NO. CCC
CARTRIDGE SERIAL NO. DDD

END PASS #1

WILL TEST DRIVES:

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DRIVE 0

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DRIVE 1

END PASS # 2

(ETC)

THE ABOVE ASSUMES NO ERRORS DETECTED.
THE NUMBER OF PASSES IS DETERMINED BY ACT/APT/XXDP

IMPORTANT: FOR VARIATIONS OF THE ABOVE, SEE XXDP, ACT/APT
CONSIDERATIONS IN SECTIONS 3.2 & 3.3.

4.7 HALTING THE PROGRAM

THE PROGRAM PROVIDES A METHOD OF HALTING ITSELF SUCH THAT
THE CARTRIDGE AND/OR DRIVE IS NOT LEFT IN AN UNDETERMINED
STATE; IE: HEADS UNLOADED OR INVALID FORMAT.

TO PROPERLY HALT, TYPE CONTROL-C (^C) ON THE CONSOLE.

IF HEADS ARE LOADED & FORMATTING IS VALID,
THE PROGRAM WILL:

1. ECHO ^C
2. TYPE "CPU HALTED"
3. HALT THE PROGRAM

IF HEADS ARE NOT LOADED AND/OR FORMATTING IS INVALID,
THE PROGRAM WILL:

1. ECHO ^C
2. TYPE 'HALT PENDING, PLEASE WAIT'
3. DO THE TEST(S) THAT LOADS HEADS AND/OR FORMATS
THE INVALID CYLS
4. TYPE 'CPU HALTED'
5. HALT THE PROGRAM

NOTES:

1. THE ABOVE EXAMPLE IS FOR THE PROGRAM RUNNING IN DUMP
MODE (MANUAL). IF THE PROGRAM IS RUNNING IN CHAIN/AUTO
MODE VIA XXDP,ACT,APT; IT WILL FIRST LOAD HEADS
AND/OR FORMAT CORRECTLY, IF REQ'D, THEN IT WILL
JUMP ON TO THE MONITOR WHERE THE NEXT PROGRAM CAN BE
CALLED IN.

THE TYPEOUTS WILL BE "ABORT PENDING - PLEASE WAIT"
& "PROGRAM ABORTING"

2. OPERATING THE 'CONTINUE' SWITCH ON THE CPU CONSOLE WILL RETURN THE
PROGRAM TO TEST 1 WHERE TESTING WILL BEGIN WITH THE 1'ST DRIVE AGAIN.

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5.0 DRIVE DIAGNOSTIC FUNCTIONAL DESCRIPTION

5.1 GENERAL

A. BASIC CONTROLLER TESTS, SIZING & SETUP

THESE TESTS DO BASIC CONTROLLER REGISTER REFERENCE TESTS, CHECKS OPERATOR INPUTS AGAINST DRIVES SEEN ON THE LINE OR DEFAULTS TO TEST ALL THE DRIVES SEEN ON THE LINE. IT CHECKS THE EXISTENCE OF AN L OR P CLOCKS FOR USE IN THE TIMING TESTS.

B. STATIC & CYCLE UP TESTS

THESE TESTS CHECK OUT THE ABILITY TO SELECT & DESELECT THE DRIVE; TO DETECT PARITY, UNSAFE, AND FAULT CONDITIONS WITH THE DRIVE READY TO OPERATE BJT WITHOUT THE SPINDLE ON.

THE ENTIRE POWER UP SEQUENCE IS TESTED BY VERIFYING ALL STATUS BITS SET/RESET IN PROPER SEQUENCE: THE BRUSH CYCLE, INNER-OUTER LIMIT DETECTION, FORWARD, REVERSE, PIP...ETC STATUS BITS ARE CHECKED.

C. SEEK, WRITE HEADER, READ HEADER TESTS

THESE TESTS CHECK THE ABILITY OF THE DRIVE TO DO SEEKS, HEADER OPERATIONS & 20, 22 SECTOR FORMATTING.

5.2 TEST DESCRIPTIONS

BASIC CONTROLLER TESTS, SIZING & SETUP

TEST 1 REFERENCE ALL CONTROLLER REGISTERS

THIS TEST VERIFIES THAT ALL THE CONTROLLER REGISTERS CAN BE ACCESSED. THE INABILITY TO BE ACCESSED WILL RESULT IN A TIMEOUT TRAP WITH AN ERROR MSG. ANY ERROR IN THIS TEST WILL RESULT IN ABORTING ALL OTHER TESTS AND JUMPING TO 'END OF PASS'

TEST 2 SIZE THE BUSS

THIS TEST IS ENTERED ONLY IF 'DRIVE SELECTION' IS DEFAULTED EITHER BY RUNNING IN THE AUTO MODE OR A 200 START IN THE MANUAL MODE.
EVERY DRIVE FROM 0 THRU 7 IS ADDRESSED.
CONTROLLER ERROR (CERR) IS EXAMINED AND IF NOT SET, THE DRIVE WILL BE TESTED AS AN RK06. IF SET, THE PROGRAM WILL BYPASS TESTING THAT DRIVE ONLY IF THE ERROR WAS A RESULT OF MDS, UFE OR NED BEING SET; OR BOTH NED & DRA RESET IN-

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DICATING THE OTHER PORT IS ACCESSED.
IF CERR DUE TO DTYE, THE DRIVE WILL BE TESTED AS AN RK07.

TEST 3 VERIFY OPERATOR DRIVE SELECTIONS

THIS TEST IS ENTERED ONLY IF DRIVE SELECTION IS NOT
DEFAULTED. EVERY DRIVE FROM 0 TO 7 IS ADDRESSED &
CONTROLLER ERROR (CERR) IS EXAMINED. IF NOT SET, THE
PROGRAM WILL ASSUME THE DRIVE IS PRESENT AS AN RK06
IF CERR WAS SET, THAT DRIVE WILL BE BYPASSED
ONLY IF THE ERROR WAS A RESULT OF MDS OR UFE SET OR BOTH
NED & DRA RESET (WRONG PORT). IF CERR IS A RESULT OF
NED ONLY, IT IS CHECKED AGAINST THE INPUTTED INFOR TO
VERIFY IT WAS NOT SPECIFIED.
IF CERR DUE TO DTYE, THE DRIVE WILL BE TESTED AS AN RK07.

TEST 4 FIND NEXT DRIVE TO BE TESTED

THIS TEST FINDS THE NEXT DRIVE PRESENT & PUTS THAT
ADDRESS IN 'DRVAD' & \$TMP4 IS SET TO CDT IF DRIVE IS RK07.
THROUGHOUT THE FOLLOWING TESTS, THE DRIVE TESTED IS
THE DRIVE WHOSE ADDRESS IS IN 'DRVAD'.

TEST 5 UNLOAD DRIVE TO BE TESTED

THIS TEST UNLOADS THE DRIVE TO BE TESTED NEXT,
WAITS FOR ATTN & VERIFIES IT CAME FROM THE CORRECT DRIVE.
IT THEN WAITS FOR SPEED OK TO GO LOW BEFORE
PROCEEDING TO THE NEXT TEST.

STATIC & CYCLE UP TESTS

TEST 6 REFERENCE & CHECK ALL STATUS BYTES IN RKMR2 & RKMR3

CHECKS THE ABILITY TO REFERENCE ALL
DRIVE REGISTERS AND THAT THEY CONTAIN CORRECT STATUS.

TEST 7 PRINT DRIVE SERIAL NUMBER

THIS TEST READS & PRINTS THE DRIVE SERIAL # FROM MSG A, WORD 11
IN DECIMAL & IS PERFORMED ON THE 1ST PASS ONLY

TEST 10 SET VV WITH PACK CMD

IF VV IS RESET, THE PACK CMD IS USED TO SET IT.

TEST 11 RELEASE DRIVE

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TESTS THE ABILITY TO RECOGNIZE THE RLS BIT AND NOT RAISE SACK

TEST 12 DRIVE TYPE TEST

THIS TEST COMPARES DRIVE TYPE IN MSG A AGAINST 'DDT' IN RKDS.
WRONG CDT IN RKCS1 IS SENT & ERRORS ARE VERIFIED.

TEST 13 C-D PARITY ERROR DETECTION

TESTS THE ABILITY OF THE DRIVE TO DETECT EVEN PARITY SENT BY
THE CONTROLLER BY SETTING 'PAT' ON RKMRI.
THE DRIVE SHOULD RESPOND WITH 'C-D PARITY ERROR'
THE DRIVE STILL SENDS ODD PARITY TO THE CONTROLLER WHICH IS NOW
CHECKING FOR EVEN PARITY THEREFORE THE CONTROLLER SHOULD DETECT
AN ERROR AND SET SPAR.
THE ERROR CONDITION IS RESET WITH THE CLEAR CMD

TEST 14 VERIFY START SPINDLE CMD

THE PROGRAM CHECKS THE ENTIRE STARTUP SEQUENCE, IE:
BRUSH CYCLE, HEADS HOME, FWD, REV ETC.
BY VERIFYING ALL APPROPRIATE STATUS BITS FOR PROPER SEQUENCING.
THE CYL ADDRESS & CYL DIFFERENCE REGS ARE CHECKED
TO BE ZERO AT THE END OF THE SEQUENCE.

SEEK/READ HEADER/WRITE HEADER TESTS

TEST 15 STATIC CYL DIFF AND CYL ADDR REG TEST; PART 1

THIS TEST CHECKS EACH BIT OF THE CYL DIFFERENCE
AND CYL ADDRESS REGISTERS BY PERFORMING SEEKS TO ALL
MAJOR CYLS (0,1,2,4,8,16,32,64,128,256) (512 FOR THE RK07)
WITH EVEN PARITY SET. THIS FREEZES THE INFORMATION IN THE ABOVE REGISTER
AND ALLOWS FOR CHECKING. THIS TEST VERIFIES C-D PARITY ERROR BIT SET,
THAT HEADS DID NOT MOVE & ALL OTHER APPLICABLE STATUS BITS & REGS.

TEST 16 STATIC CYL DIFF & CYL ADDR REG TEST-PART 2

THIS TEST CHECKS THE ABILITY OF THE DRIVE TO PROPERLY SET THE CYL
DIFF. & CYL ADDR REGS FOR ALL COMBINATIONS BY SEEKING TO
ALL CYLS FROM EVERY OTHER CYL. (N SQUARE SEEKS).
IT IS PERFORMED IN THE SAME MANNER AS THE ABOVE TEST.

TEST 17 HEAD REGISTER TEST

THIS TEST CHECKS THE ABILITY TO SELECT ALL HEADS (0,1,2)

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VIA RKDA & READING BACK FROM MSG B3 BY THE SELECT DRIVE CMD.
HEAD 3 IS CHECKED TO PRODUCE INV. ADDR.

SINCE CHANGING HEAD ADDRESSES ARE TIED TO SEEK CMDS,
SELECTING HEAD 3 MUST RESULT IN A SEEK INCOMPLETE ALONG WITH
ILLEGAL ADDRESS. IF NOT, THIS MEANS THAT CHANGING HEAD ADDRESSES
ARE NOT TIED TO SEEK CMDS

TEST 20 SEEK TO CYL 0

TESTS THE ABILITY TO DO A SEEK CMD.
VERIFIES THERE WAS NO MOVEMENT BY CHECKING ALL APPROPRIATE
STATUS BITS. VERIFIES CMD COMPLETION BETWEEN 10-15USEC.
READ HEADER IS NOT PERFORMED AS THE PACK MAY NOT BE FORMATTED.

TEST 21 TEST SECTOR COUNT REG. FOR 22 & 20 SECTOR FORMAT

TEST 22 DETECT OUTER LIMIT

THIS TEST VERIFIES THAT THE ABOVE TEST DID ACTUALLY POSITION ON CYL 0
BY DETECTING OUTER LIMIT AS THE ADJACENT CYL.
AN ERROR IN THIS TEST INDICATES:

A. HEADS WERE NOT ON CYL 0
AND/OR B. COULD NOT SEEK IN REVERSE DIRECTION.

TEST 23 BASIC WRITE/READ HEADER & HEAD SWITCHING TEST

THIS TEST CHECKS HEAD SWITCHING BY WRITING UNIQUE HEADERS
ON EACH TRACK OF CYL 0, READING BACK & VERIFYING THEY REMAINED
UNIQUE. 22 SECTOR FORMAT IS USED

I.E. TRACK 0: ALL 0'S FOR ALL SECTOR HEADERS
TRACK 1: 0101 FOR ALL SECTOR HEADERS
TRACK 2: ALL 1'S FOR ALL SECTOR HEADERS

TEST 24 BASIC WRITE/READ HEADER TEST; ALL 1'S, 20 SECTORS

USING HEAD 0, WRITE & READ 20 SECTOR HEADERS BY WRITING ALL
1'S AS HEADERS. ATTEMPT TO FIND SECTORS 20 & 21. VERIFY
THEY ARE NO LONGER THERE BY READING 22 SECTORS AND NOT
FINDING 0'S AS DATA FROM THE PREVIOUS TEST.

TEST 25 WRITE & READ HEADERS CYL 0, HEAD 0

TEST 26 SEEK FROM CYL 0 TO 1 & READ HEADERS

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THIS TEST CHECKS MSG A & B WORDS 0,1,2 FOR CORRECT STATUS AFTER RDY IS RECEIVED FROM A SEEK CMD TO DETERMINE THAT THE HEADS ARE ACTUALLY MOVING & THE CYL DIFF IS 1. AFTER ATTN IS RECEIVED, CERR IS EXAMINED FOR ANY ERRORS. CYL DIFFERENCE IN MSG A2 IS VERIFIED TO BE 0 & CYL ADDR IN MSG B2 IS VERIFIED TO BE 1.

HEADERS ARE READ FROM 1 SECTOR, HEAD 0 & VERIFIED THAT THEY ARE DIFFERENT FROM CYL 0 TO SHOW THAT THE HEADS DID ACTUALLY MOVE.

TEST 27 WRITE & READ HEADERS CYL 1, HEAD 0

TEST 30 TEST RECALIBRATE CMD & READ HEADERS

THIS TEST DOES A RECALIBRATE & READS HEADERS. IT VERIFIES THAT WRITING HEADERS ON CYL 1 FROM THE PREVIOUS TEST DID NOT OVERWRITE CYL 0 HEADERS.

AN ERROR IN THIS TEST INDICATES THAT HEADS:

OR A. MOVED TO A CYL OTHER THAN 1
 B. DID NOT GET BACK TO CYL 0

TEST 31 SINGLE INCREMENT SEEKS TO THE LAST CYLINDER

THIS TEST DOES SINGLE INCREMENT SEEKS OUT TO THE LAST CYL WITHOUT ANY WRITING OR READING SO AS NOT TO INADVERTENTLY DESTROY DATA.

TEST 32 READ & SAVE BAD SECTOR INFO & TYPE PACK SERIAL #

THIS TEST VERIFIES THAT CYL 632 (1456 FOR RK07), TRACK 2 CAN BE READ. THIS AREA CONTAINS BAD SECTOR INFO WHICH IS WRITTEN BY THE FACTORY DURING MANF. ALL BAD SECTOR INFO (BSE) WILL BE STORED AT THIS TIME TO MASK FUTURE READ HEADER OR DATA ERROR PRINTOUTS. IF BSE INFO CANNOT BE READ, OR IF AFTER READING THE BSE INFO IT IS DETERMINED THAT AN ALIGNMENT CARTRIDGE IS USED, A MSG WILL BE TYPED INDICATING THAT ALL FUTURE FORMAT AND READ-WRITE TESTS WILL BE BYPASSED. THIS IS DONE SO AS NOT TO DESTROY BSE INFO OR AN ALIGNMENT PACK BY WRITI

THE PACK SERIAL # IS TYPED IN OCTAL & FOR THE FIRST PASS ONLY.

THIS IS THE FIRST TEST WHERE THE READ DATA CMD IS PERFORMED

TEST 33 DETECT INNER LIMIT

THIS TEST VERIFIES THAT THE LAST CYL IN THE ABOVE TEST WAS 632 (1456) BY DETECTING INNER LIMIT AS THE ADJACENT CYL. IF THIS TEST FAILS, IT INDICATES THAT HEADS WERE NOT ON THE LAST CYL

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& THAT BSE INFO IS NOT VALID. THE FORMAT PACK TEST
& ALL READ-WRITE TESTS ARE BYPASSED
TO AVOID DESTROYING BSE INFO OR AN ALIGNMENT CARTRIDGE
SINCE THERE IS A SEEKING OR LIMIT DETECTION PROBLEM.

TEST 34 FORMAT PACK

THIS TEST FORMATS THE ENTIRE PACK IN 22 SECTOR FORMAT BY
DOING 1 CYL INCREMENTAL SEEKS
FROM 0 TO 632 (1456) WITH WRITE HEADER CMDS (ALL TRACKS).
HEADERS WILL BE READ IN THE NEXT TEST

TEST 35 DECREMENT FROM LAST CYL TO 0 & READ HEADERS

THIS TEST VERIFIES MOTION IN THE NEGATIVE DIRECTION BY
SINGLE CYL INCREMENTAL SEEKS.

TEST 36 SEEK FROM CYL 0 TO ALL MAJOR CYLS & READ HEADERS

THIS TEST SEEKS FROM CYL 0 TO ALL THE MAJOR CYLS & READS HEADERS.
IT THEN SEEKS CYL 0 & READS HEADERS.

MAJOR CYLS ARE: 1 (DECIMAL) = 1 (OCTAL)

2	2
4	4
8	10
16	20
32	40
64	100
128	200
256	400
512	1000 (RK07)

TEST 37 SEEK TO ALL CYLS FROM 0 & READ HEADERS

TEST 40 SEEK TO ALL CYLS FROM LAST CYL & READ HEADERS

TEST 41 SEEK TO ALL INVALID CYLINDERS

THIS TEST VERIFIES THAT 'INV ADDR' & 'SEEK INCOMPLETE' IS
PRODUCED & THAT HEADS DO NOT MOVE OR UNLOAD IF AN ILLEGAL
CYL IS SPECIFIED IN A SEEK.

INVALID CYLS ARE 633 THRU 777 (8) FOR THE RK06
& 1457 THRU 1777 FOR THE RK07

THE PROGRAM DOES NOT REQUIRE FORMATTED PACKS AS FORMATTING
IS PERFORMED IN ANY CASE.

CZR6HFO UNIBUS RK6 DR PT1
CZR6HF.P11 04-JAN-82 12:44

MACY11 30(1046) 04-JAN-82 13:01 ^{1 2}PAGE 22

SEQ 0021

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ANY TEST THAT MODIFIES STANDARD FORMATTING IS FOLLOWED BY A
'CLEAN UP' TEST TO PUT THOSE CYLS BACK TO STANDARD
FORMAT.

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6.0 ERROR REPORTING

6.1 ERROR INTERPRETATION

WHENEVER AN ERROR MSG IS PRINTED OUT, ALL REGISTERS AND OTHER DATA PERTAINING TO THE ERROR ARE ALSO GIVEN. MSG A(00), MSG B(01), RKER, RKBA...ETC, INDICATE THE CONTENTS OF THE CORRESPONDING REGISTERS AT THE TIME OF ERROR.

EVERY ERROR MSG CONTAINS A PC. THIS PC INDICATES THE POSITION IN PROGRAM WHERE THE ERROR CALL IS LOCATED. THE ERROR MSG, BECAUSE OF PRACTICAL CONSIDERATIONS IS MADE SHORT AND MEANINGFUL. THE USER IS ADVISED TO LOOK UP THE PC IN THE PROGRAM LISTING, WHERE HE WILL FIND MORE INFORMATION ABOUT THE ERROR. IN MANY INSTANCES, A SINGLE FAULT WILL GIVE RISE TO MORE THAN ONE ERROR REPORT. A LITTLE DELIBERATION AND CAREFUL EXAMINATION OF THE DATA GIVEN WILL BE CERTAINLY VERY HELPFUL IN PINPOINTING THE FAULT. A BRIEF EXPLANATION OF WHAT IS BEING CHECKED IN THE TEST IS GIVEN AT THE BEGINNING OF EVERY TEST. ALL THE NUMBERS GIVEN WITH ERROR MSGS ARE IN OCTAL.

NOTE

NO ERROR LOGGING OR OPERATION HISTORY IS PROVIDED.

6.2 ERROR PRINTOUT EXAMPLES:

EXAMPLE #1:

MSG A0 ERROR
AFTER START SPINDLE CMD & FWD SET

TEST NO.	PC						
000014	016530						
		EXPECT					
A0	B0	A1	B1	A2	B2	B3	
030144	100000	013704	000001				
		ACTUAL					
140144	100000	101744	000001				
RKCS1	RKCS2	RKASOF	RKER	RKDS	RKDC		
040200	000100	010000	000000	000000	000000		

THE ABOVE EXAMPLE SHOWS EXPECTED & ACTUAL DATA FOR MSG REGISTERS A0, B0, A1 & B1.

MSGS A2, B2 & B3 WILL BE TYPED OUT ONLY AS REQUIRED IF THE CYL DIFFERENCE/OFFSET, CYL ADDRESS & HEAD & SECTOR INFORMATION IS A VARIABLE PARAMETER OF THE TEST.

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1111
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EXAMPLE #2:

NO ATTN IN RKASOF
AFTER UNLOAD CMD

TEST NO.	PC					
000003	014330					
RKMR2	RKMR3	RKER	RKDS	RKCS1	RKCS2	RKASOF
000144	100000	000000	100101	000206	000104	000000

[END OF DOCUMENT]

%

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1128
1129
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167400
000001

```
*** PGM REV 039 ***  
.NLIST  CND,MC,MD  
.LIST   ME  
.ENABL  ABS,AMA
```

```
:DEFINE SYSMAC MACROS
```

```
$SWR= 167400  
$TN= 1
```

```
:DEFINE SWITCHES 15,14,13,11,10,9,8  
:SET FIRST TEST NO. TO 1
```

```
.TITLE  CZR6HFO UNIBUS RK6 DR PT1  
:*COPYRIGHT (C) 1976,1982  
:*DIGITAL EQUIPMENT CORP.  
:*MAYNARD, MASS. 01754  
:*  
:*PROGRAM BY GARY PAPAIZIAN  
:*  
:*THIS PROGRAM WAS ASSEMBLED USING THE PDP-11 MAINDEC SYSMAC  
:*PACKAGE (MAINDEC-11-DZQAC-C5), JAN, 1981.  
:*
```

```
.SBTTL  OPERATIONAL SWITCH SETTINGS
```

```
:*  
:*      SWITCH          USE  
:*      -----  
:*      15             HALT ON ERROR  
:*      14             LOOP ON TEST  
:*      13             INHIBIT ERROR TYPEOUTS  
:*      12             ABORT DRIVE AFTER 20 ERRORS  
:*      11             INHIBIT ITERATIONS  
:*      10             BELL ON ERROR  
:*      9              LOOP ON ERROR  
:*      8              LOOP ON TEST IN SWR<7:0>
```

```
.SBTTL  SUMMARY OF STARTING LOCATIONS
```

```
:*  
:*      200            DEFAULT PARAMETERS  
:*      204            DEFAULT PARAMETERS & BYPASS TEST 16  
:*      220            INPUT PARAMETERS  
:*      230            INPUT PARAMETERS & BYPASS TEST 16  
:*      240            ODT11  
:*      260            RUN MODULE TEST VERSION-DEFAULT MODE ONLY BYPASS  
:*                     TESTS 35,36,40 & 41  
:*      270            SAME AS 260 START BUT BYPASS TEST 16 ALSO  
:*
```



```
1180 .SBTTL BASIC DEFINITIONS
1181
1182 ;*INITIAL ADDRESS OF THE STACK POINTER *** 1100 ***
1183 STACK= 1100
1184 .EQUIV EMT,ERROR ;:BASIC DEFINITION OF ERROR CALL
1185 .EQUIV IOT,SCOPE ;:BASIC DEFINITION OF SCOPE CALL
1186
1187 ;*MISCELLANEOUS DEFINITIONS
1188 HT= 11 ;:CODE FOR HORIZONTAL TAB
1189 LF= 12 ;:CODE FOR LINE FEED
1190 CR= 15 ;:CODE FOR CARRIAGE RETURN
1191 CRLF= 200 ;:CODE FOR CARRIAGE RETURN-LINE FEED
1192 PS= 177776 ;:PROCESSOR STATUS WORD
1193 .EQUIV PS,PSW
1194 STKLMT= 177774 ;:STACK LIMIT REGISTER
1195 PIRQ= 177772 ;:PROGRAM INTERRUPT REQUEST REGISTER
1196 DSWR= 177570 ;:HARDWARE SWITCH REGISTER
1197 DDISP= 177570 ;:HARDWARE DISPLAY REGISTER
1198
1199 ;*GENERAL PURPOSE REGISTER DEFINITIONS
1200 R0= %0 ;:GENERAL REGISTER
1201 R1= %1 ;:GENERAL REGISTER
1202 R2= %2 ;:GENERAL REGISTER
1203 R3= %3 ;:GENERAL REGISTER
1204 R4= %4 ;:GENERAL REGISTER
1205 R5= %5 ;:GENERAL REGISTER
1206 R6= %6 ;:GENERAL REGISTER
1207 R7= %7 ;:GENERAL REGISTER
1208 SP= %6 ;:STACK POINTER
1209 PC= %7 ;:PROGRAM COUNTER
1210
1211 ;*PRIORITY LEVEL DEFINITIONS
1212 PR0= 0 ;:PRIORITY LEVEL 0
1213 PR1= 40 ;:PRIORITY LEVEL 1
1214 PR2= 100 ;:PRIORITY LEVEL 2
1215 PR3= 140 ;:PRIORITY LEVEL 3
1216 PR4= 200 ;:PRIORITY LEVEL 4
1217 PR5= 240 ;:PRIORITY LEVEL 5
1218 PR6= 300 ;:PRIORITY LEVEL 6
1219 PR7= 340 ;:PRIORITY LEVEL 7
1220
1221 ;*'SWITCH REGISTER' SWITCH DEFINITIONS
1222 SW15= 100000
1223 SW14= 40000
1224 SW13= 20000
1225 SW12= 10000
1226 SW11= 4000
1227 SW10= 2000
1228 SW09= 1000
1229 SW08= 400
1230 SW07= 200
1231 SW06= 100
1232 SW05= 40
1233 SW04= 20
1234 SW03= 10
1235 SW02= 4
```

BASIC DEFINITIONS

```
1236      000002      SW01= 2
1237      000001      SW00= 1
1238      .EQUIV SW09,SW9
1239      .EQUIV SW08,SW8
1240      .EQUIV SW07,SW7
1241      .EQUIV SW06,SW6
1242      .EQUIV SW05,SW5
1243      .EQUIV SW04,SW4
1244      .EQUIV SW03,SW3
1245      .EQUIV SW02,SW2
1246      .EQUIV SW01,SW1
1247      .EQUIV SW00,SW0
1248
1249      ;*DATA BIT DEFINITIONS (BIT00 TO BIT15)
1250      100000      BIT15= 100000
1251      040000      BIT14= 40000
1252      020000      BIT13= 20000
1253      010000      BIT12= 10000
1254      004000      BIT11= 4000
1255      002000      BIT10= 2000
1256      001000      BIT09= 1000
1257      000400      BIT08= 400
1258      000200      BIT07= 200
1259      000100      BIT06= 100
1260      000040      BIT05= 40
1261      000020      BIT04= 20
1262      000010      BIT03= 10
1263      000004      BIT02= 4
1264      000002      BIT01= 2
1265      000001      BIT00= 1
1266      .EQUIV BIT09,BIT9
1267      .EQUIV BIT08,BIT8
1268      .EQUIV BIT07,BIT7
1269      .EQUIV BIT06,BIT6
1270      .EQUIV BIT05,BIT5
1271      .EQUIV BIT04,BIT4
1272      .EQUIV BIT03,BIT3
1273      .EQUIV BIT02,BIT2
1274      .EQUIV BIT01,BIT1
1275      .EQUIV BIT00,BIT0
1276
1277      ;*BASIC "CPU" TRAP VECTOR ADDRESSES
1278      000004      ERRVEC= 4      ::TIME OUT AND OTHER ERRORS
1279      000010      RESVEC= 10     ::RESERVED AND ILLEGAL INSTRUCTIONS
1280      000014      TBITVEC=14    ::"T" BIT
1281      000014      TRTVEC= 14     ::TRACE TRAP
1282      000014      BPTVEC= 14     ::BREAKPOINT TRAP (BPT)
1283      000020      IOTVEC= 20     ::INPUT/OUTPUT TRAP (IOT) **SCOPE**
1284      000024      PWRVEC= 24     ::POWER FAIL
1285      000030      EMTVEC= 30     ::EMULATOR TRAP (EMT) **ERROR**
1286      000034      TRAPVEC=34    ::"TRAP" TRAP
1287      000060      TKVEC= 60      ::TTY KEYBOARD VECTOR
1288      000064      TPVEC= 64      ::TTY PRINTER VECTOR
1289      000240      PIRQVEC=240    ::PROGRAM INTERRUPT REQUEST VECTOR
1290
1291      .SBTTL RK06 CONTROLLER REGISTER DEFINITION
```



```
1292
1293           ;          $BASE=177440
1294
1295           000000          RKCS1= 0          ;CONTROL AND STATUS REGISTER 1
1296           000002          RKWC=  2          ;WORD COUNT REGISTER
1297           000004          RKBA=  4          ;BUS ADDRESS REGISTER
1298           000006          RKDA=  6          ;DESIRED TRACK SECTOR REGISTER
1299           000010          RKCS2= 10         ;CONTROL AND STATUS REGISTER 2
1300           000012          RKDS= 12         ;DRIVE STATUS REGISTER
1301           000014          RKER= 14         ;ERROR REGISTER
1302           000016          RKASOF= 16        ;ATTENTION SUMMARY AND OFFSET REGISTER
1303           000020          RKDC= 20         ;DESIRED CYL REGISTER
1304           000024          RKDB= 24         ;DATA BUFFER
1305           000026          RKMR1= 26        ;MAINTENANCE REGISTER 1
1306           000034          RKMR2= 34        ;MAINTENANCE REGISTER 2 (MSG LINE A)
1307           000036          RKMR3= 36        ;MAINTENANCE REGISTER 3 (MSG LINE B)
1308           000030          RKECPS= 30       ;ECC POSITION INFORMATION
1309           000032          RKECPT= 32       ;ECC PATTERN INFORMATION
1310
1311           .SBTTL CONTROL AND STATUS REGISTER 1 BITS (RKCS1:0)
1312
1313           ;          DRIVE CMDS
1314
1315           000001          SELDRV= 1         ;SELECT DRIVE (GET STATUS)
1316           000003          PACK=  3         ;PACK ACKNOWLEDGE
1317           000005          CLEAR=  5         ;DRIVE CLEAR
1318           000007          UNLOAD= 7         ;UNLOAD
1319           000011          SRTSPL= 11        ;START SPINDLE
1320           000013          RECAL= 13         ;RECALIBRATE
1321           000015          OFFSET= 15        ;OFFSET
1322           000017          SEEK= 17         ;SEEK
1323           000021          RDDATA= 21        ;READ DATA
1324           000023          WRDATA= 23        ;WRITE DATA
1325           000025          RDHEAD= 25        ;READ HEADER
1326           000027          WRHEAD= 27        ;WRITE HEADER AND DATA
1327           000031          WRTCHK= 31        ;WRITE CHECK
1328
1329           000001          GO=      BIT0       ;GO BIT
1330           000100          IE=      BIT6       ;INTERRUPT ENABLE
1331           000200          RDY=     BIT7       ;CONTROLLER READY
1332           000400          BA16=    BIT8       ;BUS ADDRESS BIT 16
1333           001000          BA17=    BIT9       ;BUS ADDRESS BIT 17
1334           002000          CDT=     BIT10      ;CONTROLLER DRIVE TYPE (0=RK06, 1=RK07)
1335           004000          CTO=     BIT11      ;CONTROLLER TIMEOUT
1336           010000          CFMT=    BIT12      ;CONTROLLER DRIVE FORMAT (0=22 SECTOR, 1=20 SECTOR)
1337           020000          DCPAR=   BIT13      ;SERCON PARITY ERROR DETECTED BY CONTROLLER
1338           040000          DI=     BIT14      ;DRIVE INTERRUPT
1339           100000          CERR=    BIT15      ;CONTROLLER ERROR
1340           100000          CCLR=    BIT15      ;CONTROLLER CLEAR
1341
1342           .SBTTL CONTROL AND STATUS REGISTER 2 BITS (RKCS2:10)
1343
1344           000007          DRVMSK= 7         ;MASK FOR DRIVE SELECTION CODE
1345           000010          RLS=     BIT3       ;DESELECT OR RELEASE DRIVE IN BITS 0-2
1346           000020          BAI=     BIT4       ;BUS ADDRESS INCREMENT INHIBIT
1347           000040          SCLR=    BITS       ;SUBSYSTEM CLEAR CONTROLLER AND ALL DRIVES
```

1348	000100	IR=	BIT6	: INPUT READY
1349	000200	OR=	BIT7	: OUTPUT READY
1350	000400	UFE=	BIT8	: UNIT FIELD ERROR
1351	001000	MDS=	BIT9	: MULTIPLE DRIVE SELECT
1352	002000	PGE=	BIT10	: PROGRAMMING ERROR
1353	004000	NEM=	BIT11	: NON-EXISTENT MEMORY
1354	010000	NED=	BIT12	: NON-EXISTENT DRIVE
1355	020000	UPE=	BIT13	: UNIBUS PARITY ERROR
1356	040000	WCE=	BIT14	: WRITE CHECK ERROR
1357	100000	DLT=	BIT15	: DATA LATE ERROR
1358				
1359		.SBTTL	ERROR REGISTER BIT DEFINITION (RKER:14)	
1360				
1361	000001	ILF=	BIT0	: ILLEGAL FUNCTION CODE
1362	000002	SKI=	BIT1	: SEEK INCOMPLETE
1363	000004	NXF=	BIT2	: NON-EXECUTABLE FUNCTION
1364	000010	DRPAR=	BIT3	: DRIVE DETECTED SERCON PARITY ERROR
1365	000020	FMTE=	BIT4	: FORMAT ERROR
1366	000040	DTYPE=	BIT5	: DRIVE TYPE ERROR
1367	000100	ECH=	BIT6	: ECC HARD
1368	000200	BSE=	BIT7	: BAD SECTOR ERROR
1369	000400	HVRC=	BIT8	: HEADER VRC ERROR
1370	001000	COE=	BIT9	: CYL ADDRESS OVERFLOW ERROR
1371	002000	IDAE=	BIT10	: INVALID DISK ADDRESS ERROR: HEAD/CYL
1372	004000	WLE=	BIT11	: WRITE LOCK ERROR
1373	010000	DTE=	BIT12	: DRIVE TIMING ERROR
1374	020000	OPI=	BIT13	: OPERATION (SEARCH) INCOMPLETE
1375	040000	UNS=	BIT14	: DRIVE UNSAFE
1376	100000	DCK=	BIT15	: DATA CHECK
1377				
1378		.SBTTL	STATUS REGISTER BIT DEFINITION (RKDS:12)	
1379				
1380	000001	DRA=	BIT0	: DRIVE AVAILABLE (CONTROLLER IS SET IF : THIS BIT IS RESET)
1381				
1382	000004	OFST=	BIT2	: DRIVE OFFSET
1383	000010	ACLO=	BIT3	: AC LOW
1384	000020	DCLO=	BIT4	: DC LOW
1385	000040	DROT=	BIT5	: DRIVE OFF TRACK
1386	000100	VV=	BIT6	: VOLUME VALID
1387	000200	DRDY=	BIT7	: DRIVE READY
1388	000400	DDT=	BIT8	: DRIVE TYPE (0=RK06, 1=RK07)
1389	004000	WRL=	BIT11	: WRITE LOCK
1390	020000	PIP=	BIT13	: POSITIONING IN PROGRESS
1391	040000	DSC=	BIT14	: DRIVE STATUS CHANGE
1392	100000	SVAL=	BIT15	: STATUS VALID
1393				
1394		.SBTTL	MAINTENANCE REGISTER 1 BIT DEFINITION (RKMR1:22)	
1395				
1396	000017	MESMSK=	17	: MSG MASK
1397	000020	PAT=	BIT4	: FORCE EVEN PARITY ON SERCON MSG LINES
1398	000040	DMD=	BIT5	: DIAGNOSTIC MODE
1399	000100	MSP=	BIT6	: MAINTENANCE SECTOR PULSE
1400	000200	MIND=	BIT7	: MAINTENANCE INDEX
1401	000400	MCLK=	BIT8	: MAINTENANCE CLOCK
1402	001000	MERD=	BIT9	: MAINTENANCE ENCODED READ DATA
1403	002000	MEWD=	BIT10	: MAINTENANCE ENCODED WRITE DATA

1404	004000	PCA= BIT11	:PRECOMPENSATION ADVANCE
1405	010000	PCD= BIT12	:PRECOMPENSATION DELAY
1406	020000	ECCW= BIT13	:ECC WORD IS BEING READ OR WRITTEN
1407	040000	WRTGAT= BIT14	:WRITE GATE
1408	100000	RDGATE= BIT15	:READ GATE
1409			
1410		.SBTTL DEFINITION OF DRIVE STATUS BYTE 00 MSG A (RKMR2:34)	
1411			
1412	000040	D.DRA= BIT5	:DRIVE AVAILABLE
1413	000100	D.VV= BIT6	:VOLUME VALID
1414	000200	D.DRDY= BIT7	:DRIVE READY
1415	000400	D.DDT= BIT8	:DRIVE TYPE (0=RK06,1 = RK07)
1416	001000	D.FORM= BIT9	:DRIVE FORMAT
1417	002000	D.OFF= BIT10	:OFFSET ON
1418	004000	D.WRL= BIT11	:WRITE LOCK
1419	010000	D.SPIN= BIT12	:SPINDLE ON
1420	020000	D.PIP= BIT13	:POSITIONING IN PROGRESS
1421	040000	D.DSC= BIT14	:DRIVE STATUS CHANGE
1422			
1423		.SBTTL DEFINITION OF DRIVE STATUS BYTE 01 MSG A (RKMR2:34)	
1424			
1425	000020	D.SSP= BIT4	:SERVO SIG PRESENT
1426	000040	D.HDHM= BIT5	:HEADS HOME
1427	000100	D.BRHM= BIT6	:BRUSHES HOME
1428	000200	D.DOOR= BIT7	:DOOR INTERLOCKED
1429	000400	D.CART= BIT8	:CARTRAGE INTERLOCK
1430	001000	D.SPOK= BIT9	:SPEED OK
1431	002000	D.FWD= BIT10	:FORWARD
1432	004000	D.REV= BIT11	:REVERSE
1433	010000	D.LOAD= BIT12	:HEADS LOADING
1434	020000	D.RTZ= BIT13	:RETURN TO ZERO
1435	040000	D.UNLD= BIT14	:HEADS UNLOADING
1436			
1437		.SBTTL DEFINITION OF DRIVE STATUS BYTE 00 MSG B (RKMR3:36)	
1438			
1439	000040	D.IDAE= BIT5	:INVALID DISK ADDRESS ERROR:HEAD/CYL
1440	000100	D.ACLO= BIT6	:AC LOW
1441	000200	D.FLT= BIT7	:DRIVE FAULT
1442	000400	D.ILF= BIT8	:ILLEGAL FUNCTION CODE
1443	001000	D.PAR= BIT9	:DRIVE DETECTED SERCON PARITY ERROR
1444	002000	D.SKI= BIT10	:SEEK INCOMPLETE
1445	004000	D.WLE= BIT11	:WRITE LOCK ERROR
1446	010000	D.SPLS= BIT12	:SPEED LOSS
1447	020000	D.DROT= BIT13	:DRIVE OFF TRACK
1448	040000	D.UNS= BIT14	:R/W UNSAFE
1449			
1450		.SBTTL DEFINITION OF DRIVE STATUS BYTE 01 MSG B (RKMR3:36)	
1451			
1452	000020	D.SECT= BIT4	:SECTOR ERROR
1453	000040	D.WCUR= BIT5	:WRITE CURRENT AND NO WRITE GATE
1454	000100	D.WGAT= BIT6	:WRITE GATE AND NO TRANSISTIONS
1455	000200	D.HDFL= BIT7	:HEAD FAULT
1456	000400	D.MHD= BIT8	:MULTIPLE HEAD SELECT
1457	001000	D.XERROR= BIT9	:INDEX ERROR
1458	002000	D.TIB= BIT10	:TRIBIT ERROR
1459	004000	D.PLO= BIT11	:PLO ERROR

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DEFINITION OF DRIVE STATUS BYTE 01 MSG B (RKMR3:36)

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SEQ 0030

1460	010000	D.NMOV= BIT12	:SEEK AND NO MOTION
1461	020000	D.LIMD= BIT13	:LIMIT DETECT ON SEEK
1462	040000	D.SUNS= BIT14	:SERVO UNSAFE
1463			
1464		.SBTTL COMMON MASKS AND OTHER BITS: MSG A (RKMR2:34)	
1465			
1466	000007	M.DRV= 7	:DRIVE CODE, ALL BYTES
1467	077770	M.SER= 77770	:DRIVE SERIAL #, BYTE 11
1468			
1469		.SBTTL COMMON MASKS AND OTHER BITS: MSG B (RKMR3:36)	
1470			
1471	000003	M.ID= 3	:BYTE ID, ALL BYTES
1472	040000	M.ALGN= BIT14	:ALIGN SIGN, BYTE 10
1473	000760	M.SECT= 760	:SECTOR COUNT, BYTE 11
1474	007000	M.HEAD= 7000	:HEAD DECODE, BYTE 11
1475	100000	M.PAR= BIT15	:PARITY, MESS A/B, ALL BYTES


```
1476
1477
1478
1479      000000
1480
1481
1482
1483      000174
1484 000174 000000
1485 000176 000000
1486
1487 000200 000137 007120
1488      000204
1489 000204 000137 007014
1490      000220
1491 000220 000137 006774
1492      000230
1493 000230 000137 007034
1494      000240
1495 000240 000137 070144
1496      000260
1497 000260 000137 007056
1498      000270
1499 000270 000137 007076
1500
1501
1502
1503
1504
1505      000274
1506      000046
1507 000046 043046
1508      000052
1509 000052 100000
1510      000274
1511      001000
1512
1513
1514
1515
1516
1517      001000
1518      000024
1519 000024 000200
1520      000044
1521 000044 001000
1522      001000
1523
1524
1525
1526
1527 001000
1528 001000 000000
1529 001002 001210
1530 001004 000430
1531 001006 001130

.SBTTL TRAP CATCHER
      .=0
      ;*ALL UNUSED LOCATIONS FROM 4 - 776 CONTAIN A ".+2,HALT"
      ;*SEQUENCE TO CATCH ILLEGAL TRAPS AND INTERRUPTS
      ;*LOCATION 0 CONTAINS 0 TO CATCH IMPROPERLY LOADED VECTORS
      .=174
DISPREG: .WORD 0      ;;SOFTWARE DISPLAY REGISTER
SWREG:   .WORD 0      ;;SOFTWARE SWITCH REGISTER
.SBTTL   STARTING ADDRESS(ES)
      JMP @#START ;;JUMP TO STARTING ADDRESS OF PROGRAM
      .=204
      JMP BYT16      ;BYPASS N-SQUARE TEST IN DEFAULT MODE
      .=220
      JMP PARSRT     ;INPUT ALL PARAMETERS & START TESTING
      .=230
      JMP BYT16A    ;BYPASS N-SQUARE TEST IN PARAM MODE
      .=240
      JMP O.ODT     ;ENTER ODT11
      .=260
      JMP MDTST     ;MODULE TESTS DEFAULT MODE ONLY
      .=270
      JMP MDTSTA    ;BYPASS SEVERAL TESTS
      ;SAME AS 260 & BYPASS N-SQUARE TEST ALSO

.SBTTL ACT11 HOOKS
      ;*****
      ;HOOKS REQUIRED BY ACT11
      $SVPC=.      ;SAVE PC
      .=46
      $ENDAD      ;;1)SET LOC.46 TO ADDRESS OF $ENDAD IN .$EOP
      .=52
      .WORD 100000 ;;2)SET LOC.52 TO 100000
      .=$SVPC     ;; RESTORE PC
      .=1000

.SBTTL APT PARAMETER BLOCK
      ;*****
      ;SET LOCATIONS 24 AND 44 AS REQUIRED FOR APT
      ;*****
      .$X=.      ;;SAVE CURRENT LOCATION
      .=24      ;;SET POWER FAIL TO POINT TO START OF PROGRAM
      200      ;;FOR APT START UP
      .=44      ;;POINT TO APT INDIRECT ADDRESS PNTR.
      $APTHDR   ;;POINT TO APT HEADER BLOCK
      .=$X      ;;RESET LOCATION COUNTER
      ;*****
      ;SETUP APT PARAMETER BLOCK AS DEFINED IN THE APT-PDP11 DIAGNOSTIC
      ;INTERFACE SPEC.
      $APTHD:
      $HIBTS: .WORD 0      ;;TWO HIGH BITS OF 18 BIT MAILBOX ADDR.
      $MBADR: .WORD $MAIL  ;;ADDRESS OF APT MAILBOX (BITS 0-15)
      $STMT:  .WORD 280.   ;;RUN TIM OF LONGEST TEST
      $PASTM: .WORD 600.   ;;RUN TIME IN SECS. OF 1ST PASS ON 1 UNIT (QUICK VERIFY)
```

1532 001010 001130
1533 001012 000042

SUNITM: .WORD 600. ;:ADDITIONAL RUN TIME (SECS) OF A PASS FOR EACH ADDITIONAL UNIT
.WORD \$ETEND-\$MAIL/2 ;:LENGTH MAILBOX-ETABLE(WORDS)

1534
1535
1536
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1587

```
.LIST MD
:USE LOOP X TO OMIT JSR PC, SUBCLR
:
.MACRO LOOP A
  SCOP1
  MOV #STACK,SP ;RESTORE STK PTR
. IF B A
  JSR PC,SUBCLR
  ERROR 24 ;CERR AFTER SCLR
.ENDC
.ENDM LOOP

: THIS MACRO FILLS EXPECTED MSG A0,B0,A1,B1,A2,B2 & B3 WITH STANDARD BITS SET
: A=D.DSC AFTER ATTN OR 0 AFTER DRIVE CLEAR OR ANY IMPLIED SEEKS
: NOTE: A CAN BE ANY BIT COMBINATION DESIRED
.MACRO F.EAB A
  MOV #<A!D.SPIN!D.DRDY!D.VV!D.DRA>,E.A0 ;EXPECTED MSG A0
  CLR E.B0 ;EXPECTED MSG B0
  MOV #<D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1 ;EXPECTED A1
  MOV #1,E.B1 ;MSG ID FOR EXPECTED MSG B1
  CLR E.A2 ;EXPECTED MSG A2
  MOV #2,E.B2 ;MSG ID FOR EXPECTED MSG B2
  MOV #3,E.B3 ;MSG ID FOR EXPECTED MSG B3
.ENDM F.EAB

: THIS MACRO ASSUMES DRIVE MSG A0,B0,A1,B1 WILL ALWAYS BE TESTED
: USE A,C,D,E FOR MSG A0,B0,A1,B1 ERROR NUMBERS RESP.
: USE G=T.A2 TO READ MSG A2 & PUT INFO INTO 'CYLDIF'
: H=T.B2 TO READ MSG B2 & PUT INFOR INTO 'CYLADD'
: I=T.B3 TO READ MSG B3 & PUT INFO INTO 'SECTOR' & 'HEAD'
:
: F= < ERROR DESCRIPTION>
.MACRO CHECK A,C,D,E,F,G,H,I
  JSR PC,CHKMSG ;CHECK MSGS A0,B0,A1,B1
  .WORD G!H!I ;& MSGS SPECIFIED HERE
  ERROR A ;MSG A0 ERROR F
  ERROR C ;MSG B0 ERROR
  ERROR D ;MSG A1 ERROR
  ERROR E ;MSG B1 ERROR
.ENDM CHECK

: A=CYL DIFF/OFFSET ERROR #
: B=CYL ADDR ERROR #
```



```
1588 ; C= <ERROR DESCRIPTION>
1589 ;
1590 .MACRO CWD2 A,B,C,?D,?E
1591
1592     MOV #2,RKMR1(R5) ;SELECT WORD 2
1593     JSR PC,GSTAT
1594     TST CYLDIF ;SEE IF MSG A2=0
1595     BEQ D ;BR IF YES
1596     ERROR A ;MSG A2 NOT CLEARED C
1597 D:   TST CYLADD ;SEE IF MSG B2=0
1598     BEQ E ;BR IF YES
1599     ERROR B ;MSG B2 NOT CLEARED C
1600 E:
1601 .ENDM CWD2
1602
1603
1604 .MACRO LPCHK ?A
1605     CLR $ESCAPE
1606     TST LPFLG
1607     BEQ A
1608     JMP @ $LPERR ;SW 9 WAS SET.
1609 A:   JMP @ $LPADR ;SW 14 OR 8 WAS SET
1610 .ENDM LPCHK
1611
1612 .MACRO SW814
1613     JSR PC,SWTST ;SEE IF SW 14 OR 8 IS SET
1614     SKIP R,<GO TO NEXT TEST> ;RETURN HERE IF NEITHER IS SET
1615 ;RETURN HERE IF SW 14 IS SET OR
1616 ;SW 8 WITH SWR <7:0> APPLY
1617 .ENDM SW814
1618
1619 ;
1620 ;SWR9 (LOOP ON ERROR) TEST A=BRANCH POINT TO RECONDITION DRIVE
1621 ; B=JMP POINT TO RE-ENTER MAIN LINE
1622 .MACRO TSTSW9 A,B
1623     INC LPFLG
1624     BIT #SW9,@SWR ;LOOP ON ERROR?
1625     BNE A ;YES, RECONDITION DRIVE
1626     JMP B ;RETURN TO MAINLINE
1627 .ENDM TSTSW9
1628
1629 ;
1630 ;
1631 ; USE DRCLR X TO OMIT CHECKING MSG A0,B0,A1 & B1
1632 ;
1633 .MACRO DRCLR A,?C
1634
1635     MOV #CCLR,RKCS1(R5)
1636     MOV $UNIT,RKCS2(R5) ;DRIVE#
1637     MOV #CLEAR,HCS1
1638     JSR PC,DOCMD ;DO DRIVE CLEAR CMD & GET CONTR RDY
1639     ERROR 151 ;NO RDY AFTER DRIVE CLEAR CMD
1640     JSR PC,TSTATN ;TEST FOR ATTN
1641     BR C
1642     ERROR 154 ;ATTN NOT CLEARED AFTER DRIVE CLEAR CMD
1643 C:
```

```
1644 .IF B A
1645 F.EAB 0
1646 CHECK 273,265,274,266,<AFTER DRIVE CLEAR CMD>,T.A2,T.B2,0
1647 .ENDC
1648
1649 .ENDM DRCLR
1650
1651
1652
1653 ;A=BLANK TO CHECK A0 THRU B2
1654 ;A=NON BLANK TO OMIT CHECKING A0 THRU B2
1655 ;D=BLANK TO CHECK A0 THRU B2 IN DRCLR
1656 ;D=NON-BLANK TO OMIT CHECKING A0 THRU B2 IN DRCLR
1657 ;THE FOLLOWING MACRO DEFINITION IS CHANGED ON 24-OCT-77
1658 1. ADD NEW LABEL ?F
1659 2. DEL THE MACRO CALL CWD2
1660 3. ADD 5 LINES LONG HAND CODING FOR SUBSTITUTING ITEM 2
1661 4. SELECT MESSAGE 2
1662
1663 .MACRO CALIB A,D,?C,?F
1664
1665 MOV #CCLR,RKCS1(R5)
1666 MOV $UNIT,RKCS2(R5)
1667 MOV #RECAL,HCS1
1668 JSR PC,DOCMD ;DO RECAL CMD & GET CONTR RDY
1669 ERROR 124 ;RDY NOT SET AFTER RECAL CMD
1670
1671 MOV #1,RKMR1(R5) ;SELECT WORD 1
1672 JSR PC,GSTAT
1673 BIT #D.RTZ,HMR2
1674 BNE C
1675 ERROR 244 ;RTZ NOT SET DURING RECAL CMD
1676 C: MOV T10,TEMP2 ;SETUP TIMEOUT
1677 JSR PC,FATT1 ;FIND ATTN
1678 ERROR 55 ;NO ATTN AFTER RECAL CMD
1679 .IF B A
1680 F.EAB D.DSC
1681 CHECK 221,275,222,276,<AFTER RECAL CMD>,T.A2,T.B2,T.B3
1682 : CWD2 47,50,<AFTER RECAL CMD>
1683 MOV #2,RKMR1(R5) ;SELECT THE MESSAGE
1684 JSR PC,GSTAT ;GET THE STATUS
1685 TST CYLADD ;RECAL SUCCESSFUL ?
1686 BEQ F ;BRANCH IF SO
1687 ERROR 50 ;REPORT THE ERROR
1688 F:
1689 .ENDC
1690 DRCLR D
1691
1692 .ENDM CALIB
1693
1694 ;IDAE IS CLEARED ONLY BY RECAL & DRIVE CLEAR
1695
1696 .MACRO CIDAE ?A
1697
1698 MOV #CCLR,RKCS1(R5)
1699
```



```
1700          MOV      $UNIT,RKCS2(R5)
1701          MOV      #RECAL,HCS1
1702          JSR      PC,DOCMD          ;DO RECAL CMD & GET CONTR RDY
1703          ERROR    124              ;RDY NOT FOUND AFTER RECAL CMD
1704          DRCLR   X
1705
1706          JSR      PC,GSTAT
1707          BIT      #D.IDAE,HMR3      ;SEE IF IDAE IS CLEARED
1708          BEQ      A                ;BR IF YES
1709          ERROR    155              ;IDAE NOT CLEARED AFTER RECAL CMD
1710
1711 A:          MOV      #CCLR,RKCS1(R5)
1712          MOV      T1,TEMP2          ;LOOK FOR ATTN FROM RECAL
1713          JSR      PC,FATT1
1714          ERROR    55                ;NO ATTN AFTER RECAL CMD
1715
1716          .ENDM   CIDAE
1717
1718          ;
1719          ; A=D.FWD/D.REV
1720          ;
1721          .MACRO  SKRDY A
1722
1723          MOV      #SEEK,HCS1
1724          JSR      PC,DOCMD          ;DO SEEK CMD & GET CONTR READY
1725          ERROR    131              ;NO RDY AFTER SEEK CMD
1726          MOV      #<D.PIP!D.SPIN!D.VV!D.DRA>,E.A0 ;EXPECTED A0
1727          CLR      E.B0
1728          MOV      #<A!D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1
1729          MOV      #1,E.B1
1730          CHECK    203,204,205,206,<DURING SEEK CMD>,T.A2,T.B2,0
1731
1732          .ENDM   SKRDY
1733
1734          .MACRO  SKATN ?A,?B
1735
1736          JSR      PC,FATT2          ;FIND ATTN
1737          ERROR    132              ;NO ATTN AFTER SEEK CMD
1738          BIT      #CERR,HCS1
1739          BEQ      A
1740          ERROR    210              ;CERR AFTER SEEK CMD
1741 A:          F.EAB  D.DSC
1742          CHECK    133,134,135,136,<AFTER SEEK CMD>,T.A2,T.B2,0
1743          TST     CYLDIF
1744          BEQ      B
1745          ERROR    137              ;CYL DIFF NOT CLEARED AFTER SEEK CMD
1746
1747 B:          DRCLR  SKATN
1748          .ENDM
1749
1750          ;
1751          ; QUICK START SPINDLE.
1752          ;
1753          .MACRO  QKSRT A
1754
1755          JSR      PC,SUBCLR
```

```
1756          ERROR 24          ;CERR AFTER SCLR
1757
1758          MOV      #SRTSPL,HCS1
1759          JSR      PC,DOCMD    ;DO START SPINDLE CMD & GET CONTR RDY
1760          ERROR 121          ;RDY NOT FOUND AFTER ST SPIN CMD.
1761
1762          MOV      T500,TEMP2  ;SETUP TIMEOUT
1763          JSR      PC,FATT1    ;FIND ATTN
1764          ERROR 67           ;NO ATTN AFTER ST SPIN CMD.
1765
1766          CLR      UNLD
1767          .IF B      A
1768          TSTSW9 10$,2$
1769          .ENDC
1770          .ENDM      QKSRT
1771
1772          ;
1773          ;QUICK SEEK.  ENTER WITH CYL # IN RKDC
1774          ;
1775          .MACRO    QKSEEK  ?A
1776
1777          MOV      #SEEK,HCS1
1778          JSR      PC,DOCMD    ;DO SEEK CMD & GET CONTR READY
1779          ERROR 131          ;NO RDY AFTER SEEK CMD.
1780
1781          MOV      T50000,TEMP1
1782          JSR      PC,FATT2    ;FIND ATTN
1783          ERROR 132          ;NO ATTN AFTER SEEK CMD
1784          BIT      #CERR,HCS1
1785          BEQ      A
1786          ERROR 210          ;CERR AFTER SEEK CMD.
1787
1788          A:        JSR      PC,SUBCLR
1789          ERROR 24           ;CERR AFTER SCLR
1790
1791          .ENDM      QKSEEK
1792
1793          ;
1794          ;QUICK REPETITIVE SEEKS
1795          ; A=INC/DEC  CYL#
1796          ; B=FINAL VALUE OF CYL# BEFORE EXITING
1797          ;
1798          .MACRO    QKRPSK  A,B,?C,?D
1799
1800          JSR      PC,SUBCLR
1801          ERROR 24           ;CERR AFTER SCLR
1802
1803          C:        MOV      TOCYL,RKDC(R5) ;CYL#
1804          QKSEEK
1805          CMP      TOCYL,B ;LAST CYL DONE?
1806          BEQ      D          ;BR IF YES
1807          A        TOCYL      ;ELSE DO ANOTHER
1808          BR      C
1809
1810          D:        JSR      PC,SUBCLR
1811          ERROR 24           ;CERR AFTER SCLR
```



```
1812
1813          LPCHK
1814
1815      .ENDM  QKRPSK
1816
1817      :
1818      :QUICK UNLOAD
1819      :D=BLANK TO DO SUBCLR & LPCHK
1820      :D=NON-BLANK TO BYPASS
1821      :
1822      .MACRO QKUNLD D
1823
1824          JSR    PC,SUBCLR
1825          ERROR  24          ;CERR AFTER SCLR
1826
1827          MOV    #UNLOAD,HCS1
1828          JSR    PC,DOCMD    ;DO UNLOAD CMD & GET CONTR READY
1829          ERROR  11          ;RDY NOT SET AFTER UNLOAD CMD.
1830          JSR    PC,TSTATN
1831          ERROR  12          ;NO ATTN AFTER UNLOAD CMD
1832
1833          JSR    PC,SUBCLR
1834          ERROR  24          ;CERR AFTER SCLR
1835
1836          MOV    T10,TEMP2
1837          JSR    PC,FSPOK
1838          ERROR  315        ;SPEED NOT DOWN BY TIMEOUT
1839      .IF    B    D
1840
1841          JSR    PC,SUBCLR
1842          ERROR  24          ;CERR AFTER SCLR
1843
1844          LPCHK
1845      .ENDC
1846
1847
1848
1849      .ENDM  QKUNLD
1850
1851      :
1852      : A=WRHEAD/<CFMT!WRHEAD>
1853      : USE WRHDR <A>,X TO OMIT CHECKING A0,B0,A1,B1
1854      :
1855      .MACRO WRHDR A,C,?D
1856
1857          MOV    #<A>,HCS1
1858          JSR    PC,DATCMD    ;DO DATA XFER CMD & GET CONTR RDY
1859          ERROR  200        ;NO RDY AFTER WRITE HEADER CMD
1860          JSR    PC,GSTAT    ;GET FRESH STATUS
1861          BIT    #CERR,HCS1
1862          BEQ    D
1863          ERROR  201        ;CERR AFTER WRITE HEADER CMD
1864          TYPE   ,MSG18      ;ABORTING BALANCE OF TESTS
1865          JMP    $EOP        ;ABORT DRIVE
1866      D:
1867      .IF B C
```

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```
      F.EAB 0
      CHECK 277,267,300,270,<AFTER WRITE HEADER CMD>,T.A2,T.B2,0
.ENDC
.ENDM WRHDR
:
:A=RDHEAD/<CFMT!RDHEAD>
:USE RDHDR <A>,X TO OMIT CHECKING A0,B0,A1,B1
:MACRO RDHDR A,C,?D,?E
      MOV #RHTAB,RO
      MOV #<A>,HCS1
      JSR PC,DATCMD ;DO DATA XFER CMD & GET CONTR RDY
      ERROR 171 ;NO RDY AFTER READ HEADER CMD
      BIT #CERR,HCS1
      BEQ D
      ERROR 174 ;CERR AFTER READ HEADER CMD
      TYPE ,MSG18 ;ABORT BALANCE OF TESTS
      JMP $EOP ;ABORT DRIVE
D:    MOV RKDB(R5),(R0)+ ;1'ST WORD FROM SILO TO RHTAB
      MOV RKDB(R5),(R0)+ ;2'ND WORD
      MOV RKDB(R5),(R0)+ ;3'RD WORD
      BIT #DLT,RKCS2(R5)
      BEQ E
      JSR PC,GSTAT
      ERROR 173 ;DLT AFTER READ HEADER CMD
      TYPE ,MSG18 ;ABORTING BALANCE OF TESTS
      JMP $EOP ;ABORT DRIVE
E:
:IF B C
      F.EAB 0
      CHECK 301,271,302,272,<AFTER READ HEADER CMD>,T.A2,T.B2,0
.ENDC
.ENDM RDHDR
:
:A=TOCYL/FRCYL
:B=310 FOR TOCYL/311 FOR FRCYL
:
:MACRO HDCHK3 A,B,?C
      RDHDR RDHEAD,X
      CMP RHTAB,A ;CHECK WORD 0 (CYL#) ONLY
      BEQ C ;BR IF SAME
      ERROR B ;READ CYL WORD HEADER ERROR
C:
.ENDM HDCHK3
```



```
1924 .MACRO RALLHD ?A,?B,?C,?D,?E
1925
1926     MOV     #RHTAB,R0
1927
1928 A:     MOV     #RDHEAD,HCS1
1929     JSR     PC,DATCMD      ;DO READ HEADER CMD & GET CONTR RDY
1930     ERROR   171           ;NO RDY AFTER READ HEADER CMD
1931     BIT     #CERR,HCS1
1932     BEQ     B
1933     ERROR   174           ;CERR AFTER READ HEADER CMD
1934     TYPE    ,MSG18        ;ABORTING BALANCE OF TESTS
1935     JMP     $EOP          ;ABORT DRIVE
1936
1937 B:     MOV     RKDB(R5),(R0)+ ;1'ST WORD FROM SILO TO RHTAB
1938     MOV     RKDB(R5),(R0)+ ;2'ND WORD
1939     MOV     RKDB(R5),(R0)+ ;3'RD WORD
1940
1941     BIT     #DLT,RKCS2(R5) ;SEE IF DATA LATE
1942     BEQ     C
1943     JSR     PC,GSTAT
1944     ERROR   173           ;DATA LATE ON READ HEADER
1945     TYPE    ,MSG18        ;ABORT BALANCE OF TESTS
1946     JMP     $EOP          ;ABORT DRIVE
1947
1948 C:     CMP     R0,#RHTAB+132. ;ALL 66 WORDS DONE?
1949     BNE     A             ;BR IF NO
1950
1951     JSR     PC,SORT        ;SORT RHTAB INTO SRTTAB SO THAT IT
1952                             ;BEGINS WITH SECTOR 0
1953     CLR     WDCNT          ;WORD COUNT
1954     MOV     #SRTTAB,R0    ;ACTUAL HEADER TABLE
1955     MOV     #HDTAB,R1     ;CALC HEADER TABLE
1956
1957 D:     MOV     (R0)+,HDWD
1958     MOV     (R1)+,TEMP1
1959     CMP     HDWD,TEMP1    ;COMPARE ACTUAL WITH CALCULATED WORD
1960     BEQ     E             ;BR IF COMPARE
1961     ERROR   202          ;READ HEADER MISMATCH
1962
1963 E:     INC     WDCNT
1964     CMP     WDCNT,#66.    ;ALL WORDS DONE?
1965     BNE     D             ;BR IF NO
1966
1967 .ENDM RALLHD
1968
1969
1970
1971
1972 ; A=TOCYL/FRCYL , B=HEAD# , C = 0 FOR 22 SECTOR, 1 FOR 20 SECTOR
1973
1974 .MACRO HDTBL A,B,C
1975
1976     MOV     A,CALADD      ;SETUP
1977     MOV     #B,HEAD       ;TO FILL
1978     MOV     #C,FORMAT     ;HEADER
1979     JSR     PC,FHDTAB     ;TABLE
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```
.ENDM HDTBL
:
:USE FSECA FS022,RDSEC,22 FOR 22 SECTOR FORMAT
:USE FSECA FS020,R20SEC,20 FOR 20 SECTOR FORMAT.
:
:MACRO FSECA A,B,C
:
: FIND SECTOR 0 IN C SECTOR FORMAT.
: ERROR FLAGGED IF NOT FOUND BY TIMEOUT
:
A:      MOV      TEMP1,-(SP)      ;SAVE TEMP1
        MOV      T5000,TEMP1    ;SETUP TIMEOUT
1$:     JSR      PC,B            ;READ SECTOR
        TST      SECTOR        ;LOOK FOR SECTOR C
        BNE     2$
        JSR      PC,B
        TST      SECTOR
        BEQ     3$              ;BR IF SAME TWICE
2$:     DEC      TEMP1
        BNE     1$              ;TRY AGAIN IF TIMEOUT NOT UP
        MOV      (SP)+,TEMP1    ;ELSE RESTORE TEMP1
        RTS     PC              ;EXIT
3$:     MOV      (SP)+,TEMP1
        ADD     #2,(SP)         ;SKIP OVER ERROR
        RTS     PC
:
.ENDM
:
:USE FSECB FNS22,RDSEC,22 FOR 22 SECTOR FORMAT
:USE FSECB FNS20,R20SEC,20 FOR 20 SECTOR FORMAT
:
:MACRO FSECB A,B,C
:
: FIND NEXT SECTOR IN C SECTOR FORMAT
: ERROR FLAGGED IF NOT FOUND BY TIMEOUT
:
```



```
2018 A: MOV TEMP1,-(SP) ;SAVE TEMP 1
2019 MOV T500,TEMP1 ;SETUP TIMEOUT
2020 1$: JSR PC,B ;READ SECTOR
2021 CMP PSEC,SECTOR
2022 BEQ 3$ ;BR IF SAME
2023 JSR PC,B ;ELSE TRY READ DIFFERENT TWICE
2024 CMP PSEC,SECTOR
2025 BNE 2$ ;BR IF DIFFERENT TWICE
2026 3$: DEC TEMP1 ;ELSE TRY AGAIN IF TIME LEFT
2027 BNE 1$
2028 MOV (SP)+,TEMP1 ;RESTORE TEMP 1
2029 RTS PC
2030 2$: MOV (SP)+,TEMP1 ;RESTORE TEMP 1
2031 ADD #2,(SP) ;SKIP OVER ERROR
2032 RTS PC
2033 .ENDM
2034
2035 ;
2036 ;USE SECTST FS022,FNS22,RDSEC FOR 22 SECTOR FORMAT
2037 ;USE SECTST FS020,FNS20,R20SEC FOR 20 SECTOR FORMAT
2038 ;
2039 ;THE FOLLOWING MACRO DEFINITION IS CHANGED ON 24-OCT-77
2040 ; 1. DETET 8 LINES FROM THE CODING
2041 .MACRO SECTST D,E,F?A,?B,?C
2042
2043 JSR PC,D ;FIND SECTOR 0
2044 ERROR 142 ;SECTOR 0 NOT FOUND BY TIMEOUT
2045
2046 CLR PSEC ;PREVIOUS SECTOR
2047 A: JSR PC,E ;FIND NEXT SECTOR
2048 ERROR 143 ;DIFFERENT SECTOR NOT FOUND BY TIMEOUT
2049 MOV PSEC,ESEC
2050 ADD #1,ESEC ;SETUP EXPECTED SECTOR
2051 MOV SECTOR,PSEC ;UPDATE PREV SECTOR
2052 JSR PC,F ;READ SECTOR
2053 CMP SECTOR,PSEC
2054 BEQ B ;BR IF READ SAME TWICE
2055 JSR PC,F
2056 CMP SECTOR,PSEC
2057 BEQ B ;TRY 1 MORE TIME
2058 ERROR 144 ;MSG B3 ERROR, SECTOR REG UNSTABLE
2059 ;MAY BE DURING SECTOR PULSE TIME
2060 B: CMP SECTOR,ESEC
2061 BEQ C
2062 ERROR 145 ;MSG B3 ERROR BETWEEN SECTOR COUNTS
2063 C: DEC SECT
2064 BNE A ;BR IF SECTOR COUNT NOT DONE
2065
2066 .ENDM SECTST
2067
2068 ;
2069 ;
2070 ;DETECT OUTER LIMIT: FCP1,FC,D.REV,OUTER
2071 ;DETECT INNER LIMIT: LCM1,LC,D.FWD,INNER
2072 ;
2073 ;THE FOLLOWING MACRO DEFINITION IS CHANGED ON 24-OCT-77
```

```
2074 ;1. ADD NEW LOCAL LABEL ?F
2075 ;2. DELET CHECK MACRO CALL ON LIMIT DETECT
2076 ;3. DEL CDW2 MCARD CALL
2077 .MACRO LIMIT A,B,C,D,?F
2078
2079 JSR PC,SUBCLR ;SUBSYS CLEAR & GET STATUS
2080 ERROR 24 ;CERR AFTER SCLR
2081
2082 CLR LPFLG
2083 INC BYPCERR ;BYPASS CHECKING FOR ANY CERR IN GSTAT1
2084 INC UNLD ;USED FOR VALID HALT
2085
2086 MOV #PAT,RKMR1(R5) ;PARITY & WORD 0
2087 MOV A,RKDC(R5)
2088 MOV #SEEK,HCS1
2089 JSR PC,DOCMD ;DO SEEK CMD & GET CONTR READY
2090 ERROR 122 ;NO RDY FROM SEEK WITH BAD PARITY
2091 JSR PC,TSTATN ;TEST FOR ATTN
2092 ERROR 125 ;NO ATTN FROM SEEK WITH BAD PARITY
2093 MOV #<D.DSC!D.SPIN!D.DRDY!D.VV!D.DRA>,E.A0 ;EXPECTED A0
2094 MOV #<D.FLT!D.PAR>,E.B0
2095 MOV #<D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1
2096 MOV #1,E.B1
2097 CHECK 110,111,146,147,<AFTER SEEK WITH BAD PARITY>,0,0,0
2098 DRCLR
2099
2100 MOV B,RKDC(R5)
2101 MOV #SEEK,HCS1
2102 JSR PC,DOCMD ;DO SEEK CMD & GET CONTR READY
2103 ERROR 131 ;NO RDY AFTER SEEK CMD
2104 MOV #CCLR,RKCS1(R5)
2105 JSR PC,GSTAT
2106 JSR PC,FLIM ;FIND LIMIT DETECT
2107 ERROR 160 ;LIMIT DETECT NOT FOUND BEFORE TIMEOUT
2108
2109 BIT #D.UNLD,HMR2
2110 BNE 1$
2111 ERROR 305 ;DRIVE NOT UNLOADING AFTER LIMIT DETECT
2112 JMP 30$ ;BYPASS REST OF TEST
2113
2114 1$: MOV #20$, $ESCAPE ;MUST ESCAPE TO CYCLE UP DRIVE & TEST SWR
2115 MOV #<D.DSC!D.PIP!D.SPIN!D.VV!D.DRA>,E.A0 ;EXPECTED A0
2116 MOV #<D.SKI!D.FLT>,E.B0
2117 MOV #<D.UNLD!D.REV!D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1
2118 MOV #<D.LIMD!D.NMOV!1>,E.B1
2119 ; CHECK 161,162,163,164,<AFTER D LIMIT DETECT>,0,0,0
2120 JSR PC,CHKMSG ;CHECK MESSAGE A0,B0,A1,B1
2121 .WORD 0!0!0
2122 ERROR 161 ;MAY BE A0 ERROR
2123 ERROR 162 ;MAY BE B0 ERROR
2124 ERROR 163 ;MAY BE B1 ERROR
2125 NOP ;NEED FOR THE CALLIN SEQ
2126 BIT #D.LIMD,H.B1 ;SEE IF LIMIT DETECT BIT SET IN B1
2127 BNE 3$ ;BRANCH IF 0
2128 TYPE ,EM42 ;OTHERWISE REPORT ERROR
2129 ERROR 164
```



```
2130  
2131      3$:      JSR      PC,TSTATN  
2132      ERROR   165      ;NO ATTN AFTER D LIMIT DETECT  
2133      CLR      BYPCERR  ;ALLOW CHECKING CERR IN GSTAT1  
2134  
2135      JSR      PC,SUBCLR  ;SUBSYS CLR  
2136      ERROR   24      ;CERR AFTER SCLR  
2137      MOV      T10,TEMP2 ;SET UP TIMEOUT  
2138      JSR      PC,FHDHM  ;FIND HEAD HOME  
2139      ERROR   166      ;HEAD HOME NOT FOUND BEFORE TIMEOUT  
2140      JSR      PC,FLOAD  ;FIND LOAD HEADS  
2141      ERROR   167      ;LOAD HEADS NOT FOUND BEFORE TIMEOUT  
2142      MOV      T100,TEMP2 ;SETUP TIMEOUT  
2143      JSR      PC,FATT1  ;FIND ATTN  
2144      ERROR   67      ;ATTN NOT FOUND BEFORE TIMEOUT  
2145      2$:      CLR      $ESCAPE  
2146      CLR      UNLD      ;CLEAR FLAG  
2147      F.EAB    D.DSC  
2148      CHECK   63,64,65,66,<AT END OF HEAD LOADING>,T.A2,T.B2,0  
2149      ;      CWD2     175,176,<AT END OF HEAD LOADING>  
2150      MOV      #2,RKMR1(R5) ;SELECT MESSAGE  
2151      JSR      PC,GSTAT  ;GET STATUS AND MR2,MR3  
2152      TST     CYLADD     ;RECAL SUCCFUL ?  
2153      BEQ     F          ;BRANCH IF SO  
2154      ERROR   50      ;REPORT ERROR  
2155      F:      DRCLR  
2156      SW814  
2157  
2158  
2159      .ENDM   LIMIT  
2160  
2161  
2162      ;  
2163      ; A=CYL#, B=HEAD#  
2164      ;  
2165      .MACRO  HEADER  A,B  
2166  
2167      NEWTST  <<WRITE & READ HEADERS CYL A, HEAD B>>,1  
2168      MOV     #STACK,SP  ;RESTORE STK PTR  
2169  
2170      JSR     PC,SUBCLR  ;CERR AFTER SCLR  
2171      ERROR   24  
2172  
2173      INC     BYPFMT     ;SET BIT 14 & 15 IN HEADER  
2174  
2175      MOV     #HDTAB,RKBA(R5) ;HEADER WORD TABLE  
2176      MOV     #-66.,RKWC(R5) ;WORD COUNT.  
2177      MOV     #A,TOCYL  
2178      HDTBL  TOCYL,0,0  
2179      MOV     #A,RKDC(R5)  ;CYL#  
2180      WRHDR  WRHEAD  
2181      CLR     SECNT      ;SECTOR COUNT  
2182      LOOP  
2183      MOV     #A,RKDC(R5)  ;CYL #  
2184      RALLHD  
2185
```

```
2186          CLR BYPFMT          ;ALLOW CORRECT FORMATTING
2187
2188          .ENDM HEADER
2189
2190
2191
2192
2193          ;SEEK TO MAJOR CYL: FC,FCP1,TEMP3,TEMP4,D.FWD,D.REV,ASL,MC,DEC,FC
2194          ;SEEK 0 TO ALL CYL: FC,FCP1,TEMP3,TEMP4,D.FWD,D.REV,INC,LC,DEC,FC
2195          ;SEEK 410 TO ALL CYL: LC,LCM1,TEMP4,TEMP3,D.REV,D.FWD,DEC,FC,INC,LC
2196
2197          .MACRO SKOSC A,B,C,D,E,F,G,H,I,J
2198
2199          MOV A,FRCYL ;SETUP FROM CYL
2200          MOV B,TOCYL ;SETUP TO CYL
2201
2202          1$: LOOP
2203             MOV #10$, $ESCAPE
2204             MOV FRCYL,TEMP3 ;SETUP
2205             MOV TOCYL,TEMP4 ;CYL DIFF
2206             SUB C,D ;FOR
2207             MOV D,CALDIF ;ERROR PRINTOUT
2208
2209             MOV TOCYL,RKDC(R5) ;GO TO CYL #
2210             SKRDY E
2211          2$: MOV #12$, $ESCAPE
2212             MOV T50000,TEMP1 ;SETUP TIMEOUT
2213             SKATN
2214             CMP CYLADD,TOCYL
2215             BEQ 3$
2216             ERROR 207 ;CYL ADDR IN RKMR3 NOT=RKDC
2217
2218          3$: LOOP
2219             CLR $ESCAPE
2220             MOV TOCYL,RKDC(R5) ;CYL #
2221             HDCHK3 TOCYL,310
2222
2223             LOOP
2224             MOV #14$, $ESCAPE
2225             MOV FRCYL,RKDC(R5) ;RETURN TO CYL #
2226             MOV FRCYL,CCYL ;CURRENT CYL FOR TRUERROR ROUTINE
2227             SKRDY F
2228
2229          4$: MOV #16$, $ESCAPE
2230             MOV T50000,TEMP1 ;SETUP TIMEOUT
2231             SKATN
2232             CMP CYLADD,FRCYL
2233             BEQ 5$
2234             ERROR 243 ;CYL ADDR IN RKMR3 NOT=RKDC
2235
2236          5$: LOOP
2237             CLR $ESCAPE
2238             MOV FRCYL,RKDC(R5) ;CYL #
2239             HDCHK3 FRCYL,311
2240
2241             CMP TOCYL,H ;ALL CYL DONE?
```



```
2242          BEQ      6$          :BR IF YES
2243          G        TOCYL      :ELSE DO ANOTHER
2244          JMP      1$
2245          6$:      SW814
2246          8$:      QKRPSK     I,J
2247          10$:     TSTSW9     8$,2$
2248          12$:     TSTSW9     8$,3$
2249          14$:     TSTSW9     8$,4$
2250          16$:     TSTSW9     8$,5$
2251          .ENDM   SKOSC
2252
2253          .MACRO  EOPGM
2254
2255          SCOPE
2256          CLR      $ESCAPE
2257          MOV      #1,$TIMES
2258          MOV      #STACK,$SP
2259          INC      $DEVCT
2260          CMP      DRIVS,$DEVCT :INCR COUNT FOR # OF DRIVES THAT ARE CHECKED
2261          BEQ      1$          :ARE ALL DRIVES PRESINT TESTED?
2262          CLR      BSERR      :BR IF YES
2263          JMP      NUDRV     :CLEAR BAD SECTOR ERROR FLAG
2264          1$:      CLR      BSERR :IF NOT , TEST NEXT DRIVE PRESENT
2265          BR       $EOP1+2   :CLEAR BAD SECTOR ERROR FLAG
2266          $EOP1:  SCOPE
2267          .ENDM   EOPGM
2268
2269          .NLIST  MD
```

```
2270 .SBTTL COMMON TAGS
2271
2272 ::*****
2273 ::*THIS TABLE CONTAINS VARIOUS COMMON STORAGE LOCATIONS
2274 ::*USED IN THE PROGRAM.
2275
2276 001100 .=1100
2277 001100 $CMTAG: ::START OF COMMON TAGS
2278 001100 000000 .WORD 0
2279 001102 000 $TSTNM: .BYTE 0 ::CONTAINS THE TEST NUMBER
2280 001103 000 $ERFLG: .BYTE 0 ::CONTAINS ERROR FLAG
2281 001104 000000 $ICNT: .WORD 0 ::CONTAINS SUBTEST ITERATION COUNT
2282 001106 000000 $LPADR: .WORD 0 ::CONTAINS SCOPE LOOP ADDRESS
2283 001110 000000 $LPERR: .WORD 0 ::CONTAINS SCOPE RETURN FOR ERRORS
2284 001112 000000 $ERTTL: .WORD 0 ::CONTAINS TOTAL ERRORS DETECTED
2285 001114 000 $ITEMB: .BYTE 0 ::CONTAINS ITEM CONTROL BYTE
2286 001115 001 $ERMAX: .BYTE 1 ::CONTAINS MAX. ERRORS PER TEST
2287 001116 000000 $ERRPC: .WORD 0 ::CONTAINS PC OF LAST ERROR INSTRUCTION
2288 001120 000000 $GDADR: .WORD 0 ::CONTAINS ADDRESS OF 'GOOD' DATA
2289 001122 000000 $BDADR: .WORD 0 ::CONTAINS ADDRESS OF 'BAD' DATA
2290 001124 000000 $GDDAT: .WORD 0 ::CONTAINS 'GOOD' DATA
2291 001126 000000 $BDDAT: .WORD 0 ::CONTAINS 'BAD' DATA
2292 001130 000000 .WORD 0 ::RESERVED--NOT TO BE USED
2293 001132 000000 .WORD 0
2294 001134 000 $AUTOB: .BYTE 0 ::AUTOMATIC MODE INDICATOR
2295 001135 000 $INTAG: .BYTE 0 ::INTERRUPT MODE INDICATOR
2296 001136 000000 .WORD 0
2297 001140 177570 $SWR: .WORD DSWR ::ADDRESS OF SWITCH REGISTER
2298 001142 177570 $DISPLAY: .WORD DDISP ::ADDRESS OF DISPLAY REGISTER
2299 001144 177560 $TKS: 177560 ::TTY KBD STATUS
2300 001146 177562 $TKB: 177562 ::TTY KBD BUFFER
2301 001150 177564 $TPS: 177564 ::TTY PRINTER STATUS REG. ADDRESS
2302 001152 177566 $TPB: 177566 ::TTY PRINTER BUFFER REG. ADDRESS
2303 001154 000 $NULL: .BYTE 0 ::CONTAINS NULL CHARACTER FOR FILLS
2304 001155 002 $FILLS: .BYTE 2 ::CONTAINS # OF FILLER CHARACTERS REQUIRED
2305 001156 012 $FILLC: .BYTE 12 ::INSERT FILL CHARS. AFTER A 'LINE FEED'
2306 001157 000 $STPFLG: .BYTE 0 ::'TERMINAL AVAILABLE' FLAG (BIT<07>=0=YES)
2307 001160 000000 $TMP0: .WORD 0 ::USER DEFINED
2308 001162 000000 $TMP1: .WORD 0 ::USER DEFINED
2309 001164 000000 $TMP2: .WORD 0 ::USER DEFINED
2310 001166 000000 $TMP3: .WORD 0 ::USER DEFINED
2311 001170 000000 $TMP4: .WORD 0 ::USER DEFINED
2312 001172 000000 $TMP5: .WORD 0 ::USER DEFINED
2313 001174 000000 $TIMES: 0 ::MAX. NUMBER OF ITERATIONS
2314 001176 000000 $ESCAPE: 0 ::ESCAPE ON ERROR ADDRESS
2315 001200 177607 000377 $BELL: .ASCIZ <207><377><377> ::CODE FOR BELL
2316 001204 077 $QUES: .ASCII /?/ ::QUESTION MARK
2317 001205 015 $CRLF: .ASCII <15> ::CARRIAGE RETURN
2318 001206 000012 $LF: .ASCIZ <12> ::LINE FEED
2319 ::*****
2320 .SBTTL APT MAILBOX-ETABLE
2321
2322 ::*****
2323 .EVEN
2324 001210 $MAIL: ::APT MAILBOX
2325 001210 000000 $MSGTY: .WORD AMSGTY ::MESSAGE TYPE CODE
```


2326	001212	000000	\$I ATAL: .WORD	AFATAL	:: FATAL ERROR NUMBER
2327	001214	000000	\$TESTN: .WORD	ATESTN	:: TEST NUMBER
2328	001216	000000	\$PASS: .WORD	APASS	:: PASS COUNT
2329	001220	000000	\$DEVCT: .WORD	ADEVCT	:: DEVICE COUNT
2330	001222	000000	\$UNIT: .WORD	AUNIT	:: I/O UNIT NUMBER
2331	001224	000000	\$MSGAD: .WORD	AMSGAD	:: MESSAGE ADDRESS
2332	001226	000000	\$MSGLG: .WORD	AMSGLG	:: MESSAGE LENGTH
2333	001230		\$ETABLE:		:: APT ENVIRONMENT TABLE
2334	001230	000	\$ENV: .BYTE	AENV	:: ENVIRONMENT BYTE
2335	001231	000	\$ENVM: .BYTE	AENVM	:: ENVIRONMENT MODE BITS
2336	001232	000000	\$SWREG: .WORD	ASWREG	:: APT SWITCH REGISTER
2337	001234	000000	\$USWR: .WORD	AUSWR	:: USER SWITCHES
2338	001236	000000	\$CPUOP: .WORD	ACPUOP	:: CPU TYPE, OPTIONS
2339			.*		BITS 15-11=CPU TYPE
2340			.*		11/04=01,11/05=02,11/20=03,11/40=04,11/45=05
2341			.*		11/70=06,PDQ=07,Q=10
2342			.*		BIT 10=REAL TIME CLOCK
2343			.*		BIT 9=FLOATING POINT PROCESSOR
2344			.*		BIT 8=MEMORY MANAGEMENT
2345	001240	000	\$MAMS1: .BYTE	AMAMS1	:: HIGH ADDRESS, M.S. BYTE
2346	001241	000	\$MTYP1: .BYTE	AMTYP1	:: MEM. TYPE, BLK#1
2347			.*		MEM. TYPE BYTE -- (HIGH BYTE)
2348			.*		900 NSEC CORE=001
2349			.*		300 NSEC BIPOLAR=002
2350			.*		500 NSEC MOS=003
2351	001242	000000	\$MADR1: .WORD	AMADR1	:: HIGH ADDRESS, BLK#1
2352			.*		MEM. LAST ADDR.=3 BYTES, THIS WORD AND LOW OF "TYPE" ABOVE
2353	001244	000	\$MAMS2: .BYTE	AMAMS2	:: HIGH ADDRESS, M.S. BYTE
2354	001245	000	\$MTYP2: .BYTE	AMTYP2	:: MEM. TYPE, BLK#2
2355	001246	000000	\$MADR2: .WORD	AMADR2	:: MEM. LAST ADDRESS, BLK#2
2356	001250	000	\$MAMS3: .BYTE	AMAMS3	:: HIGH ADDRESS, M.S. BYTE
2357	001251	000	\$MTYP3: .BYTE	AMTYP3	:: MEM. TYPE, BLK#3
2358	001252	000000	\$MADR3: .WORD	AMADR3	:: MEM. LAST ADDRESS, BLK#3
2359	001254	000	\$MAMS4: .BYTE	AMAMS4	:: HIGH ADDRESS, M.S. BYTE
2360	001255	000	\$MTYP4: .BYTE	AMTYP4	:: MEM. TYPE, BLK#4
2361	001256	000000	\$MADR4: .WORD	AMADR4	:: MEM. LAST ADDRESS, BLK#4
2362	001260	000000	\$VECT1: .WORD	AVECT1	:: INTERRUPT VECTOR#1, BUS PRIORITY#1
2363	001262	000000	\$VECT2: .WORD	AVECT2	:: INTERRUPT VECTOR#2, BUS PRIORITY#2
2364	001264	177440	\$BASE: .WORD	ABASE	:: BASE ADDRESS OF EQUIPMENT UNDER TEST
2365	001266	000000	\$DEVN: .WORD	ADEVN	:: DEVICE MAP
2366	001270	000000	\$CDW1: .WORD	ACDW1	:: CONTROLLER DESCRIPTION WORD#1
2367	001272	000000	\$CDW2: .WORD	ACDW2	:: CONTROLLER DESCRIPTION WORD#2
2368	001274	000000	\$DDW0: .WORD	ADDW0	:: DEVICE DESCRIPTOR WORD#0
2369	001276	000000	\$DDW1: .WORD	ADDW1	:: DEVICE DESCRIPTOR WORD#1
2370	001300	000000	\$DDW2: .WORD	ADDW2	:: DEVICE DESCRIPTOR WORD#2
2371	001302	000000	\$DDW3: .WORD	ADDW3	:: DEVICE DESCRIPTOR WORD#3
2372	001304	000000	\$DDW4: .WORD	ADDW4	:: DEVICE DESCRIPTOR WORD#4
2373	001306	000000	\$DDW5: .WORD	ADDW5	:: DEVICE DESCRIPTOR WORD#5
2374	001310	000000	\$DDW6: .WORD	ADDW6	:: DEVICE DESCRIPTOR WORD#6
2375	001312	000000	\$DDW7: .WORD	ADDW7	:: DEVICE DESCRIPTOR WORD#7
2376	001314		\$ETEND:		
2377			.MEXIT		
2378		177440	ABASE=	177440	:: DEFAULT BUSS ADDRESS
2379	001314	000210	RKVEC:	210	:: DEFAULT CONTROLLER INTERRUPT VECTOR
2380	001316	000240	RKPRI:	PR5	:: PRIORITY
2381	001320	172540	PKS:	172540	:: P-CLOCK STATUS REG

2382	001322	172542	PKSB:	172542	:P-CLOCK SET BUFFER
2383	001324	172544	PKRB:	172544	:P-CLOCK READ BUFFER
2384	001326	177546	LKS:	177546	:L-CLOCK STATUS REG.
2385					
2386	001330	000100	LCVEC:	100	:L-CLOCK INTERRUPT VECTOR
2387	001332	000104	PCVEC:	104	:P-CLOCK INTERRUPT VECTOR.
2388					
2389		000114	MEMVEC=	114	:MEMORY PARITY VECTOR
2390		172100	MEMBAS=	172100	:MEMORY PARITY OPTION CSR START ADDR
2391	001334	000000	TRAPPC:	0	:PC FOR MEMORY CHECK ENABLE TRAP
2392					
2393	001336	000000	PARAM:	0	:1 FOR 220 OR 230 START, NO DEFAULT
2394	001340	000000	BYPT16:	0	:1 FOR 210, 230, 270 START
2395	001342	000000	MODTST:	0	:1 FOR 260 OR 270 START
2396	001344	000000	FTITLE:	0	:FLAG FOR PRINTING OUT 1ST PROGRAM TITLE
2397					
2398	001346	000000	DRVPTR:	0	:CONTAINS THE POINTER TO THE DRIVE FLAG
2399					: (DRIV0-DRIV7) OF THE DRIVE TO BE CHECKED NEXT.
2400	001350	000000	FRCYL:	0	:FROM CYL
2401	001352	000000	TOCYL:	0	:TO CYL
2402	001354	000000	CCYL:	0	:CURRENT CYL, USED IN N SQUARE TEST
2403	001356	000000	PCYL:	0	:PREV CYL., USED IN N SQUARE TEST
2404	001360	000000	CALDIF:	0	:CALC CYL DIFF USED IN N SQUARE TEST
2405	001362	000000	CYLDIF:	0	:CYL DIFF, RIGHT JUSTIFIED FROM RKMR3
2406	001364	000000	CYLADD:	0	:CYL ADDR, RIGHT JUSTIFIED FROM RKMR3
2407	001366	000000	CALADD:	0	:CYL ADDR USED IN FHDTAB ROUTINE
2408					
2409	001370	000074	HZ:	60.	:60 FOR 60 CPS
2410					:50 FOR 50 CPS
2411	001372	000000	COUNT:	0	:LOADED TO 50 OR 60 TO COUNT TO 1 SEC
2412					:OR ANY OTHER NUMBER TO COUNT OFF FRACTIONAL SECOND
2413	001374	000000	SEC:	0	:SECOND COUNTER
2414	001376	000000	TIMUP:	0	:FLAG TO INDICATE TIME IS UP
2415	001400	000000	SECNT:	0	:SECTOR COUNT
2416	001402	000000	PSEC:	0	:PREVIOUS SECTOR
2417	001404	000000	ESEC:	0	:EXPECTED SECTOR
2418	001406	000000	SECTOR:	0	:SECTOR COUNT, RIGHT JUSTIFIED FROM RKMR3
2419					
2420	001410	000000	LPFLG:	0	:SET TO 0 TO RETURN TO \$LPADR
2421					:IF SW14 OR SW8 SET
2422					:SET TO 1 TO RETURN TO \$LPERR
2423					:IF SW9 SET
2424	001412	000001	T1:	1	:TIMEOUT CONSTANTS
2425	001414	000012	T10:	10.	
2426	001416	000144	T100:	100.	
2427	001420	000764	T500:	500.	
2428	001422	004704	T2500:	2500.	
2429	001424	011610	T5000:	5000.	
2430	001426	141520	T50000:	50000.	
2431					
2432	001430	000000	HEAD:	0	:HEAD NUMBER
2433	001432	000000	HEADA:	0	:HEAD # FROM H.B3 RIGHT JUSTIFIED
2434	001434	000000	HD1:	0	:SHIFTED HEAD# FOR FORMATTER ROUTINE
2435	001436	000000	FORMAT:	0	:FORMAT TYPE
2436	001440	000000	FMT1:	0	:SHIFTED FORMAT FOR FORMATTER ROUTINE
2437	001442	000000	WDCNT:	0	:WORD COUNT


```
2438
2439 001444 000000 DATA0: 0 ;ALL 0'S
2440 001446 052525 DATA01: 52525 ;0101 PATT
2441 001450 177777 DATA1: 177777 ;ALL 1'S
2442
2443 001452 000000 WORD: 0 ;HEADER/DATA WORD
2444 001454 000000 HDWD: 0 ;HEADER WORD FROM RKDB
2445
2446 001456 000000 BSERR: 0 ;CANNOT READ BSE INFO WHEN SET
2447 001460 000000 LIMERR: 0 ;LIMIT DETECT ERROR FLAG
2448
2449 001462 000000 BYPCERR:0 ;SET TO 1 TO BYPASS CKCERR IN GSTAT1 ROUTINE
2450 001464 000000 BYPFMT: 0 ;BYPASS FORMAL FORMATTING OF HEADERS
2451 ;UNTIL BSE INFO HAS BEEN STORED.
2452 ;IF SET, BIT 14,15 = 1
2453
2454 001466 000000 CHKFLG: 0 ;WORDS TO BE CHECKED
2455
2456 001470 000102 HDTAB: .BLKW 66. ;CALCULATED HEADER WORD TABLE
2457 001674 000102 RHTAB: .BLKW 66. ;FILLED AFTER READ HEADER CMD
2458 002100 000102 SRTTAB: .BLKW 66. ;ABOVE RHTAB SORTED STARTING FORM
2459 ;SECTOR 0 BY SORT ROUTINE
2460 002304 000400 BSE22H: .BLKW 256. ;22 SECTOR HARDWARE BSE INFO.
2461 ;22 SECTOR SOFTWARE BSE INFO
2462 ;OVERLAYS MSG1
2463 003304 000000 UNLD: 0 ;SET TO 0 IF HEADS ARE LOADED
2464 ;SET TO 1 IF HEADS UNLOADED
2465 003306 000000 BADHDR: 0 ;SET TO 0 IF FORMATTING OK
2466 ;SET TO 1 IF FORMATTING ALTERED
2467 003310 000000 HPEND: 0 ;SET TO 0 IF HALT NOT PENDING
2468 ;SET TO 1 IF HALT PENDING
2469
2470 ;THE ABOVE 3 FLAGS ARE USED
2471 ;BY 'STOP' ROUTINE TO BRING
2472 ;THE CPU TO A VALID HALT.
2473
2474 003312 001 002 004 ATTN: .BYTE 1,2,4,10,20,40,100,200 ;ATN 0-7 RESP.
2475 003315 010 020 040
2476 003320 100 200
2477 .EVEN
2478
2479
2480
2481 ;THE FOLLOWING ARE HOLDING REGISTERS FOR THE RK611 REGISTERS
2482 ;THEY ARE LOADED AFTER RDY IS REC'D FROM WRDY ROUTINE.
2483 ;
2484
2485 003322 000000 HCS1: 0 ;HOLD RKCS1
2486 003324 000000 HCS2: 0 ;HOLD RKCS2
2487 003326 000000 HWC: 0 ;HOLD RKWC
2488 003330 000000 HBA: 0 ;ETC.
2489 003332 000000 HDA: 0
2490 003334 000000 HDS: 0
2491 003336 000000 HER: 0
2492 003340 000000 HASOF: 0
2493 003342 000000 HDC: 0
```

```
2494 003344 000000 HDB: 0
2495 003346 000000 HMR1: 0
2496 003350 000000 HMR2: 0
2497 003352 000000 HMR3: 0
2498 003354 000000 HPOS: 0
2499 003356 000000 HPAT: 0
2500
2501 003360 000000 TEMP1: 0 ;TEMPORARY STORAGE.
2502 003362 000000 TEMP2: 0
2503 003364 000000 TEMP3: 0
2504 003366 000000 TEMP4: 0
2505 003370 000000 TEMP5: 0
2506
2507 ; THE FOLLOWING ARE HOLDING REGISTERS FOR MSG A (0-3) & MSG B (0-3)
2508 ;
2509 003372 000000 H.A0: 0
2510 003374 000000 H.B0: 0
2511 003376 000000 H.A1: 0
2512 003400 000000 H.B1: 0
2513 003402 000000 H.A2: 0
2514 003404 000000 H.B2: 0
2515 003406 000000 H.A3: 0
2516 003410 000000 H.B3: 0
2517
2518 ; THE FOLLOWING ARE 'EXPECTED' REGISTER FOR THE ABOVE
2519 ;
2520 003412 000000 E.A0: 0
2521 003414 000000 E.B0: 0
2522 003416 000000 E.A1: 0
2523 003420 000000 E.B1: 0
2524 003422 000000 E.A2: 0
2525 003424 000000 E.B2: 0
2526 003426 000000 E.A3: 0
2527 003430 000000 E.B3: 0
2528
2529 ; THE FOLLOWING ARE IDENTITIES FOR DRIVE MSG WORDS TO BE TESTED
2530 ;
2531 ;
2532 000001 T.A2=BIT0 ;TEST MSG A2 IF SET
2533 000002 T.B2=BIT1
2534 000004 T.B3=BIT2
2535
2536 ;
2537 ;ALL THE FLAGS BELOW ARE CLEARED INITIALLY BY THE CLRFLG ROUTINE.
2538 ;
2539 ;
2540 003432 000000 DDUMP: 0 ;FLAG - SET WHEN IN DDP DUMP MODE
2541 003434 000000 DDPCH: 0 ;FLAG - SET WHEN IN DDP CHAIN MODE
2542 003436 000000 ACT11: 0 ;FLAG - SET WHEN IN ACT11 MODE OF OPERATION
2543 003440 000000 PPTP: 0 ;FLAG - SET WHEN PROGRAM LOADED BY PAPER TAPE
2544 003442 000000 DRIVS: 0 ;CONTAINS THE NUMBER OF DRIVES PRESENT
2545
2546 ;THE FLAGS BELOW ARE SET TO 1 TO INDICATE THAT A PARTICULAR DRIVE
2547 ;IS PRESENT AND IS TO BE TESTED.
2548
2549 003444 000000 DRIV0: 0 ;FLAG SET TO 1 WHEN DRIVE 0 PRESENT
```


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SEQ 0051

2550	003446	000000	DRIV1:	0	:FOR DRIVE 1
2551	003450	000000	DRIV2:	0	:FOR DRIVE 2
2552	003452	000000	DRIV3:	0	:FOR DRIVE 3
2553	003454	000000	DRIV4:	0	:FOR DRIVE 4
2554	003456	000000	DRIV5:	0	:FOR DRIVE 5
2555	003460	000000	DRIV6:	0	:FOR DRIVE 6
2556	003462	000000	DRIV7:	0	:FOR DRIVE 7
2557					
2558	003464	000000	LCLKF:	0	:L-CLOCK FLAG PRESENT FLAG
2559	003466	000000	PCLKF:	0	:P-CLOCK FLAG PRESENT FLAG
2560	003470	000000	DOTIM:	0	:SET IF EITHER CLOCK PRESENT FOR TIMING TESTS.
2561	003472	000000	SIZFLG:	0	:SET IF DEFAULT DO SIZING IN TEST 1

```
2562 .SBTTL ERROR POINTER TABLE
2563
2564 ;*THIS TABLE CONTAINS THE INFORMATION FOR EACH ERROR THAT CAN OCCUR.
2565 ;*THE INFORMATION IS OBTAINED BY USING THE INDEX NUMBER FOUND IN
2566 ;*LOCATION $ITEMB. THIS NUMBER INDICATES WHICH ITEM IN THE TABLE IS PERTINENT.
2567 ;*NOTE1: IF $ITEMB IS 0 THE ONLY PERTINENT DATA IS ($ERRPC).
2568 ;*NOTE2: EACH ITEM IN THE TABLE CONTAINS 4 POINTERS EXPLAINED AS FOLLOWS:
2569
2570 ;* EM ::POINTS TO THE ERROR MESSAGE
2571 ;* DH ::POINTS TO THE DATA HEADER
2572 ;* DT ::POINTS TO THE DATA
2573 ;* DF ::POINTS TO THE DATA FORMAT
2574
2575
2576 003474 $ERRTB:
2577
2578 ;ERROR 1
2579 003474 057217 EM2 ;DR # IN RKCS2 CANNOT BE READ BACK CORRECTLY IN RKMR2
2580 003476 064170 DH1
2581 003500 066276 DT1
2582 003502 067144 DF1
2583
2584 ;ERROR 2
2585 003504 057467 EM5 ;DETECTED MDS
2586 003506 064170 DH1
2587 003510 066276 DT1
2588 003512 067144 DF1
2589
2590 ;ERROR 3
2591 003514 057510 EM6 ;DETECTED UFE
2592 003516 064170 DH1
2593 003520 066276 DT1
2594 003522 067144 DF1
2595
2596 ;ERROR 4
2597 003524 057531 EM7 ;DETECTED DRA & NED RESET (WRONG PORT SELECTED?)
2598 003526 064170 DH1
2599 003530 066276 DT1
2600 003532 067144 DF1
2601
2602 ;ERROR 5
2602 003534 057620 EM8 ;DDT NOT SET IN MR2
2603 003536 064170 DH1
2604 003540 066276 DT1
2605 003542 067144 DF1
2606
2607 ;ERROR 6
2608 003544 057652 EM9 ;DR NOT PRESENT BUT SPECIFIED BY OPERATOR
2609 003546 064170 DH1
2610 003550 066276 DT1
2611 003552 067144 DF1
2612
2613 ;ERROR 7
2614 003554 057722 EM10 ;ABORT TEST, COULD NOT REFERENCE CONTROLLER REGISTER
2615 003556 064170 DH1
2616 003560 066276 DT1
2617 003562 067144 DF1
```


2618				
2619			:ERROR 10	
2620	003564	057765	EM11	:DRA & NED BOTH SET
2621	003566	064170	DH1	
2622	003570	066276	DT1	
2623	003572	067144	DF1	
2624				
2625			:ERROR 11	
2626	003574	060031	EM12	:CONTROLLER NOT READY
2627	003576	064761	DH18	:AFTER UNLOAD CMD.
2628	003600	066276	DT1	
2629	003602	067270	DF10	
2630				
2631			:ERROR 12	
2632	003604	060062	EM13	:NO ATTN
2633	003606	064761	DH18	:AFTER UNLOAD CMD
2634	003610	066276	DT1	
2635	003612	067270	DF10	
2636			:ERROR 13	
2637	003614	060104	EM14	:WRONG ATTN
2638	003616	064761	DH18	
2639	003620	066276	DT1	
2640	003622	067270	DF10	
2641			:ERROR 14	
2642	003624	060131	EM15	:DRDY NOT CLEARED
2643	003626	064761	DH18	
2644	003630	066276	DT1	
2645	003632	067270	DF10	
2646			:ERROR 15	
2647	003634	060163	EM16	:DSC NOT SET
2648	003636	064761	DH18	
2649	003640	066276	DT1	
2650	003642	067270	DF10	
2651			:ERROR 16	
2652	003644	060210	EM17	:MSG A0 ERROR
2653	003646	064361	DH8	:IN UNLD
2654	003650	066700	DT13	
2655	003652	067440	DF20	
2656			:ERROR 17	
2657	003654	060225	EM18	:MSG B0 ERROR
2658	003656	064361	DH8	:IN UNLD
2659	003660	066700	DT13	
2660	003662	067440	DF20	
2661			:ERROR 20	
2662	003664	060242	EM19	:MSG A1 ERROR
2663	003666	064361	DH8	:IN UNLD
2664	003670	066700	DT13	
2665	003672	067440	DF20	
2666			:ERROR 21	
2667	003674	060257	EM20	:MSG B1 ERROR
2668	003676	064361	DH8	:IN UNLD
2669	003700	066700	DT13	
2670	003702	067440	DF20	
2671			:ERROR 22	
2672	003704	061672	EM46	:MSG A2 ERROR
2673	003706	064361	DH8	:IN UNLD

2674	003710	066760		DT14	
2675	003712	067514		DF22	
2676			:ERROR 23		
2677	003714	061705		EM47	:MSG B2 ERROR
2678	003716	064361		DH8	:IN UNLD
2679	003720	066760		DT14	
2680	003722	067514		DF22	
2681					
2682			:ERROR 24		
2683	003724	060274		EM21	:CERR SET
2684	003726	065050		DH21	:AFTER SCLR
2685	003730	066276		DT1	
2686	003732	067270		DF10	
2687			:ERROR 25		
2688	003734	060316		EM22	:RLS DID NOT SET CERR
2689	003736	064170		DH1	
2690	003740	066276		DT1	
2691	003742	067144		DF1	
2692					
2693			:ERROR 26		
2694	003744	060355		EM23	:SACK SET AFTER RLS SENT
2695	003746	064170		DH1	
2696	003750	066276		DT1	
2697	003752	067144		DF1	
2698					
2699			:ERROR 27		
2700	003754	060435		EM24	:VOL VALID NOT SET
2701	003756	065002		DH19	:AFTER PACK CMD
2702	003760	066276		DT1	
2703	003762	067270		DF10	
2704			:ERROR 30		
2705	003764	060461		EM25	:DRIVE TYPE SET IN MR2
2706	003766	064170		DH1	
2707	003770	066276		DT1	
2708	003772	067144		DF1	
2709			:ERROR 31		
2710	003774	060507		EM26	:DDT SET IN RKDS
2711	003776	064170		DH1	
2712	004000	066276		DT1	
2713	004002	067144		DF1	
2714			:ERROR 32		
2715	004004	060527		EM27	:DTYE SET IN RKER
2716	004006	064170		DH1	
2717	004010	066276		DT1	
2718	004012	067144		DF1	
2719			:ERROR 33		
2720	004014	060550		EM28	:DTYE NOT SET IN RKER
2721	004016	064170		DH1	
2722	004020	066276		DT1	
2723	004022	067144		DF1	
2724			:ERROR 34		
2725	004024	060626		EM29	:DTYE DID NOT SET CERR
2726	004026	064170		DH1	
2727	004030	066276		DT1	
2728	004032	067144		DF1	
2729			:ERROR 35		

2730	004034	060675	EM30	:C-D PARITY ERROR SET IN MR3
2731	004036	064170	DH1	
2732	004040	066276	DT1	
2733	004042	067144	DF1	
2734			:ERROR 36	
2735	004044	060726	EM31	:D-C PARITY SET IN CS1
2736	004046	064170	DH1	
2737	004050	066276	DT1	
2738	004052	067144	DF1	
2739			:ERROR 37	
2740	004054	060753	EM32	:FAULT NOT SET IN MR3
2741	004056	064170	DH1	
2742	004060	066276	DT1	
2743	004062	067144	DF1	
2744			:ERROR 40	
2745	004064	061000	EM33	:C-D PARITY ERROR NOT SET IN MR3
2746	004066	064170	DH1	
2747	004070	066276	DT1	
2748	004072	067144	DF1	
2749			:ERROR 41	
2750	004074	061035	EM34	:D-C PARITY NOT SET IN CS1
2751	004076	064170	DH1	
2752	004100	066276	DT1	
2753	004102	067144	DF1	
2754			:ERROR 42	
2755	004104	061066	EM35	:DCPAR DID NOT SET CERR
2756	004106	064170	DH1	
2757	004110	066276	DT1	
2758	004112	067144	DF1	
2759			:ERROR 43	
2760	004114	061141	EM36	:CYL ADDR IN B2 NOT = RKDC
2761	004116	064647	DH14	:AFTER SEEK WITH BAD PARITY
2762	004120	066760	DT14	
2763	004122	067514	DF22	
2764			:ERROR 44	
2765	004124	061201	EM37	:CYL DIFF IN A2 NOT=RKDC
2766	004126	064647	DH14	
2767	004130	066760	DT14	
2768	004132	067514	DF22	
2769			:ERROR 45	
2770	004134	061141	EM36	:CYL ADDR IN RKMR3 NOT=RKDC
2771	004136	064647	DH14	
2772	004140	066336	DT4	
2773	004142	067244	DF6	
2774			:ERROR 46	
2775	004144	061241	EM38	:CYL DIFF IN RKMR2 NOT=CALDIF
2776	004146	064647	DH14	
2777	004150	066336	DT4	
2778	004152	067244	DF6	
2779			:ERROR 47	
2780	004154	061312	EM39	:CYL DIFF/OFFSET IN RKMR2 NOT CLEARED
2781	004156	064741	DH17	:AFTER RECAL CMD
2782	004160	066760	DT14	
2783	004162	067514	DF22	
2784			:ERROR 50	
2785	004164	061347	EM40	:CYL ADDR IN RKMR3 NOT CLEARED

2786	004166	064741	DH17	:AFTER RECAL CMD
2787	004170	066760	DT14	
2788	004172	067514	DF22	
2789			:ERROR 51	
2790	004174	060210	EM17	:A0 ERROR
2791	004176	065164	DH26	:AFTER READ DATA CMD
2792	004200	066700	DT13	
2793	004202	067440	DF20	
2794			:ERROR 52	
2795	004204	060225	EM18	:B0 ERROR
2796	004206	065164	DH26	
2797	004210	066700	DT13	
2798	004212	067440	DF20	
2799			:ERROR 53	
2800	004214	061466	EM43	:HEAD DECODE IN B3 NOT CLEARED
2801	004216	064741	DH17	:AFTER RECAL CMD
2802	004220	067050	DT15	
2803	004222	067550	DF23	
2804			:ERROR 54	
2805	004224	061515	EM44	:B3 HEAD DECODE INCORRECT
2806	004226	064677	DH16	
2807	004230	067050	DT15	
2808	004232	067550	DF23	
2809			:ERROR 55	
2810	004234	060062	EM13	:NO ATTN
2811	004236	064741	DH17	:AFTER RECAL CMD
2812	004240	066276	DT1	
2813	004242	067270	DF10	
2814			:ERROR 56	
2815	004244	062637	EM64	:MSG B3 HEAD REG NOT CLEARED
2816	004246	064361	DH8	:IN UNLOAD
2817	004250	067050	DT15	
2818	004252	067550	DF23	
2819			:ERROR 57	
2820	004254	060210	EM17	:MSG A0 ERROR
2821	004256	064405	DH9	:AFTER START SPINDLE CMD REC'D BY DRIVE
2822	004260	066700	DT13	
2823	004262	067440	DF20	
2824			:ERROR 60	
2825	004264	060225	EM18	:MSG B0 ERROR
2826	004266	064405	DH9	
2827	004270	066700	DT13	
2828	004272	067440	DF20	
2829			:ERROR 61	
2830	004274	060242	EM19	:MSG A1 ERROR
2831	004276	064405	DH9	
2832	004300	066700	DT13	
2833	004302	067440	DF20	
2834			:ERROR 62	
2835	004304	060257	EM20	:MSG B1 ERROR
2836	004306	064405	DH9	
2837	004310	066700	DT13	
2838	004312	067440	DF20	
2839			:ERROR 63	
2840	004314	060210	EM17	
2841	004316	064451	DH10	:AT END OF HEAD LOADING

2842	004320	066700	DT13
2843	004322	067440	DF20
2844			:ERROR 64
2845	004324	060225	EM18
2846	004326	064451	DH10
2847	004330	066700	DT13
2848	004332	067440	DF20
2849			:ERROR 65
2850	004334	060242	EM19
2851	004336	064451	DH10
2852	004340	066700	DT13
2853	004342	067440	DF20
2854			:ERROR 66
2855	004344	060257	EM20
2856	004346	064451	DH10
2857	004350	066700	DT13
2858	004352	067440	DF20
2859			:ERROR 67
2860	004354	060062	EM13
2861	004356	064451	DH10
2862	004360	066276	DT1
2863	004362	067270	DF10
2864			:ERROR 70
2865	004364	062021	EM50
2866	004366	064170	DH1
2867	004370	066276	DT1
2868	004372	067144	DF1
2869			:ERROR 71
2870	004374	060210	EM17
2871	004376	064500	DH11
2872	004400	066700	DT13
2873	004402	067440	DF20
2874			:ERROR 72
2875	004404	060225	EM18
2876	004406	064500	DH11
2877	004410	066700	DT13
2878	004412	067440	DF20
2879			:ERROR 73
2880	004414	060242	EM19
2881	004416	064500	DH11
2882	004420	066700	DT13
2883	004422	067440	DF20
2884			:ERROR 74
2885	004424	060257	EM20
2886	004426	064500	DH11
2887	004430	066700	DT13
2888	004432	067440	DF20
2889			:ERROR 75
2890	004434	062072	EM51
2891	004436	064170	DH1
2892	004440	066276	DT1
2893	004442	067144	DF1
2894			:ERROR 76
2895	004444	060210	EM17
2896	004446	064537	DH12
2897	004450	066700	DT13

:NO ATTN
:AT END OF HEAD LOADING.

:FWD NOT SET WITHIN 60 SEC FROM
:START SPINDLE CMD.

:AFTER START SPINDLE CMD & FWD SET.

:FWD NOT CLEARED WITHIN 5 SEC OF MOTION
:FROM START SPINDLE CMD.

:AT INNER LIMIT FROM START SPINDLE CMD.

2898	004452	067440		
2899			:ERROR 77	DF20
2900	004454	060225		EM18
2901	004456	064537		DH12
2902	004460	066700		DT13
2903	004462	067440		DF20
2904			:ERROR 100	
2905	004464	060242		EM19
2906	004466	064537		DH12
2907	004470	066700		DT13
2908	004472	067440		DF20
2909			:ERROR 101	
2910	004474	060257		EM20
2911	004476	064537		DH12
2912	004500	066700		DT13
2913	004502	067440		DF20
2914			:ERROR 102	
2915	004504	061733		EM49
2916	004506	064170		DH1
2917	004510	066276		DT1
2918	004512	067144		DF1
2919			:ERROR 103	
2920	004514	060210		EM17
2921	004516	064600		DH13
2922	004520	066700		DT13
2923	004522	067440		DF20
2924			:ERROR 104	
2925	004524	060225		EM18
2926	004526	064600		DH13
2927	004530	066700		DT13
2928	004532	067440		DF20
2929			:ERROR 105	
2930	004534	060242		EM19
2931	004536	064600		DH13
2932	004540	066700		DT13
2933	004542	067440		DF20
2934			:ERROR 106	
2935	004544	060257		EM20
2936	004546	064600		DH13
2937	004550	066700		DT13
2938	004552	067440		DF20
2939			:ERROR 107	
2940	004554	061551		EM45
2941	004556	064170		DH1
2942	004560	066276		DT1
2943	004562	067144		DF1
2944			:ERROR 110	
2945	004564	060210		EM17
2946	004566	064647		DH14
2947	004570	066760		DT14
2948	004572	067514		DF22
2949			:ERROR 111	
2950	004574	060225		EM18
2951	004576	064647		DH14
2952	004600	066760		DT14
2953	004602	067514		DF22

:FWD NOT SET WITHIN 4 SEC IN RTZ PORTION
:OF START SPIN CMD.

:FROM OUTER LIMIT TO CYL 0 DURING LOADING

:DRIVE READY NOT SET WITHIN 1 SEC
:FROM FWD IN RTZ PORTION OF START SPIN CMD.

:MSG A0 ERROR
:AFTER SEEK WITH BAD PARITY

:MSG B0 ERROR
:AFTER SEEK WITH BAD PARITY

2954			:ERROR 112	
2955	004604	060242	EM19	:A1 ERROR
2956	004606	065164	DH26	:AFTER READ DATA CMD
2957	004610	066700	DT13	
2958	004612	067440	DF20	
2959			:ERROR 113	
2960	004614	060257	EM20	:B1 ERROR
2961	004616	065164	DH26	
2962	004620	066700	DT13	
2963	004622	067440	DF20	
2964			:ERROR 114	
2965	004624	060210	EM17	
2966	004626	064677	DH16	:AFTER LOADING HEAD REGISTER & SEEK CMD
2967	004630	066700	DT13	
2968	004632	067440	DF20	
2969			:ERROR 115	
2970	004634	060225	EM18	
2971	004636	064677	DH16	
2972	004640	066700	DT13	
2973	004642	067440	DF20	
2974			:ERROR 116	
2975	004644	060031	EM12	:CONT NOT RDY
2976	004646	065002	DH19	:AFTER PACK CMD
2977	004650	066276	DT1	
2978	004652	067270	DF10	
2979			:ERROR 117	
2980	004654	060031	EM12	:CONT NOT RDY
2981	004656	065021	DH20	:AFTER SEL DR CMD
2982	004660	066276	DT1	
2983	004662	067270	DF10	
2984			:ERROR 120	
2985	004664	060031	EM12	
2986	004666	065050	DH21	:AFTER SUBSYS CLEAR
2987	004670	066276	DT1	
2988	004672	067270	DF10	
2989			:ERROR 121	
2990	004674	060031	EM12	
2991	004676	064405	DH9	:AFTER START SPINDLE CMD
2992	004700	066276	DT1	
2993	004702	067270	DF10	
2994			:ERROR 122	
2995	004704	060031	EM12	
2996	004706	064647	DH14	:AFTER SEEK WITH BAD PARITY
2997	004710	066276	DT1	
2998	004712	067270	DF10	
2999			:ERROR 123	
3000	004714	063750	EM88	:NO DRIVES FOUND
3001	004716	064170	DH1	
3002	004720	066276	DT1	
3003	004722	067144	DF1	
3004			:ERROR 124	
3005	004724	060031	EM12	
3006	004726	064741	DH17	:AFTER RECAL CMD
3007	004730	066276	DT1	
3008	004732	067270	DF10	
3009			:ERROR 125	

3010	004734	060062	EM13	:NO ATTN
3011	004736	064647	DH14	:FROM SEEK WITH BAD PARITY
3012	004740	066276	DT1	
3013	004742	067270	DF10	
3014			:ERROR 126	
3015	004744	064041	EM89	:NO DRVS FOUND IN DEVICE MAP
3016	004746	064170	DH1	
3017	004750	066276	DT1	
3018	004752	067144	DF1	
3019			:ERROR 127	
3020	004754	061312	EM39	:CYL DIFF/OFFSET IN RKMR2 NOT CLEARED
3021	004756	065050	DH21	:AFTER SCLR
3022	004760	066276	DT1	
3023	004762	067270	DF10	
3024			:ERROR 130	
3025	004764	061347	EM40	:CYL ADDR IN RKMR3 NOT CLEARED
3026	004766	065050	DH21	
3027	004770	066276	DT1	
3028	004772	067270	DF10	
3029			:ERROR 131	
3030	004774	060031	EM12	:NO RDY
3031	004776	065145	DH25	:AFTER SEEK CMD
3032	005000	066276	DT1	
3033	005002	067270	DF10	
3034			:ERROR 132	
3035	005004	060062	EM13	:NO ATTN
3036	005006	065145	DH25	
3037	005010	066276	DT1	
3038	005012	067270	DF10	
3039			:ERROR 133	
3040	005014	060210	EM17	:MSG A0 ERROR
3041	005016	065145	DH25	
3042	005020	066700	DT13	
3043	005022	067440	DF20	
3044			:ERROR 134	
3045	005024	060225	EM18	:MSG B0 ERROR
3046	005026	065145	DH25	
3047	005030	066700	DT13	
3048	005032	067440	DF20	
3049			:ERROR 135	
3050	005034	060242	EM19	:MSG A1 ERROR
3051	005036	065145	DH25	
3052	005040	066700	DT13	
3053	005042	067440	DF20	
3054			:ERROR 136	
3055	005044	060257	EM20	:MSG B1 ERROR.
3056	005046	065145	DH25	
3057	005050	066700	DT13	
3058	005052	067440	DF20	
3059			:ERROR 137	
3060	005054	061312	EM39	:CYL DIFF/OFFSET IN A2 NOT CLEARED
3061	005056	065145	DH25	
3062	005060	066760	DT14	
3063	005062	067514	DF22	
3064			:ERROR 140	
3065	005064	061347	EM40	:CYL ADDR IN B2 NOT CLEARED

3066	005066	065145		DH25	
3067	005070	066760		DT14	
3068	005072	067514		DF22	
3069			:ERROR 141		
3070	005074	062172		EM52	:20 SECTOR FORMAT NOT SET IN RKMR2
3071	005076	064170		DH1	
3072	005100	066276		DT1	
3073	005102	067144		DF1	
3074			:ERROR 142		
3075	005104	062231		EM53	:SECTOR 0 NOT FOUND WITHIN 50 MS
3076	005106	064170		DH1	
3077	005110	066276		DT1	
3078	005112	067144		DF1	
3079			:ERROR 143		
3080	005114	062262		EM54	:DIFF SECTOR NOT FOUND WITHIN 3MS
3081	005116	064170		DH1	
3082	005120	066276		DT1	
3083	005122	067144		DF1	
3084			:ERROR 144		
3085	005124	061720		EM48	:MSG B3 ERROR
3086	005126	065323		DH34	:SECTOR REG UNSTABLE
3087	005130	066276		DT1	
3088	005132	067270		DF10	
3089			:ERROR 145		
3090	005134	061720		EM48	
3091	005136	065347		DH35	:BETWEEN SECTOR COUNTS
3092	005140	066404		DT6	
3093	005142	067310		DF12	
3094			:ERROR 146		
3095	005144	060242		EM19	:MSG A1 ERROR
3096	005146	064647		DH14	:AFTER SEEK WITH BAD PARITY
3097	005150	066760		DT14	
3098	005152	067514		DF22	
3099			:ERROR 147		
3100	005154	060257		EM20	:MSG B1 ERROR
3101	005156	064647		DH14	
3102	005160	066760		DT14	
3103	005162	067514		DF22	
3104			:ERROR 150		
3105	005164	060242		EM19	:MSG A1 ERROR
3106	005166	065420		DH37	
3107	005170	066276		DT1	
3108	005172	067270		DF10	
3109			:ERROR 151		
3110	005174	060031		EM12	:NO RDY
3111	005176	065076		DH22	:AFTER CLEAR CMD
3112	005200	066276		DT1	
3113	005202	067270		DF10	
3114			:ERROR 152		
3115	005204	064144		EM90	:DDT NOT SET IN RKDS
3116	005206	064170		DH1	
3117	005210	066276		DT1	
3118	005212	067144		DF1	
3119			:ERROR 153		
3120	005214	000000		0	
3121	005216	000000		0	

3122	005220	000000	0	
3123	005222	000000	0	
3124			:ERROR 154	
3125	005224	062315	EM55	:ATTN NOT CLEARED
3126	005226	065076	DH22	
3127	005230	066276	DT1	
3128	005232	067270	DF10	
3129			:ERROR 155	
3130	005234	063605	EM85	:IDAE NOT CLEARED
3131	005236	064741	DH17	:AFTER RECAL CMD
3132	005240	066276	DT1	
3133	005242	067270	DF10	
3134			:ERROR 156	
3135	005244	060031	EM12	:CONT NOT READY
3136	005246	066150	DH51	:AFTER SEEK TO SELF
3137	005250	066276	DT1	
3138	005252	067270	DF10	
3139			:ERROR 157	
3140	005254	060062	EM13	:NO ATTN
3141	005256	066150	DH51	
3142	005260	066276	DT1	
3143	005262	067270	DF10	
3144			:ERROR 160	
3145	005264	062475	EM59	:LIMIT DETECT NOT FOUND
3146	005266	064170	DH1	
3147	005270	066276	DT1	
3148	005272	067144	DF1	
3149			:ERROR 161	
3150	005274	060210	EM17	:MSG A0 ERROR
3151	005276	065464	DH38	:AFTER LIMIT DETECT
3152	005300	066700	DT13	
3153	005302	067440	DF20	
3154			:ERROR 162	
3155	005304	060225	EM18	:MSG B0 ERROR
3156	005306	065464	DH38	
3157	005310	066700	DT13	
3158	005312	067440	DF20	
3159			:ERROR 163	
3160	005314	060242	EM19	:MSG A1 ERROR
3161	005316	065464	DH38	
3162	005320	066700	DT13	
3163	005322	067440	DF20	
3164			:ERROR 164	
3165	005324	060257	EM20	:MSG B1 ERROR
3166	005326	065464	DH38	
3167	005330	066700	DT13	
3168	005332	067440	DF20	
3169			:ERROR 165	
3170	005334	060062	EM13	:NO ATTN
3171	005336	065464	DH38	
3172	005340	066276	DT1	
3173	005342	067270	DF10	
3174			:ERROR 166	
3175	005344	062526	EM60	:HEADS HOME NOT FOUND
3176	005346	065464	DH38	
3177	005350	066276	DT1	

3178	005352	067270		DF10	
3179			:ERROR 167	EM61	:LOAD HEADS NOT FOUND
3180	005354	062562		DH38	
3181	005356	065464		DT1	
3182	005360	066276		DF10	
3183	005362	067270			
3184			:ERROR 170	EM4	:FATAL ERROR
3185	005364	057354		DH45	:LIMIT DETECT ERROR ON PREVIOUS TEST
3186	005366	065716		DT1	
3187	005370	066276		DF16	
3188	005372	067374			
3189			:ERROR 171	EM12	:NO RDY
3190	005374	060031		DH30	:AFTER READ HEADER CMD
3191	005376	065230		DT1	
3192	005400	066276		DF10	
3193	005402	067270			
3194			:ERROR 172	EM39	:CYL DIFF/OFFSET NOT CLEARED
3195	005404	061312		DH30	:AFTER READ HEADER CMD
3196	005406	065230		DT14	
3197	005410	066760		DF22	
3198	005412	067514			
3199			:ERROR 173	EM63	:DLT SET
3200	005414	062616		DH30	
3201	005416	065230		DT1	
3202	005420	066276		DF15	
3203	005422	067354			
3204			:ERROR 174	EM21	:CERR SET
3205	005424	060274		DH30	
3206	005426	065230		DT1	
3207	005430	066276		DF15	
3208	005432	067354			
3209			:ERROR 175	EM39	:CYL DIFF NOT CLEARED
3210	005434	061312		DH10	:AT END OF HEAD LOADING
3211	005436	064451		DT14	
3212	005440	066760		DF22	
3213	005442	067514			
3214			:ERROR 176	EM40	:CYL ADDR NOT CLEARED.
3215	005444	061347		DH10	
3216	005446	064451		DT14	
3217	005450	066760		DF22	
3218	005452	067514			
3219			:ERROR 177	EM72	:FORMAT TEST BYPASSED
3220	005454	063114		DH46	:COULD NOT READ BSE INFO
3221	005456	065775		DT1	
3222	005460	066276		DF16	
3223	005462	067374			
3224			:ERROR 200	EM12	:NO RDY
3225	005464	060031		DH39	:AFTER WRITE HEADER CMD
3226	005466	065502		DT1	
3227	005470	066276		DF15	
3228	005472	067354			
3229			:ERROR 201	EM21	:CERR SET
3230	005474	060274		DH39	
3231	005476	065502		DT1	
3232	005500	066276		DF15	
3233	005502	067354			

3234			:ERROR 202	
3235	005504	062666	EM65	:READ HEADER ERROR
3236	005506	064170	DH1	
3237	005510	066450	DT7	
3238	005512	067334	DF14	
3239			:ERROR 203	
3240	005514	060210	EM17	:MSG A0 ERROR
3241	005516	065303	DH33	:DURING SEEK CMD
3242	005520	066700	DT13	
3243	005522	067440	DF20	
3244			:ERROR 204	
3245	005524	060225	EM18	:MSG B0 ERROR
3246	005526	065303	DH33	
3247	005530	066700	DT13	
3248	005532	067440	DF20	
3249			:ERROR 205	
3250	005534	060242	EM19	:MSG A1 ERROR
3251	005536	065303	DH33	
3252	005540	066700	DT13	
3253	005542	067440	DF20	
3254			:ERROR 206	
3255	005544	060257	EM20	:MSG B1 ERROR
3256	005546	065303	DH33	
3257	005550	066700	DT13	
3258	005552	067440	DF20	
3259			:ERROR 207	
3260	005554	061141	EM36	:CYL ADDR IN RKMR3 INCORRECT
3261	005556	065145	DH25	:AFTER SEEK CMD
3262	005560	066336	DT4	
3263	005562	067244	DF6	
3264			:ERROR 210	
3265	005564	060274	EM21	:CERR SET
3266	005566	065145	DH25	
3267	005570	066276	DT1	
3268	005572	067270	DF10	
3269			:ERROR 211	
3270	005574	062742	EM67	:READ CYL 0 HEADERS ON CYL 1
3271	005576	065145	DH25	
3272	005600	066276	DT1	
3273	005602	067270	DF10	
3274			:ERROR 212	
3275	005604	061241	EM38	:CYL DIFF IN RKMR2 NOT = CALDIF
3276	005606	065303	DH33	:DURING SEEK CMD
3277	005610	066336	DT4	
3278	005612	067244	DF6	
3279			:ERROR 213	
3280	005614	060210	EM17	:MSG A0 ERROR
3281	005616	065565	DH41	:DURING RECAL CMD
3282	005620	066700	DT13	
3283	005622	067440	DF20	
3284			:ERROR 214	
3285	005624	060225	EM18	:MSG B0 ERROR
3286	005626	065565	DH41	
3287	005630	066700	DT13	
3288	005632	067440	DF20	
3289			:ERROR 215	

3290	005634	060242	EM19	:MSG A1 ERROR
3291	005636	065565	DH41	
3292	005640	066700	DT13	
3293	005642	067440	DF20	
3294			:ERROR 216	
3295	005644	060257	EM20	:MSG B1 ERROR
3296	005646	065565	DH41	
3297	005650	066700	DT13	
3298	005652	067440	DF20	
3299			:ERROR 217	
3300	005654	061241	EM38	:CYL DIFF IN RKMR2 NOT=CALDIF
3301	005656	065565	DH41	
3302	005660	066336	DT4	
3303	005662	067244	DF6	
3304			:ERROR 220	
3305	005664	060274	EM21	:CERR SET
3306	005666	064741	DH17	:AFTER RECAL CMD
3307	005670	066276	DT1	
3308	005672	067270	DF10	
3309			:ERROR 221	
3310	005674	060210	EM17	:MSG A0 ERROR
3311	005676	064741	DH17	
3312	005700	066700	DT13	
3313	005702	067440	DF20	
3314			:ERROR 222	
3315	005704	060242	EM19	:MSG A1 ERROR
3316	005706	064741	DH17	
3317	005710	066700	DT13	
3318	005712	067440	DF20	
3319			:ERROR 223	
3320	005714	061312	EM39	:CYL DIFF/OFFSET IN RKMR2 NOT CLEARED
3321	005716	064741	DH17	
3322	005720	066276	DT1	
3323	005722	067270	DF10	
3324			:ERROR 224	
3325	005724	062706	EM66	:CYL ADDR IN RKMR3 INCORRECT

3326	005726	064741	DH17	
3327	005730	066276	DT1	
3328	005732	067270	DF10	
3329			:ERROR 225	
3330	005734	063001	EM68	:READING CYL 1 HEADERS ON CYL 0
3331	005736	064741	DH17	
3332	005740	066276	DT1	
3333	005742	067270	DF10	
3334			:ERROR 226	
3335	005744	060031	EM12	:NO RDY
3336	005746	065164	DH26	:AFTER READ DATA CMD
3337	005750	066276	DT1	
3338	005752	067270	DF10	
3339			:ERROR 227	
3340	005754	060274	EM21	:CERR SET
3341	005756	065164	DH26	
3342	005760	066276	DT1	
3343	005762	067354	DF15	
3344			:ERROR 230	
3345	005764	063723	EM87	:CANT READ BSE INFO

3346	005766	066227	DH53	:ON SECT 10,12,14,16,18,20
3347	005770	066276	DT1	
3348	005772	067414	DF17	
3349			:ERROR 231	
3350	005774	000000	0	
3351	005776	000000	0	
3352	006000	000000	0	
3353	006002	000000	0	
3354			:ERROR 232	
3355	006004	061141	EM36	:CYL ADDR IN RKMR3 NOT=RKDC
3356	006006	065145	DH25	:AFTER SEEK CMD
3357	006010	066276	DT1	
3358	006012	067224	DF5	
3359			:ERROR 233	
3360	006014	063723	EM87	:CANNOT READ BSE INFO
3361	006016	065606	DH42	:ON SECT 0,2,4,6,8
3362	006020	066276	DT1	
3363	006022	067414	DF17	
3364			:ERROR 234	
3365	006024	000000	0	
3366	006026	000000	0	
3367	006030	000000	0	
3368	006032	000000	0	
3369			:ERROR 235	
3370	006034	063040	EM69	:ALIGN CARTRIDGE USED
3371	006036	065646	DH44	:WILL BYPASS FORMAT & ALL R/W TESTS
3372	006040	066276	DT1	
3373	006042	067270	DF10	
3374			:ERROR 236	
3375	006044	062350	EM56	:UNEXP MEM PARITY TRAP
3376	006046	065124	DH23	:TEST #, TRAP PC
3377	006050	066674	DT11	
3378	006052	067204	DF3	
3379			:ERROR 237	
3380	006054	063073	EM71	:DSC SET
3381	006056	065076	DH22	:AFTER DRIVE CLEAR CMD
3382	006060	066276	DT1	
3383	006062	067270	DF10	
3384			:ERROR 240	
3385	006064	063001	EM68	:READ CYL 1 HEADERS ON CYL 0
3386	006066	064741	DH17	:AFTER RECAL CMD
3387	006070	066276	DT1	
3388	006072	067270	DF10	
3389			:ERROR 241	
3390	006074	061141	EM36	:RKMR3 NOT = RKDC
3391	006076	064647	DH14	:AFTER SEEK WITH BAD PARITY
3392	006100	066516	DT8	
3393	006102	067244	DF6	
3394			:ERROR 242	
3395	006104	061241	EM38	:CYL DIFF IN RKMR2 INCORRECT
3396	006106	064647	DH14	
3397	006110	066516	DT8	
3398	006112	067244	DF6	
3399			:ERROR 243	
3400			EM36	
3401	006114	061141		:CYL ADDR IN RKMR3 INCORRECT

3402	006116	065145	DH25	:AFTER SEEK CMD
3403	006120	066516	DT8	
3404	006122	067244	DF6	
3405			:ERROR 244	
3406	006124	063162	EM74	:RTZ NOT SET
3407	006126	065565	DH41	:DURING RECAL CMD
3408	006130	066276	DT1	
3409	006132	067270	DF10	
3410			:ERROR 245	
3411	006134	060062	EM13	:NO ATTN
3412	006136	066042	DH48	:AFTER SEEK TO INVALID CYL
3413	006140	066276	DT1	
3414	006142	067270	DF10	
3415			:ERROR 246	
3416	006144	063207	EM75	:IDAE NOT SET
3417	006146	066042	DH48	
3418	006150	066336	DT4	
3419	006152	067244	DF6	
3420			:ERROR 247	
3421	006154	060753	EM32	:FAULT NOT SET
3422	006156	066042	DH48	
3423	006160	066336	DT4	
3424	006162	067244	DF6	
3425			:ERROR 250	
3426	006164	063235	EM76	:PIP SET
3427	006166	066042	DH48	
3428	006170	066336	DT4	
3429	006172	067244	DF6	
3430			:ERROR 251	
3431	006174	060163	EM16	:DSC NOT SET
3432	006176	066042	DH48	
3433	006200	066336	DT4	
3434	006202	067244	DF6	
3435			:ERROR 252	
3436	006204	060210	EM17	:MSG A0 ERROR
3437	006206	066042	DH48	
3438	006210	066700	DT13	
3439	006212	067440	DF20	
3440			:ERROR 253	
3441	006214	060225	EM18	:MSG B0 ERROR
3442	006216	066042	DH48	
3443	006220	066700	DT13	
3444	006222	067440	DF20	
3445			:ERROR 254	
3446	006224	060242	EM19	:MSG A1 ERROR
3447	006226	066042	DH48	
3448	006230	066700	DT13	
3449	006232	067440	DF20	
3450			:ERROR 255	
3451	006234	060257	EM20	:MSG B1 ERROR
3452	006236	066042	DH48	
3453	006240	066700	DT13	
3454	006242	067440	DF20	
3455			:ERROR 256	
3456	006244	061241	EM38	:CYL DIFF IN RKMR2 NOT='CYL DIF'
3457	006246	066042	DH48	

3458	006250	066336	DT4	
3459	006252	067244	DF6	
3460			:ERROR 257	
3461	006254	061141	EM36	:CYL ADDR IN RKMR3 NOT=RKDC
3462	006256	066042	DH48	
3463	006260	066336	DT4	
3464	006262	067244	DF6	
3465			:ERROR 260	
3466	006264	000000	0	
3467	006266	000000	0	
3468	006270	000000	0	
3469	006272	000000	0	
3470			:ERROR 261	
3471	006274	000000	0	
3472	006276	000000	0	
3473	006300	000000	0	
3474	006302	000000	0	
3475			:ERROR 262	
3476	006304	063256	EM77	:FAULT NOT CLEARED
3477	006306	065076	DH22	:AFTER DRIVE CLEAR CMD
3478	006310	066276	DT1	
3479	006312	067270	DF10	
3480			:ERROR 263	
3481	006314	063304	EM78	:CYL DIFF IN RKMR2 NOT=1 IN SEEK TO SELF
3482	006316	064647	DH14	:AFTER SEEK WITH BAD PARITY
3483	006320	066516	DT8	
3484	006322	067244	DF6	
3485			:ERROR 264	
3486	006324	061375	EM41	:CYL ADDR NOT CLEARED
3487	006326	065230	DH30	:AFTER READ HEADER CMD
3488	006330	066760	DT14	
3489	006332	067514	DF22	
3490			:ERROR 265	
3491	006334	060225	EM18	:MSG B0 ERROR
3492	006336	065076	DH22	:AFTER DRIVE CLEAR CMD
3493	006340	066700	DT13	
3494	006342	067440	DF20	
3495			:ERROR 266	
3496	006344	060257	EM20	:MSG B1 ERROR
3497	006346	065076	DH22	
3498	006350	066700	DT13	
3499	006352	067440	DF20	
3500			:ERROR 267	
3501	006354	060225	EM18	
3502	006356	065502	DH39	:AFTER WRITE HEADER CMD
3503	006360	066700	DT13	
3504	006362	067440	DF20	
3505			:ERROR 270	
3506	006364	060257	EM20	
3507	006366	065502	DH39	
3508	006370	066700	DT13	
3509	006372	067440	DF20	
3510			:ERROR 271	
3511	006374	060225	EM18	
3512	006376	065230	DH30	:AFTER READ HEADER CMD
3513	006400	066700	DT13	

3514	006402	067440		DF20	
3515			:ERROR 272	EM20	
3516	006404	060257		DH30	
3517	006406	065230		DT13	
3518	006410	066700		DF20	
3519	006412	067440			
3520			:ERROR 273	EM17	:MSG A0 ERROR
3521	006414	060210		DH22	:AFTER DRV CLR CMD
3522	006416	065076		DT13	
3523	006420	066700		DF20	
3524	006422	067440			
3525			:ERROR 274	EM19	:MSG A1 ERROR
3526	006424	060242		DH22	
3527	006426	065076		DT13	
3528	006430	066700		DF20	
3529	006432	067440			
3530			:ERROR 275	EM18	:MSG B0 ERROR
3531	006434	060225		DH17	:AFTER RECAL CMD
3532	006436	064741		DT13	
3533	006440	066700		DF20	
3534	006442	067440			
3535			:ERROR 276	EM20	:MSG B1 ERROR
3536	006444	060257		DH17	
3537	006446	064741		DT13	
3538	006450	066700		DF20	
3539	006452	067440			
3540			:ERROR 277	EM17	:MSG A0 ERROR
3541	006454	060210		DH39	:AFTER WRITE HEADER CMD
3542	006456	065502		DT13	
3543	006460	066700		DF20	
3544	006462	067440			
3545			:ERROR 300	EM19	:MSG A1 ERROR
3546	006464	060242		DH39	
3547	006466	065502		DT13	
3548	006470	066700		DF20	
3549	006472	067440			
3550			:ERROR 301	EM17	
3551	006474	060210		DH30	:AFTER READ HEADER CMD
3552	006476	065230		DT13	
3553	006500	066700		DF20	
3554	006502	067440			
3555			:ERROR 302	EM19	
3556	006504	060242		DH30	
3557	006506	065230		DT13	
3558	006510	066700		DF20	
3559	006512	067440			
3560			:ERROR 303	EM39	:CYL DIFF/OFFSET NOT CLEARED
3561	006514	061312		DH39	:AFTER WRITE HEADER CMD
3562	006516	065502		DT14	
3563	006520	066760		DF22	
3564	006522	067514			
3565			:ERROR 304	EM41	:CYL ADDR NOT CLEARED
3566	006524	061375		DH39	
3567	006526	065502		DT14	
3568	006530	066760		DF22	
3569	006532	067514			

3570				
3571			:ERROR 305	
3572	006534	063412	EM80	:UNLD NOT SET
3573	006536	065464	DH38	:AFTER LIMIT DETECT
3574	006540	066276	DT1	
3575	006542	067270	DF10	
3576			:ERROR 306	
3577	006544	063442	EM81	:SPIN NOT SET
3578	006546	064405	DH9	:AFTER START SPIN CMD.
3579	006550	066276	DT1	
3580	006552	067270	DF10	
3581			:ERROR 307	
3582	006554	063470	EM82	:RTZ NOT SET
3583	006556	065565	DH41	:DURING RECAL CMD
3584	006560	066276	DT1	
3585	006562	067270	DF10	
3586			:ERROR 310	
3587	006564	063515	EM83	:READ HEADER ERROR
3588	006566	064170	DH1	
3589	006570	066564	DT9	
3590	006572	067474	DF21	
3591			:ERROR 311	
3592	006574	063515	EM83	
3593	006576	064170	DH1	
3594	006600	066630	DT10	
3595	006602	067474	DF21	
3596			:ERROR 312	
3597	006604	063555	EM84	:FORMAT NOT SET
3598	006606	065502	DH39	:AFTER WRITE HEADER CMD
3599	006610	066276	DT1	
3600	006612	067270	DF10	
3601			:ERROR 313	
3602	006614	063555	EM84	
3603	006616	065230	DH30	:AFTER READ HEADER CMD
3604	006620	066276	DT1	
3605	006622	067270	DF10	
3606			:ERROR 314	
3607	006624	062373	EM57	:WCE AT CYL 411,TRK 2,SEC 21
3608	006626	064170	DH1	
3609	006630	066276	DT1	
3610	006632	067210	DF4	
3611			:ERROR 315	
3612	006634	062427	EM58	:SPOK NOT CLEARED
3613	006636	064761	DH18	:AFTER UNLD CMD
3614	006640	066276	DT1	
3615	006642	067270	DF10	
3616			:ERROR 316	
3617	006644	063060	EM70	:UNEXP ATTN
3618	006646	064405	DH9	:AFTER START SPIN CMD
3619	006650	066276	DT1	
3620	006652	067270	DF10	
3621			:ERROR 317	
3622	006654	063060	EM70	
3623	006656	064500	DH11	:AFT SPIN CMD & FWD SET
3624	006660	066276	DT1	
3625	006662	067270	DF10	

3626			:ERROR 320	
3627	006664	063060	EM70	
3628	006666	064537	DH12	:AT INNER LIMIT FROM ST SPIN CMD
3629	006670	066276	DT1	
3630	006672	067270	DF10	
3631			:ERROR 321	
3632	006674	063060	EM70	
3633	006676	064600	DH13	:FROM OUTER LIM TO CYL 0 DURING LOADING
3634	006700	066276	DT1	
3635	006702	067270	DF10	
3636			:ERROR 322	
3637	006704	060242	EM19	:MSG A1 ERROR
3638	006706	064677	DH16	:AFT LD HEAD REG & SEEK
3639	006710	066700	DT13	
3640	006712	067440	DF20	
3641			:ERROR 323	
3642	006714	060257	EM20	:MSG B1 ERROR.
3643	006716	064677	DH16	
3644	006720	066700	DT13	
3645	006722	067440	DF20	
3646			:ERROR 324	
3647	006724	061672	EM46	:MSG A2 ERROR
3648	006726	064677	DH16	
3649	006730	066760	DT14	
3650	006732	067514	DF22	
3651			:ERROR 325	
3652	006734	061705	EM47	:MSG B2 ERROR
3653	006736	064677	DH16	
3654	006740	066760	DT14	
3655	006742	067514	DF22	
3656			:ERROR 326	
3657	006744	063141	EM73	:CTO SET
3658	006746	063636	EM86	:WHILE WAITING FOR OR REC'D CONTR RDY. MSG A&B BAD
3659	006750	066276	DT1	
3660	006752	067160	DF2	
3661			:ERROR 327	
3662	006754	063371	EM79	:NED SET
3663	006756	063636	EM86	
3664	006760	066276	DT1	
3665	006762	067160	DF2	
3666			:ERROR 330	
3667			EM5	
3668	006764	057467	EM86	:MDS SET
3669	006766	063636	DT1	
3670	006770	066276	DF2	
3671	006772	067160		


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3672
3673
3674 .SBTTL PROGRAM SETUP
3675 006774 012737 000001 001336 PARSRT: MOV #1,PARAM ;SET FLAG FOR 220 START
3676 007002 005037 001340 CLR BYPT16
3677 007006 005037 001342 CLR MODTST
3678 007012 000450 BR PRGSRT ;START PROGRAM
3679
3680 007014 005037 001336 BYT16: CLR PARAM
3681 007020 012737 000001 001340 MOV #1,BYPT16 ;SET FLAG TO BYPASS TEST 16
3682 007026 005037 001342 CLR MODTST
3683 007032 000440 BR PRGSRT
3684
3685 007034 012737 000001 001336 BYT16A: MOV #1,PARAM
3686 007042 012737 000001 001340 MOV #1,BYPT16
3687 007050 005037 001342 CLR MODTST
3688 007054 000427 BR PRGSRT
3689
3690 007056 005037 001336 MDTST: CLR PARAM
3691 007062 005037 001340 CLR BYPT16
3692 007066 012737 000001 001342 MOV #1,MDTST
3693 007074 000417 BR PRGSRT
3694
3695 007076 005037 001336 MDTSTA: CLR PARAM
3696 007102 012737 000001 001340 MOV #1,BYPT16
3697 007110 012737 000001 001342 MOV #1,MDTST
3698 007116 000406 BR PRGSRT
3699 007120 005037 001336 START: CLR PARAM ;CLEAR FOR 200 START
3700 007124 005037 001340 CLR BYPT16
3701 007130 005037 001342 CLR MODTST
3702 007134 000005 PRGSRT: RESET ;CLEAR ALL INT ENABLE & INIT
3703 007136 012706 001100 MOV #STACK,SP ;SETUP STACK POINTER
3704 007142 012746 000000 MOV #PRO,-(SP) ;PSW LOADED TO BE
3705 007146 012746 007154 MOV #1$,-(SP) ;LSI-11 COMPATABLE
3706 007152 000002 RTI ;ENABLE ALL INTERRUPTS
3707
3708 007154 004737 052266 1$: JSR PC,$TKINT ;SETUP KB VECTOR ADDR, PRIORITY 4
3709 ;& TURN ON KB INTERRUPT
3710
3711
3712 ;*** CPU PRIORITY LEVEL NOW AT 0 ***
3713 ;*** ANY DEVICE WHICH SETS ITS ***
3714 ;*** INTERRUPT ENABLE BIT WILL ***
3715 ;*** SERVICED. ***
3716
3717 ;CLOCK INTERRUPTS WILL CHANGE CPU PRIORITY TO LEVEL 6 (IN 'ST5')
3718 ;RK06 CONTROLLER INTERRUPTS WILL CHANGE CPU PRIORITY TO LEVEL 5 IN 'SETINT')
3719 ;KEYBOARD INTERRUPTS WILL CHANGE CPU PRIORITY TO LEVEL 4 (SEE ABOVE)
3720
3721 ;ALL 'SYSMAC' TRAPS WILL CHANGE CPU PRIORITY TO LEVEL 7 (SEE BELOW)
3722
3723 ;SYSMAC 'SETUP'
3724 .SBTTL INITIALIZE THE COMMON TAGS
3725 ;;CLEAR THE COMMON TAGS ($CMTAG) AREA
3726 007160 012706 001100 MOV #CMTAG,R6 ;;FIRST LOCATION TO BE CLEARED
3727 007164 005026 CLR (R6)+ ;;CLEAR MEMORY LOCATION

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3728 007166 022706 001140          CMP    #SWR,R6 ;;DONE?
3729 007172 001374          BNE    -6      ;;LOOP BACK IF NO
3730 007174 012706 001100          MOV    #STACK,SP ;;SETUP THE STACK POINTER
3731          ;;INITIALIZE A FEW VECTORS
3732 007200 012737 050322 000020          MOV    #SCOPE,@#IOTVEC ;;IOT VECTOR FOR SCOPE ROUTINE
3733 007206 012737 000340 000022          MOV    #340,@#IOTVEC+2 ;;LEVEL 7
3734 007214 012737 050602 000030          MOV    #ERROR,@#EMTVEC ;;EMT VECTOR FOR ERROR ROUTINE
3735 007222 012737 000340 000032          MOV    #340,@#EMTVEC+2 ;;LEVEL 7
3736 007230 012737 054404 000034          MOV    #STRAP,@#TRAPVEC ;;TRAP VECTOR FOR TRAP CALLS
3737 007236 012737 000340 000036          MOV    #340,@#TRAPVEC+2;LEVEL 7
3738 007244 012737 050234 000024          MOV    #SPWRDN,@#PWRVEC ;;POWER FAILURE VECTOR
3739 007252 012737 000340 000026          MOV    #340,@#PWRVEC+2 ;;LEVEL 7
3740 007260 013737 043014 043006          MOV    $ENDCT,$EOPCT ;;SETUP END-OF-PROGRAM COUNTER
3741 007266 005037 001174          CLR    $TIMES      ;;INITIALIZE NUMBER OF ITERATIONS
3742 007272 005037 001176          CLR    $ESCAPE     ;;CLEAR THE ESCAPE ON ERROR ADDRESS
3743 007276 012737 000001 001115          MOV    #1,$ERMAX   ;;ALLOW ONE ERROR PER TEST
3744 007304 012737 007304 001106          MOV    #,$SLPADR   ;;INITIALIZE THE LOOP ADDRESS FOR SCOPE
3745 007312 012737 007312 001110          MOV    #,$SLPERR   ;;SETUP THE ERROR LOOP ADDRESS
3746          ;;SIZE FOR A HARDWARE SWITCH REGISTER. IF NOT FOUND OR IT IS
3747          ;;EQUAL TO A "-1", SETUP FOR A SOFTWARE SWITCH REGISTER.
3748 007320 013746 000004          MOV    @#ERRVEC,-(SP) ;;SAVE ERROR VECTOR
3749 007324 012737 007360 000004          MOV    #64$,@#ERRVEC ;;SET UP ERROR VECTOR
3750 007332 012737 177570 001140          MOV    #DSWR,SWR   ;;SETUP FOR A HARDWARE SWICH REGISTER
3751 007340 012737 177570 001142          MOV    #DDISP,DISPLAY ;;AND A HARDWARE DISPLAY REGISTER
3752 007346 022777 177777 171564          CMP    #-1,@SWR    ;;TRY TO REFERENCE HARDWARE SWR
3753 007354 001012          BNE    66$        ;;BRANCH IF NO TIMEOUT TRAP OCCURRED
3754          ;;AND THE HARDWARE SWR IS NOT = -1
3755 007356 000403          BR    65$        ;;BRANCH IF NO TIMEOUT
3756 007360 012716 007366          64$: MOV    #65$,(SP) ;;SET UP FOR TRAP RETURN
3757 007364 000002          RTI
3758 007366 012737 000176 001140          65$: MOV    #SWREG,SWR ;;POINT TO SOFTWARE SWR
3759 007374 012737 000174 001142          MOV    #DISPREG,DISPLAY
3760 007402 012637 000004          66$: MOV    (SP)+,@#ERRVEC ;;RESTORE ERROR VECTOR
3761
3762 007406 005037 001216          CLR    $PASS      ;;CLEAR PASS COUNT
3763 007412 012737 000200 001231          BIT    #APTSIZE,$ENVM ;;TEST USER SIZE UNDER APT
3764 007420 001403          BEQ    67$        ;;YES,USE NON-APT SWITCH
3765 007422 012737 001232 001140          MOV    #SSWREG,SWR ;;NO,USE APT SWITCH REGISTER
3766 007430          67$:
3767 007430 012737 000000 000032          MOV    #PRO,EMTVEC+2 ;SET EMT PRIORITY TO 0
3768 007436 012737 000000 000036          MOV    #PRO,TRAPVEC+2 ;SET TRAP PRIORITY TO 0
3769 007444 012737 007510 000004          MEMPAR: MOV    #1$,ERRVEC ;SET TIMEOUT VECTOR
3770 007452 012737 000340 000006          MOV    #PR7,ERRVEC+2
3771
3772 007460 012701 172100          MOV    #MEMBAS,R1 ;ADDR OF MEM CSR
3773 007464 005011          3$: CLR    (R1)      ;SEE IF CAN REFERENCE
3774 007466 012711 000001          MOV    #1,(R1)    ;SET ENABLE BIT IF YES
3775 007472 012737 050152 000114          MOV    #MEMERR,MEMVEC ;LOAD MEMORY CHECK VECTOR IF NO TIMEOUT
3776 007500 012737 000340 000116          MOV    #PR7,MEMVEC+2
3777 007506 000401          BR    2$
3778
3779 007510 022626          1$: CMP    (SP)+,(SP)+ ;ADJ STACK
3780 007512 062701 000002          2$: ADD    #2,R1    ;TRY NEXT CSR
3781 007516 020127 172140          CMP    R1,#MEMBAS+40 ;SEE IF TRIED ALL
3782 007522 001360          BNE    3$        ;BR IF NO
3783 007524 012737 000006 000004          MOV    #ERRVEC+2,ERRVEC ;RESTORE TRAP CATCHER

```



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3784 007532 005037 000006          CLR      ERRVEC+2
3785
3786 007536 004737 043134          JSR      PC,CLRFLG      ;CLEAR DDUMP THRU SIZFLG
3787 007542 005037 001220          CLR      $DEVCT
3788 007546 005037 001222          CLR      $UNIT
3789
3790
3791      ;FIND OUT IF XXDP, ACT, APT; CHAIN OR DUMP MODE
3792
3793
3794 007552 005737 000042          START1: TST      42
3795 007556 001015                    BNE      1$           ;BR IF AUTO
3796 007560 004737 043154          JSR      PC,TITLE     ;MANUAL, TYPE PROG ID
3797 007564 123727 000041 000013  CMPB     41,#13      ;13=LOADED BY XXDP
3798 007572 001011                    BNE      2$
3799 007574 005237 003432          INC      DDUMP        ;SET RK06 DUMP MODE FLAG
3800 007600 104401 055503          TYPE     ,MSG2       ;REPLACE DRO PACK W/SCRATCH & DO<CR>
3801 007604 000000                    HALT
3802 007606 000137 007622          JMP      ST2
3803 007612 000137 007666          1$:      JMP      ST3
3804 007616 005237 003440          2$:      INC      PPTP      ;SET ACT/APT/PTP DUMP MODE FLAG
3805
3806
3807      ;CHECK IF ALL PARAMETERS DEFAULTED. IF NOT, BEGIN INPUT DIALOGUE
3808      ;WITH OPERATOR. THE REPLY TO 'DRIVES TO BE TESTED' SHOULD BE
3809      ;DRIVE NOS. SEPERATED BY COMMAS & TERMINATED BY <CR>
3810      ;      EX:      DRIVES TO BE TESTED: 1,2,4<CR>
3811
3812
3813 007622 005737 001336          ST2:     TST      PARAM
3814 007626 001002                    BNE      1$           ;BR IF 220 START
3815 007630 000137 007720          JMP      ST4          ;200 START, DEFAULT & SIZE THE BUSS
3816 007634 104401 055667          1$:     TYPE     ,MSG3      ;DRIVES TO BE TESTED
3817 007640 004737 043234          JSR      PC,GDRVS    ;GET DR NOS.
3818 007644 104401 055721          TYPE     ,MSG4       ;BUSS ADDR
3819 007650 004737 043374          JSR      PC,GBA      ;GET BA
3820 007654 104401 055747          TYPE     ,MSG5       ;CONT INT VECTOR
3821 007660 004737 043422          JSR      PC,GINT    ;GET INT VECTOR
3822 007664 000427          BR      ST5
3823
3824
3825      ;AUTO MODE
3826      ;CHECK IF LOADED BY XXDP OR OTHER. SET FLAGS & NO INPUT DIALOGUE.
3827      ;DEFAULT ALL PARAMETERS. TEST ONLY THOSE DRIVES THAT ARE READY
3828      ;ON THE BUSS
3829
3830
3831 007666 123727 000041 000013  ST3:     CMPB     41,#13      ;13=LOADED BY XXDP
3832 007674 001007                    BNE      1$
3833 007676 005237 003434          INC      DDPCH       ;SET RK06 CHAIN MODE FLAG
3834 007702 004737 043154          JSR      PC,TITLE
3835 007706 104401 056012          TYPE     ,MSG7       ;DRO NOT TSTD
3836 007712 000402                    BR      ST4
3837 007714 005237 003436          1$:     INC      ACT11    ;SET ACT AUTO FLAG.
3838
3839 007720 012737 177440 001264  ST4:     MOV      #177440,$BASE ;DEFAULT VALUE
```

```
3840 007726 012737 000210 001314      MOV    #210,RKVEC      ;DEFAULT VALUE
3841 007734 004737 043454      JSR    PC,SETINT
3842 007740 005237 003472      INC    SIZEFLG        ;DO "SIZE THE BUSS" TEST
3843
3844 007744 005037 003304      ST5:   CLR    UNLD      ;INITIALIZE FLAGS
3845 007750 005037 003306      CLR    BADHDR        ;USED IN 'STOP ROUTINE
3846 007754 005037 003310      CLR    HPEND         ;FOR VALID PROGRAM HALTS
3847 007760 005037 001176      CLR    $ESCAPE
3848 007764 005037 001170      CLR    $TMP4         ;CLR RK06 IND. FLAG
3849 007770 012737 003444 001346      MOV    #DRIVO,DRVPT  ;SETUP
3850 007776 005037 001220      CLR    $DEVCT        ;NO. OF DRVS DONE
3851 010002 005037 001222      CLR    $UNIT         ;CURRENT DRV UNDER TEST
3852 010006 012737 010054 000004      MOV    #1$,ERRVEC   ;SETUP TIMEOUT ERROR VECTOR
3853 010014 005777 171306      TST   @LKS           ;SEE IF L-CLOCK THERE
3854 010020 005237 003464      INC    LCLKF         ;PRESENT, SET FLAG.
3855 010024 013700 001330      MOV    LCVEC,RO      ;VECTOR ADDR
3856 010030 012737 010116 000004      MOV    #2$,ERRVEC
3857 010036 005777 171256      TST   @PKS           ;SEE IF P-CLOCK THERE
3858 010042 005237 003466      INC    PCLKF         ;PRESENT, SET FLAG
3859 010046 013700 001332      MOV    PCVEC,RO     ;VECTOR ADDR
3860 010052 000412      BR    3$
3861
3862 010054 022626      1$:   CMP    (SP)+,(SP)+  ;L-CLOCK NOT THERE, CLEAR STACK
3863 010056 012737 010122 000004      MOV    #4$,ERRVEC
3864 010064 005777 171230      TST   @PKS           ;SEE IF P-CLOCK THERE
3865 010070 005237 003466      INC    PCLKF         ;PRESENT, SET FLAG
3866 010074 013700 001332      MOV    PCVEC,RO     ;VECTOR ADDR
3867 010100 005237 003470      3$:   INC    DOTIM        ;INDICATES TIMING TESTS CAN BE DONE
3868 010104 012720 047304      MOV    #CLOCK,(RO)+ ;SERVICE ROUTINE FOR CLOCKS
3869 010110 012710 000300      MOV    #PR6,(RO)
3870 010114 000407      BR    TST1          ;:GO TO NEXT TEST
3871
3872 010116 022626      2$:   CMP    (SP)+,(SP)+  ;P-CLOCK NOT THERE, CLEAR STACK
3873 010120 000767      BR    3$
3874
3875 010122 022626      4$:   CMP    (SP)+,(SP)+  ;NEITHER CLOCK THERE, CLEAR STACK
3876 010124 005037 003470      CLR    DOTIM        ;TIMING TESTS CANNOT BE DONE.
3877 010130 104401 056253      TYPE  ,MSG13        ;ALL TIMING TESTS BYPASSED
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.SBTTL BASIC CONTROLLER TESTS, SIZING & SETUP

*TEST 1 REFERENCE ALL CONTROLLER REGISTERS

* THIS TEST VERIFIES THAT ALL THE CONTROLLER REGISTERS
* CAN BE ACCESSED. THE INABILITY TO BE ACCESSED WILL
* RESULT IN A TIMEOUT TRAP WITH AN ERROR MSG. ANY
* ERROR IN THIS TEST WILL RESULT IN ABORTING ALL OTHER
* TESTS AND JUMPING TO 'END OF PASS'

```
TST1: SCOPE
MOV #1,$TIMES      ;;DO 1 ITERATION
MOV #STACK,SP     ;;RESTORE STK PTR
MOV #PRO,-(SP)    ;RESET PSW TO PRIORITY 0
MOV #5$,-(SP)    ;& MAKE IT LSI COMPATABLE
RTI

5$:
MOV #1$,ERRVEC   ;SETUP TIMOUT ERROR VECTOR
MOV $BASE,R5     ;SETUP INDEX REG.
TST RKCS1(R5)   ;REFERENCE ALL THE
TST RKCS2(R5)   ;CONTROLLER REGISTERS
TST RKWC(R5)
TST RKBA(R5)
TST RKDA(R5)
TST RKDS(R5)    ;TIMEOUTS IN THIS SECTION
TST RKER(R5)   ;INDICATE THAT THE CONTROLLER
TST RKASOF(R5) ;REGISTERS CANNOT BE READ.
TST RKDC(R5)   ;TESTING SHOULD NOT PROCEED
TST RKDB(R5)   ;UNTIL THIS IS REMEDIED.
TST RKMR1(R5)
TST RKMR2(R5)
TST RKMR3(R5)
TST RKECPS(R5)
TST RKECPT(R5)

MOV #BADTMO,ERRVEC ;SETUP TIMEOUT HANDLER
MOV #PR7,ERRVEC+2
BR TST2          ;;GO TO NEXT TEST

1$: CMP (SP)+,(SP)+ ;RESTORE STACK POINTER
ERROR 7          ;ABORT-COULD NOT REFERENCE CONTROLLER REGISTER
JMP $EOP1
```

*TEST 2 SIZE THE BUSS

* THIS TEST IS ENTERED ONLY IF 'DRIVE SELECTION' IS DEFAULTED
* EITHER BY RUNNING IN THE AUTO MODE OR A 200 START IN THE
* MANUAL MODE.
* EVERY DRIVE FROM 0 THRU 7 IS ADDRESSED.
* CONTROLLER ERROR (CERR) IS EXAMINED AND IF NOT SET, THE

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3936 : * DRIVE WILL BE TESTED AS AN RK06. IF SET, THE PROGRAM WILL BYPASS
3937 : * TESTING THAT DRIVE ONLY IF THE ERROR WAS A RESULT OF
3938 : * MDS, UFE OR NED BEING SET; OR BOTH NED & DRA RESET IN-
3939 : * DICATING THE OTHER PORT IS ACCESSED.
3940 : * IF CERR DUE TO DTYE, THE DRIVE WILL BE TESTED AS AN RK07.
3941 : *
3942 : *****
3943 010316 000004 TST2: SCOPE
3944 010320 012737 000001 001174 MOV #1,STIMES ;:DO 1 ITERATION
3945 010326 012706 001100 MOV #STACK,SP ;:RESTORE STK PTR
3946 010332 005237 001462 INC BYPCERR ;:DO NOT TEST CERR IN 'FRDY'
3947
3948 010336 132737 000200 001231 BITB #BIT7,$ENVM ;:SEE IF USE APT SELECTED DRIVES
3949 010344 001002 BNE 14$ ;:BR IF YES
3950 010346 000137 010466 JMP 12$ ;:ELSE DO NORM SIZING OR VERIFY
3951
3952 010352 104401 056133 14$: TYPE ,MSG10 ;:WILL TEST DRIVES
3953 010356 005037 003442 CLR DRIVS ;:# OF DRIVES PRESENT
3954 010362 005000 CLR R0 ;:DRV ADDR
3955 010364 012701 003444 MOV #DRIVO,R1 ;:DRV FLAG
3956 010370 013702 001266 MOV $DEVN,R2 ;:APT DEVICE MAP
3957
3958 010374 032702 000001 15$: BIT #BIT0,R2 ;:SEE IF DRV IN DEVICE MAP
3959 010400 001410 BEQ 16$ ;:BR IF NO
3960 010402 005237 003442 INC DRIVS ;:ELSE INCR DRIVE COUNT
3961 010406 005211 INC (R1) ;:& SET DRIVE PRESENT FLAG
3962 010410 104401 001205 TYPE ,SCLF
3963 010414 010046 MOV R0,-(SP) ;:SAVE R0 FOR TYPEOUT
3964 ;:TYPE DRIVE #
3965 010416 104403 TYPOS ;:GO TYPE--OCTAL ASCII
3966 010420 001 .BYTE 1 ;:TYPE 1 DIGIT(S)
3967 010421 000 .BYTE 0 ;:SUPPRESS LEADING ZEROS
3968
3969 010422 005721 16$: TST (R1)+ ;:ADV POINTER TO NEXT FLAG
3970 010424 005200 INC R0 ;:INC DRIVE #
3971 010426 022700 000010 CMP #8,,R0 ;:ALL 8 TESTED?
3972 010432 001402 BEQ 17$ ;:BR IF YES
3973
3974 010434 006002 ROR R2 ;:ELSE GET NEXT BIT OFF DEVICE MAP
3975 010436 000756 BR 15$ ;:& TRY AGAIN
3976
3977 010440 005737 003442 17$: TST DRIVS ;:SEE IF MORE DRIVES PRESENT
3978 010444 001402 BEQ 18$ ;:BR IF NO
3979 010446 000137 011152 JMP VERIFY ;:ELSE EXIT TEST & SETUP FOR RK07'S
3980
3981 010452 104126 18$: ERROR 126 ;:NO DRIVES FOUND IN $DEVN
3982 010454 000000 HALT ;:SETUP CORRECTLY & PRESS 'CONTINUE'
3983 010456 000137 007744 JMP ST5 ;:TO TRY AGAIN
3984 010462 000137 011152 JMP VERIFY ;:DO NOT SIZE, GO TO NEXT TEST
3985 010466 012765 000040 000010 20$: MOV #SCLR,RKCS2(R5) ;:SUBSYSTEM CLEAR
3986 010474 013737 001414 003360 MOV T10,TEMP1
3987 010502 004737 043566 JSR PC,FRDY ;:FIND RDY
3988 010506 104120 ERROR 120 ;:RDY NOT SET BY END OF SCLR
3989 010510 005737 003472 TST SIZFLG ;:SIZE BUS?
3990 010514 001762 BEQ 20$ ;:BR IF NO
3991 010516 104401 056133 TYPE ,MSG10 ;:WILL TEST DRIVES
```



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3992 010522 005037 003442      CLR    DRIVS      ;# OF DRIVES PRESENT
3993 010526 005000              CLR    R0        ;DRV ADDR
3994 010530 012701 003444      MOV    #DRIVO,R1 ;DRV FLAG
3995 010534              1$:
3996 010534 104415              SCOP1
3997 010536 012706 001100      MOV    #STACK,SP ;RESTORE STK PTR
3998
3999 010542 012765 000040 000010  MOV    #SCLR,RKCS2(R5) ;SUBSYS CLEAR
4000 010550 013737 001414 003360  MOV    T10,TEMP1
4001 010556 004737 043566      JSR    PC,FRDY    ;FIND RDY
4002 010562 104120              ERROR   120       ;RDY NOT SET BY END OF SCLR
4003 010564 010065 000010  MOV    R0,RKCS2(R5) ;SELECT THE DRIVE ADDR
4004 010570 012737 000001 003322  MOV    #SELDRV,HCS1
4005 010576 053737 001170 003322  BIS    $TMP4,HCS1 ;ADD CDT IF RK07
4006 010604 013765 003322 000000  MOV    HCS1,RKCS1(R5) ;GET STATUS
4007 010612 013737 001426 003360  MOV    T50000,TEMP1
4008 010620 004737 044256      JSR    PC,DLY     ;DO DELAY TO CATCH MDS
4009 010624 013737 001414 003360  MOV    T10,TEMP1
4010 010632 004737 043566      JSR    PC,FRDY    ;FIND RDY
4011 010636 104117              ERROR   117       ;NO RDY AFTER SELECT DR. CMD
4012 010640 032737 100000 003322  BIT    #CERR,HCS1
4013 010646 001056              BNE    2$
4014 010650 013737 003350 003360  MOV    HMR2,TEMP1
4015 010656 042737 177770 003360  BIC    #^C<DRVMSK>,TEMP1
4016 010664 020037 003360      CMP    R0,TEMP1  ;S/B SAME
4017 010670 001024              BNE    3$
4018 010672 005700              TST    R0
4019 010674 001007              BNE    4$
4020 010676 005737 003434      TST    DDPCH     ;SEE IF XXDP CHAIN MODE
4021 010702 001022              BNE    5$
4022 010704 123727 000041 000013  CMPB   41,#13    ;IS DRIVE 0 TO BE TESTED
4023 010712 001416              BEQ    5$        ;BRANCH IF NOT
4024 010714 005237 003442      4$: INC    DRIVS     ;INC DRIVE COUNT.
4025 010720 005211              INC    (R1)      ;SET DRIVE PRESENT FLAG
4026 010722 053711 001170  BIS    $TMP4,(R1) ;ADD CDT IF SET
4027 010726 104401 001205      TYPE   $CRLF
4028 010732 010046      MOV    R0,-(SP)  ;;SAVE R0 FOR TYPEOUT
4029
4030 010734 104403              TYPOS
4031 010736 001              .BYTE 1          ;;TYPE DR #
4032 010737 000              .BYTE 0          ;;GO TYPE--OCTAL ASCII
4033 010740 000403      BR    5$        ;;TYPE 1 DIGIT(S)
4034
4035 010742 004737 044274      3$: JSR    PC,BYP    ;TYPE BYPASS DR #
4036 010746 104001      ERROR   1        ;SELECTED DR # DOES NOT MATCH RKMR2 DR #
4037
4038 010750 005721      5$: TST    (R1)+    ;ADVANCE PTR TO NEXT DR. FLAG
4039 010752 005200      INC    R0        ;INC DR #
4040 010754 005037 001170  CLR    $TMP4     ;CLEAR RK07 FLAG FOR NEXT DRIVE
4041 010760 022700 000010  CMP    #8.,R0
4042 010764 001263      BNE    1$
4043 010766 005737 003442      TST    DRIVS    ;MORE LEFT.
4044 010772 001065      BNE    10$
4045 010774 104123      ERROR   123     ;NO DRIVES FOUND
4046 010776 000000      HALT
4047 011000 000137 007744      JMP    ST5      ;SETUP CORRECTLY
; & PRESS 'CONT'

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4048
4049 011004 032737 000040 003336 2$: BIT #DTYE,HER
4050 011012 001405 BEQ 13$
4051 011014 012737 002000 001170 MOV #CDT,$TMP4 ;ADD CDT
4052 011022 000137 010534 JMP 1$ ;TRY AGAIN
4053 011026 032737 001000 003324 13$: BIT #MDS,HCS2
4054 011034 001015 BNE 6$
4055 011036 032737 000400 003324 BIT #UFE,HCS2
4056 011044 001015 BNE 7$
4057 011046 032737 000001 003334 BIT #DRA,HDS
4058 011054 001015 BNE 8$
4059 011056 032737 010000 003324 BIT #NED,HCS2
4060 011064 001424 BEQ 9$
4061 011066 000730 BR 5$
4062
4063 011070 004737 044274 6$: JSR PC,BYP ;TYPE BYP DR #
4064 011074 104002 ERROR 2 ;MDS DETECTED
4065 011076 000724 BR 5$
4066
4067 011100 004737 044274 7$: JSR PC,BYP
4068 011104 104003 ERROR 3 ;UFE DETECTED
4069 011106 000720 BR 5$
4070
4071 011110 032737 010000 003324 8$: BIT #NED,HCS2
4072 011116 001676 BEQ 4$
4073 011120 104401 056354 TYPE ,MSG15 ;DRV#
4074 011124 010046 MOV RO,-(SP) ;:SAVE RO FOR TYPEOUT
4075 ;:TYPE DR#
4076 011126 104403 TYPOS ;:GO TYPE--OCTAL ASCII
4077 011130 001 .BYTE 1 ;:TYPE 1 DIGIT(S)
4078 011131 000 .BYTE 0 ;:SUPPRESS LEADING ZEROS
4079 011132 104010 ERROR 10 ;:DRA & NED BOTH SET
4080 011134 000705 BR 5$
4081
4082 011136 004737 044274 9$: JSR PC,BYP
4083 011142 104004 ERROR 4 ;NO DRA & NO NED = OTHER PORT SELECTED
4084 011144 000701 BR 5$
4085 011146 000137 011544 10$: JMP NUDRV
4086
4087 011152 VERIFY:
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:*****
:*TEST 3 VERIFY OPERATOR DRIVE SELECTIONS
:*
:* THIS TEST IS ENTERED ONLY IF DRIVE SELECTION IS NOT
:* DEFAULTED. EVERY DRIVE FROM 0 TO 7 IS ADDRESSED &
:* CONTROLLER ERROR (CERR) IS EXAMINED. IF NOT SET, THE
:* PROGRAM WILL ASSUME THE DRIVE IS PRESENT AS AN RK06
:* IF CERR WAS SET, THAT DRIVE WILL BE BYPASSED
:* ONLY IF THE ERROR WAS A RESULT OF MDS OR UFE SET OR BOTH
:* NED & DRA RESET (WRONG PORT). IF CERR IS A RESULT OF
:* NED ONLY, IT IS CHECKED AGAINST THE INPUTTED INFO TO
:* VERIFY IT WAS NOT SPECIFIED.
:* IF CERR DUE TO DTYE , THE DRIVE WILL BE TESTED AS AN RK07.
:*
    
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4104 .....  
4105 011152 000004  
4106 011154 012737 000001 001174 TST3: SCOPE  
4107 011162 012706 001100 MOV #1,$TIMES ;:DO 1 ITERATION  
4108 011166 005000 MOV #STACK,SP ;:RESTORE STK PTR  
4109 011170 012701 003444 CLR R0 ;:DRIVE ADDR  
4110 011174 1$: MOV #DRIVO,R1 ;:DRIVE FLAG  
4111 011174 104415  
4112 011176 012706 001100 SCOPI  
4113 MOV #STACK,SP ;:RESTORE STK PTR  
4114 011202 012765 000040 000010 MOV #SCLR,RKCS2(R5)  
4115 011210 013737 001414 003360 MOV T10,TEMP1  
4116 011216 004737 043566 JSR PC,FRDY ;:FIND RDY  
4117 011222 104120 ERROR 120 ;:NO RDY AFTER SCLR  
4118 011224 010065 000010 MOV R0,RKCS2(R5) ;:DRV ADDR  
4119 011230 012737 000001 003322 MOV #SELDRV,HCS1  
4120 011236 053737 001170 003322 BIS $TMP4,HCS1 ;:ADD CDT IF RK07  
4121 011244 013765 003322 000000 MOV HCS1,RKCS1(R5) ;:GET STATUS  
4122 011252 013737 001426 003360 MOV T50000,TEMP1  
4123 011260 004737 044256 JSR PC,DLY ;:DO DELAY TO CATCH MDS  
4124 011264 013737 001414 003360 MOV T10,TEMP1  
4125 011272 004737 043566 JSR PC,FRDY ;:FIND RDY  
4126 011276 104117 ERROR 117 ;:NO RDY AFTER SELDRV CMD  
4127 011300 032737 100000 003322 BIT #CERR,HCS1  
4128 011306 001036 BNE 2$  
4129 011310 013737 003350 003360 MOV HMR2,TEMP1  
4130 011316 042737 177770 003360 BIC #^C<DRVMSK>,TEMP1  
4131 011324 020037 003360 CMP R0,TEMP1 ;:S/B SAME  
4132 011330 001014 BNE 3$  
4133 011332 005711 11$: TST (R1)  
4134 011334 001402 BEQ 4$  
4135 011336 053711 001170 BIS $TMP4,(R1) ;:ADD CDT IF SET  
4136 011342 005721 4$: TST (R1)+ ;:SHIFT PTR TO NEXT DR FLAG  
4137 011344 005200 INC R0 ;:INC DR#  
4138 011346 005037 001170 CLR $TMP4 ;:CLEAR CDT FLAG FOR NEXT DRIVE  
4139 011352 022700 000010 CMP #8.,R0  
4140 011356 001306 BNE 1$ ;:MORE LEFT  
4141 011360 000475 BR TST4 ;:GO TO NEXT TEST  
4142  
4143 011362 004737 044274 3$: JSR PC,BYP ;:TRY BYPASS DRIVE#  
4144 011366 104001 ERROR 1 ;:WRITTEN DR# DOES NOT MATCH RKMR2 DR#  
4145 011370 005711 TST (R1)  
4146 011372 001763 BEQ 4$ ;:BRANCH IF NOT SPEC BY INPUT  
4147 011374 005337 003442 12$: DEC DRIVS ;:DECREMENT TOTAL DRIVS  
4148 011400 005011 CLR (R1) ;:CLEAR DRIVE FLAG  
4149 011402 000757 BR 4$  
4150  
4151 011404 032737 000040 003336 2$: BIT #DTYE,HER  
4152 011412 001405 BEQ 13$  
4153 011414 012737 002000 001170 MOV #CDT,$TMP4 ;:ADD CDT  
4154 011422 000137 011174 JMP 1$ ;:TRY AGAIN  
4155  
4156 011426 032737 001000 003324 13$: BIT #MDS,HCS2  
4157 011434 001027 BNE 6$  
4158 011436 032737 000400 003324 BIT #UFE,HCS2  
4159 011444 001027 BNE 7$
```

```

4160 011446 032737 000001 003334 BIT #DRA,HDS
4161 011454 001005 BNE 8$
4162 011456 032737 010000 003324 BIT #NED,HCS2
4163 011464 001423 BEQ 9$
4164 011466 000404 BR 10$
4165 011470 032737 010000 003324 8$: BIT #NED,HCS2
4166 011476 001715 BEQ 11$
4167 011500 005711 10$: TST (R1)
4168 011502 001717 BEQ 4$
4169
4170 011504 004737 044274 JSR PC,BYP ;TYPE BYPASS DRIVE#
4171 011510 104006 ERROR 6
4172 011512 000730 BR 12$
4173
4174 011514 004737 044274 6$: JSR PC,BYP ;TYPE BYPASS DRIVE#
4175 011520 104002 ERROR 2 ;MDS DETECTED
4176 011522 000724 BR 12$
4177
4178 011524 004737 044274 7$: JSR PC,BYP
4179 011530 104003 ERROR 3 ;UFE DETECTED
4180 011532 000720 BR 12$
4181
4182 011534 004737 044274 9$: JSR PC,BYP
4183 011540 104004 ERROR 4 ;DRA & NED RESET - OTHER PORT SELECTED
4184 011542 000714 BR 12$
4185
4186
4187
4188 ;THIS PART OF THE PROGRAM WILL BE REPEATED FOR EACH
4189 ;DRIVE PRESENT
4190
4191 ;'$SUNIT' CONTAINS THE ADDRESS OF THE DRIVE CURRENTLY
4192 ;UNDER TEST
4193
4194 011544 005037 001462 NUDRV: CLR BYPCERR ;ENTER HERE FROM LAST TEST
4195 ;& TEST CERR IN 'FRDY'
4196 011550 005037 001170 CLR $TMP4 ;CLEAR RK07 IND FLAG
4197
4198
4199 ;*****
4200 ;*TEST 4 FIND NEXT DRIVE TO BE TESTED
4201 ;*
4202 ;* THIS TEST FINDS THE NEXT DRIVE PRESENT & PUTS THAT
4203 ;* ADDRESS IN '$SUNIT' & $TMP4 IS SET TO CDT IF DRIVE IS RK07.
4204 ;* THROUGHOUT THE FOLLOWING TESTS, THE DRIVE TESTED IS
4205 ;* THE DRIVE WHOSE ADDRESS IS IN '$SUNIT'.
4206 ;*****
4207 011554 000004 TST4: SCOPE
4208 011556 012737 000001 001174 MOV #1,$TIMES ;;DO 1 ITERATION
4209 011564 012706 001100 MOV #STACK,SP ;RESTORE STK PTR
4210 011570 012737 000004 001214 MOV #STN-1,$TESTN
4211 011576 012737 000004 001102 MOV #STN-1,$STNM
4212
4213 011604 005737 003442 TST DRIVS ;ANY DRIVES PRESENT?
4214 011610 001004 BNE 4$ ;YES BRANCH
4215 011612 104401 056472 TYPE ,MSG19 ;ALL DRIVES TESTED
  
```



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CZR6HFO UNIBUS RK6 DR PT1          MACY11 30(1046) 04-JAN-82 13:01 F 7 PAGE 84
CZR6HF.P11 04-JAN-82 12:44          T4      FIND NEXT DRIVE TO BE TESTED                               SEQ 0083

4216 011616 000137 042760          JMP      SEOP1          ;NO, GO TO END
4217
4218 011622 013701 001346          4$:     MOV      DRVPTR,R1      ;ADDR OF NEXT DRIVE FLAG
4219 011626 005737 001220          TST     $DEVCT          ;IS FIRST DRIVE BEING CHECKED
4220 011632 001402          BEQ     2$              ;YES, BRANCH
4221 011634 005237 001222          1$:     INC     $UNIT          ;INCR DRIVE ADDR TO NEXT DRIVE
4222 011640 005711          2$:     TST     (R1)          ;IS DRIVE PRESENT?
4223 011642 001002          BNE     5$              ;BR IF YES
4224 011644 005721          TST     (R1)+          ;ELSE FIND NEXT DRIVE
4225 011646 000772          BR      1$
4226 011650 005737 003434          5$:     TST     DDPCH          ;DDP CHAIN MODE?
4227 011654 001405          BEQ     3$              ;BR IF NO
4228 011656 005737 001222          TST     $UNIT          ;ELSE IS IT DRIVE 0?
4229 011662 001002          BNE     3$              ;BR IF NO
4230 011664 005721          TST     (R1)+          ;ELSE FIND NEXT DRIVE PRESENT
4231 011666 000762          BR      1$
4232
4233 011670 032721 002000          3$:     BIT     #CDT,(R1)+      ;SEE IF DRIVE UNDER TEST IS RK07
4234 011674 001403          BEQ     6$              ;BR IF NO
4235 011676 012737 002000 001170          MOV     #CDT,$TMP4      ;ELSE SET RK07 FLAG
4236 011704 010137 001346          6$:     MOV     R1,DRVPTR      ;STORE POINTER TO NEXT DR FLAG
4237 011710 104401 056354          TYPE   ,MSG15          ;"DRIVE"
4238 011714 013700 001222          MOV     $UNIT,R0
4239 011720 010046          MOV     R0,-($P)       ;;SAVE R0 FOR TYPEOUT
4240
4241 011722 104403          TYPOS
4242 011724 001          .BYTE 1                ;;DRIVE #
4243 011725 000          .BYTE 0                ;;GO TYPE--OCTAL ASCII
4244
4245          : TYPE ,SCLF          ;;TYPE 1 DIGIT(S)
4246
4247 011726 005737 001170          TST     $TMP4          ;;SUPPRESS LEADING ZEROS
4248 011732 001030          BNE     7$              ;
4249 011734 012737 000631 012076          MOV     #631,LCM1      ;SEE IF RK07 UNDER TEST
4250 011742 012737 000632 012100          MOV     #632,LC        ;BR IF YES
4251 011750 012737 000633 012102          MOV     #633,LCP1     ;ELSE LOAD RK06 PARAMETERS
4252 011756 005037 012114          CLR     E.DDT
4253 011762 012737 000400 012104          MOV     #400,MC
4254 011770 012737 001000 012106          MOV     #1000,MC1
4255 011776 012737 000777 012110          MOV     #777,MASK
4256 012004 012737 160017 012112          MOV     #160017,MASK1
4257 012012 000443          BR      TST5           ;;GOTO NEXT TEST
4258
4259 012014 012737 001455 012076          7$:     MOV     #1455,LCM1      ;LOAD RK07 PARAMETERS
4260 012022 012737 001456 012100          MOV     #1456,LC
4261 012030 012737 001457 012102          MOV     #1457,LCP1
4262 012036 012737 000400 012114          MOV     #D,DDT,E.DDT
4263 012044 012737 001000 012104          MOV     #1000,MC
4264 012052 012737 002000 012106          MOV     #2000,MC1
4265 012060 012737 001777 012110          MOV     #1777,MASK
4266 012066 012737 140017 012112          MOV     #140017,MASK1
4267 012074 000412          BR      TST5           ;;GOTO NEXT TEST
4268
4269 012076 000000          LCM1: 0                ;LC-1
4270 012100 000000          LC:    0                ;LAST CYL
4271 012102 000000          LCP1: 0                ;LC+1

```

4272 012104 000000
4273 012106 000000
4274 012110 000000
4275 012112 000000
4276 012114 000000
4277 012116 000000
4278 012120 000001

MC: 0 ;MAJ CYL
MC1: 0 ;MAJ CYL + 1 SHIFT
MASK: 0
MASK1: 0
E.DDT: 0 ;EXPECTED DRIVE TYPE TO E.A0
FC: 0 ;FIND CYL
FCP1: 1 ;FC+1

4279
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4287

:TEST 5 UNLOAD DRIVE TO BE TESTED
:
: THIS TEST UNLOADS THE DRIVE TO BE TESTED NEXT,
: WAITS FOR ATTN & VERIFIES IT CAME FROM THE CORRECT DRIVE.
: IT THEN WAITS FOR SPEED OK TO GO LOW BEFORE
: PROCEEDING TO THE NEXT TEST
:*****

4288 012122 000004
4289 012124 012737 000001 001174
4290 012132 012706 001100
4291
4292 012136 005237 003304
4293
4294 012142 004737 045462
4295 012146 104024
4296
4297 012150 012737 000007 003322
4298 012156 004737 043472
4299 012162 104011
4300 012164 004737 044050
4301 012170 104012
4302
4303 012172 004737 045462
4304 012176 104024
4305
4306 012200 013737 001414 003362
4307 012206 004737 046516
4308 012212 104315
4309

TST5: SCOPE
MOV #1,\$TIMES ;:DO 1 ITERATION
MOV #STACK,SP
INC UNLD ;USED TO CHECK VALID HALT
JSR PC,SUBCLR
ERROR 24 ;CERR AFTER SCLR
MOV #UNLOAD,HCS1
JSR PC,DOCMD ;DO UNLOAD CMD & GET CONTR READY
ERROR 11 ;RDY NOT SET AFTER UNLOAD CMD.
JSR PC,TSTATN
ERROR 12 ;NO ATTN AFTER UNLOAD CMD
JSR PC,SUBCLR
ERROR 24 ;CERR AFTER SCLR
MOV T10,TEMP2
JSR PC,FSPOK
ERROR 315 ;SPEED NOT DOWN BY TIMEOUT

4310
4311
4312
4313

4314 012214
4315
4316

PFSRT: ;ENTER HERE FOR POWER FAIL RESTART
.SBTTL STATIC & CYCLE UP TESTS

4317
4318
4319
4320
4321
4322
4323

:TEST 6 REFERENCE & CHECK ALL STATUS BYTES IN RKMR2 & RKMR3
:
: CHECKS THE ABILITY TO REFERENCE ALL
: DRIVE REGISTERS AND THAT THEY CONTAIN CORRECT STATUS.
:*****

4324 012214 000004
4325 012216 012737 000001 001174
4326 012224 012706 001100
4327

TST6: SCOPE
MOV #1,\$TIMES ;:DO 1 ITERATION
MOV #STACK,SP ;RESTORE STK PTR


```

4328 012230 004737 045462 JSR PC,SUBCLR
4329 012234 104024 ERROR 24 ;CERR AFTER SCLR
4330
4331 012236 004737 045132 JSR PC,GSTAT
4332 012242 032737 000100 003350 BIT #D.VV,HMR2
4333 012250 001004 BNE 4$ ;BR IF VV SET
4334 012252 012737 000040 003412 MOV #D.DRA,E.A0 ;LOAD EXPECTED VALUE FOR A0
4335 012260 000403 BR 5$
4336
4337 012262 012737 000140 003412 4$: MOV #<D.DRA!D.VV>,E.A0
4338 012270 005037 003414 5$: CLR E.B0 ;EXPECTED MSG B0
4339 012274 012737 000740 003416 MOV #<D.HDHM!D.BRHM!D.DOOR!D.CART>,E.A1 ;EXPECTED MSG A1
4340 012302 012737 000001 003420 MOV #1,E.B1 ;EXPECTED MSG B1
4341 012310 005037 003422 CLR E.A2 ;EXPECTED MSG A2
4342 012314 012737 000002 003424 MOV #2,E.B2 ;EXPECTED MSG B2
4343 012322 012737 000003 003430 MOV #3,E.B3 ;EXPECTED MSG B3
4344
4345 012330 004737 044310 JSR PC,CHKMSG ;CHECK MSGS A0,B0,A1,B1
4346 012334 000007 .WORD T.A2!T.B2!T.B3 ;& MSGS SPECIFIED HERE
4347 012336 104016 ERROR 16 ;MSG A0 ERROR FOR DRIVE UNLOADED
4348 012340 104017 ERROR 17 ;MSG B0 ERROR
4349 012342 104020 ERROR 20 ;MSG A1 ERROR
4350 012344 104021 ERROR 21 ;MSG B1 ERROR
4351
4352 012346 012765 000002 000026 MOV #2,RKMR1(R5) ;SELECT WORD 2
4353 012354 004737 045132 JSR PC,GSTAT
4354 012360 005737 001362 TST CYLDIF ;SEE IF MSG A2=0
4355 012364 001401 BEQ 64$ ;BR IF YES
4356 012366 104022 ERROR 22 ;MSG A2 NOT CLEARED FOR DRIVE UNLOADED
4357 012370 005737 001364 64$: TST CYLADD ;SEE IF MSG B2=0
4358 012374 001401 BEQ 65$ ;BR IF YES
4359 012376 104023 ERROR 23 ;MSG B2 NOT CLEARED FOR DRIVE UNLOADED
4360 012400 65$:
4361 012400 023727 001432 000001 CMP HEADA,#1 ;FOR HEAD 0, B3=1
4362 012406 001401 BEQ TST7 ;GO TO NXT TST IF YES
4363 012410 104056 ERROR 56 ;HEAD REG IN B3 NOT 0 IN UNLOAD
4364
4365
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4367
4368
4369
4370
4371
4372
4373
4374 012412 000004 TST7: SCOPE
4375 012414 012737 000001 001174 MOV #1,$TIMES ;DO 1 ITERATION
4376 012422 012706 001100 MOV #STACK,SP ;RESTORE STK PTR
4377
4378 012426 005737 001216 TST $PASS
4379 012432 001046 BNE TST10 ;GO TO NEXT IF NOT FIRST PASS
4380 012434 004737 045462 JSR PC,SUBCLR ;DO SUBSYS CLEAR
4381 012440 104024 ERROR 24 ;CERR AFTER SCLR
4382
4383 012442 104401 056366 TYPE ,MSG16 ;DRIVE SERIAL NO.

```

```

*****
*TEST 7 PRINT DRIVE SERIAL NUMBER
*
* THIS TEST READS & PRINTS THE DRIVE SERIAL # FROM MSG A, WORD 11
* IN BCD & IS PERFORMED ON THE 1ST PASS ONLY
*
*****

```

```

4384 012446 012765 000003 000026 MOV #3,RKMR1(R5) ;SELECT BYTE 3
4385 012454 004737 045132 JSR PC,GSTAT ;GET STATUS
4386 012460 013701 003350 MOV HMR2,R1 ;GET SERIAL #
4387 012464 012704 054002 MOV #SOCTLV,R4 ;GET ADDR CHAR BUFF
4388 012470 010446 MOV R4,-(SP) ;STORE ON STACK FOR $SUPRS
4389 012472 012703 000003 MOV #3,R3 ;SETUP CHAR COOUNT
4390 012476 006101 ROL R1 ;INITIALIZE BIT POSITIONS
4391 012500 006101 ROL R1
4392 012502 006101 1$: ROL R1 ;GET NEXT 4 BITS
4393 012504 006101 ROL R1
4394 012506 006101 ROL R1
4395 012510 006101 ROL R1
4396 012512 010100 MOV R1,R0 ;GET WORKING COPY
4397 012514 042700 177760 BIC #177760,R0 ;CLEAR ALL BUT LOW 4 BITS
4398 012520 052700 000060 BIS #60,R0 ;CONVERT TO ASCII DIGIT
4399 012524 110024 MOV R0,(R4)+ ;PUT ASCII DIGIT INTO CHAR BUFF
4400 012526 005303 DEC R3
4401 012530 001364 BNE 1$ ;BR IF ALL 3 CHARS NOT DONE
4402 012532 105014 CLRB (R4) ;ELSE INSERT NULL TERMINATOR
4403 012534 004737 054250 JSR PC,$SUPRS ;TYPE
4404 012540 104401 001205 TYPE ,SCLF
4405 012544 104401 001205 TYPE ,SCLF
4406
4407
4408 *****
4409 *TEST 10 SET VV WITH PACK CMD
4410 *
4411 * IF VV IS RESET, THE PACK CMD IS USED TO SET IT.
4412 *
4413 *****
4414 012550 000004 TST10: SCOPE
4415 012552 012737 000001 001174 MOV #1,$TIMES ;:DO 1 ITERATION
4416 012560 012706 001100 MOV #STACK,SP ;:RESTORE STK PTR
4417 012564 005065 000026 CLR RKMR1(R5) ;:SELECT BYTE 0
4418 012570 004737 045132 JSR PC,GSTAT ;:GET STATUS
4419 012574 032737 000100 003350 BIT #D.VV,HMR2
4420 012602 001021 BNE TST11 ;:GO TO NEXT TEST IF VV SET
4421
4422 012604 104415 SCOP1
4423 012606 012706 001100 MOV #STACK,SP ;:RESTORE STK PTR
4424
4425 012612 004737 045462 JSR PC,SUBCLR
4426 012616 104024 ERROR 24 ;:CERR AFTER SCLR
4427
4428 012620 012737 000003 003322 MOV #PACK,HCS1
4429 012626 004737 043472 JSR PC,DOCMD ;:DO PACK CMD & GET CONTR READY
4430 012632 104116 ERROR 116 ;:RDY NOT SET AFTER PACK CMD
4431
4432 012634 032737 000100 003350 BIT #D.VV,HMR2
4433 012642 001001 BNE TST11 ;:GO TO NEXT TEST IF VV NOW SET
4434 012644 104027 ERROR 27 ;:PACK DID NOT SET V.V.
4435
4436 *****
4437 *TEST 11 RELEASE DRIVE
4438 *
4439 * TESTS THE ABILITY TO RECOGNIZE THE RLS BIT AND NOT RAISE SACK

```



```
4440  
4441  
4442 012646 000004  
4443 012650 012737 000001 001174  
4444 012656 012706 001100  
4445  
4446 012662 004737 045462  
4447 012666 104024  
4448 012670 032737 000400 003324  
4449 012676 001401  
4450 012700 104003  
4451  
4452 012702  
4453 012702 104415  
4454 012704 012706 001100  
4455  
4456 012710 004737 045462  
4457 012714 104024  
4458  
4459 012716 062765 000010 000010  
4460 012724 004737 045132  
4461  
4462 012730 032737 100000 003322  
4463 012736 001401  
4464 012740 104025  
4465 012742 032737 000400 003324 2$:  
4466 012750 001401  
4467 012752 104026
```

TST11: SCOPE
MOV #1,\$TIMES ;:DO 1 ITERATION
MOV #STACK,SP ;:RESTORE STK PTR
JSR PC,SUBCLR ;:DO SUBSYS CLEAR & GET STATUS
ERROR 24 ;:CONTR ERROR SET AFTER SCLR
BIT #UFE,HCS2
BEQ 1\$
ERROR 3 ;:UFE SET AFTER SCLR
1\$:
SCOP1
MOV #STACK,SP ;:RESTORE STK PTR
JSP PC,SUBCLR
ERRKJR 24 ;:CERR AFTER SCLR
ADD #RLS,RKCS2(R5) ;:ADD RELEASE BIT TO \$UNIT
JSR PC,GSTAT ;:GET STATUS
BIT #CERR,HCS1 ;:CHECK FOR CONTR ERROR
BEQ 2\$
ERROR 25 ;:RLS SET CERR
BIT #UFE,HCS2
BEQ TST12 ;:GO TO NEXT TEST IF SET
ERROR 26 ;:SACK SET AFTER RLS SENT

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SEQ 0088

4468


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4469
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4477 012754 000004
4478 012756 012737 000001 001174
4479 012764 012706 001100
4480
4481 012770 004737 045462
4482 012774 104024
4483 012776 005737 001170
4484 013002 001013
4485 013004 032737 000400 003350
4486 013012 001401
4487 013014 104030
4488 013016 032737 000400 003334 2$:
4489 013024 001414
4490 013026 104031
4491 013030 000412
4492
4493 013032 032737 000400 003350 1$:
4494 013040 001001
4495 013042 104005
4496 013044 032737 000400 003334 8$:
4497 013052 001001
4498 013054 104152
4499 013056 032737 000040 003336 3$:
4500 013064 001401
4501 013066 104032
4502
4503 013070
4504 013070 104415
4505 013072 012706 001100
4506
4507 013076 004737 045462
4508 013102 104024
4509
4510 013104 005737 001170
4511 013110 001404
4512 013112 012765 000001 000000
4513 013120 000403
4514 013122 012765 002001 000000 9$:
4515 013130 013737 001414 003360 10$:
4516 013136 004737 043566
4517 013142 104117
4518 013144 005737 001170
4519 013150 001013
4520 013152 032737 000400 003350
4521 013160 001401
4522 013162 104030
4523 013164 032737 000400 003334 5$:
4524 013172 001414
```

:TEST 12 DRIVE TYPE TEST
:*

:* THIS TEST COMPARES DRIVE TYPE IN MSG A AGAINST 'DDT' IN RKDS.
:* WRONG CDT IN RKCS1 IS SENT & ERRORS ARE VERIFIED.
:*

TST12: SCOPE
MOV #1,\$TIMES ;:DO 1 ITERATION
MOV #STACK,SP ;:RESTORE STK PTR
JSR PC,SUBCLR ;:SUBSYS CLEAR & GET STATUS
ERROR 24 ;:CONT ERROR SET AFT SUBSYS CLEAR
TST \$TMP4
BNE 1\$
BIT #D.DDT,HMR2
BEQ 2\$
ERROR 30 ;:DR TYPE SET IN MR2
BIT #DDT,HDS
BEQ 3\$
ERROR 31 ;:DDT SET IN RKDS
BR 3\$
BIT #D.DDT,HMR2
BNE 8\$
ERROR 5 ;:DR TYPE NOT SET IN MR2
BIT #DDT,HDS
BNE 3\$
ERROR 152
BIT #DTYE,HER
BEQ 4\$
ERROR 32 ;:DTYE SET IN RKER
4\$:
SCOP1
MOV #STACK,SP ;:RESTORE STK PTR
JSR PC,SUBCLR
ERROR 24 ;:CERR AFTER SCLR
TST \$TMP4 ;:SEE IF RK07
BEQ 9\$;:BR IF NO
MOV #SELDRV,RKCS1(R5)
BR 10\$
MOV #<CDT!SELDRV>,RKCS1(R5) ;:GET STATUS WITH CDT SET
MOV T10,TEMP1
JSR PC,FRDY ;:FIND RDY
ERROR 117 ;:RDY NOT SET BY END OF SEL DRV CMD
TST \$TMP4
BNE 11\$
BIT #D.DDT,HMR2
BEQ 5\$
ERROR 30 ;:DR TYPE SET IN MR2
BIT #DDT,HDS
BEQ 6\$

```

4525 013174 104031 ERROR 31 ;DDT SET IN RKDS
4526 013176 000412 BR 6$
4527
4528 013200 032737 000400 003350 11$: BIT #D.DDT,HMR2
4529 013206 001001 BNE 12$
4530 013210 104005 ERROR 5 ;DR TYPE NOT SET IN MR2
4531 013212 032737 000400 003334 12$: BIT #DDT,HDS
4532 013220 001001 BNE 6$
4533 013222 104152 ERROR 152 ;DDT NOT SET IN RKDS
4534 013224 032737 000040 003336 6$: BIT #DTYE,HER ;DTYE=DDT(NOT)*CDT
4535 013232 001001 BNE 7$
4536 013234 104033 ERROR 33 ;DTYE NOT SET AFT WRITING WRONG CDT
4537 013236 032737 100000 003322 7$: BIT #CERR,HCS1
4538 013244 001001 BNE TST13 ;:GO TO NEXT TEST
4539 013246 104034 ERROR 34 ;CERR NOT SET AFT WRITING WRONG CDT
4540
4541
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4548
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4550
4551
4552

```

 :*TEST 13 C-D PARITY ERROR DETECTION
 :*

TESTS THE ABILITY OF THE DRIVE TO DETECT EVEN PARITY SENT BY
 THE CONTROLLER BY SETTING 'PAT' ON RKMR1.
 THE DRIVE SHOULD RESPOND WITH 'C-D PARITY ERROR'
 THE DRIVE STILL SENDS ODD PARITY TO THE CONTROLLER WHICH IS NOW
 CHECKING FOR EVEN PARITY THEREFORE THE CONTROLLER SHOULD DETECT
 AN ERROR AND SET DCPAR.
 THE ERROR CONDITION IS RESET WITH THE CLEAR CMD

 TST13: SCOPE

```

4553 013250 000004 TST13: SCOPE
4554 013252 012737 000001 001174 MOV #1,$TIMES ;:DO 1 ITERATION
4555 013260 012706 001100 MOV #STACK,SP ;:RESTORE STK PTR
4556
4557 013264 004737 045462 JSR PC,SUBCLR ;SUBSYS CLEAR & GET STATUS
4558 013270 104024 ERROR 24 ;CONT ERROR AFTER SUBSYS CLR
4559 013272 032737 001000 003352 BIT #D.PAR,HMR3
4560 013300 001401 BEQ 2$
4561 013302 104035 ERROR 35 ;C-D PARITY ERROR SET IN MR3
4562 013304 032737 020000 003322 2$: BIT #DCPAR,HCS1

```


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SEQ 0091

4563	013312	001401			BEQ	3\$		
4564	013314	104036			ERROR	36		;DCPAR SET IN CS1
4565								
4566	013316			3\$:				
4567	013316	104415			SCOP1			
4568	013320	012706	001100		MOV	#STACK,SP		;RESTORE STK PTR
4569								
4570	013324	004737	045462		JSR	PC,SUBCLR		
4571	013330	104024			ERROR	24		;CERR AFTER SCLR
4572								
4573	013332	012765	000020	000026	MOV	#PAT,RKMR1(R5)		;SELECT BYTE 0 & EVEN PARITY
4574	013340	004737	045132		JSR	PC,GSTAT		;GET STATUS

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C-D PARITY ERROR DETECTION

B 8

SEQ 0092

4575 013344 032737 000200 003352
4576 013352 001001

BIT #D.FLT,HMR3
BNE 4\$


```

4577 013354 104037          ERROR 37          ;FAULT NOT SET IN MR3
4578 013356 032737 001000 003352 4$: BIT #D.PAR,HMR3
4579 013364 001001          BNE 5$
4580 013366 104040          ERROR 40          ;C-D PARITY ERROR NOT SET IN MR3
4581 013370 032737 020000 003322 5$: BIT #DCPAR,HCS1
4582 013376 001001          BNE 6$
4583 013400 104041          ERROR 41          ;DCPAR NOT SET AFT WRITING PAT IN MR1
4584 013402 032737 100000 003322 6$: BIT #CERR,HCS1
4585 013410 001001          BNE TST14        ;;GO TO NEXT TEST
4586 013412 104042          ERROR 42          ;CERR NOT SET BY WRITING PAT IN MR1
4587
4588
4589
4590
4591
4592
4593
4594
4595
4596
4597
4598 013414 000004          TST14: SCOPE
4599 013416 012737 000001 001174  MOV #1,$TIMES ;;DO 1 ITERATION
4600 013424 012706 001100          MOV #STACK,SP ;RESTORE STK PTR
4601
4602 013430 004737 045462          JSR PC,SUBCLR ;SUBSYS CLEAR & GET STATUS
4603 013434 104024          ERROR 24 ;CERR AFTER SCLR
4604
4605 013436 012737 000011 003322  MOV #SRTSPL,HCS1
4606 013444 004737 043472          JSR PC,DOCMD ;DO START SPINDLE CMD & GET CONTR RDY
4607 013450 104121          ERROR 121 ;RDY NOT SET AFTER START SPIN CMD
4608
4609 013452 004737 045132          JSR PC,GSTAT ;WORD 0
4610 013456 032737 010000 003350  BIT #D.SPIN,HMR2
4611 013464 001001          BNE 13$
4612 013466 104306          ERROR 306 ;SPIN NOT SET AFTER START SPIN CMD
4613
4614 013470 012737 014666 001176 13$: MOV #25$,$ESCAPE
4615 013476 004737 044050          JSR PC,TSTATN ;TEST FOR ATTN
4616 013502 000401          BR 15$
4617 013504 104316          ERROR 316
4618 013506 012737 010140 003412 15$: MOV #<D.SPIN!D.VV!D.DRA>,E.A0 ;LOAD IN EXPECTED VALUES
4619 013514 005037 003414          CLR E.B0
4620 013520 012737 000740 003416  MOV #<D.CART!D.DOOR!D.HDHM!D.BRHM>,E.A1
4621 013526 012737 000001 003420  MOV #1,E.B1
4622
4623 013534 004737 044310          JSR PC,CHKMSG ;CHECK MSGS A0,B0,A1,B1
4624 013540 000000          .WORD 0!0!0 ;& MSGS SPECIFIED HERE
4625 013542 104057          ERROR 57 ;MSG A0 ERROR AFTER START SPIN CMD REC'D BY DRIVE
4626 013544 104060          ERROR 60 ;MSG B0 ERROR
4627 013546 104061          ERROR 61 ;MSG A1 ERROR
4628 013550 104062          ERROR 62 ;MSG B1 ERROR
4629 013552 005737 003470          TST DOTIM
4630 013556 001126          BNE 3$ ;BRANCH IF P OR L CLOCK PRESENT
4631 013560 012737 014712 001176 1$: MOV #30$,$ESCAPE
4632 013566 012765 100000 000000  MOV #CCLR,RKCS1(R5)
  
```

```

4633 013574 013737 001416 003362 MOV T100,TEMP2 ;SETUP TIMEOUT
4634 013602 004737 044102 JSR PC,FATT1 ;FIND ATTN
4635 013606 104067 ERROR 67 ;NO ATTN AFTER HEAD LOADING
4636 013610
4637
4638 013610 012737 050340 003412 MOV #<D.DSC!D.SPIN!D.DRDY!D.VV!D.DRA>,E.A0 ;EXPECTED MSG A0
4639 013616 005037 003414 CLR E.B0 ;EXPECTED MSG B0
4640 013622 012737 001720 003416 MOV #<D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1 ;EXPECTED A1
4641 013630 012737 000001 003420 MOV #1,E.B1 ;MSG ID FOR EXPECTED MSG B1
4642 013636 005037 003422 CLR E.A2 ;EXPECTED MSG A2
4643 013642 012737 000002 003424 MOV #2,E.B2 ;MSG ID FOR EXPECTED MSG B2
4644 013650 012737 000003 003430 MOV #3,E.B3 ;MSG ID FOR EXPECTED MSG B3
4645
4646 013656 004737 044310 JSR PC,CHKMSG ;CHECK MSGS A0,B0,A1,B1
4647 013662 000003 .WORD T.A2!T.B2!0 ;& MSGS SPECIFIED HERE
4648 013664 104063 ERROR 63 ;MSG A0 ERROR AT END OF HEAD LOAD
4649 013666 104064 ERROR 64 ;MSG B0 ERROR
4650 013670 104065 ERROR 65 ;MSG A1 ERROR
4651 013672 104066 ERROR 66 ;MSG B1 ERROR
4652 013674 005737 001364 TST CYLADD ;SEE IF MSG B2 = 0
4653 013700 001401 BEQ 16$ ;BR IF YES
4654 013702 104176 ERROR 176 ;CYL ADDR NOT 0 AT END OF HEAD LOAD
4655 013704
4656
4657 013704 012765 100000 000000 MOV #CLR,RKCS1(R5)
4658 013712 013765 001222 000010 MOV $UNIT,RKCS2(R5) ;DRIVE#
4659 013720 012737 000005 003322 MOV #CLEAR,HCS1
4660 013726 004737 043472 JSR PC,DOCMD ;DO DRIVE CLEAR CMD & GET CONTR RDY
4661 013732 104151 ERROR 151 ;NO RDY AFTER DRIVE CLEAR CMD
4662 013734 004737 044050 JSR PC,TSTATN ;TEST FOR ATTN
4663 013740 000401 BR 64$
4664 013742 104154 ERROR 154 ;ATTN NOT CLEARED AFTER DRIVE CLEAR CMD
4665 013744
4666
4667 013744 012737 010340 003412 MOV #<0!D.SPIN!D.DRDY!D.VV!D.DRA>,E.A0 ;EXPECTED MSG A0
4668 013752 005037 003414 CLR E.B0 ;EXPECTED MSG B0
4669 013756 012737 001720 003416 MOV #<D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1 ;EXPECTED A1
4670 013764 012737 000001 003420 MOV #1,E.B1 ;MSG ID FOR EXPECTED MSG B1
4671 013772 005037 003422 CLR E.A2 ;EXPECTED MSG A2
4672 013776 012737 000002 003424 MOV #2,E.B2 ;MSG ID FOR EXPECTED MSG B2
4673 014004 012737 000003 003430 MOV #3,E.B3 ;MSG ID FOR EXPECTED MSG B3
4674
4675 014012 004737 044310 JSR PC,CHKMSG ;CHECK MSGS A0,B0,A1,B1
4676 014016 000003 .WORD T.A2!T.B2!0 ;& MSGS SPECIFIED HERE
4677 014020 104273 ERROR 273 ;MSG A0 ERROR AFTER DRIVE CLEAR CMD
4678 014022 104265 ERROR 265 ;MSG B0 ERROR
4679 014024 104274 ERROR 274 ;MSG A1 ERROR
4680 014026 104266 ERROR 266 ;MSG B1 ERROR
4681
4682 014030 000137 014506 JMP 12$
4683
4684 014034 012765 100000 000000 3$: MOV #CLR,RKCS1(R5)
4685 014042 013737 001370 001372 MOV HZ,COUNT
4686 014050 012737 000074 001374 MOV #60,SEC
4687 014056 004737 047244 JSR PC,CLKON ;TURN CLK INTR ON FOR 60 SEC MAX
4688 014062 012765 000001 000026 MOV #1,RKMR1(R5) ;SELECT WORD 1

```



```

4689 014070 004737 045132      4$: JSR   PC,GSTAT
4690 014074 032737 002000 003350 BIT   #D.FWD,HMR2
4691 014102 001004          BNE   5$
4692 014104 005737 001376      TST   TIMUP           ;IS 60 SEC DELAY UP?
4693 014110 001767          BEQ   4$           ;BRANCH IF NO & REPEAT
4694 014112 104070          ERROR 70           ;FWD NOT SET WITHIN 60 SEC FROM
4695                                ;START SPINDLE CMD.
4696 014114 004737 047340      5$: JSR   PC,CLKOF
4697 014120 012765 100000 000000 MOV   #CCLR,RKCS1(R5) ;TURN OFF CLOCK INTERRUPT
4698
4699 014126 013737 001370 001372 MOV   HZ,COUNT
4700 014134 012737 000005 001374 MOV   #5,SEC
4701 014142 004737 047244          JSR   PC,CLKON
4702 014146 012765 000001 000026 6$: MOV   #1,RKMR1(R5) ;TURN CLK INTR ON FOR 5 SEC MAX
4703 014154 004737 045132          JSR   PC,GSTAT
4704 014160 032737 002000 003350 BIT   #D.FWD,HMR2
4705 014166 001404          BEQ   7$
4706 014170 005737 001376      TST   TIMUP
4707 014174 001764          BEQ   6$
4708 014176 104075          ERROR 75           ;FWD NOT CLEARED WITHIN 5 SEC OF MOTION
4709                                ;FROM START SPINDLE CMD.
4710 014200 004737 047340      7$: JSR   PC,CLKOF
4711 014204 004737 044050          JSR   PC,TSTATN
4712 014210 000401          BR    17$
4713 014212 104320          ERROR 320
4714 014214 012737 030140 003412 17$: MOV   #<D.PIP!D.SPIN!D.VV!D.DRA>,E.A0 ;UNEXP ATTN AFTER INNER LIM DETECT
4715 014222 005037 003414          CLR   E.B0           ;EXPECTED A0
4716 014226 012737 025720 003416 MOV   #<D.RTZ!D.REV!D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1
4717 014234 012737 000001 003420 MOV   #1,E.B1
4718
4719 014242 004737 044310          JSR   PC,CHKMSG
4720 014246 000000          .WORD 0!0!0
4721 014250 104076          ERROR 76           ;CHECK MSGS A0,B0,A1,B1
4722 014252 104077          ERROR 77           ;& MSGS SPECIFIED HERE
4723 014254 104100          ERROR 100          ;MSG A0 ERROR AT INNER LIMIT DETECT
4724 014256 104101          ERROR 101          ;MSG B0 ERROR
4725                                ;MSG A1 ERROR
4726 014260 013737 001370 001372 MOV   HZ,COUNT
4727 014266 012737 000004 001374 MOV   #4,SEC
4728 014274 004737 047244          JSR   PC,CLKON
4729 014300 012765 000001 000026 8$: MOV   #1,RKMR1(R5) ;TURN CLK INTR ON FOR 4 SEC MAX
4730 014306 004737 045132          JSR   PC,GSTAT
4731 014312 032737 002000 003350 BIT   #D.FWD,HMR2
4732 014320 001004          BNE   9$
4733 014322 005737 001376      TST   TIMUP
4734 014326 001764          BEQ   8$
4735 014330 104102          ERROR 102          ;FWD NOT DETECTED WITHIN 4 SEC IN RTZ PORTION OF
4736                                ;START SPINDLE CMD.
4737 014332 004737 047340      9$: JSR   PC,CLKOF
4738 014336 004737 044050          JSR   PC,TSTATN
4739 014342 000401          BR    18$
4740 014344 104321          ERROR 321
4741 014346 012737 030140 003412 18$: MOV   #<D.PIP!D.SPIN!D.VV!D.DRA>,E.A0 ;UNEXP ATTN AFTER OUTER LIM TO CYL 0
4742 014354 005037 003414          CLR   E.B0           ;EXPECTED A0
4743 014360 012737 023720 003416 MOV   #<D.RTZ!D.FWD!D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1
4744 014366 012737 000001 003420 MOV   #1,E.B1
  
```

4745									
4746	014374	004737	044310			JSR	PC,CHKMSG		:CHECK MSGS A0,B0,A1,B1
4747	014400	000000				.WORD	0!0!0		:& MSGS SPECIFIED HERE
4748	014402	104103				ERROR	103		:MSG A0 ERROR FROM OUT LIM TO CYL 0 DURING LOAD
4749	014404	104104				ERROR	104		:MSG B0 ERROR
4750	014406	104105				ERROR	105		:MSG A1 ERROR
4751	014410	104106				ERROR	106		:MSG B1 ERROR
4752	014412	013737	001370	001372		MOV	HZ,COUNT		
4753	014420	012737	000001	001374		MOV	#1,SEC		
4754	014426	004737	047244			JSR	PC,CLKON		:TURN CLK INTR ON FOR 1 SEC MAX
4755	014432	005065	000026		10\$:	CLR	RKMR1(R5)		:WORD 0
4756	014436	004737	045132			JSR	PC,GSTAT		
4757	014442	032737	000200	003350		BIT	#D.DRDY,HMR2		:SEE IF DRIVE READY
4758	014450	001004				BNE	11\$		
4759	014452	005737	001376			TST	TIMUP		
4760	014456	001765				BEQ	10\$		
4761	014460	104107				ERROR	107		:DRIVE READY NOT SET WITHIN 1 SEC FROM
4762									:FWD IN RTZ PORTION OF START SPIN CMD
4763	014462	004737	047340		11\$:	JSR	PC,CLKOF		:TURN CLOCKS OFF
4764	014466	004737	044050			JSR	PC,TSTATN		:TEST ATTN
4765	014472	104067				ERROR	67		:NO ATTN AFTER START SPIN CMD
4766	014474	012737	014712	001176		MOV	#30\$,\$ESCAPE		
4767	014502	000137	013610			JMP	2\$:CHECK RKMR 2 & 3 WORDS 0 & 1
4768									
4769	014506	005037	001176		12\$:	CLR	\$ESCAPE		
4770	014512	012765	100000	000000		MOV	#CCLR,RKCS1(R5)		
4771	014520	005065	000026			CLR	RKMR1(R5)		
4772	014524	004737	045132			JSR	PC,GSTAT		
4773	014530	032737	010000	003350		BIT	#D.SPIN,HMR2		
4774	014536	001003				BNE	14\$		
4775	014540	104306				ERROR	306		:SPIN NOT SET AFTER ST. SPIN CMD
4776	014542	000137	042706			JMP	\$EOP		:ABORT DRIVE
4777	014546	005037	003304		14\$:	CLR	UNLD		:USED FOR VALID HALT
4778	014552	004737	047466			JSR	PC,SWTST		:SEE IF SW 14 OR 8 IS SET
4779	014556	000467				BR	TST15		:GO TO NEXT TEST
4780									:RETURN HERE IF SW 14 IS SET OR
4781									:SW 8 WITH SWR <7:0> APPLY
4782	014560	005237	003304		20\$:	INC	UNLD		
4783									
4784	014564	004737	045462			JSR	PC,SUBCLR		
4785	014570	104024				ERROR	24		:CERR AFTER SCLR
4786									
4787	014572	012737	000007	003322		MOV	#UNLOAD,HCS1		
4788	014600	004737	043472			JSR	PC,DOCMD		:DO UNLOAD CMD & GET CONTR READY
4789	014604	104011				ERROR	11		:RDY NOT SET AFTER UNLOAD CMD.
4790	014606	004737	044050			JSR	PC,TSTATN		
4791	014612	104012				ERROR	12		:NO ATTN AFTER UNLOAD CMD
4792									
4793	014614	004737	045462			JSR	PC,SUBCLR		
4794	014620	104024				ERROR	24		:CERR AFTER SCLR
4795									
4796	014622	013737	001414	003362		MOV	T10,TEMP2		
4797	014630	004737	046516			JSR	PC,FSPOK		
4798	014634	104315				ERROR	315		:SPEED NOT DOWN BY TIMEOUT
4799									
4800	014636	004737	045462			JSR	PC,SUBCLR		


```

4801 014642 104024          ERROR 24          ;CERR AFTER SCLR
4802
4803 014644 005037 001176    CLR      $ESCAPE
4804 014650 005737 001410    TST     LPFLG
4805 014654 001402          BEQ     65$
4806 014656 000177 164226    JMP     @SLPERR      ;SW 9 WAS SET.
4807 014662 000177 164220    JMP     @SLPADR     ;SW 14 OR 8 WAS SET
4808
4809
4810
  
```

```

4811 014666 004737 047340    25$:   JSR     PC,CLKOF
4812 014672 005237 001410    INC     LPFLG
4813 014676 032777 001000 164234  BIT     #SW9,@SWR   ;LOOP ON ERROR?
4814 014704 001325          BNE     20$         ;YES, RECONDITION DRIVE
4815 014706 000137 013560    JMP     1$         ;RETURN TO MAINLINE
4816 014712 004737 047340    30$:   JSR     PC,CLKOF
4817 014716 005237 001410    INC     LPFLG
4818 014722 032777 001000 164210  BIT     #SW9,@SWR   ;LOOP ON ERROR?
4819 014730 001313          BNE     20$         ;YES, RECONDITION DRIVE
4820 014732 000137 014506    JMP     12$        ;RETURN TO MAINLINE
  
```

.SBTTL SEEK/READ HEADER/WRITE HEADER TESTS

```

:*****
:*TEST 15          STATIC CYL DIFF AND CYL ADDR REG TEST; PART 1
:*
:* THIS TEST CHECKS EACH BIT OF THE CYL DIFFERENCE
:* AND CYL ADDRESS REGISTERS BY PERFORMING SEEKS TO ALL
:* MAJOR CYLS (0,1,2,4,8,16,32,64,128,256) (512 FOR RK07) WITH EVEN PARITY SET.
:* THIS FREEZES THE INFORMATION IN THE ABOVE REGISTERS & ALLOWS FOR CHECKING.
:* THIS TEST VERIFIES C-D PARITY ERROR BIT SET, THAT HEADS DID
:* NOT MOVE & ALL OTHER APPLICABLE STATUS BITS & REGS.
:*
:*****
  
```

```

4834 014736 000004          TST15: SCOPE
4835 014740 012737 000001 001174  MOV     #1,$TIMES   ;;DO 1 ITERATION
4836 014746 012706 001100          MOV     #STACK,SP   ;RESTORE STK PTR
4837
4838 014752 005000          CLR     R0          ;CYL # REGISTER
4839 014754 012737 100000 003370  MOV     #BIT15,TEMP5
4840
4841 014762          1$:   SCOP1
4842 014762 104415          MOV     #STACK,SP   ;RESTORE STK PTR
4843 014764 012706 001100
4844
4845 014770 004737 045462    JSR     PC,SUBCLR
4846 014774 104024          ERROR 24          ;CERR AFTER SCLR
4847
4848
4849 014776 012765 100000 000000  MOV     #CCLR,RKCS1(R5)
4850 015004 013765 001222 000010  MOV     $UNIT,RKCS2(R5)
4851 015012 012737 000013 003322  MOV     #RECAL,HCS1
4852 015020 004737 043472    JSR     PC,DOCMD    ;DO RECAL CMD & GET CONTR RDY
4853 015024 104124          ERROR 124         ;RDY NOT SET AFTER RECAL CMD
4854
4855 015026 012765 000001 000026  MOV     #1,RKMR1(R5) ;SELECT WORD 1
4856 015034 004737 045132    JSR     PC,GSTAT
  
```

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4857	015040	032737	020000	003350	BIT	#D.RTZ,HMR2			
4858	015046	001001			BNE	64\$			
4859	015050	104244			ERROR	244		:RTZ NOT SET DURING RECAL CMD	
4860	015052	013737	001414	003362	64\$:	MOV T10,TEMP2		:SETUP TIMEOUT	
4861	015060	004737	044102		JSR	PC,FATT1		:FIND ATTN	
4862	015064	104055			ERROR	55		:NO ATTN AFTER RECAL CMD	
4863									
4864	015066	012737	050340	003412	MOV	#<D.DSC!D.SPIN!D.DRDY!D.VV!D.DRA>,E.A0		:EXPECTED MSG A0	
4865	015074	005037	003414		CLR	E.B0		:EXPECTED MSG B0	
4866	015100	012737	001720	003416	MOV	#<D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1		:EXPECTED A1	
4867	015106	012737	000001	003420	MOV	#1,E.B1		:MSG ID FOR EXPECTED MSG B1	
4868	015114	005037	003422		CLR	E.A2		:EXPECTED MSG A2	
4869	015120	012737	000002	003424	MOV	#2,E.B2		:MSG ID FOR EXPECTED MSG B2	
4870	015126	012737	000003	003430	MOV	#3,E.B3		:MSG ID FOR EXPECTED MSG B3	
4871									
4872	015134	004737	044310		JSR	PC,CHKMSG		:CHECK MSGS A0,B0,A1,B1	
4873	015140	000007			.WORD	T.A2!T.B2!T.B3		:& MSGS SPECIFIED HERE	
4874	015142	104221			ERROR	221		:MSG A0 ERROR AFTER RECAL CMD	
4875	015144	104275			ERROR	275		:MSG B0 ERROR	
4876	015146	104222			ERROR	222		:MSG A1 ERROR	
4877	015150	104276			ERROR	276		:MSG B1 ERROR	
4878					CWD2	47,50,<AFTER RECAL CMD>			
4879	015152	012765	000002	000026	MOV	#2,RKMR1(R5)		:SELECT THE MESSAGE	
4880	015160	004737	045132		JSR	PC,GSTAT		:GET THE STATUS	
4881	015164	005737	001364		TST	CYLADD		:RECAL SUCCESSFUL ?	
4882	015170	001401			BEQ	65\$:BRANCH IF SO	
4883	015172	104050			ERROR	50		:REPORT THE ERROR	
4884	015174								
4885									
4886	015174	012765	100000	000000	MOV	#CCLR,RKCS1(R5)			
4887	015202	013765	001222	000010	MOV	\$UNIT,RKCS2(R5)		:DRIVE#	
4888	015210	012737	000005	003322	MOV	#CLEAR,HCS1			
4889	015216	004737	043472		JSR	PC,DOCMD		:DO DRIVE CLEAR CMD & GET CONTR RDY	
4890	015222	104151			ERROR	151		:NO RDY AFTER DRIVE CLEAR CMD	
4891	015224	004737	044050		JSR	PC,TSTATN		:TEST FOR ATTN	
4892	015230	000401			BR	66\$			
4893	015232	104154			ERROR	154		:ATTN NOT CLEARED AFTER DRIVE CLEAR CMD	
4894	015234								
4895									
4896	015234	012737	010340	003412	MOV	#<0!D.SPIN!D.DRDY!D.VV!D.DRA>,E.A0		:EXPECTED MSG A0	
4897	015242	005037	003414		CLR	E.B0		:EXPECTED MSG B0	
4898	015246	012737	001720	003416	MOV	#<D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1		:EXPECTED A1	
4899	015254	012737	000001	003420	MOV	#1,E.B1		:MSG ID FOR EXPECTED MSG B1	
4900	015262	005037	003422		CLR	E.A2		:EXPECTED MSG A2	
4901	015266	012737	000002	003424	MOV	#2,E.B2		:MSG ID FOR EXPECTED MSG B2	
4902	015274	012737	000003	003430	MOV	#3,E.B3		:MSG ID FOR EXPECTED MSG B3	
4903									
4904	015302	004737	044310		JSR	PC,CHKMSG		:CHECK MSGS A0,B0,A1,B1	
4905	015306	000003			.WORD	T.A2!T.B2!0		:& MSGS SPECIFIED HERE	
4906	015310	104273			ERROR	273		:MSG A0 ERROR AFTER DRIVE CLEAR CMD	
4907	015312	104265			ERROR	265		:MSG B0 ERROR	
4908	015314	104274			ERROR	274		:MSG A1 ERROR	
4909	015316	104266			ERROR	266		:MSG B1 ERROR	
4910									
4911									
4912	015320	104415			SCOP1				


```

4913 015322 012706 001100 MOV #STACK,SP ;RESTORE STK PTR
4914
4915 015326 004737 045462 JSR PC,SUBCLR
4916 015332 104024 ERROR 24 ;CERR AFTER SCLR
4917
4918 015334 005237 001462 INC BYPCERR ;DO NOT TEST CERR IN GSTAT1
4919 015340 012765 000020 000026 MOV #PAT,RKMR1(R5) ;EVEN PARITY
4920 015346 010065 000020 MOV R0,RKDC(R5) ;CYL ADDR
4921 015352 012737 000017 003322 MOV #SEEK,HCS1
4922 015360 004737 043472 JSR PC,DOCMD ;DO SEEK CMD & GET CONTR RDY
4923 015364 104122 ERROR 122 ;NO RDY FROM SEEK WITH BAD PARITY
4924 015366 004737 044050 JSR PC,TSTATN ;TEST FOR ATTN
4925 015372 104125 ERROR 125 ;NO ATTN FROM SEEK & BAD PARITY
4926 015374 012737 050340 003412 MOV #<D.DSC!D.SPIN!D.DRDY!D.VV!D.DRA>,E.A0 ;EXPECTED A0
4927 015402 012737 001200 003414 MOV #<D.FLT!D.PAR>,E.B0
4928 015410 012737 001720 003416 MOV #<D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1
4929 015416 012737 000001 003420 MOV #1,E.B1
4930 015424 010037 003422 MOV R0,E.A2
4931 015430 006137 003422 ROL E.A2
4932 015434 006137 003422 ROL E.A2
4933 015440 006137 003422 ROL E.A2
4934 015444 006137 003422 ROL E.A2
4935 015450 013737 003422 003424 MOV E.A2,E.B2
4936 015456 052737 000002 003424 BIS #2,E.B2 ;ADD MSG ID
4937
4938 015464 004737 044310 JSR PC,CHKMSG ;CHECK MSGS A0,B0,A1,B1
4939 015470 000003 .WORD T.A2!T.B2!0 ;& MSGS SPECIFIED HERE
4940 015472 104110 ERROR 110 ;MSG A0 ERROR AFTER SEEK WITH BAD PARITY
4941 015474 104111 ERROR 111 ;MSG B0 ERROR
4942 015476 104146 ERROR 146 ;MSG A1 ERROR
4943 015500 104147 ERROR 147 ;MSG B1 ERROR
4944
4945 015502 020037 001364 CMP R0,CYLADD
4946 015506 001401 BEQ 2$
4947 015510 104043 ERROR 43 ;CYL ADDR IN B2 NOT=RKDC
4948
4949 015512 020037 001362 2$: CMP R0,CYLDIF
4950 015516 001401 BEQ 3$
4951 015520 104044 ERROR 44 ;CYL DIFF IN A2 NOT=RKDC
4952
4953 015522 005037 001462 3$: CLR BYPCERR ;ALLOW CHECKING FOR ANY CERR IN GSTAT1
4954 015526 006137 003370 ROL TEMP5 ;SET CARRY ONLY ONCE
4955 015532 006100 ROL R0 ;SELECT NEXT MAJOR CYL
4956 015534 020037 012106 CMP R0,MC1 ;ALL MAJOR CYL DONE?
4957 015540 001001 BNE 4$ ;BRANCH IF NO
4958 015542 000402 BR TST16 ;GO TO NEXT TST
4959 015544 000137 014762 4$: JMP 1$

```

```

4960
4961 *****
4962 *TEST 16 STATIC CYL DIFF & CYL ADDR REG TEST-PART 2
4963 *
4964 * THIS TEST CHECKS THE ABILITY OF THE DRIVE TO PROPERLY SET THE CYL
4965 * DIFF. & CYL ADDR REGS FOR ALL COMBINATIONS BY SEEKING TO
4966 * ALL CYLS FROM EVERY OTHER CYL. (N SQUARE SEEKS).
4967 * IT IS PERFORMED IN THE SAME MANNER AS THE ABOVE TEST.
4968 *

```

```
*****
4969
4970 015550 000004
4971 015552 012737 000001 001174 TST16: SCOPE
4972 015560 012706 001100 MOV #1,$TIMES ;DO 1 ITERATION
4973 MOV #STACK,SP ;RESTORE STK PTR
4974 015564 005737 001340 TST BYPT16
4975 015570 001404 BEQ 13$
4976 015572 104401 056105 TYPE ,MSG9 ;BYPASSING TEST 16
4977 015576 000137 017012 JMP 12$
4978
4979 015602 005737 001170 13$: TST $TMP4 ;SEE IF RK07
4980 015606 001403 BEQ 15$ ;BR OF NO
4981 015610 104401 057001 TYPE ,MSG23 ;10-12 MIN TEST
4982 015614 000402 BR 16$
4983 015616 104401 056047 15$: TYPE ,MSG8 ;2-4 MIN TEST
4984
4985 015622 005037 001350 16$: CLR FRCYL ;FROM CYL
4986 015626 005037 001352 CLR TOCYL ;TO CYL
4987 015632 005037 001354 CLR CCYL ;CURRENT CYL
4988 015636 005037 001356 CLR PCYL ;PREV CYL
4989
4990 015642 104415 SCOP1
4991 015644 012706 001100 MOV #STACK,SP ;RESTORE STK PTR
4992
4993 015650 004737 045462 JSR PC,SUBCLR
4994 015654 104024 ERROR 24 ;CERR AFTER SCLR
4995
4996
4997 015656 012765 100000 000000 MOV #CCLR,RKCS1(R5)
4998 015664 013765 001222 000010 MOV $UNIT,RKCS2(R5)
4999 015672 012737 000013 003322 MOV #RECAL,HCS1
5000 015700 004737 043472 JSR PC,DOCMD ;DO RECAL CMD & GET CONTR RDY
5001 015704 104124 ERROR 124 ;RDY NOT SET AFTER RECAL CMD
5002
5003 015706 012765 000001 000026 MOV #1,RKMR1(R5) ;SELECT WORD 1
5004 015714 004737 045132 JSR PC,GSTAT
5005 015720 032737 020000 003350 BIT #D.RTZ,HMR2
5006 015726 001001 BNE 64$
5007 015730 104244 ERROR 244 ;RTZ NOT SET DURING RECAL CMD
5008 015732 013737 001414 003362 64$: MOV T10,TEMP2 ;SETUP TIMEOUT
5009 015740 004737 044102 JSR PC,FATT1 ;FIND ATTN
5010 015744 104055 ERROR 55 ;NO ATTN AFTER RECAL CMD
5011
5012 015746 012737 050340 003412 MOV #<D.DSC!D.SPIN!D.DRDY!D.VV!D.DRA>,E.A0 ;EXPECTED MSG A0
5013 015754 005037 003414 CLR E.B0 ;EXPECTED MSG B0
5014 015760 012737 001720 003416 MOV #<D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1 ;EXPECTED A1
5015 015766 012737 000001 003420 MOV #1,E.B1 ;MSG ID FOR EXPECTED MSG B1
5016 015774 005037 003422 CLR E.A2 ;EXPECTED MSG A2
5017 016000 012737 000002 003424 MOV #2,E.B2 ;MSG ID FOR EXPECTED MSG B2
5018 016006 012737 000003 003430 MOV #3,E.B3 ;MSG ID FOR EXPECTED MSG B3
5019
5020 016014 004737 044310 JSR PC,CHKMSG ;CHECK MSGS A0,B0,A1,B1
5021 016020 000007 .WORD T.A2!T.B2!T.B3 ;& MSGS SPECIFIED HERE
5022 016022 104221 ERROR 221 ;MSG A0 ERROR AFTER RECAL CMD
5023 016024 104275 ERROR 275 ;MSG B0 ERROR
5024 016026 104222 ERROR 222 ;MSG A1 ERROR
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CZR6HF0 UNIBUS RK6 DR PT1		MACY11	30(1046)	04-JAN-82	13:01	K 8	PAGE 102	SEQ 0101
CZR6HF.P11 04-JAN-82 12:44		T16		STATIC CYL DIFF & CYL ADDR REG TEST-PART 2				
5025	016030	104276			ERROR	276	:MSG B1 ERROR	
5026					CWD2	47,50,<AFTER RECAL CMD>		
5027	016032	012765	000002	000026	MOV	#2,RKMR1(R5)	:SELECT THE MESSAGE	
5028	016040	004737	045132		JSR	PC,GSTAT	:GET THE STATUS	
5029	016044	005737	001364		TST	CYLADD	:RECAL SUCCESSFUL ?	
5030	016050	001401			BEQ	65\$:BRANCH IF SO	
5031	016052	104050			ERROR	50	:REPORT THE ERROR	
5032	016054					65\$:		
5033								
5034	016054	012765	100000	000000	MOV	#CCLR,RKCS1(R5)		
5035	016062	013765	001222	000010	MOV	\$UNIT,RKCS2(R5)	:DRIVE#	
5036	016070	012737	000005	003322	MOV	#CLEAR,HCS1		
5037	016076	004737	043472		JSR	PC,DOCMD	:DO DRIVE CLEAR CMD & GET CONTR RDY	
5038	016102	104151			ERROR	151	:NO RDY AFTER DRIVE CLEAR CMD	
5039	016104	004737	044050		JSR	PC,TSTATN	:TEST FOR ATTN	
5040	016110	000401			BR	66\$		
5041	016112	104154			ERROR	154	:ATTN NOT CLEARED AFTER DRIVE CLEAR CMD	
5042	016114					66\$:		
5043								
5044	016114	012737	010340	003412	MOV	#<0!D.SPIN!D.DRDY!D.VV!D.DRA>,E.A0	:EXPECTED MSG A0	
5045	016122	005037	003414		CLR	E.B0	:EXPECTED MSG B0	
5046	016126	012737	001720	003416	MOV	#<D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1	:EXPECTED A1	
5047	016134	012737	000001	003420	MOV	#1,E.B1	:MSG ID FOR EXPECTED MSG B1	
5048	016142	005037	003422		CLR	E.A2	:EXPECTED MSG A2	
5049	016146	012737	000002	003424	MOV	#2,E.B2	:MSG ID FOR EXPECTED MSG B2	
5050	016154	012737	000003	003430	MOV	#3,E.B3	:MSG ID FOR EXPECTED MSG B3	
5051								
5052	016162	004737	044310		JSR	PC,CHKMSG	:CHECK MSGS A0,B0,A1,B1	
5053	016166	000003			.WORD	T.A2!T.B2!0	:& MSGS SPECIFIED HERE	
5054	016170	104273			ERROR	273	:MSG A0 ERROR AFTER DRIVE CLEAR CMD	
5055	016172	104265			ERROR	265	:MSG B0 ERROR	
5056	016174	104274			ERROR	274	:MSG A1 ERROR	
5057	016176	104266			ERROR	266	:MSG B1 ERROR	
5058								
5059								
5060	016200	104415			SCOP1			
5061	016202	012706	001100		MOV	#STACK,SP	:RESTORE STK PTR	
5062								
5063	016206	004737	045462		JSR	PC,SUBCLR		
5064	016212	104024			ERROR	24	:CERR AFTER SCLR	
5065								
5066								
5067	016214	012765	000020	000026	1\$:	MOV	#PAT,RKMR1(R5)	:EVEN PARITY
5068	016222	013765	001352	000020	MOV	TOCYL,RKDC(R5)	:SET TO CYL ADDR	
5069	016230	013737	001352	001354	MOV	TOCYL,CCYL	:CURRENT CYL	
5070	016236	013737	001354	003364	MOV	CCYL,TEMP3		
5071	016244	013737	001356	003366	MOV	PCYL,TEMP4	:PREV CYL	
5072	016252	163737	003364	003366	SUB	TEMP3,TEMP4		
5073	016260	100002			BPL	2\$:BR IF TEMP4 IS POS	
5074	016262	005437	003366		NEG	TEMP4		
5075	016266	013737	003366	001360	2\$:	MOV	TEMP4,CALDIF	
5076	016274	013737	001354	001356	MOV	CCYL,PCYL		
5077	016302	012737	000017	003322	MOV	#SEEK,HCS1		
5078	016310	004737	043472		JSR	PC,DOCMD	:DO SEEK CMD & GET CONTR RDY	
5079	016314	104122			ERROR	122	:NO RDY AFTER SEEK WITH BAD PARITY	
5080	016316	004737	044050		JSR	PC,TSTATN	:TEST FOR ATTN	

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CZR6HFO UNIBUS RK6 DR PT1 MACY11 30(1046) 04-JAN-82 13:01 PAGE 103
CZR6HF.P11 04-JAN-82 12:44 T16 STATIC CYL DIFF & CYL ADDR REG TEST-PART 2 SEG 0102

5081	016322	104125				ERROR	125		:NO ATTN FROM SEEK & BAD PARITY
5082	016324	012765	100000	000000		MOV	#CCLR,RKCS1(R5)		:CLEAR ERROR
5083	016332	013765	001352	000020		MOV	TOCYL,RKDC(R5)		:RESTOR RKDC AFT CCLR
5084	016340	004737	046052			JSR	PC,RDCYLA		:READ CYL ADDR
5085	016344	023737	001352	001364		CMP	TOCYL,CYLADD		:SEE IF TO CYL ECHOED OK
5086	016352	001401				BEQ	3\$		
5087	016354	104045				ERROR	45		:MR3 NOT=RKDC
5088									
5089	016356	004737	045766		3\$:	JSR	PC,RDCYLD		:READ CYL DIFF
5090	016362	023737	001360	001362		CMP	CALDIF,CYLDIF		:SEE IF CYL DIFF CORRECT
5091	016370	001401				BEQ	4\$		
5092	016372	104046				ERROR	46		:CYL DIFF IN RKMR2 INCORRECT
5093									
5094	016374				4\$:				
5095									
5096	016374	012765	100000	000000		MOV	#CCLR,RKCS1(R5)		
5097	016402	013765	001222	000010		MOV	\$UNIT,RKCS2(R5)		:DRIVE#
5098	016410	012737	000005	003322		MOV	#CLEAR,HCS1		
5099	016416	004737	043472			JSR	PC,DOCMD		:DO DRIVE CLEAR CMD & GET CONTR RDY
5100	016422	104151				ERROR	151		:NO RDY AFTER DRIVE CLEAR CMD
5101	016424	004737	044050			JSR	PC,TSTATN		:TEST FOR ATTN
5102	016430	000401				BR	67\$		
5103	016432	104154				ERROR	154		:ATTN NOT CLEARED AFTER DRIVE CLEAR CMD
5104	016434				67\$:				
5105									
5106									
5107	016434	104415				SCOP1			
5108	016436	012706	001100			MOV	#STACK,SP		:RESTORE STK PTR
5109									
5110	016442	004737	045462			JSR	PC,SUBCLR		
5111	016446	104024				ERROR	24		:CERR AFTER SCLR
5112									
5113	016450	012765	000020	000026		MOV	#PAT,RKMR1(R5)		:EVEN PARITY
5114	016456	013765	001350	000020		MOV	FRCYL,RKDC(R5)		:SET RETURN CYL ADDR
5115	016464	013737	001350	001354		MOV	FRCYL,CCYL		
5116	016472	013737	001354	003364		MOV	CCYL,TEMP3		
5117	016500	013737	001356	003366		MOV	PCYL,TEMP4		
5118	016506	163737	003364	003366		SUB	TEMP3,TEMP4		
5119	016514	100002				BPL	5\$:BR IF TEMP4 IS POS
5120	016516	005437	003366			NEG	TEMP4		
5121	016522	013737	003366	001360	5\$:	MOV	TEMP4,CALDIF		
5122	016530	013737	001354	001356		MOV	CCYL,PCYL		
5123	016536	012737	000017	003322		MOV	#SEEK,HCS1		
5124	016544	004737	043472			JSR	PC,DOCMD		:DO SEEK CMD & GET CONTR RDY
5125	016550	104122				ERROR	122		:NO RDY AFTER SEEK WITH BAD PARITY
5126	016552	004737	044050			JSR	PC,TSTATN		:TEST FOR ATTN
5127	016556	104125				ERROR	125		:NO ATTN FROM SEEK & BAD PARITY
5128	016560	012765	100000	000000		MOV	#CCLR,RKCS1(R5)		:CLEAR ERROR
5129	016566	013765	001350	000020		MOV	FRCYL,RKDC(R5)		:RESTOR RKDC AFT CCLR
5130	016574	004737	046052			JSR	PC,RDCYLA		:READ CYL ADDR
5131	016600	023737	001350	001364		CMP	FRCYL,CYLADD		:SEE IF RETURN CYL ECHOED OK
5132	016606	001401				BEQ	6\$		
5133	016610	104241				ERROR	241		:MR3 NOT=RKDC
5134									
5135	016612	023737	001352	001350	6\$:	CMP	TOCYL,FRCYL		:SEE IF TO=FROM
5136	016620	001022				BNE	10\$:DO NORMAL TEST IF NO


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5137 016622 005737 001352 TST TOCYL ;SEE IF=0
5138 016626 001007 BNE 9$
5139 016630 004737 045766 JSR PC,RDCYLD ;CYL DIFF S/B 0 ON CYL 0
5140 016634 005737 001362 TST CYLDIF
5141 016640 001421 BEQ 7$
5142 016642 104242 ERROR 242 ;CYL DIFF IN RKMR2 INCORRECT
5143 016644 000417 BR 7$
5144
5145 016646 004737 045766 9$: JSR PC,RDCYLD ;CYL DIFF/OFFSET SHOULD NOT
5146 016652 023727 001362 000001 CMP CYLDIF,#1 ;CHANGE IN SEEK TO SELF
5147 016660 001411 BEQ 7$ ;SHOULD = 1 IN THIS TEST
5148 016662 104263 ERROR 263 ;CYL DIFF IN RKMR2 DID NOT REMAIN = 0
5149 016664 000407 BR 7$
5150 016666 004737 045766 10$: JSR PC,RDCYLD ;READ CYL DIFF
5151 016672 023737 001362 001362 CMP CALDIF,CYLDIF ;SEE IF CYL DIFF OK
5152 016700 001401 BEQ 7$
5153 016702 104242 ERROR 242 ;CYL DIFF IN RKMR2 INCORRECT
5154
5155 016704 7$:
5156
5157 016704 012765 100000 000000 MOV #CCLR,RKCS1(R5)
5158 016712 013765 001222 000010 MOV $UNIT,RKCS2(R5) ;DRIVE#
5159 016720 012737 000005 003322 MOV #CLEAR,HCS1
5160 016726 004737 043472 JSR PC,DOCMD ;DO DRIVE CLEAR CMD & GET CONTR RDY
5161 016732 104151 ERROR 151 ;NO RDY AFTER DRIVE CLEAR CMD
5162 016734 004737 044050 JSR PC,TSTATN ;TEST FOR ATTN
5163 016740 000401 BR 68$
5164 016742 104154 ERROR 154 ;ATTN NOT CLEARED AFTER DRIVE CLEAR CMD
5165 016744 68$:
5166
5167
5168 016744 005237 001352 INC TOCYL
5169 016750 023737 001352 012102 CMP TOCYL,LCP1 ;SEE IF SCANNED ALL CYLS
5170 016756 001402 BEQ 8$ ;BR IF YES
5171 016760 000137 016214 JMP 1$ ;ELSE REPEAT
5172
5173 016764 005237 001350 8$: INC FRCYL
5174 016770 023737 001350 012102 CMP FRCYL,LCP1 ;SEE IF ALL DONE
5175 016776 001405 BEQ TST17 ;GO TO NEXT TST
5176 017000 013737 001350 001352 MOV FRCYL,TOCYL ;FRCYL ALWAYS = OR > TOCYL
5177 017006 000137 016214 JMP 1$ ;ELSE REPEAT
5178 017012 12$:
5179
5180
5181
5182
5183
5184
5185
5186
5187
5188
5189
5190
5191
5192

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*****
*TEST 17 HEAD REGISTER TEST
*
* THIS TEST CHECKS THE ABILITY TO SELECT ALL HEADS (0,1,2)
* VIA RKDA & READING BACK FROM MSG B3 BY THE SELECT DRIVE CMD.
* HEAD 3 IS CHECKED TO PRODUCE INV. ADDR.
*
* SINCE CHANGING HEAD ADDRESSES ARE TIED TO SEEK CMDS,
* SELECTING HEAD 3 MUST RESULT IN A SEEK INCOMPLETE ALONG WITH
* ILLEGAL ADDRESS. IF NOT, THIS MEANS THAT CHANGING HEAD ADDRESSES
* ARE NOT TIED TO SEEK CMDS
*

```

```
5193 .....  
5194 017012 000004  
5195 017014 012737 000001 001174 TST17: SCOPE  
5196 017022 012706 001100 MOV #1,STIMES ;DO 1 ITERATION  
5197 MOV #STACK,SP ;RESTORE STK PTR  
5198 017026 005000 CLR R0 ;HEAD #  
5199 017030 1$: SCOP1  
5200 017030 104415 MOV #STACK,SP ;RESTORE STK PTR  
5201 017032 012706 001100 JSR PC,SUBCLR  
5202 JSR ERROR 24 ;CERR AFTER SCLR  
5203 017036 004737 045462  
5204 017042 104024  
5205  
5206  
5207  
5208 017044 012765 100000 000000 MOV #CCLR,RKCS1(R5)  
5209 017052 013765 001222 000010 MOV $UNIT,RKCS2(R5)  
5210 017060 012737 000013 003322 MOV #RECAL,HCS1  
5211 017066 004737 043472 JSR PC,DOCMD ;DO RECAL CMD & GET CONTR RDY  
5212 017072 104124 ERROR 124 ;RDY NOT SET AFTER RECAL CMD  
5213  
5214 017074 012765 000001 000026 MOV #1,RKMR1(R5) ;SELECT WORD 1  
5215 017102 004737 045132 JSR PC,GSTAT  
5216 017106 032737 020000 003350 BIT #D.RTZ,HMR2  
5217 017114 001001 BNE 64$  
5218 017116 104244 ERROR 244 ;RTZ NOT SET DURING RECAL CMD  
5219 017120 013737 001414 003362 64$: MOV T10,TEMP2 ;SETUP TIMEOUT  
5220 017126 004737 044102 JSR PC,FATT1 ;FIND ATTN  
5221 017132 104055 ERROR 55 ;NO ATTN AFTER RECAL CMD  
5222  
5223 017134 012737 050340 003412 MOV #<D.DSC!D.SPIN!D.DRDY!D.VV!D.DRA>,E.A0 ;EXPECTED MSG A0  
5224 017142 005037 003414 CLR E.B0 ;EXPECTED MSG B0  
5225 017146 012737 001720 003416 MOV #<D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1 ;EXPECTED A1  
5226 017154 012737 000001 003420 MOV #1,E.B1 ;MSG ID FOR EXPECTED MSG B1  
5227 017162 005037 003422 CLR E.A2 ;EXPECTED MSG A2  
5228 017166 012737 000002 003424 MOV #2,E.B2 ;MSG ID FOR EXPECTED MSG B2  
5229 017174 012737 000003 003430 MOV #3,E.B3 ;MSG ID FOR EXPECTED MSG B3  
5230  
5231 017202 004737 044310 JSR PC,CHKMSG ;CHECK MSGS A0,B0,A1,B1  
5232 017206 000007 .WORD T.A2!T.B2!T.B3 ;& MSGS SPECIFIED HERE  
5233 017210 104221 ERROR 221 ;MSG A0 ERROR AFTER RECAL CMD  
5234 017212 104275 ERROR 275 ;MSG B0 ERROR  
5235 017214 104222 ERROR 222 ;MSG A1 ERROR  
5236 017216 104276 ERROR 276 ;MSG B1 ERROR  
5237 CWD2 47,50,<AFTER RECAL CMD>  
5238 017220 012765 000002 000026 : MOV #2,RKMR1(R5) ;SELECT THE MESSAGE  
5239 017226 004737 045132 JSR PC,GSTAT ;GET THE STATUS  
5240 017232 005737 001364 TST CYLADD ;RECAL SUCCESSFUL ?  
5241 017236 001401 BEQ 65$ ;BRANCH IF SO  
5242 017240 104050 ERROR 50 ;REPORT THE ERROR  
5243 017242 65$:  
5244  
5245 017242 012765 100000 000000 MOV #CCLR,RKCS1(R5)  
5246 017250 013765 001222 000010 MOV $UNIT,RKCS2(R5) ;DRIVE#  
5247 017256 012737 000005 003322 MOV #CLEAR,HCS1  
5248 017264 004737 043472 JSR PC,DOCMD ;DO DRIVE CLEAR CMD & GET CONTR RDY
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5249	017270	104151			ERROR	151		;NO RDY AFTER DRIVE CLEAR CMD
5250	017272	004737	044050		JSR	PC,TSTATN		;TEST FOR ATTN
5251	017276	000401			BR	66\$		
5252	017300	104154			ERROR	154		;ATTN NOT CLEARED AFTER DRIVE CLEAR CMD
5253	017302			66\$:				
5254								
5255	017302	012737	010340	003412	MOV	#<0!D.SPIN!D.DRDY!D.VV!D.DRA>,E.A0		;EXPECTED MSG A0
5256	017310	005037	003414		CLR	E.B0		;EXPECTED MSG B0
5257	017314	012737	001720	003416	MOV	#<D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1		;EXPECTED A1
5258	017322	012737	000001	003420	MOV	#1,E.B1		;MSG ID FOR EXPECTED MSG B1
5259	017330	005037	003422		CLR	E.A2		;EXPECTED MSG A2
5260	017334	012737	000002	003424	MOV	#2,E.B2		;MSG ID FOR EXPECTED MSG B2
5261	017342	012737	000003	003430	MOV	#3,E.B3		;MSG ID FOR EXPECTED MSG B3
5262								
5263	017350	004737	044310		JSR	PC,CHKMSG		;CHECK MSGS A0,B0,A1,B1
5264	017354	000003			.WORD	T.A2!T.B2!0		;# MSGS SPECIFIED HERE
5265	017356	104273			ERROR	273		;MSG A0 ERROR AFTER DRIVE CLEAR CMD
5266	017360	104265			ERROR	265		;MSG B0 ERROR
5267	017362	104274			ERROR	274		;MSG A1 ERROR
5268	017364	104266			ERROR	266		;MSG B1 ERROR
5269								
5270								
5271	017366	023727	001432	000001	CMP	HEADA,#1		;FOR HEAD 0, B3=1
5272	017374	001401			BEQ	3\$		
5273	017376	104053			ERROR	53		;RECAL DID NOT RESET HEAD REG IN B3.
5274								
5275	017400			3\$:				
5276	017400	104415			SCOP1			
5277	017402	012706	001100		MOV	#STACK,SP		;RESTORE STK PTR
5278								
5279	017406	004737	045462		JSR	PC,SUBCLR		
5280	017412	104024			ERROR	24		;CERR AFTER SCLR
5281								
5282	017414	000300			SWAB	R0		
5283	017416	010065	000006		MOV	R0,RKDA(R5)		;HEAD #
5284	017422	000300			SWAB	R0		
5285								
5286	017424	012737	000017	003322	MOV	#SEEK,HCS1		
5287	017432	004737	043472		JSR	PC,DOCMD		;DO SEEK CMD & GET CONTR RDY
5288	017436	104156			ERROR	156		;NO RDY AFTER SEEK TO SELF
5289	017440	004737	044050		JSR	PC,TSTATN		
5290	017444	104157			ERROR	157		;NO ATTN AFTER SEEK TO SELF
5291	017446	012737	050340	003412	MOV	#<D.DSC!D.SPIN!D.DRDY!D.VV!D.DRA>,E.A0		;EXPECTED A0
5292	017454	020027	000003		CMP	R0,#3		
5293	017460	001403			BEQ	4\$;BR FOR HEAD 3
5294	017462	005037	003414		CLR	E.B0		;FOR HEADS 0,1,2
5295	017466	000403			BR	5\$		
5296	017470	012737	002240	003414	MOV	#<D.SKI!D.FLT!D.IDAE>,E.B0		;FOR HEAD 3
5297	017476	012737	001720	003416	MOV	#<D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1		
5298	017504	012737	000001	003420	MOV	#1,E.B1		
5299	017512	005037	003422		CLR	E.A2		
5300	017516	012737	000002	003424	MOV	#2,E.B2		
5301	017524	005700			TST	R0		;SEE IF HEAD 0
5302	017526	001004			BNE	6\$;BR IF NO
5303	017530	012737	001003	003430	MOV	#<BIT9!3>,E.B3		;LOAD EXPECTED B3 FOR HEAD 0 & MSG ID
5304	017536	000412			BR	8\$		

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5305 017540 020027 000001      6$:  CMP      R0,#1      ;SEE IF HEAD 1
5306 017544 001004              BNE      7$          ;BR IF NO
5307 017546 012737 002003 003430  MOV      #<BIT10!3>,E.B3 ;B3 FOR HEAD 1
5308 017554 000403              BR       8$          ;
5309 017556 012737 004003 003430  7$:  MOV      #<BIT11!3>,E.B3 ;B3 FOR HEAD 2
5310 017564              8$:
5311
5312 017564 004737 044310      JSR      PC,CHKMSG   ;CHECK MSGS A0,B0,A1,B1
5313 017570 000007              .WORD   T.A2!T.B2!T.B3 ;& MSGS SPECIFIED HERE
5314 017572 104114              ERROR   114         ;MSG A0 ERROR AFTER LOAD HEAD REG & SEEK CMD
5315 017574 104115              ERROR   115         ;MSG B0 ERROR
5316 017576 104322              ERROR   322         ;MSG A1 ERROR
5317 017600 104323              ERROR   323         ;MSG B1 ERROR
5318
5319 017602 012765 000002 000026  MOV      #2,RKMR1(R5) ;SELECT WORD 2
5320 017610 004737 045132      JSR      PC,GSTAT
5321 017614 005737 001362      TST     CYLDIF
5322 017620 001401              BEQ     67$         ;SEE IF MSG A2=0
5323 017622 104324              ERROR   324         ;BR IF YES
5324 017624 005737 001364      67$:  TST     CYLADD
5325 017630 001401              BEQ     68$         ;MSG A2 NOT CLEARED AFTER LOAD HEAD REG & SEEK CMD
5326 017632 104325              ERROR   325         ;SEE IF MSG B2=0
5327 017634              68$:  ERROR   325         ;MSG B2 NOT CLEARED AFTER LOAD HEAD REG & SEEK CMD
5328
5329 017634 020027 000003      CMP     R0,#3
5330 017640 001412              BEQ     9$          ;BR IF HEAD 3
5331
5332 017642 005037 003360      CLR     TEMP1
5333 017646 116037 003312 003360  MOV     ATTN(R0),TEMP1
5334 017654 023737 003360 001432  CMP     TEMP1,HEADA ;FOR RKDA=HEAD 0, HEAD=1 IN B3
5335                                     ;FOR RKDA=HEAD 1, HEAD=2 IN B3
5336                                     ;FOR RKDA=HEAD 2, HEAD=4 IN B3
5337 017662 001401              BEQ     9$
5338 017664 104054              ERROR   54
5339                                     ;HEAD DECODE IN B3 INCORRECT
5340
5341 017666 005200      9$:  INC     R0
5342 017670 020027 000004      CMP     R0,#4
5343 017674 001402              BEQ     10$         ;0 THRU 3 DONE?
5344 017676 000137 017030      JMP     1$          ;BR IF YES
5345                                     ;ELSE REPEAT
5346 017702      10$:
5347
5348 017702 012765 100000 000000  MOV     #CCLR,RKCS1(R5)
5349 017710 013765 001222 000010  MOV     $UNIT,RKCS2(R5)
5350 017716 012737 000013 003322  MOV     #RECAL,HCS1
5351 017724 004737 043472      JSR     PC,DOCMD
5352 017730 104124              ERROR   124
5353                                     ;DO RECAL CMD & GET CONTR RDY
5354                                     ;RDY NOT FOUND AFTER RECAL CMD
5355 017732 012765 100000 000000  MOV     #CCLR,RKCS1(R5)
5356 017740 013765 001222 000010  MOV     $UNIT,RKCS2(R5) ;DRIVE#
5357 017746 012737 000005 003322  MOV     #CLEAR,HCS1
5358 017754 004737 043472      JSR     PC,DOCMD
5359 017760 104151              ERROR   151
5360 017762 004737 044050      JSR     PC,TSTATN
5361 017766 000401              BR      70$

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5361 017770 104154          ERROR 154          ;ATTN NOT CLEARED AFTER DRIVE CLEAR CMD
5362 017772          70$:
5363
5364
5365 017772 004737 045132          JSR    PC,GSTAT
5366 017776 032737 000040 003352          BIT    #D.IDAE,HMR3          ;SEE IF IDAE IS CLEARED
5367 020004 001401          BEQ    69$          ;BR IF YES
5368 020006 104155          ERROR 155          ;IDAE NOT CLEARED AFTER RECAL CMD
5369
5370 020010 012765 100000 000000 69$:          MOV    #CCLR,RKCS1(R5)
5371 020016 013737 001412 003362          MOV    T1,TEMP2          ;LOOK FOR ATTN FROM RECAL
5372 020024 004737 044102          JSR    PC,FATT1
5373 020030 104055          ERROR 55          ;NO ATTN AFTER RECAL CMD
5374
5375
5376          ;*****
5377          ;*TEST 20          SEEK TO CYL 0
5378          ;*
5379          ;*          TESTS THE ABILITY TO DO A SEEK CMD.
5380          ;*          VERIFIES THERE WAS NO MOVEMENT BY CHECKING ALL APPROPRIATE
5381          ;*          STATUS BITS. VERIFIES CMD COMPLETION BETWEEN 10-15USEC.
5382          ;*          READ HEADER IS NOT PERFORMED AS THE PACK MAY NOT BE FORMATTED.
5383          ;*
5384          ;*****
5385 020032 000004          TST20:          SCOPE
5386 020034 012737 000001 001174          MOV    #1,$TIMES          ;;DO 1 ITERATION
5387 020042 012706 001100          MOV    #STACK,SP          ;RESTORE STK PTR
5388
5389 020046 004737 045462          JSR    PC,SUBCLR          ;SUBSYS CLEAR & GET STATUS
5390 020052 104024          ERROR 24          ;CERR AFTER SCLR
5391 020054 004737 046052          JSR    PC,RDCYLA          ;READ CYL ADDR IN RKMR3
5392 020060 005737 001364          TST    CYLADD
5393 020064 001401          BEQ    1$
5394 020066 104130          ERROR 130          ;CYL ADDR NOT CLEARED AFTER SCLR
5395 020070          1$:
5396 020070 104415          SCOP1
5397 020072 012706 001100          MOV    #STACK,SP          ;RESTORE STK PTR
5398
5399 020076 004737 045462          JSR    PC,SUBCLR          ;CERR AFTER SCLR
5400 020102 104024          ERROR 24
5401
5402 020104 012737 000017 003322          MOV    #SEEK,HCS1
5403 020112 004737 043472          JSR    PC,DOCMD          ;DO SEEK CMD & GET CONTR RDY
5404 020116 104131          ERROR 131          ;RDY NOT SET AFTER SEEK CMD
5405 020120 012737 000005 003360          MOV    #5,TEMP1          ;SETUP 100US TIMEOUT
5406
5407 020126 004737 044176          JSR    PC,FATT2          ;FIND ATTN
5408 020132 104132          ERROR 132          ;NO ATTN AFTER SEEK CMD
5409 020134 032737 100000 003322          BIT    #CERR,HCS1
5410 020142 001401          BEQ    64$
5411 020144 104210          ERROR 210          ;CERR AFTER SEEK CMD
5412 020146          64$:
5413
5414 020146 012737 050340 003412          MOV    #<D.DSC!D.SPIN!D.DRDY!D.VV!D.DRA>,E.A0 ;EXPECTED MSG A0
5415 020154 005037 003414          CLR    E.B0          ;EXPECTED MSG B0
5416 020160 012737 001720 003416          MOV    #<D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1 ;EXPECTED A1
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5417 020166 012737 000001 003420 MOV #1,E.B1 ;MSG ID FOR EXPECTED MSG B1
5418 020174 005037 003422 CLR E.A2 ;EXPECTED MSG A2
5419 020200 012737 000002 003424 MOV #2,E.B2 ;MSG ID FOR EXPECTED MSG B2
5420 020206 012737 000003 003430 MOV #3,E.B3 ;MSG ID FOR EXPECTED MSG B3
5421
5422 020214 004737 044310 JSR PC,CHKMSG ;CHECK MSGS A0,B0,A1,B1
5423 020220 000003 .WORD T.A2!T.B2!0 ;& MSGS SPECIFIED HERE
5424 020222 104133 ERROR 133 ;MSG A0 ERROR AFTER SEEK CMD
5425 020224 104134 ERROR 134 ;MSG B0 ERROR
5426 020226 104135 ERROR 135 ;MSG A1 ERROR
5427 020230 104136 ERROR 136 ;MSG B1 ERROR
5428 020232 005737 001362 TST CYLDIF
5429 020236 001401 BEQ 65$
5430 020240 104137 ERROR 137 ;CYL DIFF NOT CLEARED AFTER SEEK CMD
5431
5432 020242 65$:
5433
5434 020242 012765 100000 000000 MOV #CCLR,RKCS1(R5)
5435 020250 013765 001222 000010 MOV $UNIT,RKCS2(R5) ;DRIVE#
5436 020256 012737 000005 003322 MOV #CLEAR,HCS1
5437 020264 004737 043472 JSR PC,DOCMD ;DO DRIVE CLEAR CMD & GET CONTR RDY
5438 020270 104151 ERROR 151 ;NO RDY AFTER DRIVE CLEAR CMD
5439 020272 004737 044050 JSR PC,TSTATN ;TEST FOR ATTN
5440 020276 000401 BR 66$
5441 020300 104154 ERROR 154 ;ATTN NOT CLEARED AFTER DRIVE CLEAR CMD
5442 020302 66$:
5443
5444 020302 012737 010340 003412 MOV #<0!D.SPIN!D.DRDY!D.VV!D.DRA>,E.A0 ;EXPECTED MSG A0
5445 020310 005037 003414 CLR E.B0 ;EXPECTED MSG B0
5446 020314 012737 001720 003416 MOV #<D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1 ;EXPECTED A1
5447 020322 012737 000001 003420 MOV #1,E.B1 ;MSG ID FOR EXPECTED MSG B1
5448 020330 005037 003422 CLR E.A2 ;EXPECTED MSG A2
5449 020334 012737 000002 003424 MOV #2,E.B2 ;MSG ID FOR EXPECTED MSG B2
5450 020342 012737 000003 003430 MOV #3,E.B3 ;MSG ID FOR EXPECTED MSG B3
5451
5452 020350 004737 044310 JSR PC,CHKMSG ;CHECK MSGS A0,B0,A1,B1
5453 020354 000003 .WORD T.A2!T.B2!0 ;& MSGS SPECIFIED HERE
5454 020356 104273 ERROR 273 ;MSG A0 ERROR AFTER DRIVE CLEAR CMD
5455 020360 104265 ERROR 265 ;MSG B0 ERROR
5456 020362 104274 ERROR 274 ;MSG A1 ERROR
5457 020364 104266 ERROR 266 ;MSG B1 ERROR
5458
5459 020366 005737 001364 TST CYLADD
5460 020372 001401 BEQ TST21 ;:GO TO NEXT TEST
5461 020374 104140 ERROR 140 ;CYL ADDR IN B2 NOT CLEARED AFT SEEK CMD.
5462
5463 ;*****
5464 ;*TEST 21 TEST SECTOR COUNT REG. IN MSG B3
5465 ;*****
5466 020376 000004 TST21: SCOPE
5467 020400 012737 000001 001174 MOV #1,$TIMES ;:DO 1 ITERATION
5468 020406 012706 001100 MOV #STACK,SP ;:RESTORE STK PTR
5469
5470 020412 004737 045462 JSR PC,SUBCLR ;SUBSYS CLEAR & GET STATUS
5471 020416 104024 ERROR 24 ;CERR AFTER SCLR
5472 020420 012737 020516 001176 MOV #2,$ESCAPE ;GO TO NEXT TEST IF ANY ERROR DETECTED

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5529 020610 004737 044050 JSR PC,TSTATN ;TEST FOR ATTN
5530 020614 104125 ERROR 125 ;NO ATTN FROM SEEK WITH BAD PARITY
5531 020616 012737 050340 003412 MOV #<D.DSC!D.SPIN!D.DRDY!D.VV!D.DRA>,E.A0 ;EXPECTED A0
5532 020624 012737 001200 003414 MOV #<D.FLT!D.PAR>,E.B0
5533 020632 012737 001720 003416 MOV #<D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1
5534 020640 012737 000001 003420 MOV #1,E.B1
5535
5536 020646 004737 044310 JSR PC,CHKMSG ;CHECK MSGS A0,B0,A1,B1
5537 020652 000000 .WORD 0!0!0 ;& MSGS SPECIFIED HERE
5538 020654 104110 ERROR 110 ;MSG A0 ERROR AFTER SEEK WITH BAD PARITY
5539 020656 104111 ERROR 111 ;MSG B0 ERROR
5540 020660 104146 ERROR 146 ;MSG A1 ERROR
5541 020662 104147 ERROR 147 ;MSG B1 ERROR
5542
5543 020664 012765 100000 000000 MOV #CLR,RKCS1(R5)
5544 020672 013765 001222 000010 MOV $UNIT,RKCS2(R5) ;DRIVE#
5545 020700 012737 000005 003322 MOV #CLEAR,HCS1
5546 020706 004737 043472 JSR PC,DOCMD ;DO DRIVE CLEAR CMD & GET CONTR RDY
5547 020712 104151 ERROR 151 ;NO RDY AFTER DRIVE CLEAR CMD
5548 020714 004737 044050 JSR PC,TSTATN ;TEST FOR ATTN
5549 020720 000401 BR 65$
5550 020722 104154 ERROR 154 ;ATTN NOT CLEARED AFTER DRIVE CLEAR CMD
5551 020724 65$:
5552
5553 020724 012737 010340 003412 MOV #<0!D.SPIN!D.DRDY!D.VV!D.DRA>,E.A0 ;EXPECTED MSG A0
5554 020732 005037 003414 CLR E.B0 ;EXPECTED MSG B0
5555 020736 012737 001720 003416 MOV #<D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1 ;EXPECTED A1
5556 020744 012737 000001 003420 MOV #1,E.B1 ;MSG ID FOR EXPECTED MSG B1
5557 020752 005037 003422 CLR E.A2 ;EXPECTED MSG A2
5558 020756 012737 000002 003424 MOV #2,E.B2 ;MSG ID FOR EXPECTED MSG B2
5559 020764 012737 000003 003430 MOV #3,E.B3 ;MSG ID FOR EXPECTED MSG B3
5560
5561 020772 004737 044310 JSR PC,CHKMSG ;CHECK MSGS A0,B0,A1,B1
5562 020776 000003 .WORD T.A2!T.B2!0 ;& MSGS SPECIFIED HERE
5563 021000 104273 ERROR 273 ;MSG A0 ERROR AFTER DRIVE CLEAR CMD
5564 021002 104265 ERROR 265 ;MSG B0 ERROR
5565 021004 104274 ERROR 274 ;MSG A1 ERROR
5566 021006 104266 ERROR 266 ;MSG B1 ERROR
5567
5568
5569 021010 013765 012116 000020 MOV FC,RKDC(R5)
5570 021016 012737 000017 003322 MOV #SEEK,HCS1
5571 021024 004737 043472 JSR PC,DOCMD ;DO SEEK CMD & GET CONTR READY
5572 021030 104131 ERROR 131 ;NO RDY AFTER SEEK CMD
5573 021032 012765 100000 000000 MOV #CLR,RKCS1(R5)
5574 021040 004737 045132 JSR PC,GSTAT
5575 021044 004737 046316 JSR PC,FLIM ;FIND LIMIT DETECT
5576 021050 104160 ERROR 160 ;LIMIT DETECT NOT FOUND BEFORE TIMEOUT
5577
5578 021052 032737 040000 003350 BIT #D.UNLD,HMR2
5579 021060 001003 BNE 1$
5580 021062 104305 ERROR 305 ;DRIVE NOT UNLOADING AFTER LIMIT DETECT
5581 021064 000137 021602 JMP 30$ ;BYPASS REST OF TEST
5582
5583 021070 012737 021520 001176 1$: MOV #20$, $ESCAPE ;MUST ESCAPE TO CYCLE UP DRIVE & TEST SWR
5584 021076 012737 070140 003412 MOV #<D.DSC!D.PIP!D.SPIN!D.VV!D.DRA>,E.A0 ;EXPECTED A0
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5585 021104 012737 002200 003414 MOV #<D.SKI!D.FLT>,E.B0
5586 021112 012737 045720 003416 MOV #<D.UNLD!D.REV!D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1
5587 021120 012737 030001 003420 MOV #<D.LIMD!D.NMOV!1>,E.B1
5588 ; CHECK 161,162,163,164,<AFTER OUTER LIMIT DETECT>,0,0,0
5589 021126 004737 044310 JSR PC,CHKMSG ;CHECK MESSAGE A0,B0,A1,B1
5590 021132 000000 .WORD 0!0!0
5591 021134 104161 ERROR 161 ;MAY BE A0 ERROR
5592 021136 104162 ERROR 162 ;MAY BE B0 ERROR
5593 021140 104163 ERROR 163 ;MAY BE B1 ERROR
5594 021142 000240 NOP ;NEED FOR THE CALLIN SEQ
5595 021144 032737 020000 003400 BIT #D.LIMD,H.B1 ;SEE IF LIMIT DETECT BIT SET IN B1
5596 021152 001003 BNE 3$ ;BRANCH IF SO
5597 021154 104401 061436 TYPE EM42 ;OTHERWISE REPORT ERROR
5598 021160 104164 ERROR 164
5599
5600 021162 004737 044050 3$: JSR PC,TSTATN
5601 021166 104165 ERROR 165 ;NO ATTN AFTER OUTER LIMIT DETECT
5602 021170 005037 001462 CLR BYPCERR ;ALLOW CHECKING CERR IN GSTAT1
5603
5604 021174 004737 045462 JSR PC,SUBCLR ;SUBSYS CLR
5605 021200 104024 ERROR 24 ;CERR AFTER SCLR
5606 021202 013737 001414 003362 MOV T10,TEMP2 ;SET UP TIMEOUT
5607 021210 004737 046374 JSR PC,FHDHM ;FIND HEAD HOME
5608 021214 104166 ERROR 166 ;HEAD HOME NOT FOUND BEFORE TIMEOUT
5609 021216 004737 046450 JSR PC,FLOAD ;FIND LOAD HEADS
5610 021222 104167 ERROR 167 ;LOAD HEADS NOT FOUND BEFORE TIMEOUT
5611 021224 013737 001416 003362 MOV T100,TEMP2 ;SETUP TIMEOUT
5612 021232 004737 044102 JSR PC,FATT1 ;FIND ATTN
5613 021236 104067 ERROR 67 ;ATTN NOT FOUND BEFORE TIMEOUT
5614 021240 005037 001176 2$: CLR $ESCAPE
5615 021244 005037 003304 CLR UNLD ;CLEAR FLAG
5616
5617 021250 012737 050340 003412 MOV #<D.DSC!D.SPIN!D.DRDY!D.VV!D.DRA>,E.A0 ;EXPECTED MSG A0
5618 021256 005037 003414 CLR E.B0 ;EXPECTED MSG B0
5619 021262 012737 001720 003416 MOV #<D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1 ;EXPECTED A1
5620 021270 012737 000001 003420 MOV #1,E.B1 ;MSG ID FOR EXPECTED MSG B1
5621 021276 005037 003422 CLR E.A2 ;EXPECTED MSG A2
5622 021302 012737 000002 003424 MOV #2,E.B2 ;MSG ID FOR EXPECTED MSG B2
5623 021310 012737 000003 003430 MOV #3,E.B3 ;MSG ID FOR EXPECTED MSG B3
5624
5625 021316 004737 044310 JSR PC,CHKMSG ;CHECK MSGS A0,B0,A1,B1
5626 021322 000003 .WORD T.A2!T.B2!0 ;& MSGS SPECIFIED HERE
5627 021324 104063 ERROR 63 ;MSG A0 ERROR AT END OF HEAD LOADING
5628 021326 104064 ERROR 64 ;MSG B0 ERROR
5629 021330 104065 ERROR 65 ;MSG A1 ERROR
5630 021332 104066 ERROR 66 ;MSG B1 ERROR
5631 ; CWD2 175,176,<AT END OF HEAD LOADING>
5632 021334 012765 000002 000026 MOV #2,RKMR1(R5) ;SELECT MESSAGE
5633 021342 004737 045132 JSR PC,GSTAT ;GET STATUS AND MR2,MR3
5634 021346 005737 001364 TST CYLADD ;RECAL SUCCESSFUL ?
5635 021352 001401 BEQ 64$ ;BRANCH IF SO
5636 021354 104050 ERROR 50 ;REPORT ERROR
5637 021356 64$:
5638
5639 021356 012765 100000 000000 MOV #CLR,RKCS1(R5)
5640 021364 013765 001222 000010 MOV $UNIT,RKCS2(R5) ;DRIVE#

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5641 021372 012737 000005 003322 MOV #CLEAR,HCS1
5642 021400 004737 043472 JSR PC,DOCMD ;DO DRIVE CLEAR CMD & GET CONTR RDY
5643 021404 104151 ERROR 151 ;NO RDY AFTER DRIVE CLEAR CMD
5644 021406 004737 044050 JSR PC,TSTATN ;TEST FOR ATTN
5645 021412 000401 BR 66$
5646 021414 104154 ERROR 154 ;ATTN NOT CLEARED AFTER DRIVE CLEAR CMD
5647 021416
5648 66$:
5649 021416 012737 010340 003412 MOV #<0!D.SPIN!D.DRDY!D.VV!D.DRA>,E.A0 ;EXPECTED MSG A0
5650 021424 005037 003414 CLR E.B0 ;EXPECTED MSG B0
5651 021430 012737 001720 003416 MOV #<D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1 ;EXPECTED A1
5652 021436 012737 000001 003420 MOV #1,E.B1 ;MSG ID FOR EXPECTED MSG B1
5653 021444 005037 003422 CLR E.A2 ;EXPECTED MSG A2
5654 021450 012737 000002 003424 MOV #2,E.B2 ;MSG ID FOR EXPECTED MSG B2
5655 021456 012737 000003 003430 MOV #3,E.B3 ;MSG ID FOR EXPECTED MSG B3
5656
5657 021464 004737 044310 JSR PC,CHKMSG ;CHECK MSGS A0,B0,A1,B1
5658 021470 000003 .WORD T.A2!T.B2!0 ;& MSGS SPECIFIED HERE
5659 021472 104273 ERROR 273 ;MSG A0 ERROR AFTER DRIVE CLEAR CMD
5660 021474 104265 ERROR 265 ;MSG B0 ERROR
5661 021476 104274 ERROR 274 ;MSG A1 ERROR
5662 021500 104266 ERROR 266 ;MSG B1 ERROR
5663
5664 021502 004737 047466 JSR PC,SWTST ;SEE IF SW 14 OR 8 IS SET
5665 021506 000435 BR TST23 ;GO TO NEXT TEST
5666 ;RETURN HERE IF SW 14 IS SET OR
5667 ;SW 8 WITH SWR <7:0> APPLY
5668
5669
5670 021510 005037 001176 10$: CLR $ESCAPE
5671 021514 000177 157366 JMP @SLPADR
5672 021520 20$:
5673
5674 021520 004737 045462 JSR PC,SUBCLR
5675 021524 104024 ERROR 24 ;CERR AFTER SCLR
5676
5677 021526 012737 000011 003322 MOV #SRTSPL,HCS1
5678 021534 004737 043472 JSR PC,DOCMD ;DO START SPINDLE CMD & GET CONTR RDY
5679 021540 104121 ERROR 121 ;RDY NOT FOUND AFTER ST SPIN CMD.
5680
5681 021542 013737 001420 003362 MOV T500,TEMP2 ;SETUP TIMEOUT
5682 021550 004737 044102 JSR PC,FATT1 ;FIND ATTN
5683 021554 104067 ERROR 67 ;NO ATTN AFTER ST SPIN CMD.
5684
5685 021556 005037 003304 CLR UNLD
5686 021562 005237 001410 INC LPFLG
5687 021566 032777 001000 157344 BIT #SW9,@SWR ;LOOP ON ERROR?
5688 021574 001345 BNE 10$ ;YES, RECONDITION DRIVE
5689 021576 000137 021240 JMP 2$ ;RETURN TO MAINLINE
5690 021602 30$:
5691
5692
5693
5694
5695
5696
  
```

```
5697 : *TEST 23 BASIC WRITE/READ HEADER & HEAD SWITCHING TEST
5698 : *
5699 : * THIS TEST CHECKS HEAD SWITCHING BY WRITING UNIQUE HEADERS
5700 : * ON EACH TRACK OF CYL 0, READING BACK & VERIFYING THEY REMAINED
5701 : * UNIQUE. 22 SECTOR FORMAT IS USED
5702 : *
5703 : * I.E. TRACK 0: ALL 0'S FOR ALL SECTOR HEADERS
5704 : * TRACK 1: 0101 FOR ALL SECTOR HEADERS
5705 : * TRACK 2: ALL 1'S FOR ALL SECTOR HEADERS
5706 : *
5707 : *****
5708 021602 00C004 TST23: SCOPE
5709 021604 012737 000001 001174 MOV #1,$TIMES ;;DO 1 ITERATION
5710 021612 012706 001100 MOV #STACK,SP ;RESTORE STK PTR
5711 021616 005737 001460 TST LIMERR ;CHK FOR LIMIT ERROR
5712 021622 001403 BEQ 5$ ;BR IF NO
5713 021624 104170 ERROR 170 ;FATAL ERROR
5714 021626 000137 042706 JMP $EOP ;ABORT BAL OF TESTS
5715 021632 5$:
5716
5717 021632 005237 003306 INC BADHDR ;USED FOR VALID HALT
5718 021636 005037 001430 CLR HEAD ;HEAD CTR
5719
5720 021642 104415 SCOP1
5721 021644 012706 001100 MOV #STACK,SP ;RESTORE STK PTR
5722
5723 021650 004737 045462 JSR PC,SUBCLR
5724 021654 104024 ERROR 24 ;CERR AFTER SCLR
5725
5726 021656 052765 000020 000010 1$: BIS #BAI,RKCS2(R5) ;SET BUSS ADDR INCR INHIBIT
5727 021664 012765 001470 000004 MOV #HDTAB,RKBA(R5) ;HEADER WORD TABLE
5728 021672 012765 177676 000002 MOV #-66.,RKWC(R5) ;WORD COUNT.
5729 021700 000337 001430 SWAB HEAD
5730 021704 013765 001430 000006 MOV HEAD,RKDA(R5) ;SETUP HEAD ADDR
5731 021712 000337 001430 SWAB HEAD
5732
5733 021716 013700 001430 MOV HEAD,R0
5734 021722 006300 ASL R0 ;DOUBLE R0
5735 021724 016037 001444 001470 MOV DATA0(R0),HDTAB ;SETUP HEADER WORD FOR RKBA
5736
5737 021732 012737 000027 003322 MOV #<WRHEAD>,HCS1
5738 021740 004737 043530 JSR PC,DATCMD ;DO DATA XFER CMD & GET CONTR RDY
5739 021744 104200 ERROR 200 ;NO RDY AFTER WRITE HEADER CMD
5740 021746 004737 045132 JSR PC,GSTAT ;GET FRESH STATUS
5741 021752 032737 100000 003322 BIT #CERR,HCS1
5742 021760 001405 BEQ 64$
5743 021762 104201 ERROR 201 ;CERR AFTER WRITE HEADER CMD
5744 021764 104401 056436 TYPE ,MSG18 ;ABORTING BALANCE OF TESTS
5745 021770 000137 042706 JMP $EOP ;ABORT DRIVE
5746 021774 64$:
5747
5748 021774 012737 010340 003412 MOV #<0!D.SPIN!D.DRDY!D.VV!D.DRA>,E.A0 ;EXPECTED MSG A0
5749 022002 005037 003414 CLR E.B0 ;EXPECTED MSG B0
5750 022006 012737 001720 003416 MOV #<D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1 ;EXPECTED A1
5751 022014 012737 000001 003420 MOV #1,E.B1 ;MSG ID FOR EXPECTED MSG B1
5752 022022 005037 003422 CLR E.A2 ;EXPECTED MSG A2
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K 9

CZR6HFO UNIBUS RK6 DR PT1 MACY11 30(1046) 04-JAN-82 13:01 PAGE 115
CZR6HF .P11 04-JAN-82 12:44 T23 BASIC WRITE/READ HEADER & HEAD SWITCHING TEST SEQ 0114

5753	022026	012737	000002	003424	MOV	#2,E.B2	;MSG ID FOR EXPECTED MSG B2
5754	022034	012737	000003	003430	MOV	#3,E.B3	;MSG ID FOR EXPECTED MSG B3
5755							
5756	022042	004737	044310		JSR	PC,CHKMSG	;CHECK MSGS A0,B0,A1,B1
5757	022046	000003			.WORD	T.A2!T.B2!0	; & MSGS SPECIFIED HERE
5758	022050	104277			ERROR	277	;MSG A0 ERROR AFTER WRITE HEADER CMD
5759	022052	104267			ERROR	267	;MSG B0 ERROR
5760	022054	104300			ERROR	300	;MSG A1 ERROR
5761	022056	104270			ERROR	270	;MSG B1 ERROR
5762							
5763							
5764	022060	012765	000002	000026	MOV	#2,RKMR1(R5)	;SELECT WORD 2
5765	022066	004737	045132		JSR	PC,GSTAT	
5766	022072	005737	001362		TST	CYLDIF	;SEE IF MSG A2=0
5767	022076	001401			BEQ	65\$;BR IF YES
5768	022100	104303			ERROR	303	;MSG A2 NOT CLEARED AFTER WRITE HEADER CMD
5769	022102	005737	001364	65\$:	TST	CYLADD	;SEE IF MSG B2=0
5770	022106	001401			BEQ	66\$;BR IF YES
5771	022110	104304			ERROR	304	;MSG B2 NOT CLEARED AFTER WRITE HEADER CMD
5772	022112			66\$:			
5773							
5774	022112	005237	001430		INC	HEAD	
5775	022116	023727	001430	000003	CMP	HEAD,#3	
5776	022124	001254			BNE	1\$	
5777							
5778	022126	005037	001430		CLR	HEAD	;HEAD CTR
5779	022132	104415			SCOP1		
5780	022134	012706	001100		MOV	#STACK,SP	;RESTORE STK PTR
5781							
5782	022140	004737	045462		JSR	PC,SUBCLR	
5783	022144	104024			ERROR	24	;CERR AFTER SCLR
5784							
5785							
5786							
5787	022146	000337	001430	2\$:	SWAB	HEAD	
5788	022152	013765	001430	000006	MOV	HEAD,RKDA(R5)	;SETUP HEAD ADDR
5789	022160	000337	001430		SWAB	HEAD	
5790							
5791	022164	012700	001674		MOV	#RHTAB,RO	
5792	022170	012737	000025	003322	MOV	#<RDHEAD>,HCS1	
5793	022176	004737	043530		JSR	PC,DATCMD	;DO DATA XFER CMD & GET CONTR RDY
5794	022202	104171			ERROR	171	;NO RDY AFTER READ HEADER CMD
5795	022204	032737	100000	003322	BIT	#CERR,HCS1	
5796	022212	001405			BEQ	67\$	
5797	022214	104174			ERROR	174	;CERR AFTER READ HEADER CMD
5798	022216	104401	056436		TYPE	,MSG18	;ABORT BALANCE OF TESTS
5799	022222	000137	042706		JMP	\$EOP	;ABORT DRIVE
5800							
5801	022226	016520	000024	67\$:	MOV	RKDB(R5),(R0)+	;1'ST WORD FROM SILO TO RHTAB
5802	022232	016520	000024		MOV	RKDB(R5),(R0)+	;2'ND WORD
5803	022236	016520	000024		MOV	RKDB(R5),(R0)+	;3'RD WORD
5804							
5805							
5806	022242	032765	100000	000010	BIT	#DLT,RKCS2(R5)	
5807	022250	001407			BEQ	68\$	
5808	022252	004737	045132		JSR	PC,GSTAT	


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5809 022256 104173          ERROR 173          ;DLT AFTER READ HEADER CMD
5810 022260 104401 056436    TYPE   ,MSG18      ;ABORTING BALANCE OF TESTS
5811 022264 000137 042706    JMP    $EOP        ;ABORT DRIVE
5812 022270          68$:
5813
5814 022270 012737 010340 003412    MOV    #<0!D.SPIN!D.DRDY!D.VV!D.DRA>,E.A0 ;EXPECTED MSG A0
5815 022276 005037 003414          CLR    E.B0        ;EXPECTED MSG B0
5816 022302 012737 001720 003416    MOV    #<D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1 ;EXPECTED A1
5817 022310 012737 000001 003420    MOV    #1,E.B1     ;MSG ID FOR EXPECTED MSG B1
5818 022316 005037 003422          CLR    E.A2        ;EXPECTED MSG A2
5819 022322 012737 000002 003424    MOV    #2,E.B2     ;MSG ID FOR EXPECTED MSG B2
5820 022330 012737 000003 003430    MOV    #3,E.B3     ;MSG ID FOR EXPECTED MSG B3
5821
5822 022336 004737 044310          JSR    PC,CHKMSG   ;CHECK MSGS A0,B0,A1,B1
5823 022342 000003          .WORD T.A2!T.B2!0 ;& MSGS SPECIFIED HERE
5824 022344 104301          ERROR 301         ;MSG A0 ERROR AFTER READ HEADER CMD
5825 022346 104271          ERROR 271        ;MSG B0 ERROR
5826 022350 104302          ERROR 302        ;MSG A1 ERROR
5827 022352 104272          ERROR 272        ;MSG B1 ERROR
5828
5829
5830 022354 012765 000002 000026    MOV    #2,RKMR1(R5) ;SELECT WORD 2
5831 022362 004737 045132          JSR    PC,GSTAT
5832 022366 005737 001362          TST   CYLDIF      ;SEE IF MSG A2=0
5833 022372 001401          BEQ   69$         ;BR IF YES
5834 022374 104172          ERROR 172        ;MSG A2 NOT CLEARED AFTER READ HEADER CMD
5835 022376 005737 001364          TST   CYLADD      ;SEE IF MSG B2=0
5836 022402 001401          BEQ   70$         ;BR IF YES
5837 022404 104264          ERROR 264        ;MSG B2 NOT CLEARED AFTER READ HEADER CMD
5838 022406          70$:
5839 022406 000337 001430          SWAB  HEAD
5840 022412 013765 001430 000006    MOV    HEAD,RKDA(R5) ;RESTORE RKDA
5841 022420 000337 001430          SWAB  HEAD
5842
5843 022424 012701 001674          MOV    #RHTAB,R1
5844
5845 022430 005037 001442          CLR    WDCNT      ;HEADER WORD COUNT
5846 022434 013700 001430          MOV    HEAD,RO
5847 022440 006300          ASL   RO          ;DOUBLE RO
5848 022442 016037 001444 003360    MOV    DATA0(RO),TEMP1 ;GET THE 'SHOULD BE' DATA
5849 022450 012137 001454 003360    MOV    (R1)+,HDWD ;READ HEADER WORD
5850 022454 023737 001454 003360    CMP   HDWD,TEMP1
5851 022462 001401          BEQ   4$
5852 022464 104202          ERROR 202        ;READ HEADER MISMATCH
5853 022466 005237 001442          INC   WDCNT
5854 022472 023727 001442 000003    CMP   WDCNT,#3   ;DO ONLY 1 SECTOR
5855 022500 001363          BNE  3$
5856
5857 022502 005237 001430          INC   HEAD
5858 022506 023727 001430 000003    CMP   HEAD,#3
5859 022514 001402          BEQ   TST24
5860 022516 000137 022146          JMP   2$         ;ELSE REPEAT
5861
5862 *****
5863 ;*TEST 24 BASIC WRITE/READ HEADER TEST; ALL 1'S, 20 SECTORS
5864 ;*
```

```
5865 :* USING HEAD 0, WRITE & READ 20 SECTOR HEADERS BY WRITING ALL
5866 :* 1'S AS HEADERS. ATTEMPT TO FIND SECTORS 20 & 21. VERIFY
5867 :* THEY ARE NO LONGER THERE BY READING 22 SECTORS AND NOT
5868 :* FINDING 0'S AS DATA FROM THE PREVIOUS TEST.
5869 :*
5870 :*****
5871 022522 000004 TST24: SCOPE
5872 022524 012737 000001 001174 MOV #1,$TIMES ;;DO 1 ITERATION
5873 022532 012706 001100 MOV #STACK,SP ;RESTORE STK PTR
5874
5875 022536 004737 045462 JSR PC,SUBCLR
5876 022542 104024 ERROR 24 ;CERR AFTER SCLR
5877 022544 052765 000020 000010 BIS #BA1,RKCS2(R5) ;SET BUSS ADDR INCR INHIBIT
5878 022552 012765 001450 000004 MOV #DATA1,RKBA(R5) ;XFER 1'S ONLY
5879 022560 012765 177704 000002 MOV #-50.,RKWC(R5) ;WORD COUNT
5880
5881
5882 022566 012737 010027 003322 MOV #<CFMT!WRHEAD>,HCS1
5883 022574 004737 043530 JSR PC,DATCMD ;DO DATA XFER CMD & GET CONTR RDY
5884 022600 104200 ERROR 200 ;NO RDY AFTER WRITE HEADER CMD
5885 022602 004737 045132 JSR PC,GSTAT ;GET FRESH STATUS
5886 022606 032737 100000 003322 BIT #CERR,HCS1
5887 022614 001405 BEQ 64$
5888 022616 104201 ERROR 201 ;CERR AFTER WRITE HEADER CMD
5889 022620 104401 056436 TYPE ,MSG18 ;ABORTING BALANCE OF TESTS
5890 022624 000137 042706 JMP $EOP ;ABORT DRIVE
5891 022630
5892
5893 022630 012737 010001 003322 MOV #<CFMT!SELDRV>,HCS1 ;GET 20 SECTOR STATUS
5894 022636 004737 043472 JSR PC,DOCMD ;DO COMMAND
5895 022642 104117 ERROR 117 ;NO RDY AFTER SELDRV CMD
5896 022644 032737 001000 003350 BIT #D.FORM,HMR2
5897 022652 001001 BNE 1$
5898 022654 104312 ERROR 312 ;FORMAT NOT SET AFTER WRITE HDR CMD
5899
5900 022656
5901
5902 022656 012737 010340 003412 MOV #<0!D.SPIN!D.DRDY!D.VV!D.DRA>,E.A0 ;EXPECTED MSG A0
5903 022664 005037 003414 CLR E.B0 ;EXPECTED MSG B0
5904 022670 012737 001720 003416 MOV #<D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1 ;EXPECTED A1
5905 022676 012737 000001 003420 MOV #1,E.B1 ;MSG ID FOR EXPECTED MSG B1
5906 022704 005037 003422 CLR E.A2 ;EXPECTED MSG A2
5907 022710 012737 000002 003424 MOV #2,E.B2 ;MSG ID FOR EXPECTED MSG B2
5908 022716 012737 000003 003430 MOV #3,E.B3 ;MSG ID FOR EXPECTED MSG B3
5909
5910 022724 004737 044310 JSR PC,CHKMSG ;CHECK MSGS A0,B0,A1,B1
5911 022730 000000 .WORD 0!0!0 ;8 MSGS SPECIFIED HERE
5912 022732 104277 ERROR 277 ;MSG A0 ERROR AFTER WRITE HEADER CMD
5913 022734 104267 ERROR 267 ;MSG B0 ERROR
5914 022736 104300 ERROR 300 ;MSG A1 ERROR
5915 022740 104270 ERROR 270 ;MSG B1 ERROR
5916
5917 022742 005037 001400 CLR SECNT ;SECTOR COUNT
5918 022746
5919 022746 104415
5920 022750 012706 001100 SCOP1
MOV #STACK,SP ;RESTORE STK PTR
```



```
5921
5922 022754 004737 045462 JSR PC,SUBCLR
5923 022760 104024 ERROR 24 ;CERR AFTER SCLR
5924
5925
5926 022762 012700 001674 MOV #RHTAB,R0
5927 022766 012737 010025 003322 MOV #<CFMT!RDHEAD>,HCS1
5928 022774 004737 043530 JSR PC,DATCMD ;DO DATA XFER CMD & GET CONTR RDY
5929 023000 104171 ERROR 171 ;NO RDY AFTER READ HEADER CMD
5930 023002 032737 100000 003322 BIT #CERR,HCS1
5931 023010 001405 BEQ 65$
5932 023012 104174 ERROR 174 ;CERR AFTER READ HEADER CMD
5933 023014 104401 056436 TYPE ,MSG18 ;ABORT BALANCE OF TESTS
5934 023020 000137 042706 JMP $EOP ;ABORT DRIVE
5935
5936 023024 016520 000024 65$: MOV RKDB(R5),(R0)+ ;1'ST WORD FROM SILO TO RHTAB
5937 023030 016520 000024 MOV RKDB(R5),(R0)+ ;2'ND WORD
5938 023034 016520 000024 MOV RKDB(R5),(R0)+ ;3'RD WORD
5939
5940
5941 023040 032765 100000 000010 BIT #DLT,RKCS2(R5)
5942 023046 001407 BEQ 66$
5943 023050 004737 045132 JSR PC,GSTAT
5944 023054 104173 ERROR 173 ;DLT AFTER READ HEADER CMD
5945 023056 104401 056436 TYPE ,MSG18 ;ABORTING BALANCE OF TESTS
5946 023062 000137 042706 JMP $EOP ;ABORT DRIVE
5947 023066 66$:
5948
5949 023066 012737 010001 003322 MOV #<CFMT!SELDRV>,HCS1
5950 023074 004737 043472 JSR PC,DOCMD ;DO CMD.
5951 023100 104117 ERROR 117 ;NO RDY AFTER SELDRV CMD
5952 023102 032737 001000 003350 BIT #D.FORM,HMR2
5953 023110 001001 BNE 6$
5954 023112 104313 ERROR 313 ;FORMAT NOT SET AFTER READ HDR CMD
5955
5956 023114 6$:
5957
5958 023114 012737 010340 003412 MOV #<0!D.SPIN!D.DRDY!D.VV!D.DRA>,E.A0 ;EXPECTED MSG A0
5959 023122 005037 003414 CLR E.B0 ;EXPECTED MSG B0
5960 023126 012737 001720 003416 MOV #<D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1 ;EXPECTED A1
5961 023134 012737 000001 003420 MOV #1,E.B1 ;MSG ID FOR EXPECTED MSG B1
5962 023142 005037 003422 CLR E.A2 ;EXPECTED MSG A2
5963 023146 012737 000002 003424 MOV #2,E.B2 ;MSG ID FOR EXPECTED MSG B2
5964 023154 012737 000003 003430 MOV #3,E.B3 ;MSG ID FOR EXPECTED MSG B3
5965
5966 023162 004737 044310 JSR PC,CHKMSG ;CHECK MSGS A0,B0,A1,B1
5967 023166 000003 .WORD T.A2!T.B2!0 ;& MSGS SPECIFIED HERE
5968 023170 104301 ERROR 301 ;MSG A0 ERROR AFTER READ HEADER CMD
5969 023172 104271 ERROR 271 ;MSG B0 ERROR
5970 023174 104302 ERROR 302 ;MSG A1 ERROR
5971 023176 104272 ERROR 272 ;MSG B1 ERROR
5972
5973 023200 012765 000002 000026 MOV #2,RKMR1(R5) ;SELECT WORD 2
5974 023206 004737 045132 JSR PC,GSTAT
5975 023212 005737 001362 TST CYLDIF ;SEE IF MSG A2=0
5976 023216 001401 BEQ 67$ ;BR IF YES
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CZR6HFO UNIBUS RK6 DR PT1          MACY11 30(1046) 04-JAN-82 13:01  B 10  PAGE 119
CZR6HF.P11 04-JAN-82 12:44        T24      BASIC WRITE/READ HEADER TEST; ALL 1'S, 20 SECTORS
                                                    SEQ 0118

5977 023220 104172
5978 023222 005737 001364
5979 023226 001401
5980 023230 104264
5981 023232
5982 023232 012701 001674
5983
5984 023236 005037 001442
5985 023242 013737 001450 003360
5986 023250 012137 001454 003360
5987 023254 023737 001454 003360
5988 023262 001401
5989 023264 104202
5990 023266 005237 001442
5991 023272 023727 001442 000003
5992 023300 001363
5993
5994
5995
5996
5997
5998
5999
6000 023302 000004
6001 023304 012737 000001 001174
6002 023312 012706 001100
6003
6004 023316 004737 045462
6005 023322 104024
6006
6007 023324 005237 001464
6008
6009 023330 012765 001470 000004
6010 023336 012765 177676 000002
6011 023344 012737 000000 001352
6012
6013 023352 013737 001352 001366
6014 023360 012737 000000 001430
6015 023366 012737 000000 001436
6016 023374 004737 046572
6017
6018 023400 012765 000000 000020
6019
6020 023406 012737 000027 003322
6021 023414 004737 043530
6022 023420 104200
6023 023422 004737 045132
6024 023426 032737 100000 003322
6025 023434 001405
6026 023436 104201
6027 023440 104401 056436
6028 023444 000137 042706
6029 023450
6030
6031 023450 012737 010340 003412
6032 023456 005037 003414

        ERROR 172 ;MSG A2 NOT CLEARED AFTER READ HEADER CMD
        TST CYLADD ;SEE IF MSG B2=0
        BEQ 68$ ;BR IF YES
        ERROR 264 ;MSG B2 NOT CLEARED AFTER READ HEADER CMD

68$: MOV #RHTAB,R1

3$: CLR WDCNT ;HEADER WORD COUNT
MOV DATA1,TEMP1 ;GET 'SHOULD BE' DATA
4$: MOV (R1)+,HDWD ;READ HEADER WORD
CMP HDWD,TEMP1 ;MATCH OK?
BEQ 5$ ;BR IF YES
ERROR 202 ;READ HEADER MISMATCH
5$: INC WDCNT
CMP WDCNT,#3 ;JUST 1 SECTOR AND 1 HEAD
BNE 4$

*****
;*TEST 25 WRITE & READ HEADERS CYL 0, HEAD 0
*****
TST25: SCOPE
MOV #1,$TIMES ;DO 1 ITERATION
MOV #STACK,SP ;RESTORE STK PTR

JSR PC,SUBCLR
ERROR 24 ;CERR AFTER SCLR

INC BYPFMT ;SET BIT 14 & 15 IN HEADER

MOV #HDTAB,RKBA(R5) ;HEADER WORD TABLE
MOV #-66.,RKWC(R5) ;WORD COUNT.
MOV #0,TOCYL

MOV TOCYL,CALADD ;SETUP
MOV #0,HEAD ;TO FILL
MOV #0,FORMAT ;HEADER
JSR PC,FHDTAB ;TABLE

MOV #0,RKDC(R5) ;CYL#

MCV #<WRHEAD>,HCS1
JSR PC,DATCMD ;DO DATA XFER CMD & GET CONTR RDY
ERROR 200 ;NO RDY AFTER WRITE HEADER CMD
JSR PC,GSTAT ;GET FRESH STATUS
BIT #CERR,HCS1
BEQ 64$
ERROR 201 ;CERR AFTER WRITE HEADER CMD
TYPE MSG18 ;ABORTING BALANCE OF TESTS
JMP $EOP ;ABORT DRIVE

64$:

MOV #<O!D.SPIN!D.DRDY!D.VV!D.DRA>,E.A0 ;EXPECTED MSG A0
CLR E.B0 ;EXPECTED MSG B0

```



```

6033 023462 012737 001720 003416 MOV #<D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1 ;EXPECTED A1
6034 023470 012737 000001 003420 MOV #1,E.B1 ;MSG ID FOR EXPECTED MSG B1
6035 023476 005037 003422 CLR E.A2 ;EXPECTED MSG A2
6036 023502 012737 000002 003424 MOV #2,E.B2 ;MSG ID FOR EXPECTED MSG B2
6037 023510 012737 000003 003430 MOV #3,E.B3 ;MSG ID FOR EXPECTED MSG B3
6038
6039 023516 004737 044310 JSR PC,CHKMSG ;CHECK MSGS A0,B0,A1,B1
6040 023522 000003 .WORD T.A2!T.B2!0 ;& MSGS SPECIFIED HERE
6041 023524 104277 ERROR 277 ;MSG A0 ERROR AFTER WRITE HEADER CMD
6042 023526 104267 ERROR 267 ;MSG B0 ERROR
6043 023530 104300 ERROR 300 ;MSG A1 ERROR
6044 023532 104270 ERROR 270 ;MSG B1 ERROR
6045
6046 023534 005037 001400 CLR SECNT ;SECTOR COUNT
6047 023540 104415 SCOP1
6048 023542 012706 001100 MOV #STACK,SP ;RESTORE STK PTR
6049
6050 023546 004737 045462 JSR PC,SUBCLR
6051 023552 104024 ERROR 24 ;CERR AFTER SCLR
6052
6053 023554 012765 000000 000020 MOV #0,RKDC(R5) ;CYL #
6054
6055 023562 012700 001674 MOV #RHTAB,RO
6056
6057 023566 012737 000025 003322 65$: MOV #RDHEAD,HCS1
6058 023574 004737 043530 JSR PC,DATCMD ;DO READ HEADER CMD & GET CONTR RDY
6059 023600 104171 ERROR 171 ;NO RDY AFTER READ HEADER CMD
6060 023602 032737 100000 003322 BIT #CERR,HCS1
6061 023610 001405 BEQ 66$
6062 023612 104174 ERROR 174 ;CERR AFTER READ HEADER CMD
6063 023614 104401 056436 TYPE ,MSG18 ;ABORTING BALANCE OF TESTS
6064 023620 000137 042706 JMP $EOP ;ABORT DRIVE
6065
6066 023624 016520 000024 66$: MOV RKDB(R5),(R0)+ ;1'ST WORD FROM SILO TO RHTAB
6067 023630 016520 000024 MOV RKDB(R5),(R0)+ ;2'ND WORD
6068 023634 016520 000024 MOV RKDB(R5),(R0)+ ;3'RD WORD
6069
6070 023640 032765 100000 000010 BIT #DLT,RKCS2(R5) ;SEE IF DATA LATE
6071 023646 001407 BEQ 67$
6072 023650 004737 045132 JSR PC,GSTAT
6073 023654 104173 ERROR 173 ;DATA LATE ON READ HEADER
6074 023656 104401 056436 TYPE ,MSG18 ;ABORT BALANCE OF TESTS
6075 023662 000137 042706 JMP $EOP ;ABORT DRIVE
6076
6077 023666 020027 002100 67$: CMP RO,#RHTAB+132. ;ALL 66 WORDS DONE?
6078 023672 001335 BNE 65$ ;BR IF NO
6079
6080 023674 004737 047114 JSR PC,SORT ;SORT RHTAB INTO SRTTAB SO THAT IT
6081 ;BEGINS WITH SECTOR 0
6082 023700 005037 001442 CLR WDCNT ;WORD COUNT
6083 023704 012700 002100 MOV #SRTTAB,RO ;ACTUAL HEADER TABLE
6084 023710 012701 001470 MOV #HDTAB,R1 ;CALC HEADER TABLE
6085
6086 023714 012037 001454 68$: MOV (R0)+,HDWD
6087 023720 012137 003360 MOV (R1)+,TEMP1
6088 023724 023737 001454 003360 CMP HDWD,TEMP1 ;COMPARE ACTUAL WITH CALCULATED WORD

```

```

6089 023732 001401 BEQ 69$ ;BR IF COMPARE
6090 023734 104202 ERROR 202 ;READ HEADER MISMATCH
6091
6092 023736 005237 001442 69$: INC WDCNT
6093 023742 023727 001442 000102 CMP WDCNT,#66. ;ALL WORDS DONE?
6094 023750 001361 BNE 68$ ;BR IF NO
6095
6096
6097 023752 005037 001464 CLR BYPFMT ;ALLOW CORRECT FORMATTING
6098
6099
6100
6101
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6103
6104
6105
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6115

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```

*****
:TEST 26 SEEK FROM CYL 0 TO 1 & READ HEADERS
:
:*
:* THIS TEST CHECKS MSG A & B WORDS 0,1,2 FOR CORRECT STATUS AFTER RDY
:* IS RECEIVED FROM A SEEK CMD TO DETERMINE
:* THAT THE HEADS ARE ACTUALLY MOVING & THE CYL DIFF IS 1.
:* AFTER ATTN IS RECEIVED, CERR IS EXAMINED FOR ANY ERRORS.
:* CYL DIFFERENCE IN MSG A2 IS VERIFIED TO BE 0 & CYL ADDR
:* IN MSG B2 IS VERIFIED TO BE 1.
:*
:* HEADERS ARE READ FROM 1 SECTOR, HEAD 0 & VERIFIED THAT THEY ARE
:* DIFFERENT FROM CYL 0 TO SHOW THAT THE HEADS DID ACTUALLY MOVE.
:
:
*****

```

```

6116 023756 000004 TST26: SCOPE
6117 023760 012737 000001 001174 MOV #1,$TIMES ;:DO 1 ITERATION
6118 023766 012706 001100 MOV #STACK,SP ;:RESTORE STK PTR
6119 023772 004737 045462 JSR PC,SUBCLR
6120 023776 104024 ERROR 24 ;CERR AFTER SCLR
6121 024000 005037 001350 CLR FRCYL
6122 024004 012737 000001 001352 MOV #1,TOCYL
6123 024012 012737 000001 001360 MOV #1,CALDIF
6124 024020 012765 000001 000020 MOV #1,RKDC(R5) ;SET FOR CYL 1
6125 024026 012737 025012 001176 MOV #10$, $ESCAPE
6126
6127 024034 012737 000017 003322 MOV #SEEK,HCS1
6128 024042 004737 043472 JSR PC,DOCMD ;DO SEEK CMD & GET CONTR READY
6129 024046 104131 ERROR 131 ;NO RDY AFTER SEEK CMD
6130 024050 012737 030140 003412 MOV #<D.PIP!D.SPIN!D.VV!D.DRA>,E.A0 ;EXPECTED A0
6131 024056 005037 003414 CLR E.B0
6132 024062 012737 003720 003416 MOV #<D.FWD!D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1
6133 024070 012737 000001 003420 MOV #1,E.B1
6134
6135 024076 004737 044310 JSR PC,CHKMSG ;CHECK MSGS A0,B0,A1,B1
6136 024102 000003 .WORD T.A2!T.B2!0 ;& MSGS SPECIFIED HERE
6137 024104 104203 ERROR 203 ;MSG A0 ERROR DURING SEEK CMD
6138 024106 104204 ERROR 204 ;MSG B0 ERROR
6139 024110 104205 ERROR 205 ;MSG A1 ERROR
6140 024112 104206 ERROR 206 ;MSG B1 ERROR
6141
6142 024114 023727 001362 000001 CMP CYLDIF,#1
6143 024122 001401 BEQ 1$
6144 024124 104212 ERROR 212 ;CYL DIFF INCORRECT DURING SEEK CMD.

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6145									
6146									
6147	024126	012737	025032	001176	1\$:	MOV	#12\$,\$ESCAPE		
6148	024134	013737	001422	003360		MOV	T2500,TEMP1	;SETUP TIMEOUT	
6149									
6150									
6151	024142	004737	044176			JSR	PC,FATT2	;FIND ATTN	
6152	024146	104132				ERROR	132	;NO ATTN AFTER SEEK CMD	
6153	024150	032737	100000	003322		BIT	#CERR,HCS1		
6154	024156	001401				BEQ	64\$		
6155	024160	104210				ERROR	210	;CERR AFTER SEEK CMD	
6156	024162				64\$:				
6157									
6158	024162	012737	050340	003412		MOV	#<D.DSC!D.SPIN!D.DRDY!D.VV!D.DRA>,E.A0	;EXPECTED MSG A0	
6159	024170	005037	003414			CLR	E.B0	;EXPECTED MSG B0	
6160	024174	012737	001720	003416		MOV	#<D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1	;EXPECTED A1	
6161	024202	012737	000001	003420		MOV	#1,E.B1	;MSG ID FOR EXPECTED MSG B1	
6162	024210	005037	003422			CLR	E.A2	;EXPECTED MSG A2	
6163	024214	012737	000002	003424		MOV	#2,E.B2	;MSG ID FOR EXPECTED MSG B2	
6164	024222	012737	000003	003430		MOV	#3,E.B3	;MSG ID FOR EXPECTED MSG B3	
6165									
6166	024230	004737	044310			JSR	PC,CHKMSG	;CHECK MSGS A0,B0,A1,B1	
6167	024234	000003				.WORD	T.A2!T.B2!0	; & MSGS SPECIFIED HERE	
6168	024236	104133				ERROR	133	;MSG A0 ERROR AFTER SEEK CMD	
6169	024240	104134				ERROR	134	;MSG B0 ERROR	
6170	024242	104135				ERROR	135	;MSG A1 ERROR	
6171	024244	104136				ERROR	136	;MSG B1 ERROR	
6172	024246	005737	001362			TST	CYLDIF		
6173	024252	001401				BEQ	65\$		
6174	024254	104137				ERROR	137	;CYL DIFF NOT CLEARED AFTER SEEK CMD	
6175									
6176	024256				65\$:				
6177									
6178	024256	012765	100000	000000		MOV	#CLR,RKCS1(R5)		
6179	024264	013765	001222	000010		MOV	\$UNIT,RKCS2(R5)	;DRIVE#	
6180	024272	012737	000005	003322		MOV	#CLEAR,HCS1		
6181	024300	004737	043472			JSR	PC,DOCMD	;DO DRIVE CLEAR CMD & GET CONTR RDY	
6182	024304	104151				ERROR	151	;NO RDY AFTER DRIVE CLEAR CMD	
6183	024306	004737	044050			JSR	PC,TSTATN	;TEST FOR ATTN	
6184	024312	000401				BR	66\$		
6185	024314	104154				ERROR	154	;ATTN NOT CLEARED AFTER DRIVE CLEAR CMD	
6186	024316				66\$:				
6187									
6188	024316	012737	010340	003412		MOV	#<0!D.SPIN!D.DRDY!D.VV!D.DRA>,E.A0	;EXPECTED MSG A0	
6189	024324	005037	003414			CLR	E.B0	;EXPECTED MSG B0	
6190	024330	012737	001720	003416		MOV	#<D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1	;EXPECTED A1	
6191	024336	012737	000001	003420		MOV	#1,E.B1	;MSG ID FOR EXPECTED MSG B1	
6192	024344	005037	003422			CLR	E.A2	;EXPECTED MSG A2	
6193	024350	012737	000002	003424		MOV	#2,E.B2	;MSG ID FOR EXPECTED MSG B2	
6194	024356	012737	000003	003430		MOV	#3,E.B3	;MSG ID FOR EXPECTED MSG B3	
6195									
6196	024364	004737	044310			JSR	PC,CHKMSG	;CHECK MSGS A0,B0,A1,B1	
6197	024370	000003				.WORD	T.A2!T.B2!0	; & MSGS SPECIFIED HERE	
6198	024372	104273				ERROR	273	;MSG A0 ERROR AFTER DRIVE CLEAR CMD	
6199	024374	104265				ERROR	265	;MSG B0 ERROR	
6200	024376	104274				ERROR	274	;MSG A1 ERROR	

6201	024400	104266			ERROR	266		;MSG B1 ERROR
6202								
6203	024402	005737	001364		TST	CYLADD		
6204	024406	023727	001364	000001	CMP	CYLADD,#1		
6205	024414	001401			BEQ	2\$		
6206	024416	104207			ERROR	207		;CYL ADDR INCORRECT AFTER SEEK CMD
6207								
6208								
6209	024420				2\$:			
6210	024420	104415			SCOP1			
6211	024422	012706	001100		MOV	#STACK,SP		;RESTORE STK PTR
6212								
6213	024426	004737	045462		JSR	PC,SUBCLR		
6214	024432	104024			ERROR	24		;CERR AFTER SCLR
6215								
6216	024434	005037	001176		CLR	\$ESCAPE		
6217	024440	012765	000001	000020	MOV	#1,RKDC(R5)		;CYL #
6218								
6219	024446	012700	001574		MOV	#RHTAB,R0		
6220	024452	012737	000025	003322	MOV	#<RDHEAD>,HCS1		
6221	024460	004737	043530		JSR	PC,DATCMD		;DO DATA XFER CMD & GET CONTR RDY
6222	024464	104171			ERROR	171		;NO RDY AFTER READ HEADER CMD
6223	024466	032737	100000	003322	BIT	#CERR,HCS1		
6224	024474	001405			BEQ	67\$		
6225	024476	104174			ERROR	174		;CERR AFTER READ HEADER CMD
6226	024500	104401	056436		TYPE	,MSG18		;ABORT BALANCE OF TESTS
6227	024504	000137	042706		JMP	\$EOP		;ABORT DRIVE
6228								
6229	024510	016520	000024		67\$:	MOV	RKDB(R5),(R0)+	;1'ST WORD FROM SILO TO RHTAB
6230	024514	016520	000024		MOV	RKDB(R5),(R0)+		;2'ND WORD
6231	024520	016520	000024		MOV	RKDB(R5),(R0)+		;3'RD WORD
6232								
6233								
6234	024524	032765	100000	000010	BIT	#DLT,RKCS2(R5)		
6235	024532	001407			BEQ	68\$		
6236	024534	004737	045132		JSR	PC,GSTAT		
6237	024540	104173			ERROR	173		;DLT AFTER READ HEADER CMD
6238	024542	104401	056436		TYPE	,MSG18		;ABORTING BALANCE OF TESTS
6239	024546	000137	042706		JMP	\$EOP		;ABORT DRIVE
6240	024552				68\$:			
6241								
6242								
6243	024552	005737	001674		TST	RHTAB		;CHECK 1'ST WORD ONLY:CYL#
6244	024556	001001			BNE	3\$		
6245	024560	104211			ERROR	211		;CYL 0 HEADER ON CYL 1
6246								
6247	024562	013737	001674	001454	3\$:	MOV	RHTAB,HDWD	
6248	024570	012737	000001	003360	MOV	#1,TEMP1		
6249	024576	023737	001454	003360	CMP	HDWD,TEMP1		
6250	024604	001401			BEQ	4\$		
6251	024606	104202			ERROR	202		;READ CYL WORD HEADER ERROR
6252	024610				4\$:			
6253	024610	004737	047466		JSR	PC,SWTST		;SEE IF SW 14 OR 8 IS SET
6254	024614	000516			BR	TST27		;GO TO NEXT TEST
6255								;RETURN HERE IF SW 14 IS SET OR
6256								;SW 8 WITH SWR <7:0> APPLY

CZR6HFO UNIBUS RK6 DR PT1
CZR6HF.P11 04-JAN-82 12:44

MACY11 30(1046)
T26

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SEEK FROM CYL 0 TO 1 & READ HEADERS

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SEQ 0123

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6257 024616 004737 045462      6$: JSR PC,SUBCLR
6258 024622 104024              ERROR 24          ;CERR AFTER SCLR
6259
6260 024624 012737 000017 003322  MOV #SEEK,HCS1
6261 024632 004737 043472      JSR PC,DOCMD      ;DO SEEK CMD & GET CONTR READY
6262 024636 104131              ERROR 131        ;NO RDY AFTER SEEK CMD.
6263
6264 024640 013737 001426 003360  MOV T50000,TEMP1
6265 024646 004737 044176      JSR PC,FATT2      ;FIND ATTN
6266 024652 104132              ERROR 132        ;NO ATTN AFTER SEEK CMD
6267 024654 032737 100000 003322  BIT #CERR,HCS1
6268 024662 001401              BEQ 69$
6269 024664 104210              ERROR 210        ;CERR AFTER SEEK CMD.
6270
6271 024666 004737 045462      69$: JSR PC,SUBCLR
6272 024672 104024              ERROR 24          ;CERR AFTER SCLR
6273
6274
6275 024674 012737 050340 003412  MOV #<D.DSC!D.SPIN!D.DRDY!D.VV!D.DRA>,E.A0 ;EXPECTED MSG A0
6276 024702 005037 003414      CLR E.B0          ;EXPECTED MSG B0
6277 024706 012737 001720 003416  MOV #<D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1 ;EXPECTED A1
6278 024714 012737 000001 003420  MOV #1,E.B1       ;MSG ID FOR EXPECTED MSG B1
6279 024722 005037 003422      CLR E.A2          ;EXPECTED MSG A2
6280 024726 012737 000002 003424  MOV #2,E.B2       ;MSG ID FOR EXPECTED MSG B2
6281 024734 012737 000003 003430  MOV #3,E.B3       ;MSG ID FOR EXPECTED MSG B3
6282
6283 024742 004737 044310      JSR PC,CHKMSG     ;CHECK MSGS A0,B0,A1,B1
6284 024746 000003              .WORD T.A2!T.B2!0 ;& MSGS SPECIFIED HERE
6285 024750 104133              ERROR 133        ;MSG A0 ERROR AFTER SEEK CMD
6286 024752 104134              ERROR 134        ;MSG B0 ERROR
6287 024754 104135              ERROR 135        ;MSG A1 ERROR
6288 024756 104136              ERROR 136        ;MSG B1 ERROR
6289 024760 005737 001364      TST CYLADD
6290 024764 001401              BEQ 7$
6291 024766 104043              ERROR 43         ;CYL ADDR IN RKMR3 NOT=RKDC
6292
6293 024770      7$: CLR $ESCAPE
6294 024770 005037 001176      TST LPFLG
6295 024774 005737 001410      BEQ 70$
6296 025000 001402
6297 025002 000177 154102      JMP @SLPERR      ;SW 9 WAS SET.
6298 025006 000177 154074      70$: JMP @SLPADR      ;SW 14 OR 8 WAS SET
6299
6300 025012      10$:
6301 025012 005237 001410      INC LPFLG
6302 025016 032777 001000 154114  BIT #SW9,@SWR   ;LOOP ON ERROR?
6303 025024 001274              BNE 6$          ;YES, RECONDITION DRIVE
6304 025026 000137 024126      JMP 1$          ;RETURN TO MAINLINE
6305 025032      12$:
6306 025032 005237 001410      INC LPFLG
6307 025036 032777 001000 154074  BIT #SW9,@SWR   ;LOOP ON ERROR?
6308 025044 001264              BNE 6$          ;YES, RECONDITION DRIVE
6309 025046 000137 024420      JMP 2$          ;RETURN TO MAINLINE
6310
6311
6312
```

```
6313
6314
6315
6316
6317 025052 000004
6318 025054 012737 000001 001174
6319 025062 012706 001100
6320
6321 025066 004737 045462
6322 025072 104024
6323
6324 025074 005237 001464
6325
6326 025100 012765 001470 000004
6327 025106 012765 177676 000002
6328 025114 012737 000001 001352
6329
6330 025122 013737 001352 001366
6331 025130 012737 000000 001430
6332 025136 012737 000000 001436
6333 025144 004737 046572
6334
6335 025150 012765 000001 000020
6336
6337 025156 012737 000027 003322
6338 025164 004737 043530
6339 025170 104200
6340 025172 004737 045132
6341 025176 032737 100000 003322
6342 025204 001405
6343 025206 104201
6344 025210 104401 056436
6345 025214 000137 042706
6346 025220
6347
6348 025220 012737 010340 003412
6349 025226 005037 003414
6350 025232 012737 001720 003416
6351 025240 012737 000001 003420
6352 025246 005037 003422
6353 025252 012737 000002 003424
6354 025260 012737 000003 003430
6355
6356 025266 004737 044310
6357 025272 000003
6358 025274 104277
6359 025276 104267
6360 025300 104300
6361 025302 104270
6362
6363 025304 005037 001400
6364 025310 104415
6365 025312 012706 001100
6366
6367 025316 004737 045462
6368 025322 104024
```

```
*****
*TEST 27 WRITE & READ HEADERS CYL 1, HEAD 0
*****
TST27: SCOPE
MOV #1,$TIMES ;DO 1 ITERATION
MOV #STACK,SP ;RESTORE STK PTR
JSR PC,SUBCLR
ERROR 24 ;CERR AFTER SCLR
INC BYPFMT ;SET BIT 14 & 15 IN HEADER
MOV #HDTAB,RKBA(R5) ;HEADER WORD TABLE
MOV #-66.,RKWC(R5) ;WORD COUNT.
MOV #1,TOCYL
MOV TOCYL,CALADD ;SETUP
MOV #0,HEAD ;TO FILL
MOV #0,FORMAT ;HEADER
JSR PC,FHDTAB ;TABLE
MOV #1,RKDC(R5) ;CYL#
MOV #<WRHEAD>,HCS1
JSR PC,DATCMD ;DO DATA XFER CMD & GET CONTR RDY
ERROR 200 ;NO RDY AFTER WRITE HEADER CMD
JSR PC,GSTAT ;GET FRESH STATUS
BIT #CERR,HCS1
BEQ 64$
ERROR 201 ;CERR AFTER WRITE HEADER CMD
TYPE ,MSG18 ;ABORTING BALANCE OF TESTS
JMP $EOP ;ABORT DRIVE

64$:
MOV #<0!D.SPIN!D.DRDY!D.VV!D.DRA>,E.A0 ;EXPECTED MSG A0
CLR E.B0 ;EXPECTED MSG B0
MOV #<D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1 ;EXPECTED A1
MOV #1,E.B1 ;MSG ID FOR EXPECTED MSG B1
CLR E.A2 ;EXPECTED MSG A2
MOV #2,E.B2 ;MSG ID FOR EXPECTED MSG B2
MOV #3,E.B3 ;MSG ID FOR EXPECTED MSG B3
JSR PC,CHKMSG ;CHECK MSGS A0,B0,A1,B1
WORD T.A2!T.B2!0 ;& MSGS SPECIFIED HERE
ERROR 277 ;MSG A0 ERROR AFTER WRITE HEADER CMD
ERROR 267 ;MSG B0 ERROR
ERROR 300 ;MSG A1 ERROR
ERROR 270 ;MSG B1 ERROR
CLR SECNT ;SECTOR COUNT
SCOP1
MOV #STACK,SP ;RESTORE STK PTR
JSR PC,SUBCLR
ERROR 24 ;CERR AFTER SCLR
```



```

6369
6370 025324 012765 000001 000020      MOV      #1,RKDC(R5)      ;CYL #
6371
6372 025332 012700 001674              MOV      #RHTAB,R0
6373
6374 025336 012737 000025 003322 65$:  MOV      #RDHEAD,HCS1
6375 025344 004737 043530              JSR      PC,DATCMD      ;DO READ HEADER CMD & GET CONTR RDY
6376 025350 104171              ERROR   171            ;NO RDY AFTER READ HEADER CMD
6377 025352 032737 100000 003322      BIT      #CERR,HCS1
6378 025360 001405              BEQ     66$
6379 025362 104174              ERROR   174            ;CERR AFTER READ HEADER CMD
6380 025364 104401 056436              TYPE   ,MSG18          ;ABORTING BALANCE OF TESTS
6381 025370 000137 042706              JMP     $EOP           ;ABORT DRIVE
6382
6383 025374 016520 000024              66$:  MOV      RKDB(R5),(R0)+  ;1'ST WORD FROM SILO TO RHTAB
6384 025400 016520 000024              MOV      RKDB(R5),(R0)+  ;2'ND WORD
6385 025404 016520 000024              MOV      RKDB(R5),(R0)+  ;3'RD WORD
6386
6387 025410 032765 100000 000010      BIT      #DLT,RKCS2(R5) ;SEE IF DATA LATE
6388 025416 001407              BEQ     67$
6389 025420 004737 045132              JSR      PC,GSTAT
6390 025424 104173              ERROR   173            ;DATA LATE ON READ HEADER
6391 025426 104401 056436              TYPE   ,MSG18          ;ABORT BALANCE OF TESTS
6392 025432 000137 042706              JMP     $EOP           ;ABORT DRIVE
6393
6394 025436 020027 002100              67$:  CMP      R0,#RHTAB+132.  ;ALL 66 WORDS DONE?
6395 025442 001335              BNE     65$            ;BR IF NO
6396
6397 025444 004737 047114              JSR      PC,SORT       ;SORT RHTAB INTO SRTTAB SO THAT IT
6398                                ;BEGINS WITH SECTOR 0
6399                                ;WORD COUNT
6400                                ;ACTUAL HEADER TABLE
6401                                ;CALC HEADER TABLE
6402 025460 012701 001470              MOV      #SRTTAB,R0
6403 025464 012037 001454              68$:  MOV      (R0)+,HDWD
6404 025470 012137 003360              MOV      (R1)+,TEMP1
6405 025474 023737 001454 003360      CMP      HDWD,TEMP1     ;COMPARE ACTUAL WITH CALCULATED WORD
6406 025502 001401              BEQ     69$            ;BR IF COMPARE
6407 025504 104202              ERROR   202            ;READ HEADER MISMATCH
6408
6409 025506 005237 001442              69$:  INC      WDCNT
6410 025512 023727 001442 000102      CMP      WDCNT,#66.    ;ALL WORDS DONE?
6411 025520 001361              BNE     68$            ;BR IF NO
6412
6413
6414 025522 005037 001464              CLR     BYPFMT        ;ALLOW CORRECT FORMATTING
6415
6416
6417
6418
6419
6420
6421
6422
6423
6424

```

```

*****
*TEST 30      TEST RECALIBRATE CMD & READ HEADERS
*
*      THIS TEST DOES A RECALIBRATE & READS HEADERS.
*      IT VERIFIES THAT WRITING HEADERS ON CYL 1 FROM THE PREVIOUS
*      TEST DID NOT OVERWRITE CYL 0 HEADERS.
*      AN ERROR IN THIS TEST INDICATES THAT HEADS:

```

```
6425
6426
6427
6428
6429
6430
6431 025526 000004
6432 025530 012737 000001 001174
6433 025536 012706 001100
6434
6435 025542 004737 045462
6436 025546 104024
6437 025550 012737 000001 001350
6438 025556 005037 001352
6439 025562 012737 000001 001360
6440 025570 012737 026566 001176
6441 025576 012737 000013 003322
6442 025604 004737 043472
6443 025610 104124
6444 025612 012765 100000 000000
6445 025620 012765 000001 000026
6446 025626 004737 045132
6447 025632 032737 020000 003350
6448 025640 001001
6449 025642 104307
6450
6451 025644 012737 030140 003412 1$:
6452 025652 005037 003414
6453 025656 012737 025720 003416
6454 025664 012737 000001 003420
6455
6456 025672 004737 044310
6457 025676 000001
6458 025700 104213
6459 025702 104214
6460 025704 104215
6461 025706 104216
6462
6463
6464
6465
6466 025710 012737 026606 001176 2$:
6467 025716 012737 177777 003360
6468 025724 004737 044176
6469 025730 104055
6470 025732 032737 100000 003322
6471 025740 001401
6472 025742 104220
6473 025744 3$:
6474
6475 025744 012737 050340 003412
6476 025752 005037 003414
6477 025756 012737 001720 003416
6478 025764 012737 000001 003420
6479 025772 005037 003422
6480 025776 012737 000002 003424
```

TST30: SCOPE
MOV #1,\$TIMES ;:DO 1 ITERATION
MOV #STACK,SP ;:RESTORE STK PTR
JSR PC,SUBCLR
ERROR 24 ;:CERR AFTER SCLR
MOV #1,FRCYL ;:PARAMETERS
CLR TOCYL ;:FOR
MOV #1,CALDIF ;:ERROR TYPEOUTS
MOV #10\$, \$ESCAPE
MOV #RECAL,HCS1
JSR PC,DOCMD ;:DO RECAL CMD & GET CONTR RDY
ERROR 124 ;:NO RDY AFTER RECAL CMD
MOV #CCLR,RKCS1(R5)
MOV #1,RKMR1(R5) ;:SELECT WORD 1
JSR PC,GSTAT
BIT #D.RTZ,HMR2
BNE 1\$
ERROR 307 ;:RTZ NOT SET DURING RECAL CMD
MOV #<D.PIP!D.SPIN!D.VV!D.DRA>,E.A0 ;:EXPECTED A0
CLR E.B0
MOV #<D.RTZ!D.REV!D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1
MOV #1,E.B1
JSR PC,CHKMSG ;:CHECK MSGS A0,B0,A1,B1
.WORD T.A2!0!0 ;:& MSGS SPECIFIED HERE
ERROR 213 ;:MSG A0 ERROR DURING RECAL CMD
ERROR 214 ;:MSG B0 ERROR
ERROR 215 ;:MSG A1 ERROR
ERROR 216 ;:MSG B1 ERROR
TST CYLDIF
BEQ 2\$
ERROR 217 ;:CYL DIFF INCORRECT DURING RECAL CMD.
MOV #12\$, \$ESCAPE
MOV #-1,TEMP1 ;:SETUP TIMEOUT
JSR PC,FATT2 ;:FIND ATTN
ERROR 55 ;:NO ATTN AFTER RECAL CMD
BIT #CERR,HCS1
BEQ 3\$
ERROR 220 ;:CERR AFTER RECAL CMD
MOV #<D.DSC!D.SPIN!D.DRDY!D.VV!D.DRA>,E.A0 ;:EXPECTED MSG A0
CLR E.B0 ;:EXPECTED MSG B0
MOV #<D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1 ;:EXPECTED A1
MOV #1,E.B1 ;:MSG ID FOR EXPECTED MSG B1
CLR E.A2 ;:EXPECTED MSG A2
MOV #2,E.B2 ;:MSG ID FOR EXPECTED MSG B2

6481	026004	012737	000003	003430	MOV	#3,E.B3	;MSG ID FOR EXPECTED MSG B3
6482							
6483	026012	004737	044310		JSR	PC,CHKMSG	;CHECK MSGS A0,B0,A1,B1
6484	026016	000003			.WORD	T.A2!T.B2!0	; & MSGS SPECIFIED HERE
6485	026020	104221			ERROR	221	;MSG A0 ERROR AFTER RECAL CMD
6486	026022	104275			ERROR	275	;MSG B0 ERROR
6487	026024	104222			ERROR	222	;MSG A1 ERROR
6488	026026	104276			ERROR	276	;MSG B1 ERROR
6489							
6490	026030	012765	000002	000026	MOV	#2,RKMR1(R5)	;SELECT WORD 2
6491	026036	004737	045132		JSR	PC,GSTAT	
6492	026042	005737	001362		TST	CYLDIF	;SEE IF MSG A2=0
6493	026046	001401			BEQ	64\$;BR IF YES
6494	026050	104047			ERROR	47	;MSG A2 NOT CLEARED AFTER RECAL CMD
6495	026052	005737	001364		TST	CYLADD	;SEE IF MSG B2=0
6496	026056	001401			BEQ	65\$;BR IF YES
6497	026060	104050			ERROR	50	;MSG B2 NOT CLEARED AFTER RECAL CMD
6498	026062						
6499							
6500	026062	012765	100000	000000	MOV	#CCLR,RKCS1(R5)	
6501	026070	013765	001222	000010	MOV	\$UNIT,RKCS2(R5)	;DRIVE#
6502	026076	012737	000005	003322	MOV	#CLEAR,HCS1	
6503	026104	004737	043472		JSR	PC,DOCMD	;DO DRIVE CLEAR CMD & GET CONTR RDY
6504	026110	104151			ERROR	151	;NO RDY AFTER DRIVE CLEAR CMD
6505	026112	004737	044050		JSR	PC,TSTATN	;TEST FOR ATTN
6506	026116	000401			BR	66\$	
6507	026120	104154			ERROR	154	;ATTN NOT CLEARED AFTER DRIVE CLEAR CMD
6508	026122						
6509							
6510	026122	012737	010340	003412	MOV	#<0!D.SPIN!D.DRDY!D.VV!D.DRA>,E.A0	;EXPECTED MSG A0
6511	026130	005037	003414		CLR	E.B0	;EXPECTED MSG B0
6512	026134	012737	001720	003416	MOV	#<D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1	;EXPECTED A1
6513	026142	012737	000001	003420	MOV	#1,E.B1	;MSG ID FOR EXPECTED MSG B1
6514	026150	005037	003422		CLR	E.A2	;EXPECTED MSG A2
6515	026154	012737	000002	003424	MOV	#2,E.B2	;MSG ID FOR EXPECTED MSG B2
6516	026162	012737	000003	003430	MOV	#3,E.B3	;MSG ID FOR EXPECTED MSG B3
6517							
6518	026170	004737	044310		JSR	PC,CHKMSG	;CHECK MSGS A0,B0,A1,B1
6519	026174	000003			.WORD	T.A2!T.B2!0	; & MSGS SPECIFIED HERE
6520	026176	104273			ERROR	273	;MSG A0 ERROR AFTER DRIVE CLEAR CMD
6521	026200	104265			ERROR	265	;MSG B0 ERROR
6522	026202	104274			ERROR	274	;MSG A1 ERROR
6523	026204	104266			ERROR	266	;MSG B1 ERROR
6524							
6525	026206	004737	045462		JSR	PC,SUBCLR	
6526	026212	104024			ERROR	24	;CERR AFTER SCLR
6527							
6528	026214	005037	001176		CLR	\$ESCAPE	
6529							
6530	026220	012700	001674		MOV	#RHTAB,R0	
6531	026224	012737	000025	003322	MOV	#<RDHEAD>,HCS1	
6532	026232	004737	043530		JSR	PC,DATCMD	;DO DATA XFER CMD & GET CONTR RDY
6533	026236	104171			ERROR	171	;NO RDY AFTER READ HEADER CMD
6534	026240	032737	100000	003322	BIT	#CERR,HCS1	
6535	026246	001405			BEQ	67\$	
6536	026250	104174			ERROR	174	;CERR AFTER READ HEADER CMD

6537	026252	104401	056436			TYPE	,MSG18	:ABORT BALANCE OF TESTS
6538	026256	000137	042706			JMP	\$EOP	:ABORT DRIVE
6539								
6540	026262	016520	000024		67\$:	MOV	RKDB(R5),(R0)+	:1'ST WORD FROM SILO TO RHTAB
6541	026266	016520	000024			MOV	RKDB(R5),(R0)+	:2'ND WORD
6542	026272	016520	000024			MOV	RKDB(R5),(R0)+	:3'RD WORD
6543								
6544								
6545	026276	032765	100000	000010		BIT	#DLT,RKCS2(R5)	
6546	026304	001407				BEQ	68\$	
6547	026306	004737	045132			JSR	PC,GSTAT	
6548	026312	104173				ERROR	173	:DLT AFTER READ HEADER CMD
6549	026314	104401	056436			TYPE	,MSG18	:ABORTING BALANCE OF TESTS
6550	026320	000137	042706			JMP	\$EOP	:ABORT DRIVE
6551	026324				68\$:			
6552								
6553	026324	023727	001674	000001		CMP	RHTAB,#1	:CHECK WORD 0, CYL # ONLY
6554	026332	001001				BNE	5\$	
6555	026334	104240				ERROR	240	:CYL 1 HEADERS ON CYL 0
6556								
6557	026336	005737	001674		5\$:	TST	RHTAB	
6558	026342	001401				BEQ	6\$	
6559	026344	104202				ERROR	202	:READ CYL WORD HEADER ERROR
6560	026346				6\$:			
6561	026346	004737	047466			JSR	PC,SWTST	:SEE IF SW 14 OR 8 IS SET
6562	026352	000525				BR	TST31	::GO TO NEXT TEST
6563								:RETURN HERE IF SW 14 IS SET OR
6564								:SW 8 WITH SWR <7:0> APPLY
6565	026354	004737	045462		8\$:	JSR	PC,SUBCLR	
6566	026360	104024				ERROR	24	:CERR AFTER SCLR
6567	026362	012765	000001	000020		MOV	#1,RKDC(R5)	:RECONDITION BACK TO CYL 1
6568								
6569	026370	012737	000017	003322		MOV	#SEEK,HCS1	
6570	026376	004737	043472			JSR	PC,DOCMD	:DO SEEK CMD & GET CONTR READY
6571	026402	104131				ERROR	131	:NO RDY AFTER SEEK CMD.
6572								
6573	026404	013737	001426	003360		MOV	T50000,TEMP1	
6574	026412	004737	044176			JSR	PC,FATT2	:FIND ATTN
6575	026416	104132				ERROR	132	:NO ATTN AFTER SEEK CMD
6576	026420	032737	100000	003322		BIT	#CERR,HCS1	
6577	026426	001401				BEQ	69\$	
6578	026430	104210				ERROR	210	:CERR AFTER SEEK CMD.
6579								
6580	026432	004737	045462		69\$:	JSR	PC,SUBCLR	
6581	026436	104024				ERROR	24	:CERR AFTER SCLR
6582								
6583								
6584								
6585	026440	012737	010340	003412		MOV	#<0!D.SPIN!D.DRDY!D.VV!D.DRA>,E.A0	:EXPECTED MSG A0
6586	026446	005037	003414			CLR	E.B0	:EXPECTED MSG B0
6587	026452	012737	001720	003416		MOV	#<D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1	:EXPECTED A1
6588	026460	012737	000001	003420		MOV	#1,E.B1	:MSG ID FOR EXPECTED MSG B1
6589	026466	005037	003422			CLR	E.A2	:EXPECTED MSG A2
6590	026472	012737	000002	003424		MOV	#2,E.B2	:MSG ID FOR EXPECTED MSG B2
6591	026500	012737	000003	003430		MOV	#3,E.B3	:MSG ID FOR EXPECTED MSG B3
6592	026506	012737	000022	003424		MOV	#<BIT4!2>,E.B2	:EXPECTED MSG B2 & ID FOR CYL 1


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6593
6594 026514 004737 044310 JSR PC,CHKMSG ;CHECK MSGS A0,B0,A1,B1
6595 026520 000003 .WORD T,A2!T.B2!0 ;& MSGS SPECIFIED HERE
6596 026522 104133 ERROR 133 ;MSG A0 ERROR AFTER SEEK CMD
6597 026524 104134 ERROR 134 ;MSG B0 ERROR
6598 026526 104135 ERROR 135 ;MSG A1 ERROR
6599 026530 104136 ERROR 136 ;MSG B1 ERROR
6600 026532 023727 001364 000001 CMP CYLADD,#1
6601 026540 001401 BEQ 9$
6602 026542 104043 ERROR 43 ;CYL ADDR IN RKMR3 NOT=RKDC
6603 026544 9$:
6604 026544 005037 001176 CLR $ESCAPE
6605 026550 005737 001410 TST LPFLG
6606 026554 001402 BEQ 70$
6607 026556 000177 152326 JMP @SLPERR ;SW 9 WAS SET.
6608 026562 000177 152320 JMP @SLPADR ;SW 14 OR 8 WAS SET
6609 026566 10$:
6610 026566 005237 001410 INC LPFLG
6611 026572 032777 001000 152340 BIT #SW9,@SWR ;LOOP ON ERROR?
6612 026600 001265 BNE 8$ ;YES, RECONDITION DRIVE
6613 026602 000137 025710 JMP 2$ ;RETURN TO MAINLINE
6614 026606 12$:
6615 026606 005237 001410 INC LPFLG
6616 026612 032777 001000 152320 BIT #SW9,@SWR ;LOOP ON ERROR?
6617 026620 001255 BNE 8$ ;YES, RECONDITION DRIVE
6618 026622 000137 026206 JMP 4$ ;RETURN TO MAINLINE
6619 *****
6620 *TEST 31 SINGLE INCREMENT SEEKS TO LAST CYL
6621 *
6622 * THIS TEST DOES SINGLE INCREMENT SEEKS OUT TO THE LAST CYL
6623 * WITHOUT ANY WRITING OR READING SO AS NOT TO INADVERTENTLY
6624 * DESTROY DATA.
6625 *
6626 *****
6627 026626 000004 TST31: SCOPE
6628 026630 012737 000001 001174 MOV #1,$TIMES ;:DO 1 ITERATION
6629 026636 012706 001100 MOV #STACK,SP ;:RESTORE STK PTR
6630
6631 026642 004737 045462 JSR PC,SUBCLR
6632 026646 104024 ERROR 24 ;:CERR AFTER SCLR
6633 026650 005037 001350 CLR FRCYL ;:FROM CYL
6634 026654 012737 000001 001352 MOV #1,TOCYL ;:TO CYL
6635 026662 012737 000001 001360 MOV #1,CALDIF ;:CALCULATED DIFF.
6636
6637 026670 1$:
6638 026670 104415 SCOP1
6639 026672 012706 001100 MOV #STACK,SP ;:RESTORE STK PTR
6640
6641 026676 004737 045462 JSR PC,SUBCLR
6642 026702 104024 ERROR 24 ;:CERR AFTER SCLR
6643
6644 026704 012737 027466 001176 MOV #10,$ESCAPE
6645 026712 013765 001352 000020 MOV TOCYL,RKDC(R5) ;:CYL TO SEEK TO
6646
6647 026720 012737 000017 003322 MOV #SEEK,HCS1
6648 026726 004737 043472 JSR PC,DOCMD ;:DO SEEK CMD & GET CONTR READY
```

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CZR6HFO UNIBUS RK6 DR PT1          MACY11 30(1046) 04-JAN-82 13:01 N 10 PAGE 131
CZR6HF.P11 04-JAN-82 12:44          T31      SINGLE INCREMENT SEEKS TO LAST CYL          SEQ 0130

6649 026732 104131                ERROR 131                ;NO RDY AFTER SEEK CMD
6650 026734 012737 030140 003412  MOV   #<D.PIP!D.SPIN!D.VV!D.DRA>,E.A0 ;EXPECTED A0
6651 026742 005037 003414                CLR   E.B0
6652 026746 012737 003720 003416  MOV   #<D.FWD!D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1
6653 026754 012737 000001 003420  MOV   #1,E.B1
6654
6655 026762 004737 044310                JSR   PC,CHKMSG          ;CHECK MSGS A0,B0,A1,B1
6656 026766 000003                .WORD T.A2!T.B2!0        ;& MSGS SPECIFIED HERE
6657 026770 104203                ERROR 203                ;MSG A0 ERROR DURING SEEK CMD
6658 026772 104204                ERROR 204                ;MSG B0 ERROR
6659 026774 104205                ERROR 205                ;MSG A1 ERROR
6660 026776 104206                ERROR 206                ;MSG B1 ERROR
6661
6662 027000 023727 001362 000001  CMP   CYLDIF,#1
6663 027006 001401                BEQ   2$
6664 027010 104212                ERROR 212                ;CYL DIFF INCORRECT DURING SEEK
6665
6666 027012 012737 027506 001176 2$:  MOV   #12$, $ESCAPE
6667 027020 013737 001422 003360  MOV   T2500,TEMP1        ;SETUP TIMEOUT
6668
6669 027026 004737 044176                JSR   PC,FATT2           ;FIND ATTN
6670 027032 104132                ERROR 132                ;NO ATTN AFTER SEEK CMD
6671 027034 032737 100000 003322  BIT   #CERR,HCS1
6672 027042 001401                BEQ   64$
6673 027044 104210                ERROR 210                ;CERR AFTER SEEK CMD
6674 027046                64$:
6675
6676 027046 012737 050340 003412  MOV   #<D.DSC!D.SPIN!D.DRDY!D.VV!D.DRA>,E.A0 ;EXPECTED MSG A0
6677 027054 005037 003414                CLR   E.B0                ;EXPECTED MSG B0
6678 027060 012737 001720 003416  MOV   #<D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1 ;EXPECTED A1
6679 027066 012737 000001 003420  MOV   #1,E.B1                ;MSG ID FOR EXPECTED MSG B1
6680 027074 005037 003422                CLR   E.A2                ;EXPECTED MSG A2
6681 027100 012737 000002 003424  MOV   #2,E.B2                ;MSG ID FOR EXPECTED MSG B2
6682 027106 012737 000003 003430  MOV   #3,E.B3                ;MSG ID FOR EXPECTED MSG B3
6683
6684 027114 004737 044310                JSR   PC,CHKMSG          ;CHECK MSGS A0,B0,A1,B1
6685 027120 000003                .WORD T.A2!T.B2!0        ;& MSGS SPECIFIED HERE
6686 027122 104133                ERROR 133                ;MSG A0 ERROR AFTER SEEK CMD
6687 027124 104134                ERROR 134                ;MSG B0 ERROR
6688 027126 104135                ERROR 135                ;MSG A1 ERROR
6689 027130 104136                ERROR 136                ;MSG B1 ERROR
6690 027132 005737 001362                TST   CYLDIF
6691 027136 001401                BEQ   65$
6692 027140 104137                ERROR 137                ;CYL DIFF NOT CLEARED AFTER SEEK CMD
6693
6694 027142                65$:
6695
6696 027142 012765 100000 000000  MOV   #CCLR,RKCS1(R5)
6697 027150 013765 001222 000010  MOV   $UNIT,RKCS2(R5) ;DRIVE#
6698 027156 012737 000005 003322  MOV   #CLEAR,HCS1
6699 027164 004737 043472                JSR   PC,DOCMD           ;DO DRIVE CLEAR CMD & GET CONTR RDY
6700 027170 104151                ERROR 151                ;NO RDY AFTER DRIVE CLEAR CMD
6701 027172 004737 044050                JSR   PC,TSTATN         ;TEST FOR ATTN
6702 027176 000401                BR    66$
6703 027200 104154                ERROR 154                ;ATTN NOT CLEARED AFTER DRIVE CLEAR CMD
6704 027202                66$:

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6817 027550 005037 003362 CLR TEMP2 ;SECTOR CTR
6818 027554 005037 003364 CLR TEMP3 ;0=22 SECTOR HARDWARE DETECTED TABLE
6819 ;1=22 SECTOR SOFTWARE DETECTED TABLE
6820 ;2=DONE
6821 027560 012737 002304 003366 MOV #BSE22H,TEMP4 ;STORE 22 SECTOR HARDWARE BSE INFO
6822 027566 013765 003366 000004 MOV TEMP4,RKBA(R5)
6823 027574 012737 001000 003370 MOV #1000,TEMP5 ;TRACK 2, SECTOR 0
6824 027602 013765 003370 000006 MOV TEMP5,RKDA(R5)
6825
6826 027610 013765 012100 000020 1$: MOV LC,RKDC(R5) ;LAST CYL
6827 027616 012765 177400 000002 MOV #-256,RKWC(R5) ;LOAD WORD CT
6828 027624 012737 000021 003322 MOV #RDATA,HCS1
6829 027632 004737 043530 JSR PC,DATCMD ;DO COMMAND
6830 027636 104226 ERROR 226 ;NO RDY AFTER READ DATA CMD
6831 027640 004737 045132 JSR PC,GSTAT ;GET FRESH STATUS
6832 027644 032737 100000 003322 BIT #CERR,HCS1
6833 027652 001470 BEQ 8$
6834 027654 104227 ERROR 227 ;CERR AFTER READ DATA CMD
6835
6836 027656 012737 010340 003412 MOV #<0!D.SPIN!D.DRDY!D.VV!D.DRA>,E.A0 ;EXPECTED MSG A0
6837 027664 005037 003414 CLR E.B0 ;EXPECTED MSG B0
6838 027670 012737 001720 003416 MOV #<D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1 ;EXPECTED A1
6839 027676 012737 000001 003420 MOV #1,E.B1 ;MSG ID FOR EXPECTED MSG B1
6840 027704 005037 003422 CLR E.A2 ;EXPECTED MSG A2
6841 027710 012737 000002 003424 MOV #2,E.B2 ;MSG ID FOR EXPECTED MSG B2
6842 027716 012737 000003 003430 MOV #3,E.B3 ;MSG ID FOR EXPECTED MSG B3
6843
6844 027724 004737 044310 JSR PC,CHKMSG ;CHECK MSGS A0,B0,A1,B1
6845 027730 000000 .WORD 0!0!0 ;8 MSGS SPECIFIED HERE
6846 027732 104051 ERROR 51 ;MSG A0 ERROR AFTER READ DATA CMD
6847 027734 104052 ERROR 52 ;MSG B0 ERROR
6848 027736 104112 ERROR 112 ;MSG A1 ERROR
6849 027740 104113 ERROR 113 ;MSG B1 ERROR
6850
6851 027742 004737 045462 JSR PC,SUBCLR
6852 027746 104024 ERROR 24 ;CERR AFTER SUBCLR
6853
6854 027750 005237 003362 INC TEMP2
6855 027754 023727 003362 000005 CMP TEMP2,#5 ;READ ALL 5 SECTORS?
6856 027762 001007 BNE 5$
6857 027764 005737 003364 TST TEMP3
6858 027770 001002 BNE 2$
6859 027772 104233 ERROR 233 ;CANT READ SECTORS 0,2,4,6,8
6860 027774 000414 BR 3$
6861 027776 104230 2$: ERROR 230 ;CANT READ SECTORS 10,12,14,16,18,20
6862 030000 000412 BR 3$
6863
6864 030002 013765 003366 000004 5$: MOV TEMP4,RKBA(R5) ;RESTORE TABLE ADDR
6865 030010 062737 000002 003370 ADD #2,TEMP5 ;SETUP TO READ 2 SECTORS FROM LAST
6866 030016 013765 003370 000006 MOV TEMP5,RKDA(R5)
6867 030024 000671 BR 1$
6868
6869 030026 005237 001456 3$: INC BSERR ;SET BSE FLAG
6870 030032 000454 BR TST33 ;GO TO NEXT TEST
6871
6872 030034 005737 002312 8$: TST BSE22H+6 ;TEST CARTRIDGE TYPE

```

```

6873 030040 001404          BEQ      9$          ;BRANCH IF DATA CARTRIDGE
6874 030042 104235          ERROR   235         ;ALIGNMENT CARTRIDGE USED
6875 030044 005237 001456  INC      BSERR       ;SET BSE ERROR FLAG
6876 030050 000426          BR       10$
6877
6878 030052 005237 003364  9$:    INC      TEMP3
6879 030056 023727 003364 000001  CMP     TEMP3,#1
6880 030064 001020          BNE     10$
6881 030066 005037 003362  CLR     TEMP2
6882 030072 012737 054474 003366  MOV     #BSE22S,TEMP4 ;STORE 22 SECTOR SOFTWARE BSE ADDR
6883 030100 013765 003366 000004  MOV     TEMP4,RKBA(R5)
6884 030106 012737 001012 003370  MOV     #1012,TEMP5   ;TRACK 2, SECTOR 12
6885 030114 013765 003370 000006  MOV     TEMP5,RKDA(R5)
6886 030122 000137 027610  JMP     1$           ;REPEAT
6887
6888 030126 005737 001216  10$:   TST     $PASS
6889 030132 001014          BNE     TST33        ;;GO TO NEXT TST IF NOT 1'ST PASS
6890 030134 104401 056407  TYPE    ,MSG17       ;CART SERIAL #
6891 030140 012746 002304  MOV     #BSE22H,-(SP)
6892 030144 004737 053700  JSR     PC,$DB20     ;CONVERT DBL BINARY WORD TO OCTAL
6893 030150 004737 054250  JSR     PC,$SUPRS    ;TYPE SERIAL #
6894 030154 104401 001205  TYPE    ,$CRLF
6895 030160 104401 001205  TYPE    ,$CRLF
6896
6897
6898
6899
6900
6901
6902
6903
6904
6905
6906
6907
6908

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```

*****
*TEST 33          DETECT INNER LIMIT
*
* THIS TEST VERIFIES THAT THE LAST CYL IN THE ABOVE
* TEST WAS 632 (1456) BY DETECTING INNER LIMIT AS THE ADJACENT CYL.
* IF THIS TEST FAILS, IT INDICATES THAT HEADS WERE NOT ON THE LAST CYL
* & THAT BSE INFO IS NOT VALID. THE FORMAT PACK TEST
* & ALL READ-WRITE TESTS ARE BYPASSED
* TO AVOID DESTROYING BSE INFO OR AN ALIGNMENT CARTRIDGE
* SINCE THERE IS A SEEKING OR LIMIT DETECTION PROBLEM.
*****

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6909 030164 000004          TST33: SCOPE
6910 030166 012737 000001 001174  MOV     #1,$TIMES    ;;DO 1 ITERATION
6911 030174 012706 001100  MOV     #STACK,SP   ;RESTORE STK PTR
6912
6913 030200 004737 045462  JSR     PC,SUBCLR   ;SUBSYS CLEAR & GET STATUS
6914 030204 104024  ERROR   24         ;CERR AFTER SCLR
6915
6916 030206 005037 001410  CLR     LPFLG
6917 030212 005237 001462  INC     BYPCERR     ;BYPASS CHECKING FOR ANY CERR IN GSTAT1
6918 030216 005237 003304  INC     UNLD        ;USED FOR VALID HALT
6919
6920 030222 012765 000020 000026  MOV     #PAT,RKMR1(R5) ;PARITY & WORD 0
6921 030230 013765 012076 000020  MOV     LCM1,RKDC(R5)
6922 030236 012737 000017 003322  MOV     #SEEK,HCS1
6923 030244 004737 043472  JSR     PC,DOCMD    ;DO SEEK CMD & GET CONTR READY
6924 030250 104122  ERROR   122        ;NO RDY FROM SEEK WITH BAD PARITY
6925 030252 004737 044050  JSR     PC,TSTATN   ;TEST FOR ATTN
6926 030256 104125  ERROR   125        ;NO ATTN FROM SEEK WITH BAD PARITY
6927 030260 012737 050340 003412  MOV     #<D.DSC!D.SPIN!D.DRDY!D.VV!D.DRA>,E.A0 ;EXPECTED A0
6928 030266 012737 001200 003414  MOV     #<D.FLT!D.PAR>,E.B0

```



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6929 030274 012737 001720 003416 MOV #<D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1
6930 030302 012737 000001 003420 MOV #1,E.B1
6931
6932 030310 004737 044310 JSR PC,CHKMSG ;CHECK MSGS A0,B0,A1,B1
6933 030314 000000 .WORD 0!0!0 ;& MSGS SPECIFIED HERE
6934 030316 104110 ERROR 110 ;MSG A0 ERROR AFTER SEEK WITH BAD PARITY
6935 030320 104111 ERROR 111 ;MSG B0 ERROR
6936 030322 104146 ERROR 146 ;MSG A1 ERROR
6937 030324 104147 ERROR 147 ;MSG B1 ERROR
6938
6939 030326 012765 100000 000000 MOV #CCLR,RKCS1(R5)
6940 030334 013765 001222 000010 MOV $UNIT,RKCS2(R5) ;DRIVE#
6941 030342 012737 000005 003322 MOV #CLEAR,HCS1
6942 030350 004737 043472 JSR PC,DOCMD ;DO DRIVE CLEAR CMD & GET CONTR RDY
6943 030354 104151 ERROR 151 ;NO RDY AFTER DRIVE CLEAR CMD
6944 030356 004737 044050 JSR PC,TSTATN ;TEST FOR ATTN
6945 030362 000401 BR 65$
6946 030364 104154 ERROR 154 ;ATTN NOT CLEARED AFTER DRIVE CLEAR CMD
6947 030366
6948
6949 030366 012737 010340 003412 MOV #<0!D.SPIN!D.DRDY!D.VV!D.DRA>,E.A0 ;EXPECTED MSG A0
6950 030374 005037 003414 CLR E.B0 ;EXPECTED MSG B0
6951 030400 012737 001720 003416 MOV #<D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1 ;EXPECTED A1
6952 030406 012737 000001 003420 MOV #1,E.B1 ;MSG ID FOR EXPECTED MSG B1
6953 030414 005037 003422 CLR E.A2 ;EXPECTED MSG A2
6954 030420 012737 000002 003424 MOV #2,E.B2 ;MSG ID FOR EXPECTED MSG B2
6955 030426 012737 000003 003430 MOV #3,E.B3 ;MSG ID FOR EXPECTED MSG B3
6956
6957 030434 004737 044310 JSR PC,CHKMSG ;CHECK MSGS A0,B0,A1,B1
6958 030440 000003 .WORD T.A2!T.B2!0 ;& MSGS SPECIFIED HERE
6959 030442 104273 ERROR 273 ;MSG A0 ERROR AFTER DRIVE CLEAR CMD
6960 030444 104265 ERROR 265 ;MSG B0 ERROR
6961 030446 104274 ERROR 274 ;MSG A1 ERROR
6962 030450 104266 ERROR 266 ;MSG B1 ERROR
6963
6964
6965 030452 013765 012100 000020 MOV LC,RKDC(R5)
6966 030460 012737 000017 003322 MOV #SEEK,HCS1
6967 030466 004737 043472 JSR PC,DOCMD ;DO SEEK CMD & GET CONTR READY
6968 030472 104131 ERROR 131 ;NO RDY AFTER SEEK CMD
6969 030474 012765 100000 000000 MOV #CCLR,RKCS1(R5)
6970 030502 004737 045132 JSR PC,GSTAT
6971 030506 004737 046316 JSR PC,FLIM ;FIND LIMIT DETECT
6972 030512 104160 ERROR 160 ;LIMIT DETECT NOT FOUND BEFORE TIMEOUT
6973
6974 030514 032737 040000 003350 BIT #D.UNLD,HMR2
6975 030522 001003 BNE 1$
6976 030524 104305 ERROR 305 ;DRIVE NOT UNLOADING AFTER LIMIT DETECT
6977 030526 000137 031366 JMP 30$ ;BYPASS REST OF TEST
6978
6979 030532 012737 031304 001176 1$: MOV #20$,SESCAPE ;MUST ESCAPE TO CYCLE UP DRIVE & TEST SWR
6980 030540 012737 070140 003412 MOV #<D.DSC!D.PIP!D.SPIN!D.VV!D.DRA>,E.A0 ;EXPECTED A0
6981 030546 012737 002200 003414 MOV #<D.SKI!D.FLT>,E.B0
6982 030554 012737 045720 003416 MOV #<D.UNLD!D.REV!D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1
6983 030562 012737 030001 003420 MOV #<D.LIMD!D.NMOV!1>,E.B1
6984 ; CHECK 161,162,163,164,<AFTER INNER LIMIT DETECT>,0,0,0

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6985	030570	004737	044310		JSR	PC,CHKMSG	:CHECK MESSAGE A0,B0,A1,B1
6986	030574	000000			.WORD	0!0!0	
6987	030576	104161			ERROR	161	:MAY BE A0 ERROR
6988	030600	104162			ERROR	162	:MAY BE B0 ERROR
6989	030602	104163			ERROR	163	:MAY BE B1 ERROR
6990	030604	000240			NOP		:NEED FOR THE CALLIN SEQ
6991	030606	032737	020000	003400	BIT	#D.LIMD,H.B1	:SEE IF LIMIT DETECT BIT SET IN B1
6992	030614	001003			BNE	3\$:BRANCH IF SO
6993	030616	104401	061436		TYPE	,EM42	
6994	030622	104164			ERROR	164	:OTHERWISE REPORT ERROR
6995							
6996	030624	004737	044050		3\$: JSR	PC,TSTATN	
6997	030630	104165			ERROR	165	:NO ATTN AFTER INNER LIMIT DETECT
6998	030632	005037	001462		CLR	BYPCERR	:ALLOW CHECKING CERR IN GSTAT1
6999							
7000	030636	004737	045462		JSR	PC,SUBCLR	:SUBSYS CLR
7001	030642	104024			ERROR	24	:CERR AFTER SCLR
7002	030644	013737	001414	003362	MOV	T10,TEMP2	:SET UP TIMEOUT
7003	030652	004737	046374		JSR	PC,FHDHM	:FIND HEAD HOME
7004	030656	104166			ERROR	166	:HEAD HOME NOT FOUND BEFORE TIMEOUT
7005	030660	004737	046450		JSR	PC,FLOAD	:FIND LOAD HEADS
7006	030664	104167			ERROR	167	:LOAD HEADS NOT FOUND BEFORE TIMEOUT
7007	030666	013737	001416	003362	MOV	T100,TEMP2	:SETUP TIMEOUT
7008	030674	004737	044102		JSR	PC,FATT1	:FIND ATTN
7009	030700	104067			ERROR	67	:ATTN NOT FOUND BEFORE TIMEOUT
7010	030702	005037	001176		2\$: CLR	\$ESCAPE	
7011	030706	005037	003304		CLR	UNLD	:CLEAR FLAG
7012							
7013	030712	012737	050340	003412	MOV	#<D.DSC!D.SPIN!D.DRDY!D.VV!D.DRA>,E.A0	:EXPECTED MSG A0
7014	030720	005037	003414		CLR	E.B0	:EXPECTED MSG B0
7015	030724	012737	001720	003416	MOV	#<D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1	:EXPECTED A1
7016	030732	012737	000001	003420	MOV	#1,E.B1	:MSG ID FOR EXPECTED MSG B1
7017	030740	005037	003422		CLR	E.A2	:EXPECTED MSG A2
7018	030744	012737	000002	003424	MOV	#2,E.B2	:MSG ID FOR EXPECTED MSG B2
7019	030752	012737	000003	003430	MOV	#3,E.B3	:MSG ID FOR EXPECTED MSG B3
7020							
7021	030760	004737	044310		JSR	PC,CHKMSG	:CHECK MSGS A0,B0,A1,B1
7022	030764	000003			.WORD	T.A2!T.B2!0	:& MSGS SPECIFIED HERE
7023	030766	104063			ERROR	63	:MSG A0 ERROR AT END OF HEAD LOADING
7024	030770	104064			ERROR	64	:MSG B0 ERROR
7025	030772	104065			ERROR	65	:MSG A1 ERROR
7026	030774	104066			ERROR	66	:MSG B1 ERROR
7027					CWD2	175,176,<AT END	:OF HEAD LOADING>
7028	030776	012765	000002	000026	MOV	#2,RKMR1(R5)	:SELECT MESSAGE
7029	031004	004737	045132		JSR	PC,GSTAT	:GET STATUS AND MR2,MR3
7030	031010	005737	001364		TST	CYLADD	:RECAL SUCCESSFUL ?
7031	031014	001401			BEQ	64\$:BRANCH IF SO
7032	031016	104050			ERROR	50	:REPORT ERROR
7033	031020				64\$:		
7034							
7035	031020	012765	100000	000000	MOV	#CLR,RKCS1(R5)	
7036	031026	013765	001222	000010	MOV	\$UNIT,RKCS2(R5)	:DRIVE#
7037	031034	012737	000005	003322	MOV	#CLEAR,HCS1	
7038	031042	004737	043472		JSR	PC,DOCMD	:DO DRIVE CLEAR CMD & GET CONTR RDY
7039	031046	104151			ERROR	151	:NO RDY AFTER DRIVE CLEAR CMD
7040	031050	004737	044050		JSR	PC,TSTATN	:TEST FOR ATTN


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7041 031054 000401 BR 66$
7042 031056 104154 ERROR 154 ;ATTN NOT CLEARED AFTER DRIVE CLEAR CMD
7043 031060 66$:
7044
7045 031060 012737 010340 003412 MOV #<0!D.SPIN!D.DRDY!D.VV!D.DRA>,E.A0 ;EXPECTED MSG A0
7046 031066 005037 003414 CLR E.B0 ;EXPECTED MSG B0
7047 031072 012737 001720 003416 MOV #<D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1 ;EXPECTED A1
7048 031100 012737 000001 003420 MOV #1,E.B1 ;MSG ID FOR EXPECTED MSG B1
7049 031106 005037 003422 CLR E.A2 ;EXPECTED MSG A2
7050 031112 012737 000002 003424 MOV #2,E.B2 ;MSG ID FOR EXPECTED MSG B2
7051 031120 012737 000003 003430 MOV #3,E.B3 ;MSG ID FOR EXPECTED MSG B3
7052
7053 031126 004737 044310 JSR PC,CHKMSG ;CHECK MSGS A0,B0,A1,B1
7054 031132 000003 .WORD T.A2!T.B2!0 ;& MSGS SPECIFIED HERE
7055 031134 104273 ERROR 273 ;MSG A0 ERROR AFTER DRIVE CLEAR CMD
7056 031136 104265 ERROR 265 ;MSG B0 ERROR
7057 031140 104274 ERROR 274 ;MSG A1 ERROR
7058 031142 104266 ERROR 266 ;MSG B1 ERROR
7059
7060 031144 004737 047466 JSR PC,SWTST ;SEE IF SW 14 OR 8 IS SET
7061 031150 000506 BR TST34 ;GO TO NEXT TEST
7062 ;RETURN HERE IF SW 14 IS SET OR
7063 ;SW 8 WITH SWR <7:0> APPLY
7064
7065
7066 031152 10$:
7067
7068 031152 004737 045462 JSR PC,SUBCLR
7069 031156 104024 ERROR 24 ;CERR AFTER SCRL
7070
7071 031160 013765 001352 000020 67$: MOV TOCYL,RKDC(R5) ;CYL#
7072
7073 031166 012737 000017 003322 MOV #SEEK,HCS1
7074 031174 004737 043472 JSR PC,DOCMD ;DO SEEK CMD & GET CONTR READY
7075 031200 104131 ERROR 131 ;NO RDY AFTER SEEK CMD.
7076
7077 031202 013737 001426 003360 MOV T50000,TEMP1
7078 031210 004737 044176 JSR PC,FATT2 ;FIND ATTN
7079 031214 104132 ERROR 132 ;NO ATTN AFTER SEEK CMD
7080 031216 032737 100000 003322 BIT #CERR,HCS1
7081 031224 001401 BEQ 69$
7082 031226 104210 ERROR 210 ;CERR AFTER SEEK CMD.
7083
7084 031230 004737 045462 69$: JSR PC,SUBCLR
7085 031234 104024 ERROR 24 ;CERR AFTER SCLR
7086
7087 031236 023737 001352 012100 CMP TOCYL,LC ;LAST CYL DONE?
7088 031244 001403 BEQ 68$ ;BR IF YES
7089 031246 005237 001352 INC TOCYL ;ELSE DO ANOTHER
7090 031252 000742 BR 67$
7091
7092 031254 004737 045462 68$: JSR PC,SUBCLR
7093 031260 104024 ERROR 24 ;CERR AFTER SCLR
7094
7095 031262 005037 001176 CLR $ESCAPE
7096 031266 005737 001410 TST LPFLG

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7097 031272 001402          BEQ      70$
7098 031274 000177 147610   JMP      @SLPERR      ;SW 9 WAS SET.
7099 031300 000177 147602   70$:    JMP      @SLPADR      ;SW 14 OR 8 WAS SET
7100
7101 031304          20$:
7102
7103 031304 004737 045462   JSR      PC,SUBCLR
7104 031310 104024          ERROR    24          ;CERR AFTER SCLR
7105
7106 031312 012737 000011 003322   MOV      #SRTSPL,HCS1
7107 031320 004737 043472   JSR      PC,DOCMD      ;DO START SPINDLE CMD & GET CONTR RDY
7108 031324 104121          ERROR    121         ;RDY NOT FOUND AFTER ST SPIN CMD.
7109
7110 031326 013737 001420 003362   MOV      T500,TEMP2    ;SETUP TIMEOUT
7111 031334 004737 044102   JSR      PC,FATT1      ;FIND ATTN
7112 031340 104067          ERROR    67          ;NO ATTN AFTER ST SPIN CMD.
7113
7114 031342 005037 003304          CLR      UNLD
7115 031346 005237 001410          INC      LPFLG
7116 031352 032777 001000 147560   BIT      #SW9,@SWR      ;LOOP ON ERROR?
7117 031360 001274          BNE      10$          ;YES, RECONDITION DRIVE
7118 031362 000137 030702          JMP      2$          ;RETURN TO MAINLINE
7119 031366          30$:
7120
7121 031366          FORM:
7122          :*****
7123          :*TEST 34          FORMAT PACK
7124          :*
7125          :*          THIS TEST FORMATS THE ENTIRE PACK IN 22 SECTOR FORMAT BY
7126          :*          DOING 1 CYL INCREMENTAL SEEKS
7127          :*          FROM 0 TO 632 (1456 FOR RK07) WITH WRITE HEADER CMDS (ALL TRACKS).
7128          :*          HEADERS WILL BE READ IN THE NEXT TEST
7129          :*
7130          :*****
7131 031366 000004          TST34: SCOPE
7132 031370 012737 000001 001174   MOV      #1,$TIMES      ;:DO 1 ITERATION
7133
7134
7135 031376 012706 001100          MOV      #STACK,SP
7136 031402 005737 001342          TST      MODTST
7137 031406 001402          BEQ      22$          ;:RESTORE STK PTR
7138 031410 104401 056522          TYPE     ,MSG20        ;:SEE IF MODULE TESTING
7139          ;:BR IF NO
7140 031414 005737 001460          22$:    TST      LIMERR      ;:RUNNING MODIFIED VERSION OF TEST
7141 031420 001403          BEQ      1$          ;:CHECK IF FOUND LIMIT DETECT ERROR
7142 031422 104170          ERROR    170         ;:FATAL ERROR
7143 031424 000137 042706          JMP      $EOP
7144 031430 005737 001456          1$:    TST      BSERR
7145 031434 001403          BEQ      2$          ;:ABORT BAL OF TESTS
7146 031436 104177          ERROR    177         ;:CHECK IF FOUND BSE INFO OK
7147 031440 000137 042706          JMP      $EOP
7148          ;:FORMAT TEST BYPASSED-BSE ERROR
7149 031444 004737 045462          2$:    JSR      PC,SUBCLR
7150 031450 104024          ERROR    24          ;:CERR AFTER SCLR
7151
7152 031452 104401 056222          TYPE     ,MSG12        ;:FORMATTING PACK, PLEASE WAIT

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7153
7154 031456 005037 001352 CLR TOCYL
7155
7156 031462 013737 001352 001366 MOV TOCYL,CALADD ;SETUP
7157 031470 012737 000000 001430 MOV #0,HEAD ;TO FILL
7158 031476 012737 000000 001436 MOV #0,FORMAT ;HEADER
7159 031504 004737 046572 JSR PC,FHDTAB ;TABLE
7160
7161
7162 031510 012765 001470 000004 9$: MOV #HDTAB,RKBA(R5) ;THIS SECTION
7163 031516 012765 177676 000002 MOV #-66.,RKWC(R5) ;OF CODE
7164 031524 000337 001430 SWAB HEAD ;IS TO RESTORE STANDARD FORMAT
7165 031530 013765 001430 000006 MOV HEAD,RKDA(R5) ;TO CYL 0
7166 031536 000337 001430 SWAB HEAD ;HEAD 0,1 & 2
7167
7168 031542 012737 000027 003322 MOV #<WRHEAD>,HCS1
7169 031550 004737 043530 JSR PC,DATCMD ;DO DATA XFER CMD & GET CONTR RDY
7170 031554 104200 ERROR 200 ;NO RDY AFTER WRITE HEADER CMD
7171 031556 004737 045132 JSR PC,GSTAT ;GET FRESH STATUS
7172 031562 032737 100000 003322 BIT #CERR,HCS1
7173 031570 001405 BEQ 64$
7174 031572 104201 ERROR 201 ;CERR AFTER WRITE HEADER CMD
7175 031574 104401 056436 TYPE ,MSG18 ;ABORTING BALANCE OF TESTS
7176 031600 000137 042706 JMP $EOP ;ABORT DRIVE
7177 031604 64$:
7178
7179 031604 005237 001430 INC HEAD
7180 031610 023727 001430 000003 CMP HEAD,#3
7181 031616 001403 BEQ 11$ ;BR IF ALL HEADS DONE
7182
7183 031620 004737 046572 JSR PC,FHDTAB
7184 031624 000731 BR 9$
7185
7186 031626 012737 000001 001366 11$: MOV #1,CALADD ;SETUP
7187 031634 005037 001430 CLR HEAD ;FOR
7188 031640 005037 001436 CLR FORMAT ;FHDTAB ROUTINE
7189
7190 031644 012737 000001 001360 MOV #1,CALDIF ;SETUP
7191 031652 005037 001350 CLR FRCYL ;FOR
7192 031656 012737 000001 001352 MOV #1,TOCYL ;ERROR REPORT
7193 ;START FORMATTING CYL 1 TO 410 HERE
7194
7195
7196 031664 3$:
7197 031664 104415 SCOP1
7198 031666 012706 001100 MOV #STACK,SP ;RESTORE STK PTR
7199
7200 031672 004737 045462 JSR PC,SUBCLR
7201 031676 104024 ERROR 24 ;CERR AFTER SCLR
7202
7203 031700 005737 001342 TST MODTST ;SEE IF MODULE TESTING
7204 031704 001404 BEQ 18$ ;BR IF NO
7205 031706 012737 033376 001176 MOV #16$,SESCAPE
7206 031714 000403 BR 19$
7207 031716 012737 033102 001176 18$: MOV #10$,SESCAPE
7208 031724 013765 001366 000020 19$: MOV CALADD,RKDC(R5) ;CYL #
    
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7209	031732	000337	001430		SWAB	HEAD	
7210	031736	013765	001430	000006	MOV	HEAD,RKDA(R5)	;HEAD #
7211	031744	000337	001430		SWAB	HEAD	
7212							
7213	031750	012737	000017	003322	MOV	#SEEK,HCS1	
7214	031756	004737	043472		JSR	PC,DOCMD	;DO SEEK CMD & GET CONTR READY
7215	031762	104131			ERROR	131	;NO RDY AFTER SEEK CMD
7216	031764	012737	030140	003412	MOV	#<D.PIP!D.SPIN!D.VV!D.DRA>,E.A0	;EXPECTED A0
7217	031772	005037	003414		CLR	E.B0	
7218	031776	012737	003720	003416	MOV	#<D.FWD!D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1	
7219	032004	012737	000001	003420	MOV	#1,E.B1	
7220							
7221	032012	004737	044310		JSR	PC,CHKMSG	;CHECK MSGS A0,B0,A1,B1
7222	032016	000003			.WORD	T.A2!T.B2!0	; & MSGS SPECIFIED HERE
7223	032020	104203			ERROR	203	;MSG A0 ERROR DURING SEEK CMD
7224	032022	104204			ERROR	204	;MSG B0 ERROR
7225	032024	104205			ERROR	205	;MSG A1 ERROR
7226	032026	104206			ERROR	206	;MSG B1 ERROR
7227							
7228	032030	023727	001362	000001	CMP	CYLDIF,#1	
7229	032036	001401			BEQ	4\$	
7230	032040	104212			ERROR	212	;CYL DIFF INCORRECT DURING SEEK
7231							
7232	032042	005737	001342		4\$: TST	MODTST	;SEE IF MODULE TESTING
7233	032046	001404			BEQ	20\$;BR IF NO
7234	032050	012737	033416	001176	MOV	#17\$, \$ESCAPE	
7235	032056	000403			BR	21\$	
7236							
7237	032060	012737	033122	001176	20\$: MOV	#12\$, \$ESCAPE	
7238	032066	012737	004704	003360	21\$: MOV	#2500.,TEMP1	;SETUP TIMEOUT
7239							
7240	032074	004737	044176		JSR	PC,FATT2	;FIND ATTN
7241	032100	104132			ERROR	132	;NO ATTN AFTER SEEK CMD
7242	032102	032737	100000	003322	BIT	#CERR,HCS1	
7243	032110	001401			BEQ	65\$	
7244	032112	104210			ERROR	210	;CERR AFTER SEEK CMD
7245	032114				65\$:		
7246							
7247	032114	012737	050340	003412	MOV	#<D.DSC!D.SPIN!D.DRDY!D.VV!D.DRA>,E.A0	;EXPECTED MSG A0
7248	032122	005037	003414		CLR	E.B0	;EXPECTED MSG B0
7249	032126	012737	001720	003416	MOV	#<D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1	;EXPECTED A1
7250	032134	012737	000001	003420	MOV	#1,E.B1	;MSG ID FOR EXPECTED MSG B1
7251	032142	005037	003422		CLR	E.A2	;EXPECTED MSG A2
7252	032146	012737	000002	003424	MOV	#2,E.B2	;MSG ID FOR EXPECTED MSG B2
7253	032154	012737	000003	003430	MOV	#3,E.B3	;MSG ID FOR EXPECTED MSG B3
7254							
7255	032162	004737	044310		JSR	PC,CHKMSG	;CHECK MSGS A0,B0,A1,B1
7256	032166	000003			.WORD	T.A2!T.B2!0	; & MSGS SPECIFIED HERE
7257	032170	104133			ERROR	133	;MSG A0 ERROR AFTER SEEK CMD
7258	032172	104134			ERROR	134	;MSG B0 ERROR
7259	032174	104135			ERROR	135	;MSG A1 ERROR
7260	032176	104136			ERROR	136	;MSG B1 ERROR
7261	032200	005737	001362		TST	CYLDIF	
7262	032204	001401			BEQ	66\$	
7263	032206	104137			ERROR	137	;CYL DIFF NOT CLEARED AFTER SEEK CMD
7264							


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7265 032210          66$:
7266
7267 032210 012765 100000 000000  MOV #CCLR,RKCS1(R5)
7268 032216 013765 001222 000010  MOV $UNIT,RKCS2(R5) ;DRIVE#
7269 032224 012737 000005 003322  MOV #CLEAR,HCS1
7270 032232 004737 043472  JSR PC,DOCMD ;DO DRIVE CLEAR CMD & GET CONTR RDY
7271 032236 104151  ERROR 151 ;NO RDY AFTER DRIVE CLEAR CMD
7272 032240 004737 044050  JSR PC,TSTATN ;TEST FOR ATTN
7273 032244 000401  BR 67$
7274 032246 104154  ERROR 154 ;ATTN NOT CLEARED AFTER DRIVE CLEAR CMD
7275 032250          67$:
7276
7277 032250 012737 010340 003412  MOV #<0!D.SPIN!D.DRDY!D.VV!D.DRA>,E.A0 ;EXPECTED MSG A0
7278 032256 005037 003414  CLR E.B0 ;EXPECTED MSG B0
7279 032262 012737 001720 003416  MOV #<D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1 ;EXPECTED A1
7280 032270 012737 000001 003420  MOV #1,E.B1 ;MSG ID FOR EXPECTED MSG B1
7281 032276 005037 003422  CLR E.A2 ;EXPECTED MSG A2
7282 032302 012737 000002 003424  MOV #2,E.B2 ;MSG ID FOR EXPECTED MSG B2
7283 032310 012737 000003 003430  MOV #3,E.B3 ;MSG ID FOR EXPECTED MSG B3
7284
7285 032316 004737 044310  JSR PC,CHKMSG ;CHECK MSGS A0,B0,A1,B1
7286 032322 000003  WORD T.A2!T.B2!0 ;& MSGS SPECIFIED HERE
7287 032324 104273  ERROR 273 ;MSG A0 ERROR AFTER DRIVE CLEAR CMD
7288 032326 104265  ERROR 265 ;MSG B0 ERROR
7289 032330 104274  ERROR 274 ;MSG A1 ERROR
7290 032332 104266  ERROR 266 ;MSG B1 ERROR
7291
7292 032334 023737 001364 001366  CMP CYLADD,CALADD
7293 032342 001401  BEQ 5$
7294 032344 104232  ERROR 232 ;CYL ADDR IN RKMR2 NOT=RKDC
7295
7296 032346          5$:
7297 032346 104415  SCOP1
7298 032350 012706 001100  MOV #STACK,SP ;RESTORE STK PTR
7299
7300 032354 004737 045462  JSR PC,SUBCLR
7301 032360 104024  ERROR 24 ;CERR AFTER SCLR
7302
7303 032362 005037 001176  CLR $ESCAPE
7304 032366 004737 046572  JSR PC,FHDTAB ;FILL HEADER TABLE
7305 032372 000337 001430  SWAB HEAD
7306 032376 013765 001430 000006  MOV HEAD,RKDA(R5) ;SET TRACK #
7307 032404 000337 001430  SWAB HEAD
7308 032410 012765 001470 000004  MOV #HDTAB,RKBA(R5) ;HEADER WORD TABLE
7309 032416 012765 177676 000002  MOV #-66.,RKWC(R5) ;WORD CT
7310 032424 013765 001366 000020  MOV CALADD,RKDC(R5) ;CYL #
7311
7312
7313 032432 012737 000027 003322  MOV #<WRHEAD>,HCS1
7314 032440 004737 043530  JSR PC,DATCMD ;DO DATA XFER CMD & GET CONTR RDY
7315 032444 104200  ERROR 200 ;NO RDY AFTER WRITE HEADER CMD
7316 032446 004737 045132  JSR PC,GSTAT ;GET FRESH STATUS
7317 032452 032737 100000 003322  BIT #CERR,HCS1
7318 032460 001405  BEQ 68$
7319 032462 104201  ERROR 201 ;CERR AFTER WRITE HEADER CMD
7320 032464 104401 056436  TYPE ,MSG18 ;ABORTING BALANCE OF TESTS

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7321 032470 000137 042706          JMP      $EOP          ;ABORT DRIVE
7322 032474          68$:
7323
7324 032474 012737 010340 003412      MOV      #<0!D.SPIN!D.DRDY!D.VV!D.DRA>,E.A0 ;EXPECTED MSG A0
7325 032502 005037 003414          CLR      E.B0          ;EXPECTED MSG B0
7326 032506 012737 001720 003416      MOV      #<D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1 ;EXPECTED A1
7327 032514 012737 000001 003420      MOV      #1,E.B1       ;MSG ID FOR EXPECTED MSG B1
7328 032522 005037 003422          CLR      E.A2          ;EXPECTED MSG A2
7329 032526 012737 000002 003424      MOV      #2,E.B2       ;MSG ID FOR EXPECTED MSG B2
7330 032534 012737 000003 003430      MOV      #3,E.B3       ;MSG ID FOR EXPECTED MSG B3
7331
7332 032542 004737 044310          JSR      PC,CHKMSG     ;CHECK MSGS A0,B0,A1,B1
7333 032546 000003          .WORD   T.A2!T.B2!0   ;& MSGS SPECIFIED HERE
7334 032550 104277          ERROR   277           ;MSG A0 ERROR AFTER WRITE HEADER CMD
7335 032552 104267          ERROR   267           ;MSG B0 ERROR
7336 032554 104300          ERROR   300           ;MSG A1 ERROR
7337 032556 104270          ERROR   270           ;MSG B1 ERROR
7338
7339
7340 032560 005737 001342          TST      MODTST        ;SEE IF MODULE TESTING
7341 032564 001402          BEQ      23$           ;BR IF NO
7342 032566 000137 033142          JMP      14$           ;ELSE RESTORE HEADERS ONLY
  
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7343									
7344	032572	005237	001430		23\$:	INC	HEAD		
7345	032576	023727	001430	000002		CMP	HEAD,#2		
7346	032604	001006				BNE	6\$		
7347	032606	023737	001366	012100		CMP	CALADD,LC		:HEAD 2, SEE IF ON LAST CYL
7348	032614	001002				BNE	6\$:DO NOT WRITE ON LAST CYL HEAD 2
7349	032616	000137	033042			JMP	7\$		
7350									
7351	032622	023727	001430	000003	6\$:	CMP	HEAD,#3		:ALL HEADS DONE?
7352	032630	001246				BNE	5\$:BR IF NO
7353	032632	005037	001430			CLR	HEAD		:ALL HEADS ON CYL DONE
7354	032636	005237	001366			INC	CALADD		:GO TO NEXT CYL
7355	032642	005237	001350			INC	FRCYL		:FOR ERROR REPORT
7356	032646	005237	001352			INC	TOCYL		:FOR ERROR REPORT
7357	032652	005737	003310			TST	HPEND		:SEE IF HALT PENDING
7358	032656	001002				BNE	24\$:BR IF YES
7359	032660	000137	031664			JMP	3\$:ELSE KEEP FORMATTING
7360									
7361	032664	005037	003310		24\$:	CLR	HPEND		:CLEAR FOR FUTURE FORMATTING
7362	032670	005037	003306			CLR	BADHDR		:HEADERS NOW OK
7363	032674	000137	047536			JMP	STOP		:GO & HALT THE CPU
7364									
7365	032700	005037	001366		8\$:	CLR	CALADD		
7366	032704	005037	001350			CLR	FRCYL		
7367									
7368	032710	004737	045462			JSR	PC,SUBCLR		
7369	032714	104024				ERROR	24		:CERR AFTER SCRL
7370									
7371	032716	013765	001352	000020	69\$:	MOV	TOCYL,RKDC(R5)		:CYL#
7372									
7373	032724	012737	000017	003322		MOV	#SEEK,HCS1		
7374	032732	004737	043472			JSR	PC,DOCMD		:DO SEEK CMD & GET CONTR READY
7375	032736	104131				ERROR	131		:NO RDY AFTER SEEK CMD.
7376									
7377	032740	013737	001426	003360		MOV	T50000,TEMP1		
7378	032746	004737	044176			JSR	PC,FATT2		:FIND ATTN
7379	032752	104132				ERROR	132		:NO ATTN AFTER SEEK CMD
7380	032754	032737	100000	003322		BIT	#CERR,HCS1		
7381	032762	001401				BEQ	71\$		
7382	032764	104210				ERROR	210		:CERR AFTER SEEK CMD.
7383									
7384	032766	004737	045462		71\$:	JSR	PC,SUBCLR		
7385	032772	104024				ERROR	24		:CERR AFTER SCLR
7386									
7387	032774	023737	001352	000000		CMP	TOCYL,0 ;LAST CYL DONE?		
7388	033002	001403				BEQ	70\$:BR IF YES
7389	033004	005337	001352			DEC	TOCYL		:ELSE DO ANOTHER
7390	033010	000742				BR	69\$		
7391									
7392	033012	004737	045462		70\$:	JSR	PC,SUBCLR		
7393	033016	104024				ERROR	24		:CERR AFTER SCLR
7394									
7395	033020	005037	001176			CLR	\$ESCAPE		
7396	033024	005737	001410			TST	LPFLG		
7397	033030	001402				BEQ	72\$		
7398	033032	000177	146052			JMP	@\$LPERR		:SW 9 WAS SET.

7399	033036	000177	146044		72\$:	JMP	@\$LPADR		;SW 14 OR 8 WAS SET
7400									
7401									
7402	033042	004737	050044		7\$:	JSR	PC,HPEN		;SEE IF HALT PENDING
7403	033046	000137	032664			JMP	24\$;RET HERE IF YES & EXIT
7404									;ELSE RET HERE
7405	033052	004737	047466			JSR	PC,SWTST		;SEE IF SW 14 OR 8 IS SET
7406	033056	000567				BR	TST35		;GO TO NEXT TEST
7407									;RETURN HERE IF SW 14 IS SET OR
7408									;SW 8 WITH SWR <7:0> APPLY
7409	033060	005037	001176			CLR	\$ESCAPE		
7410	033064	005737	001410			TST	LPFLG		
7411	033070	001402				BEQ	73\$		
7412	033072	000177	146012			JMP	@\$LPERR		;SW 9 WAS SET.
7413	033076	000177	146004		73\$:	JMP	@\$LPADR		;SW 14 OR 8 WAS SET
7414									
7415	033102				10\$:				
7416	033102	005237	001410			INC	LPFLG		
7417	033106	032777	001000	146024		BIT	#SW9,@SWR		;LOOP ON ERROR?
7418	033114	001271				BNE	8\$;YES, RECONDITION DRIVE
7419	033116	000137	032042			JMP	4\$;RETURN TO MAINLINE
7420									
7421	033122				12\$:				
7422	033122	005237	001410			INC	LPFLG		
7423	033126	032777	001000	146004		BIT	#SW9,@SWR		;LOOP ON ERROR?
7424	033134	001261				BNE	8\$;YES, RECONDITION DRIVE
7425	033136	000137	032346			JMP	5\$;RETURN TO MAINLINE
7426									
7427									
7428	033142	005237	001430		14\$:	INC	HEAD		
7429	033146	023727	001430	000003		CMP	HEAD,#3		;SEE IF ALL HEADS DONE
7430	033154	001402				BEQ	15\$;BR IF YES TO GO BACK TO CYL 0
7431	033156	000137	032346			JMP	5\$;ELSE REPEAT FOR NEXT HEAD
7432									
7433	033162	005065	000006		15\$:	CLR	RKDA(R5)		;SEEK TO CYL 0 & READ HEADERS
7434	033166	005037	001352			CLR	TOCYL		;TO RECONDITION DRIVE
7435									
7436	033172	012737	000017	003322		MOV	#SEEK,HCS1		
7437	033200	004737	043472			JSR	PC,DOCMD		;DO SEEK CMD & GET CONTR READY
7438	033204	104131				ERROR	131		;NO RDY AFTER SEEK CMD.
7439									
7440	033206	013737	001426	003360		MOV	T50000,TEMP1		
7441	033214	004737	044176			JSR	PC,FATT2		;FIND ATTN
7442	033220	104132				ERROR	132		;NO ATTN AFTER SEEK CMD
7443	033222	032737	100000	003322		BIT	#CERR,HCS1		
7444	033230	001401				BEQ	74\$		
7445	033232	104210				ERROR	210		;CERR AFTER SEEK CMD.
7446									
7447	033234	004737	045462		74\$:	JSR	PC,SUBCLR		
7448	033240	104024				ERROR	24		;CERR AFTER SCLR
7449									
7450									
7451									
7452	033242	012700	001674			MOV	#RHTAB,RO		
7453	033246	012737	000025	003322		MOV	#<RDHEAD>,HCS1		
7454	033254	004737	043530			JSR	PC,DATCMD		;DO DATA XFER CMD & GET CONTR RDY


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7455 033260 104171          ERROR 171          ;NO RDY AFTER READ HEADER CMD
7456 033262 032737 100000 003322 BIT   #CERR,HCS1
7457 033270 001405          BEQ   76$
7458 033272 104174          ERROR 174          ;CERR AFTER READ HEADER CMD
7459 033274 104401 056436 TYPE  ,MSG18       ;ABORT BALANCE OF TESTS
7460 033300 000137 042706 JMP   $EOP         ;ABORT DRIVE
7461
7462 033304 016520 000024 76$:  MOV  RKDB(R5),(R0)+ ;1'ST WORD FROM SILO TO RHTAB
7463 033310 016520 000024      MOV  RKDB(R5),(R0)+ ;2'ND WORD
7464 033314 016520 000024      MOV  RKDB(R5),(R0)+ ;3'RD WORD
7465
7466
7467 033320 032765 100000 000010 BIT   #DLT,RKCS2(R5)
7468 033326 001407          BEQ   77$
7469 033330 004737 045132 JSR  PC,GSTAT
7470 033334 104173          ERROR 173          ;DLT AFTER READ HEADER CMD
7471 033336 104401 056436 TYPE  ,MSG18       ;ABORTING BALANCE OF TESTS
7472 033342 000137 042706 JMP   $EOP         ;ABORT DRIVE
7473 033346          77$:
7474
7475 033346 023737 001674 001352 CMP  RHTAB,TOCYL  ;CHECK WORD 0 (CYL#) ONLY
7476 033354 001401          BEQ   75$
7477 033356 104310          ERROR 310          ;BR IF SAME
7478 033360          75$:
7479
7480 033360 004737 050044 JSR  PC,HPEN      ;SEE IF HALT PENDING
7481 033364 000137 032664 JMP   24$        ;RET HERE IF YES
7482
7483 033370 004737 047466 JSR  PC,SWTST    ;ELSE RET HERE & EXIT
7484 033374 000420 BR    TST35      ;SEE IF SW 14 OR 8 IS SET
7485
7486
7487 033376          16$:
7488 033376 005237 001410 INC  LPFLG
7489 033402 032777 001000 145530 BIT   #SW9,@SWR   ;LOOP ON ERROR?
7490 033410 001264          BNE  15$         ;YES, RECONDITION DRIVE
7491 033412 000137 032042 JMP   4$         ;RETURN TO MAINLINE
7492 033416          17$:
7493 033416 005237 001410 INC  LPFLG
7494 033422 032777 001000 145510 BIT   #SW9,@SWR   ;LOOP ON ERROR?
7495 033430 001254          BNE  15$         ;YES, RECONDITION DRIVE
7496 033432 000137 032346 JMP   5$         ;RETURN TO MAINLINE
7497
7498
7499 033436          13$:
7500
7501
7502
7503
7504
7505
7506
7507
7508
7509
7510 033436 000004

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:*****
:*TEST 35      DECREMENT FROM LAST CYL TO 0 & READ HEADERS
:*
:*          THIS TEST VERIFIES MOTION IN THE NEGATIVE DIRECTION BY
:*          SINGLE CYL INCREMENTAL SEEKS.
:*
:*****
TST35: SCOPE

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CZR6HFO UNIBUS RK6 DR PT1 MACY11 30(1046) 04-JAN-82 13:01 DECREMENT FROM LAST CYL TO 0 & READ HEADERS SEQ 0146
CZR6HF.P11 04-JAN-82 12:44 T35

7511	033440	012737	000001	001174	MOV	#1,\$TIMES	::DO 1 ITERATION
7512	033446	012706	001100		MOV	#STACK,SP	:RESTORE STK PTR
7513							
7514	033452	104401	056751		TYPE	,MSG22	:FORMATTING FINISHED
7515	033456	005737	001342		TST	MODTST	:SEE IF MODULE TESTING
7516	033462	001404			BEQ	5\$:BR IF NO
7517	033464	104401	056671		TYPE	,MSG21	:BYP TESTS 36,40,41
7518	033470	000137	034514		JMP	13\$	
7519	033474	013737	012100	001350	MOV	LC,FRCYL	:FROM CYL
7520	033502	013737	012076	001352	MOV	LCM1,TOCYL	:TO CYL
7521							
7522	033510						
7523	033510	104415			SCOP1		
7524	033512	012706	001100		MOV	#STACK,SP	:RESTORE STK PTR
7525							
7526	033516	004737	045462		JSR	PC,SUBCLR	
7527	033522	104024			ERROR	24	:CERR AFTER SCLR
7528							
7529	033524	012737	034454	001176	MOV	#10\$,SESCAPE	
7530	033532	013765	001352	000020	MOV	TOCYL,RKDC(R5)	:CYL #
7531							
7532	033540	012737	000017	003322	MOV	#SEEK,HCS1	
7533	033546	004737	043472		JSR	PC,DOCMD	:DO SEEK CMD & GET CONTR READY
7534	033552	104131			ERROR	131	:NO RDY AFTER SEEK CMD
7535	033554	012737	030140	003412	MOV	#<D.PIP!D.SPIN!D.VV!D.DRA>,E.A0	:EXPECTED A0
7536	033562	005037	003414		CLR	E.B0	
7537	033566	012737	005720	003416	MOV	#<D.REV!D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1	
7538	033574	012737	000001	003420	MOV	#1,E.B1	
7539							
7540	033602	004737	044310		JSR	PC,CHKMSG	:CHECK MSGS A0,B0,A1,B1
7541	033606	000003			.WORD	T.A2!T.B2!0	:& MSGS SPECIFIED HERE
7542	033610	104203			ERROR	203	:MSG A0 ERROR DURING SEEK CMD
7543	033612	104204			ERROR	204	:MSG B0 ERROR
7544	033614	104205			ERROR	205	:MSG A1 ERROR
7545	033616	104206			ERROR	206	:MSG B1 ERROR
7546							
7547	033620	023727	001362	000001	CMP	CYLDIF,#1	
7548	033626	001406			BEQ	2\$	
7549	033630	012765	000002	000026	MOV	#2,RKMR1(R5)	:SELECT WD 2
7550	033636	004737	045132		JSR	PC,GSTAT	
7551	033642	104212			ERROR	212	:CYL DIFF INCORRECT DURING SEEK
7552							
7553	033644	012737	034474	001176	MOV	#12\$,SESCAPE	
7554	033652	012737	004704	003360	MOV	#2500.,TEMP1	:SETUP TIMEOUT
7555							
7556	033660	004737	044176		JSR	PC,FATT2	:FIND ATTN
7557	033664	104132			ERROR	132	:NO ATTN AFTER SEEK CMD
7558	033666	032737	100000	003322	BIT	#CERR,HCS1	
7559	033674	001401			BEQ	64\$	
7560	033676	104210			ERROR	210	:CERR AFTER SEEK CMD
7561	033700						
7562							
7563	033700	012737	050340	003412	MOV	#<D.DSC!D.SPIN!D.DRDY!D.VV!D.DRA>,E.A0	:EXPECTED MSG A0
7564	033706	005037	003414		CLR	E.B0	:EXPECTED MSG B0
7565	033712	012737	001720	003416	MOV	#<D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1	:EXPECTED A1
7566	033720	012737	000001	003420	MOV	#1,E.B1	:MSG ID FOR EXPECTED MSG B1

7623									
7624	034160	012700	001674		MOV	#RHTAB,RO			
7625	034164	012737	000025	003322	MOV	#<RDHEAD>,HCS1			
7626	034172	004737	043530		JSR	PC,DATCMD		:DO DATA XFER CMD & GET CONTR RDY	
7627	034176	104171			ERROR	171		:NO RDY AFTER READ HEADER CMD	
7628	034200	032737	100000	003322	BIT	#CERR,HCS1			
7629	034206	001405			BEQ	68\$			
7630	034210	104174			ERROR	174		:CERR AFTER READ HEADER CMD	
7631	034212	104401	056436		TYPE	,MSG18		:ABORT BALANCE OF TESTS	
7632	034216	000137	042706		JMP	\$EOP		:ABORT DRIVE	
7633									
7634	034222	016520	000024		68\$: MOV	RKDB(R5),(R0)+		:1'ST WORD FROM SILO TO RHTAB	
7635	034226	016520	000024		MOV	RKDB(R5),(R0)+		:2'ND WORD	
7636	034232	016520	000024		MOV	RKDB(R5),(R0)+		:3'RD WORD	
7637									
7638									
7639	034236	032765	100000	000010	BIT	#DLT,RKCS2(R5)			
7640	034244	001407			BEQ	69\$			
7641	034246	004737	045132		JSR	PC,GSTAT			
7642	034252	104173			ERROR	173		:DLT AFTER READ HEADER CMD	
7643	034254	104401	056436		TYPE	,MSG18		:ABORTING BALANCE OF TESTS	
7644	034260	000137	042706		JMP	\$EOP		:ABORT DRIVE	
7645	034264				69\$:				
7646									
7647	034264	023737	001674	001352	CMP	RHTAB,TOCYL		:CHECK WORD 0 (CYL#) ONLY	
7648	034272	001401			BEQ	67\$:BR IF SAME	
7649	034274	104310			ERROR	310		:READ CYL WORD HEADER ERROR	
7650	034276				67\$:				
7651									
7652	034276	005337	001350		DEC	FRCYL			
7653	034302	001404			BEQ	4\$			
7654	034304	005337	001352		DEC	TOCYL			
7655	034310	000137	033510		JMP	1\$			
7656									
7657	034314				4\$:				
7658	034314	004737	047466		JSR	PC,SWTST		:SEE IF SW 14 OR 8 IS SET	
7659	034320	000475			BR	TST36		:GO TO NEXT TEST	
7660								:RETURN HERE IF SW 14 IS SET OR	
7661								:SW 8 WITH SWR <7:0> APPLY	
7662									
7663									
7664	034322				6\$:				
7665									
7666	034322	004737	045462		JSR	PC,SUBCLR			
7667	034326	104024			ERROR	24		:CERR AFTER SCRL	
7668									
7669	034330	013765	001352	000020	70\$: MOV	TOCYL,RKDC(R5)		:CYL#	
7670									
7671	034336	012737	000017	003322	MOV	#SEEK,HCS1			
7672	034344	004737	043472		JSR	PC,DOCMD		:DO SEEK CMD & GET CONTR READY	
7673	034350	104131			ERROR	131		:NO RDY AFTER SEEK CMD.	
7674									
7675	034352	013737	001426	003360	MOV	T50000,TEMP1			
7676	034360	004737	044176		JSR	PC,FATT2		:FIND ATTN	
7677	034364	104132			ERROR	132		:NO ATTN AFTER SEEK CMD	
7678	034366	032737	100000	003322	BIT	#CERR,HCS1			

7679	034374	001401			BEQ	72\$		
7680	034376	104210			ERROR	210		:CERR AFTER SEEK CMD.
7681								
7682	034400	004737	045462		72\$:	JSR	PC,SUBCLR	
7683	034404	104024			ERROR	24		:CERR AFTER SCLR
7684								
7685	034406	023737	001352	012100	CMP	TOCYL,LC		:LAST CYL DONE?
7686	034414	001403			BEQ	71\$:BR IF YES
7687	034416	005237	001352		INC	TOCYL		:ELSE DO ANOTHER
7688	034422	000742			BR	70\$		
7689								
7690	034424	004737	045462		71\$:	JSR	PC,SUBCLR	
7691	034430	104024			ERROR	24		:CERR AFTER SCLR
7692								
7693	034432	005037	001176		CLR	\$ESCAPE		
7694	034436	005737	001410		TST	LPFLG		
7695	034442	001402			BEQ	73\$		
7696	034444	000177	144440		JMP	@\$LPERR		:SW 9 WAS SET.
7697	034450	000177	144432		73\$:	JMP	@\$LPADR	:SW 14 OR 8 WAS SET
7698								
7699								
7700								
7701	034454				10\$:			
7702	034454	005237	001410		INC	LPFLG		
7703	034460	032777	001000	144452	BIT	#SW9,@SWR		:LOOP ON ERROR?
7704	034466	001315			BNE	6\$:YES, RECONDITION DRIVE
7705	034470	000137	033644		JMP	2\$:RETURN TO MAINLINE
7706								
7707	034474				12\$:			
7708	034474	005237	001410		INC	LPFLG		
7709	034500	032777	001000	144432	BIT	#SW9,@SWR		:LOOP ON ERROR?
7710	034506	001305			BNE	6\$:YES, RECONDITION DRIVE
7711	034510	000137	034132		JMP	3\$:RETURN TO MAINLINE
7712								
7713	034514				13\$:			
7714								
7715								
7716								
7717								
7718								
7719								
7720								
7721								
7722								
7723								
7724								
7725								
7726								
7727								
7728								
7729								
7730								
7731								
7732								
7733								
7734	034514	000004						

```
*****
*TEST 36      SEEK FROM CYL 0 TO ALL MAJOR CYLS & READ HEADERS
*
* THIS TEST SEEKS FROM CYL 0 TO ALL THE MAJOR CYLS & READS HEADERS.
* IT THEN SEEKS CYL 0 & READS HEADERS.
*
* MAJOR CYLS ARE: 1 (DECIMAL) = 1 (OCTAL)
*                   2           2
*                   4           4
*                   8           10
*                  16           20
*                  32           40
*                  64           100
*                 128           200
*                 256           400
*                 512           1000 (RK07)
*****
TST36: SCOPE
```

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CZR6MFO UNIBUS RK6 DR PT1          MACY11 30(1046) 04-JAN-82 13:01 H 12
CZR6HF.P11 04-JAN-82 12:44         T36          SEEK FROM CYL 0 TO ALL MAJOR CYLS & READ HEADERS PAGE 151
                                                                    SEQ 0150

7735 034516 012737 000001 001174  MOV      #1,$TIMES      ;;DO 1 ITERATION
7736 034524 012706 001100          MOV      #STACK,SP      ;RESTORE STK PTR
7737                                     ;
7738 034530 013737 012116 001350  MOV      FC,FRCYL      ;SETUP FROM CYL
7739 034536 013737 012120 001352  MOV      FCP1,TOCYL    ;SETUP TO CYL
7740                                     ;
7741 034544                                     1$:
7742 034544 104415                                     SCOP1
7743 034546 012706 001100          MOV      #STACK,SP      ;RESTORE STK PTR
7744                                     ;
7745 034552 004737 045462          JSR      PC,SUBCLR     ;
7746 034556 104024          ERROR   24            ;CERR AFTER SCLR
7747                                     ;
7748 034560 012737 036266 001176  MOV      #10$, $ESCAPE
7749 034566 013737 001350 003364  MOV      FRCYL,TEMP3   ;SETUP
7750 034574 013737 001352 003366  MOV      TOCYL,TEMP4   ;CYL DIFF
7751 034602 163737 003364 003366  SUB      TEMP3,TEMP4   ;FOR
7752 034610 013737 003366 001360  MOV      TEMP4,CALDIF  ;ERROR PRINTOUT
7753                                     ;
7754 034616 013765 001352 000020  MOV      TOCYL,RKDC(R5) ;GO TO CYL #
7755                                     ;
7756 034624 012737 000017 003322  MOV      #SEEK,HCS1
7757 034632 004737 043472          JSR      PC,DOCMD      ;DO SEEK CMD & GET CONTR READY
7758 034636 104131          ERROR   131          ;NO RDY AFTER SEEK CMD
7759 034640 012737 030140 003412  MOV      #<D.PIP!D.SPIN!D.VV!D.DRA>,E.A0 ;EXPECTED A0
7760 034646 005037 003414          CLR      E.B0
7761 034652 012737 003720 003416  MOV      #<D.FWD!D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1
7762 034660 012737 000001 003420  MOV      #1,E.B1
7763                                     ;
7764 034666 004737 044310          JSR      PC,CHKMSG    ;CHECK MSGS A0,B0,A1,B1
7765 034672 000003          .WORD   T.A2!T.B2!0 ;& MSGS SPECIFIED HERE
7766 034674 104203          ERROR   203          ;MSG A0 ERROR DURING SEEK CMD
7767 034676 104204          ERROR   204          ;MSG B0 ERROR
7768 034700 104205          ERROR   205          ;MSG A1 ERROR
7769 034702 104206          ERROR   206          ;MSG B1 ERROR
7770                                     ;
7771 034704 012737 036306 001176 2$:  MOV      #12$, $ESCAPE
7772 034712 013737 001426 003360  MOV      T5000,TEMP1   ;SETUP TIMEOUT
7773                                     ;
7774 034720 004737 044176          JSR      PC,FATT2     ;FIND A.TN
7775 034724 104132          ERROR   132          ;NO ATTN AFTER SEEK CMD
7776 034726 032737 100000 003322  BIT      #CERR,HCS1
7777 034734 001401          BEQ     64$
7778 034736 104210          ERROR   210          ;CERR AFTER SEEK CMD
7779 034740                                     64$:
7780                                     ;
7781 034740 012737 050340 003412  MOV      #<D.DSC!D.SPIN!D.DRDY!D.VV!D.DRA>,E.A0 ;EXPECTED MSG A0
7782 034746 005037 003414          CLR      E.B0        ;EXPECTED MSG B0
7783 034752 012737 001720 003416  MOV      #<D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1 ;EXPECTED A1
7784 034760 012737 000001 003420  MOV      #1,E.B1      ;MSG ID FOR EXPECTED MSG B1
7785 034766 005037 003422          CLR      E.A2        ;EXPECTED MSG A2
7786 034772 012737 000002 003424  MOV      #2,E.B2      ;MSG ID FOR EXPECTED MSG B2
7787 035000 012737 000003 003430  MOV      #3,E.B3      ;MSG ID FOR EXPECTED MSG B3
7788                                     ;
7789 035006 004737 044310          JSR      PC,CHKMSG    ;CHECK MSGS A0,B0,A1,B1
7790 035012 000003          .WORD   T.A2!T.B2!0 ;& MSGS SPECIFIED HERE

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7791	035014	104133			ERROR	133		:MSG A0 ERROR AFTER SEEK CMD
7792	035016	104134			ERROR	134		:MSG B0 ERROR
7793	035020	104135			ERROR	135		:MSG A1 ERROR
7794	035022	104136			ERROR	136		:MSG B1 ERROR
7795	035024	005737	001362		TST	CYLDIF		
7796	035030	001401			BEQ	65\$		
7797	035032	104137			ERROR	137		:CYL DIFF NOT CLEARED AFTER SEEK CMD
7798								
7799	035034						65\$:	
7800								
7801	035034	012765	100000	000000	MOV	#CCLR,RKCS1(R5)		
7802	035042	013765	001222	000010	MOV	SUNIT,RKCS2(R5)	:DRIVE#	
7803	035050	012737	000005	003322	MOV	#CLEAR,HCS1		
7804	035056	004737	043472		JSR	PC,DOCMD		:DO DRIVE CLEAR CMD & GET CONTR RDY
7805	035062	104151			ERROR	151		:NO RDY AFTER DRIVE CLEAR CMD
7806	035064	004737	044050		JSR	PC,TSTATN		:TEST FOR ATTN
7807	035070	000401			BR	66\$		
7808	035072	104154			ERROR	154		:ATTN NOT CLEARED AFTER DRIVE CLEAR CMD
7809	035074						66\$:	
7810								
7811	035074	012737	010340	003412	MOV	#<0!D.SPIN!D.DRDY!D.VV!D.DRA>,E.A0		:EXPECTED MSG A0
7812	035102	005037	003414		CLR	E.B0		:EXPECTED MSG B0
7813	035106	012737	001720	003416	MOV	#<D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1		:EXPECTED A1
7814	035114	012737	000001	003420	MOV	#1,E.B1		:MSG ID FOR EXPECTED MSG B1
7815	035122	005037	003422		CLR	E.A2		:EXPECTED MSG A2
7816	035126	012737	000002	003424	MOV	#2,E.B2		:MSG ID FOR EXPECTED MSG B2
7817	035134	012737	000003	003430	MOV	#3,E.B3		:MSG ID FOR EXPECTED MSG B3
7818								
7819	035142	004737	044310		JSR	PC,CHKMSG		:CHECK MSGS A0,B0,A1,B1
7820	035146	000003			.WORD	T.A2!T.B2!0		:& MSGS SPECIFIED HERE
7821	035150	104273			ERROR	273		:MSG A0 ERROR AFTER DRIVE CLEAR CMD
7822	035152	104265			ERROR	265		:MSG B0 ERROR
7823	035154	104274			ERROR	274		:MSG A1 ERROR
7824	035156	104266			ERROR	266		:MSG B1 ERROR
7825								
7826	035160	023737	001364	001352	CMP	CYLADD,TOCYL		
7827	035166	001401			BEQ	3\$		
7828	035170	104207			ERROR	207		:CYL ADDR IN RKMR3 NOT=RKDC
7829								
7830	035172						3\$:	
7831	035172	104415			SCOP1			
7832	035174	012706	001100		MOV	#STACK,SP		:RESTORE STK PTR
7833								
7834	035200	004737	045462		JSR	PC,SUBCLR		
7835	035204	104024			ERROR	24		:CERR AFTER SCLR
7836								
7837	035206	005037	001176		CLR	\$ESCAPE		
7838	035212	013765	001352	000020	MOV	TOCYL,RKDC(R5)	:CYL #	
7839								
7840								
7841	035220	012700	001674		MOV	#RHTAB,RO		
7842	035224	012737	000025	003322	MOV	#<RDHEAD>,HCS1		
7843	035232	004737	043530		JSR	PC,DATCMD		:DO DATA XFER CMD & GET CONTR RDY
7844	035236	104171			ERROR	171		:NO RDY AFTER READ HEADER CMD
7845	035240	032737	100000	003322	BIT	#CERR,HCS1		
7846	035246	001405			BEQ	68\$		

J 12

CZR6HFO UNIBUS RK6 DR PT1 MACY11 30(1046) 04-JAN-82 13:01 PAGE 153
CZR6HF.P11 04-JAN-82 12:44 T36 SEEK FROM CYL 0 TO ALL MAJOR CYLS & READ HEADERS SEQ 0152

7847	035250	104174			ERROR	174		:CERR AFTER READ HEADER CMD
7848	035252	104401	056436		TYPE	,MSG18		:ABORT BALANCE OF TESTS
7849	035256	000137	042706		JMP	\$EOP		:ABORT DRIVE
7850								
7851	035262	016520	000024	68\$:	MOV	RKDB(R5),(R0)+		:1'ST WORD FROM SILO TO RHTAB
7852	035266	016520	000024		MOV	RKDB(R5),(R0)+		:2'ND WORD
7853	035272	016520	000024		MOV	RKDB(R5),(R0)+		:3'RD WORD
7854								
7855								
7856	035276	032765	100000	000010	BIT	#DLT,RKCS2(R5)		
7857	035304	001407			BEQ	69\$		
7858	035306	004737	045132		JSR	PC,GSTAT		
7859	035312	104173			ERROR	173		:DLT AFTER READ HEADER CMD
7860	035314	104401	056436		TYPE	,MSG18		:ABORTING BALANCE OF TESTS
7861	035320	000137	042706		JMP	\$EOP		:ABORT DRIVE
7862	035324			69\$:				
7863								
7864	035324	023737	001674	001352	CMP	RHTAB,TOCYL		:CHECK WORD 0 (CYL#) ONLY
7865	035332	001401			BEQ	67\$:BR IF SAME
7866	035334	104310			ERROR	310		:READ CYL WORD HEADER ERROR
7867	035336			67\$:				
7868								
7869								
7870	035336	104415			SCOP1			
7871	035340	012706	001100		MOV	#STACK,SP		:RESTORE STK PTR
7872								
7873	035344	004737	045462		JSR	PC,SUBCLR		
7874	035350	104024			ERROR	24		:CERR AFTER SCLR
7875								
7876	035352	012737	036326	001176	MOV	#14\$, \$ESCAPE		
7877	035360	013765	001350	000020	MOV	FRCYL,RKDC(R5)		:RETURN TO CYL #
7878	035366	013737	001350	001354	MOV	FRCYL,CCYL		:CURRENT CYL FOR TRUERROR ROUTINE
7879								
7880	035374	012737	000017	003322	MOV	#SEEK,HCS1		
7881	035402	004737	043472		JSR	PC,DOCMD		:DO SEEK CMD & GET CONTR READY
7882	035406	104131			ERROR	131		:NO RDY AFTER SEEK CMD
7883	035410	012737	030140	003412	MOV	#<D.PIP!D.SPIN!D.VV!D.DRA>,E.A0		:EXPECTED A0
7884	035416	005037	003414		CLR	E.B0		
7885	035422	012737	005720	003416	MOV	#<D.REV!D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1		
7886	035430	012737	000001	003420	MOV	#1,E.B1		
7887								
7888	035436	004737	044310		JSR	PC,CHKMSG		:CHECK MSGS A0,B0,A1,B1
7889	035442	000003			.WORD	T.A2!T.B2!0		: & MSGS SPECIFIED HERE
7890	035444	104203			ERROR	203		:MSG A0 ERROR DURING SEEK CMD
7891	035446	104204			ERROR	204		:MSG B0 ERROR
7892	035450	104205			ERROR	205		:MSG A1 ERROR
7893	035452	104206			ERROR	206		:MSG B1 ERROR
7894								
7895								
7896	035454	012737	036346	001176	MOV	#16\$, \$ESCAPE		
7897	035462	013737	001426	003360	MOV	T50000,TEMP1		:SETUP TIMEOUT
7898								
7899	035470	004737	044176		JSR	PC,FATT2		:FIND ATTN
7900	035474	104132			ERROR	132		:NO ATTN AFTER SEEK CMD
7901	035476	032737	100000	003322	BIT	#CERR,HCS1		
7902	035504	001401			BEQ	70\$		


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CZR6HF0 UNIBUS RK6 DR PT1          MACY11 30(1046) 04-JAN-82 13:01 L 12
CZR6HF.P11 04-JAN-82 12:44          T36        SEEK FROM CYL 0 TO ALL MAJOR CYLS & READ HEADERS PAGE 155
                                                    SEQ 0154
7959 035750 004737 045462          JSR    PC,SUBCLR
7960 035754 104024          ERROR  24          ;CERR AFTER SCLR
7961
7962 035756 005037 001176          CLR
7963 035762 013765 001350 000020  MOV    $ESCAPE
7964          MOV    FRCYL,RKDC(R5) ;CYL #
7965
7966 035770 012700 001674          MOV
7967 035774 012737 000025 003322  MOV    #RHTAB,R0
7968 036002 004737 043530          MOV    #<RDHEAD>,HCS1
7969 036006 104171          JSR    PC,DATCMD ;DO DATA XFER CMD & GET CONTR RDY
7970 036010 032737 100000 003322  ERROR  171 ;NO RDY AFTER READ HEADER CMD
7971 036016 001405          BIT    #CERR,HCS1
7972 036020 104174          BEQ    74$
7973 036022 104401 056436          ERROR  174 ;CERR AFTER READ HEADER CMD
7974 036026 000137 042706          TYPE  ,MSG18 ;ABORT BALANCE OF TESTS
7975          JMP    $EOP ;ABORT DRIVE
7976 036032 016520 000024          74$: MOV    RKDB(R5),(R0)+ ;1'ST WORD FROM SILO TO RHTAB
7977 036036 016520 000024          MOV    RKDB(R5),(R0)+ ;2'ND WORD
7978 036042 016520 000024          MOV    RKDB(R5),(R0)+ ;3'RD WORD
7979
7980
7981 036046 032765 100000 000010  BIT    #DLT,RKCS2(R5)
7982 036054 001407          BEQ    75$
7983 036056 004737 045132          JSR    PC,GSTAT
7984 036062 104173          ERROR  173 ;DLT AFTER READ HEADER CMD
7985 036064 104401 056436          TYPE  ,MSG18 ;ABORTING BALANCE OF TESTS
7986 036070 000137 042706          JMP    $EOP ;ABORT DRIVE
7987 036074          75$:
7988
7989 036074 023737 001674 001350  CMP    RHTAB,FRCYL ;CHECK WORD 0 (CYL#) ONLY
7990 036102 001401          BEQ    73$ ;BR IF SAME
7991 036104 104311          ERROR  311 ;READ CYL WORD HEADER ERROR
7992 036106          73$:
7993
7994
7995 036106 023737 001352 012104  CMP    TOCYL,MC ;ALL CYL DONE?
7996 036114 001404          BEQ    6$ ;BR IF YES
7997 036116 006337 001352          ASL    TOCYL ;ELSE DO ANOTHER
7998 036122 000137 034544          JMP    1$
7999 036126          6$:
8000 036126 004737 047466          JSR    PC,SWTST ;SEE IF SW 14 OR 8 IS SET
8001 036132 000515          BR     TST37 ;GO TO NEXT TEST
8002          ;RETURN HERE IF SW 14 IS SET OR
8003          ;SW 8 WITH SWR <7:0> APPLY
8004 036134          8$:
8005
8006 036134 004737 045462          JSR    PC,SUBCLR
8007 036140 104024          ERROR  24          ;CERR AFTER SCRL
8008
8009 036142 013765 001352 000020 76$: MOV    TOCYL,RKDC(R5) ;CYL#
8010
8011 036150 012737 000017 003322  MOV
8012 036156 004737 043472          JSR    #SEEK,HCS1
8013 036162 104131          ERROR  PC,DOCMD ;DO SEEK CMD & GET CONTR READY
8014          131 ;NO RDY AFTER SEEK CMD.

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8015 036164 013737 001426 003360      MOV      T50000,TEMP1
8016 036172 004737 044176              JSR      PC,FATT2      ;FIND ATTN
8017 036176 104132              ERROR   132           ;NO ATTN AFTER SEEK CMD
8018 036200 032737 100000 003322      BIT      #CERR,HCS1
8019 036206 001401              BEQ     78$
8020 036210 104210              ERROR   210           ;CERR AFTER SEEK CMD.
8021
8022 036212 004737 045462      78$:    JSR      PC,SUBCLR
8023 036216 104024              ERROR   24           ;CERR AFTER SCLR
8024
8025 036220 023737 001352 012116      CMP     TOCYL,FC      ;LAST CYL DONE?
8026 036226 001403              BEQ     77$           ;BR IF YES
8027 036230 005337 001352              DEC     TOCYL         ;ELSE DO ANOTHER
8028 036234 000742              BR      76$
8029
8030 036236 004737 045462      77$:    JSR      PC,SUBCLR
8031 036242 104024              ERROR   24           ;CERR AFTER SCLR
8032
8033 036244 005037 001176      CLR     $ESCAPE
8034 036250 005737 001410      TST    LPFLG
8035 036254 001402              BEQ     79$
8036 036256 000177 142626      JMP     @SLPERR      ;SW 9 WAS SET.
8037 036262 000177 142620      79$:    JMP     @SLPADR      ;SW 14 OR 8 WAS SET
8038
8039 036266              10$:
8040 036266 005237 001410      INC     LPFLG
8041 036272 032777 001000 142640      BIT     #SW9,@SWR   ;LOOP ON ERROR?
8042 036300 001315              BNE     8$           ;YES, RECONDITION DRIVE
8043 036302 000137 034704              JMP     2$           ;RETURN TO MAINLINE
8044 036306              12$:
8045 036306 005237 001410      INC     LPFLG
8046 036312 032777 001000 142620      BIT     #SW9,@SWR   ;LOOP ON ERROR?
8047 036320 001305              BNE     8$           ;YES, RECONDITION DRIVE
8048 036322 000137 035172              JMP     3$           ;RETURN TO MAINLINE
8049 036326              14$:
8050 036326 005237 001410      INC     LPFLG
8051 036332 032777 001000 142600      BIT     #SW9,@SWR   ;LOOP ON ERROR?
8052 036340 001275              BNE     8$           ;YES, RECONDITION DRIVE
8053 036342 000137 035454              JMP     4$           ;RETURN TO MAINLINE
8054 036346              16$:
8055 036346 005237 001410      INC     LPFLG
8056 036352 032777 001000 142560      BIT     #SW9,@SWR   ;LOOP ON ERROR?
8057 036360 001265              BNE     8$           ;YES, RECONDITION DRIVE
8058 036362 000137 035742              JMP     5$           ;RETURN TO MAINLINE
8059
8060
8061
8062
8063      ;*****
8064      ;*TEST 37      SEEK TO ALL CYLS FROM 0 & READ HEADERS
8065      ;*****
8065 036366 000004      TST37: SCOPE
8066 036370 012737 000001 001174      MOV     #1,$TIMES   ;;DO 1 ITERATION
8067 036376 012706 001100              MOV     #STACK,SP   ;RESTORE STK PTR
8068
8069 036402 005737 001342              TST    MODTST      ;SEE IF MODULE TESTING
8070 036406 001402              BEQ     DOSEEK      ;BR IF NO
  
```

CZ
CZ

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8071 036410 000137 042210          JMP      CYLINV          ;ELSE BYPASS TESTS 40 & 41
8072 036414          DOSEEK:
8073
8074 036414 013737 012116 001350      MOV      FC,FRCYL      ;SETUP FROM CYL
8075 036422 013737 012120 001352      MOV      FCP1,TOCYL   ;SETUP TO CYL
8076
8077 036430          1$:
8078 036430 104415          SCOP1
8079 036432 012706 001100      MOV      #STACK,SP    ;RESTORE STK PTR
8080
8081 036436 004737 045462          JSR      PC,SUBCLR    ;
8082 036442 104024          ERROR   24           ;CERR AFTER SCLR
8083
8084 036444 012737 040152 001176      MOV      #10$,SESCAPE
8085 036452 013737 001350 003364      MOV      FRCYL,TEMP3  ;SETUP
8086 036460 013737 001352 003366      MOV      TOCYL,TEMP4  ;CYL DIFF
8087 036466 163737 003364 003366      SUB      TEMP3,TEMP4  ;FOR
8088 036474 013737 003366 001360      MOV      TEMP4,CALDIF ;ERROR PRINTOUT
8089
8090 036502 013765 001352 000020      MOV      TOCYL,RKDC(R5) ;GO TO CYL #
8091
8092 036510 012737 000017 003322      MOV      #SEEK,HCS1
8093 036516 004737 043472          JSR      PC,DOCMD     ;DO SEEK CMD & GET CONTR READY
8094 036522 104131          ERROR   131         ;NO RDY AFTER SEEK CMD
8095 036524 012737 030140 003412      MOV      #<D.PIP!D.SPIN!D.VV!D.DRA>,E.A0 ;EXPECTED A0
8096 036532 005037 003414          CLR      E.B0
8097 036536 012737 003720 003416      MOV      #<D.FWD!D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1
8098 036544 012737 000001 003420      MOV      #1,E.B1
8099
8100 036552 004737 044310          JSR      PC,CHKMSG    ;CHECK MSGS A0,B0,A1,B1
8101 036556 000003          .WORD   T.A2!T.B2!0  ;& MSGS SPECIFIED HERE
8102 036560 104203          ERROR   203         ;MSG A0 ERROR DURING SEEK CMD
8103 036562 104204          ERROR   204         ;MSG B0 ERROR
8104 036564 104205          ERROR   205         ;MSG A1 ERROR
8105 036566 104206          ERROR   206         ;MSG B1 ERROR
8106
8107 036570 012737 040172 001176      2$:  MOV      #12$,SESCAPE
8108 036576 013737 001426 003360      MOV      T5000,TEMP1  ;SETUP TIMEOUT
8109
8110 036604 004737 044176          JSR      PC,FATT2     ;FIND ATTN
8111 036610 104132          ERROR   132         ;NO ATTN AFTER SEEK CMD
8112 036612 032737 100000 003322      BIT      #CERR,HCS1
8113 036620 001401          BEQ     64$
8114 036622 104210          ERROR   210         ;CERR AFTER SEEK CMD
8115 036624          64$:
8116
8117 036624 012737 050340 003412      MOV      #<D.DSC!D.SPIN!D.DRDY!D.VV!D.DRA>,E.A0 ;EXPECTED MSG A0
8118 036632 005037 003414          CLR      E.B0        ;EXPECTED MSG B0
8119 036636 012737 001720 003416      MOV      #<D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1 ;EXPECTED A1
8120 036644 012737 000001 003420      MOV      #1,E.B1     ;MSG ID FOR EXPECTED MSG B1
8121 036652 005037 003422          CLR      E.A2        ;EXPECTED MSG A2
8122 036656 012737 000002 003424      MOV      #2,E.B2     ;MSG ID FOR EXPECTED MSG B2
8123 036664 012737 000003 003430      MOV      #3,E.B3     ;MSG ID FOR EXPECTED MSG B3
8124
8125 036672 004737 044310          JSR      PC,CHKMSG    ;CHECK MSGS A0,B0,A1,B1
8126 036676 000003          .WORD   T.A2!T.B2!0  ;& MSGS SPECIFIED HERE
  
```


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CZR6HFO UNIBUS RK6 DR PT1 MACY11 30(1046) 04-JAN-82 13:01 PAGE 158
CZR6HF.P11 04-JAN-82 12:44 T37 SEEK TO ALL CYLS FROM 0 & READ HEADERS SEQ 0157

8127	036700	104133			ERROR	133			:MSG A0 ERROR AFTER SEEK CMD
8128	036702	104134			ERROR	134			:MSG B0 ERROR
8129	036704	104135			ERROR	135			:MSG A1 ERROR
8130	036706	104136			ERROR	136			:MSG B1 ERROR
8131	036710	005737	001362		TST	CYLDIF			
8132	036714	001401			BEQ	65\$			
8133	036716	104137			ERROR	137			:CYL DIFF NOT CLEARED AFTER SEEK CMD
8134									
8135	036720								
8136									
8137	036720	012765	100000	000000	MOV	#CCLR,RKCS1(R5)			
8138	036726	013765	001222	000010	MOV	\$UNIT,RKCS2(R5)	:DRIVE#		
8139	036734	012737	000005	003322	MOV	#CLEAR,HCS1			
8140	036742	004737	043472		JSR	PC,DOCMD			:DO DRIVE CLEAR CMD & GET CONTR RDY
8141	036746	104151			ERROR	151			:NO RDY AFTER DRIVE CLEAR CMD
8142	036750	004737	044050		JSR	PC,TSTATN			:TEST FOR ATTN
8143	036754	000401			BR	66\$			
8144	036756	104154			ERROR	154			:ATTN NOT CLEARED AFTER DRIVE CLEAR CMD
8145	036760								
8146									
8147	036760	012737	010340	003412	MOV	#<0!D.SPIN!D.DRDY!D.VV!D.DRA>,E.A0			:EXPECTED MSG A0
8148	036766	005037	003414		CLR	E.B0			:EXPECTED MSG B0
8149	036772	012737	001720	003416	MOV	#<D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1			:EXPECTED A1
8150	037000	012737	000001	003420	MOV	#1,E.B1			:MSG ID FOR EXPECTED MSG B1
8151	037006	005037	003422		CLR	E.A2			:EXPECTED MSG A2
8152	037012	012737	000002	003424	MOV	#2,E.B2			:MSG ID FOR EXPECTED MSG B2
8153	037020	012737	000003	003430	MOV	#3,E.B3			:MSG ID FOR EXPECTED MSG B3
8154									
8155	037026	004737	044310		JSR	PC,CHKMSG			:CHECK MSGS A0,B0,A1,B1
8156	037032	000003			.WORD	T.A2!T.B2!0			:& MSGS SPECIFIED HERE
8157	037034	104273			ERROR	273			:MSG A0 ERROR AFTER DRIVE CLEAR CMD
8158	037036	104265			ERROR	265			:MSG B0 ERROR
8159	037040	104274			ERROR	274			:MSG A1 ERROR
8160	037042	104266			ERROR	266			:MSG B1 ERROR
8161									
8162	037044	023737	001364	001352	CMP	CYLADD,TOCYL			
8163	037052	001401			BEQ	3\$			
8164	037054	104207			ERROR	207			:CYL ADDR IN RKMR3 NOT=RKDC
8165									
8166	037056								
8167	037056	104415			SCOP1				
8168	037060	012706	001100		MOV	#STACK,SP			:RESTORE STK PTR
8169									
8170	037064	004737	045462		JSR	PC,SUBCLR			
8171	037070	104024			ERROR	24			:CERR AFTER SCLR
8172									
8173	037072	005037	001176		CLR	\$ESCAPE			
8174	037076	013765	001352	000020	MOV	TOCYL,RKDC(R5)			:CYL #
8175									
8176									
8177	037104	012700	001674		MOV	#RHTAB,RO			
8178	037110	012737	000025	003322	MOV	#<RDHEAD>,HCS1			
8179	037116	004737	043530		JSR	PC,DATCMD			:DO DATA XFER CMD & GET CONTR RDY
8180	037122	104171			ERROR	171			:NO RDY AFTER READ HEADER CMD
8181	037124	032737	100000	003322	BIT	#CERR,HCS1			
8182	037132	001405			BEQ	68\$			

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CZR6HFO UNIBUS RK6 DR PT1 MACY11 30(1046) 04-JAN-82 13:01 PAGE 159
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8183	037134	104174			ERROR	174		:CERR AFTER READ HEADER CMD
8184	037136	104401	056436		TYPE	MSG18		:ABORT BALANCE OF TESTS
8185	037142	000137	042706		JMP	\$EOP		:ABORT DRIVE
8186								
8187	037146	016520	000024	68\$:	MOV	RKDB(R5),(R0)+		:1'ST WORD FROM SILO TO RHTAB
8188	037152	016520	000024		MOV	RKDB(R5),(R0)+		:2'ND WORD
8189	037156	016520	000024		MOV	RKDB(R5),(R0)+		:3'RD WORD
8190								
8191								
8192	037162	032765	100000	000010	BIT	#DLT,RKCS2(R5)		
8193	037170	001407			BEQ	69\$		
8194	037172	004737	045132		JSR	PC,GSTAT		
8195	037176	104173			ERROR	173		:DLT AFTER READ HEADER CMD
8196	037200	104401	056436		TYPE	MSG18		:ABORTING BALANCE OF TESTS
8197	037204	000137	042706		JMP	\$EOP		:ABORT DRIVE
8198	037210			69\$:				
8199								
8200	037210	023737	001674	001352	CMP	RHTAB,TOCYL		:CHECK WORD 0 (CYL#) ONLY
8201	037216	001401			BEQ	67\$:BR IF SAME
8202	037220	104310			ERROR	310		:READ CYL WORD HEADER ERROR
8203	037222			67\$:				
8204								
8205								
8206	037222	104415			SCOP1			
8207	037224	012706	001100		MOV	#STACK,SP		:RESTORE STK PTR
8208								
8209	037230	004737	045462		JSR	PC,SUBCLR		
8210	037234	104024			ERROR	24		:CERR AFTER SCLR
8211								
8212	037236	012737	040212	001176	MOV	#14\$, \$ESCAPE		
8213	037244	013765	001350	000020	MOV	FRCYL,RKDC(R5)		:RETURN TO CYL #
8214	037252	013737	001350	001354	MOV	FRCYL,CCYL		:CURRENT CYL FOR TRUERROR ROUTINE
8215								
8216	037260	012737	000017	003322	MOV	#SEEK,HCS1		
8217	037266	004737	043472		JSR	PC,DOCMD		:DO SEEK CMD & GET CONTR READY
8218	037272	104131			ERROR	131		:NO RDY AFTER SEEK CMD
8219	037274	012737	030140	003412	MOV	#<D.PIP!D.SPIN!D.VV!D.DRA>,E.A0		:EXPECTED A0
8220	037302	005037	003414		CLR	E.B0		
8221	037306	012737	005720	003416	MOV	#<D.REV!D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1		
8222	037314	012737	000001	003420	MOV	#1,E.B1		
8223								
8224	037322	004737	044310		JSR	PC,CHKMSG		:CHECK MSGS A0,B0,A1,B1
8225	037326	000003			.WORD	T.A2!T.B2!0		:& MSGS SPECIFIED HERE
8226	037330	104203			ERROR	203		:MSG A0 ERROR DURING SEEK CMD
8227	037332	104204			ERROR	204		:MSG B0 ERROR
8228	037334	104205			ERROR	205		:MSG A1 ERROR
8229	037336	104206			ERROR	206		:MSG B1 ERROR
8230								
8231								
8232	037340	012737	040232	001176	4\$:	MOV	#16\$, \$ESCAPE	
8233	037346	013737	001426	003360	MOV	T50000,TEMP1		:SETUP TIMEOUT
8234								
8235	037354	004737	044176		JSR	PC,FATT2		:FIND ATTN
8236	037360	104132			ERROR	132		:NO ATTN AFTER SEEK CMD
8237	037362	032737	100000	003322	BIT	#CERR,HCS1		
8238	037370	001401			BEQ	70\$		

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CZR6HF.P11 04-JAN-82 12:44 T37 SEEK TO ALL CYLS FROM 0 & READ HEADERS SEQ 0160

8295	037634	004737	045462		JSR	PC,SUBCLR	
8296	037640	104024			ERROR	24	:CERR AFTER SCLR
8297							
8298	037642	005037	001176		CLR	\$ESCAPE	
8299	037646	013765	001350	000020	MOV	FRCYL,RKDC(R5)	:CYL #
8300							
8301							
8302	037654	012700	001674		MOV	#RHTAB,RO	
8303	037660	012737	000025	003322	MOV	#<RDHEAD>,HCS1	
8304	037666	004737	043530		JSR	PC,DATCMD	:DO DATA XFER CMD & GET CONTR RDY
8305	037672	104171			ERROR	171	:NO RDY AFTER READ HEADER CMD
8306	037674	032737	100000	003322	BIT	#CERR,HCS1	
8307	037702	001405			BEQ	74\$	
8308	037704	104174			ERROR	174	:CERR AFTER READ HEADER CMD
8309	037706	104401	056436		TYPE	,MSG18	:ABORT BALANCE OF TESTS
8310	037712	000137	042706		JMP	\$EOP	:ABORT DRIVE
8311							
8312	037716	016520	000024		MOV	RKDB(R5),(R0)+	:1'ST WORD FROM SILO TO RHTAB
8313	037722	016520	000024		MOV	RKDB(R5),(R0)+	:2'ND WORD
8314	037726	016520	000024		MOV	RKDB(R5),(R0)+	:3'RD WORD
8315							
8316							
8317	037732	032765	100000	000010	BIT	#DLT,RKCS2(R5)	
8318	037740	001407			BEQ	75\$	
8319	037742	004737	045132		JSR	PC,GSTAT	
8320	037746	104173			ERROR	173	:DLT AFTER READ HEADER CMD
8321	037750	104401	056436		TYPE	,MSG18	:ABORTING BALANCE OF TESTS
8322	037754	000137	042706		JMP	\$EOP	:ABORT DRIVE
8323	037760						
8324							
8325	037760	023737	001674	001350	CMP	RHTAB,FRCYL	:CHECK WORD 0 (CYL#) ONLY
8326	037766	001401			BEQ	73\$:BR IF SAME
8327	037770	104311			ERROR	311	:READ CYL WORD HEADER ERROR
8328	037772						
8329							
8330							
8331	037772	023737	001352	012100	CMP	TOCYL,LC	:ALL CYL DONE?
8332	040000	001404			BEQ	6\$:BR IF YES
8333	040002	005237	001352		INC	TOCYL	:ELSE DO ANOTHER
8334	040006	000137	036430		JMP	1\$	
8335	040012						
8336	040012	004737	047466		JSR	PC,SWTST	:SEE IF SW 14 OR 8 IS SET
8337	040016	000515			BR	TST40	:GO TO NEXT TEST
8338							:RETURN HERE IF SW 14 IS SET OR
8339							:SW 8 WITH SWR <7:0> APPLY
8340	040020						
8341							
8342	040020	004737	045462		JSR	PC,SUBCLR	
8343	040024	104024			ERROR	24	:CERR AFTER SCRL
8344							
8345	040026	013765	001352	000020	MOV	TOCYL,RKDC(R5)	:CYL#
8346							
8347	040034	012737	000017	003322	MOV	#SEEK,HCS1	
8348	040042	004737	043472		JSR	PC,DOCMD	:DO SEEK CMD & GET CONTR READY
8349	040046	104131			ERROR	131	:NO RDY AFTER SEEK CMD.
8350							

8351	040050	013737	001426	003360	MOV	T50000,TEMP1	
8352	040056	004737	044176		JSR	PC,FATT2	:FIND ATTN
8353	040062	104132			ERROR	132	:NO ATTN AFTER SEEK CMD
8354	040064	032737	100000	003322	BIT	#CERR,HCS1	
8355	040072	001401			BEQ	78\$	
8356	040074	104210			ERROR	210	:CERR AFTER SEEK CMD.
8357							
8358	040076	004737	045462		78\$: JSR	PC,SUBCLR	
8359	040102	104024			ERROR	24	:CERR AFTER SCLR
8360							
8361	040104	023737	001352	012116	CMP	TOCYL,FC	:LAST CYL DONE?
8362	040112	001403			BEQ	77\$:BR IF YES
8363	040114	005337	001352		DEC	TOCYL	:ELSE DO ANOTHER
8364	040120	000742			BR	76\$	
8365							
8366	040122	004737	045462		77\$: JSR	PC,SUBCLR	
8367	040126	104024			ERROR	24	:CERR AFTER SCLR
8368							
8369	040130	005037	001176		CLR	\$ESCAPE	
8370	040134	005737	001410		TST	LPFLG	
8371	040140	001402			BEQ	79\$	
8372	040142	000177	140742		JMP	@\$LPERR	:SW 9 WAS SET.
8373	040146	000177	140734		79\$: JMP	@\$LPADR	:SW 14 OR 8 WAS SET
8374							
8375	040152				10\$:		
8376	040152	005237	001410		INC	LPFLG	
8377	040156	032777	001000	140754	BIT	#SW9,@SWR	:LOOP ON ERROR?
8378	040164	001315			BNE	8\$:YES, RECONDITION DRIVE
8379	040166	000137	036570		JMP	2\$:RETURN TO MAINLINE
8380	040172				12\$:		
8381	040172	005237	001410		INC	LPFLG	
8382	040176	032777	001000	140734	BIT	#SW9,@SWR	:LOOP ON ERROR?
8383	040204	001305			BNE	8\$:YES, RECONDITION DRIVE
8384	040206	000137	037056		JMP	3\$:RETURN TO MAINLINE
8385	040212				14\$:		
8386	040212	005237	001410		INC	LPFLG	
8387	040216	032777	001000	140714	BIT	#SW9,@SWR	:LOOP ON ERROR?
8388	040224	001275			BNE	8\$:YES, RECONDITION DRIVE
8389	040226	000137	037340		JMP	4\$:RETURN TO MAINLINE
8390	040232				16\$:		
8391	040232	005237	001410		INC	LPFLG	
8392	040236	032777	001000	140674	BIT	#SW9,@SWR	:LOOP ON ERROR?
8393	040244	001265			BNE	8\$:YES, RECONDITION DRIVE
8394	040246	000137	037626		JMP	5\$:RETURN TO MAINLINE

 :*TEST 40 SEEK TO ALL CYLS FROM LAST CYL & READ HEADERS

8395					TST40:	SCOPE	
8396							
8397							
8398							
8399	040252	000004					
8400	040254	012737	000001	001174	MOV	#1,\$TIMES	::DO 1 ITERATION
8401	040262	012706	001100		MOV	#STACK,SP	:RESTORE STK PTR
8402							
8403							
8404	040266	004737	045462		JSR	PC,SUBCLR	
8405	040272	104024			ERROR	24	:CERR AFTER SCLR
8406							

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CZR6HFO UNIBUS RK6 DR PT1 MACY11 30(1046) 04-JAN-82 13:01 SEEK TO ALL CYLS FROM LAST CYL & READ HEADERS SEQ 0162
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8407	040274	013765	012100	000020	MOV	LC,RKDC(R5)	:QUICK SEEK TO LAST CYL
8408							
8409	040302	012737	000017	003322	MOV	#SEEK,HCS1	
8410	040310	004737	043472		JSR	PC,DOCMD	:DO SEEK CMD & GET CONTR READY
8411	040314	104131			ERROR	131	:NO RDY AFTER SEEK CMD.
8412							
8413	040316	013737	001426	003360	MOV	T50000,TEMP1	
8414	040324	004737	044176		JSR	PC,FATT2	:FIND ATTN
8415	040330	104132			ERROR	132	:NO ATTN AFTER SEEK CMD
8416	040332	032737	100000	003322	BIT	#CERR,HCS1	
8417	040340	001401			BEQ	64\$	
8418	040342	104210			ERROR	210	:CERR AFTER SEEK CMD.
8419							
8420	040344	004737	045462		JSR	PC,SUBCLR	
8421	040350	104024		64\$:	ERROR	24	:CERR AFTER SCLR
8422							
8423							
8424	040352	013737	012100	001350	MOV	LC,FRCYL	:SETUP FROM CYL
8425	040360	013737	012076	001352	MOV	LCM1,TOCYL	:SETUP TO CYL
8426							
8427	040366						1\$:
8428	040366	104415			SCOP1		
8429	040370	012706	001100		MOV	#STACK,SP	:RESTORE STK PTR
8430							
8431	040374	004737	045462		JSR	PC,SUBCLR	
8432	040400	104024			ERROR	24	:CERR AFTER SCLR
8433							
8434	040402	012737	042110	001176	MOV	#10\$,SESCAPE	
8435	040410	013737	001350	003364	MOV	FRCYL,TEMP3	:SETUP
8436	040416	013737	001352	003366	MOV	TOCYL,TEMP4	:CYL DIFF
8437	040424	013737	003366	003364	SUB	TEMP4,TEMP3	:FOR
8438	040432	013737	003364	001360	MOV	TEMP3,CALDIF	:ERROR PRINTOUT
8439							
8440	040440	013765	001352	000020	MOV	TOCYL,RKDC(R5)	:GO TO CYL #
8441							
8442	040446	012737	000017	003322	MOV	#SEEK,HCS1	
8443	040454	004737	043472		JSR	PC,DOCMD	:DO SEEK CMD & GET CONTR READY
8444	040460	104131			ERROR	131	:NO RDY AFTER SEEK CMD
8445	040462	012737	030140	003412	MOV	#<D.PIP!D.SPIN!D.VV!D.DRA>,E.A0	:EXPECTED A0
8446	040470	005037	003414		CLR	E.B0	
8447	040474	012737	005720	003416	MOV	#<D.REV!D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1	
8448	040502	012737	000001	003420	MOV	#1,E.B1	
8449							
8450	040510	004737	044310		JSR	PC,CHKMSG	:CHECK MSGS A0,B0,A1,B1
8451	040514	000003			.WORD	T.A2!T.B2!0	:& MSGS SPECIFIED HERE
8452	040516	104203			ERROR	203	:MSG A0 ERROR DURING SEEK CMD
8453	040520	104204			ERROR	204	:MSG B0 ERROR
8454	040522	104205			ERROR	205	:MSG A1 ERROR
8455	040524	104206			ERROR	206	:MSG B1 ERROR
8456							
8457	040526	012737	042130	001176	MOV	#12\$,SESCAPE	
8458	040534	013737	001426	003360	MOV	T50000,TEMP1	:SETUP TIMEOUT
8459							
8460	040542	004737	044176		JSR	PC,FATT2	:FIND ATTN
8461	040546	104132			ERROR	132	:NO ATTN AFTER SEEK CMD
8462	040550	032737	100000	003322	BIT	#CERR,HCS1	

8519									
8520	041022	004737	045462		JSR	PC,SUBCLR			
8521	041026	104024			ERROR	24		;CERR AFTER SCLR	
8522									
8523	041030	005037	001176		CLR	\$ESCAPE			
8524	041034	013765	001352	000020	MOV	TOCYL,RKDC(R5)		;CYL #	
8525									
8526									
8527	041042	012700	001674		MOV	#RHTAB,RO			
8528	041046	012737	000025	003322	MOV	#<RDHEAD>,HCS1			
8529	041054	004737	043530		JSR	PC,DATCMD		;DO DATA XFER CMD & GET CONTR RDY	
8530	041060	104171			ERROR	171		;NO RDY AFTER READ HEADER CMD	
8531	041062	032737	100000	003322	BIT	#CERR,HCS1			
8532	041070	001405			BEQ	69\$			
8533	041072	104174			ERROR	174		;CERR AFTER READ HEADER CMD	
8534	041074	104401	056436		TYPE	,MSG18		;ABORT BALANCE OF TESTS	
8535	041100	000137	042706		JMP	\$EOP		;ABORT DRIVE	
8536									
8537	041104	016520	000024		MOV	RKDB(R5),(R0)+	69\$:	;1'ST WORD FROM SILO TO RHTAB	
8538	041110	016520	000024		MOV	RKDB(R5),(R0)+		;2'ND WORD	
8539	041114	016520	000024		MOV	RKDB(R5),(R0)+		;3'RD WORD	
8540									
8541									
8542	041120	032765	100000	000010	BIT	#DLT,RKCS2(R5)			
8543	041126	001407			BEQ	70\$			
8544	041130	004737	045132		JSR	PC,GSTAT			
8545	041134	104173			ERROR	173		;DLT AFTER READ HEADER CMD	
8546	041136	104401	056436		TYPE	,MSG18		;ABORTING BALANCE OF TESTS	
8547	041142	000137	042706		JMP	\$EOP		;ABORT DRIVE	
8548	041146						70\$:		
8549									
8550	041146	023737	001674	001352	CMP	RHTAB,TOCYL		;CHECK WORD 0 (CYL#) ONLY	
8551	041154	001401			BEQ	68\$;BR IF SAME	
8552	041156	104310			ERROR	310		;READ CYL WORD HEADER ERROR	
8553	041160						68\$:		
8554									
8555									
8556	041160	104415			SCOP1				
8557	041162	012706	001100		MOV	#STACK,SP		;RESTORE STK PTR	
8558									
8559	041166	004737	045462		JSR	PC,SUBCLR			
8560	041172	104024			ERROR	24		;CERR AFTER SCLR	
8561									
8562	041174	012737	042150	001176	MOV	#14\$, \$ESCAPE			
8563	041202	013765	001350	000020	MOV	FRCYL,RKDC(R5)		;RETURN TO CYL #	
8564	041210	013737	001350	001354	MOV	FRCYL,CCYL		;CURRENT CYL FOR TRUERROR ROUTINE	
8565									
8566	041216	012737	000017	003322	MOV	#SEEK,HCS1			
8567	041224	004737	043472		JSR	PC,DOCMD		;DO SEEK CMD & GET CONTR READY	
8568	041230	104131			ERROR	131		;NO RDY AFTER SEEK CMD	
8569	041232	012737	030140	003412	MOV	#<D.PIP!D.SPIN!D.VV!D.DRA>,E.A0		;EXPECTED A0	
8570	041240	005037	003414		CLR	E.B0			
8571	041244	012737	003720	003416	MOV	#<D.FWD!D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1			
8572	041252	012737	000001	003420	MOV	#1,E.B1			
8573									
8574	041260	004737	044310		JSR	PC,CHKMSG		;CHECK MSGS A0,B0,A1,B1	

8575	041264	000003				.WORD	T.A2!T.B2!0		:& MSGS SPECIFIED HERE
8576	041266	104203				ERROR	203		:MSG A0 ERROR DURING SEEK CMD
8577	041270	104204				ERROR	204		:MSG B0 ERROR
8578	041272	104205				ERROR	205		:MSG A1 ERROR
8579	041274	104206				ERROR	206		:MSG B1 ERROR
8580									
8581									
8582	041276	012737	042170	001176	4\$:	MOV	#16\$, \$ESCAPE		
8583	041304	013737	001426	003360		MOV	T50000,TEMP1		:SETUP TIMEOUT
8584									
8585	041312	004737	044176			JSR	PC,FATT2		:FIND ATTN
8586	041316	104132				ERROR	132		:NO ATTN AFTER SEEK CMD
8587	041320	032737	100000	003322		BIT	#CERR,HCS1		
8588	041326	001401				BEQ	71\$		
8589	041330	104210				ERROR	210		:CERR AFTER SEEK CMD
8590	041332				71\$:				
8591									
8592	041332	012737	050340	003412		MOV	#<D.DSC!D.SPIN!D.DRDY!D.VV!D.DRA>,E.A0		:EXPECTED MSG A0
8593	041340	005037	003414			CLR	E.B0		:EXPECTED MSG B0
8594	041344	012737	001720	003416		MOV	#<D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1		:EXPECTED A1
8595	041352	012737	000001	003420		MOV	#1,E.B1		:MSG ID FOR EXPECTED MSG B1
8596	041360	005037	003422			CLR	E.A2		:EXPECTED MSG A2
8597	041364	012737	000002	003424		MOV	#2,E.B2		:MSG ID FOR EXPECTED MSG B2
8598	041372	012737	000003	003430		MOV	#3,E.B3		:MSG ID FOR EXPECTED MSG B3
8599									
8600	041400	004737	044310			JSR	PC,CHKMSG		:CHECK MSGS A0,B0,A1,B1
8601	041404	000003				.WORD	T.A2!T.B2!0		:& MSGS SPECIFIED HERE
8602	041406	104133				ERROR	133		:MSG A0 ERROR AFTER SEEK CMD
8603	041410	104134				ERROR	134		:MSG B0 ERROR
8604	041412	104135				ERROR	135		:MSG A1 ERROR
8605	041414	104136				ERROR	136		:MSG B1 ERROR
8606	041416	005737	001362			TST	CYLDIF		
8607	041422	001401				BEQ	72\$		
8608	041424	104137				ERROR	137		:CYL DIFF NOT CLEARED AFTER SEEK CMD
8609									
8610	041426				72\$:				
8611									
8612	041426	012765	100000	000000		MOV	#CCLR,RKCS1(R5)		
8613	041434	013765	001222	000010		MOV	\$UNIT,RKCS2(R5)		:DRIVE#
8614	041442	012737	000005	003322		MOV	#CLEAR,HCS1		
8615	041450	004737	043472			JSR	PC,DOCMD		:DO DRIVE CLEAR CMD & GET CONTR RDY
8616	041454	104151				ERROR	151		:NO RDY AFTER DRIVE CLEAR CMD
8617	041456	004737	044050			JSR	PC,TSTATN		:TEST FOR ATTN
8618	041462	000401				BR	73\$		
8619	041464	104154				ERROR	154		:ATTN NOT CLEARED AFTER DRIVE CLEAR CMD
8620	041466				73\$:				
8621									
8622	041466	012737	010340	003412		MOV	#<0!D.SPIN!D.DRDY!D.VV!D.DRA>,E.A0		:EXPECTED MSG A0
8623	041474	005037	003414			CLR	E.B0		:EXPECTED MSG B0
8624	041500	012737	001720	003416		MOV	#<D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1		:EXPECTED A1
8625	041506	012737	000001	003420		MOV	#1,E.B1		:MSG ID FOR EXPECTED MSG B1
8626	041514	005037	003422			CLR	E.A2		:EXPECTED MSG A2
8627	041520	012737	000002	003424		MOV	#2,E.B2		:MSG ID FOR EXPECTED MSG B2
8628	041526	012737	000003	003430		MOV	#3,E.B3		:MSG ID FOR EXPECTED MSG B3
8629									
8630	041534	004737	044310			JSR	PC,CHKMSG		:CHECK MSGS A0,B0,A1,B1

8631	041540	000003			.WORD	T.A2!T.B2!0		:& MSGS SPECIFIED HERE
8632	041542	104273			ERROR	273		:MSG A0 ERROR AFTER DRIVE CLEAR CMD
8633	041544	104265			ERROR	265		:MSG B0 ERROR
8634	041546	104274			ERROR	274		:MSG A1 ERROR
8635	041550	104266			ERROR	266		:MSG B1 ERROR
8636								
8637	041552	023737	001364	001350	CMP	CYLADD,FRCYL		
8638	041560	001401			BEQ	5\$		
8639	041562	104243			ERROR	243		:CYL ADDR IN RKMR3 NOT=RKDC
8640								
8641	041564				5\$:			
8642	041564	104415			SCOP1			
8643	041566	012706	001100		MOV	#STACK,SP		:RESTORE STK PTR
8644								
8645	041572	004737	045462		JSR	PC,SUBCLR		
8646	041576	104024			ERROR	24		:CERR AFTER SCLR
8647								
8648	041600	005037	001176		CLR	\$ESCAPE		
8649	041604	013765	001350	000020	MOV	FRCYL,RKDC(R5)		:CYL #
8650								
8651								
8652	041612	012700	001674		MOV	#RHTAB,R0		
8653	041616	012737	000025	003322	MOV	#<RDHEAD>,HCS1		
8654	041624	004737	043530		JSR	PC,DATCMD		:DO DATA XFER CMD & GET CONTR RDY
8655	041630	104171			ERROR	171		:NO RDY AFTER READ HEADER CMD
8656	041632	032737	100000	003322	BIT	#CERR,HCS1		
8657	041640	001405			BEQ	75\$		
8658	041642	104174			ERROR	174		:CERR AFTER READ HEADER CMD
8659	041644	104401	056436		TYPE	,MSG18		:ABORT BALANCE OF TESTS
8660	041650	000137	042706		JMP	\$EOP		:ABORT DRIVE
8661								
8662	041654	016520	000024		75\$:	MOV	RKDB(R5),(R0)+	:1'ST WORD FROM SILO TO RHTAB
8663	041660	016520	000024			MOV	RKDB(R5),(R0)+	:2'ND WORD
8664	041664	016520	000024			MOV	RKDB(R5),(R0)+	:3'RD WORD
8665								
8666								
8667	041670	032765	100000	000010	BIT	#DLT,RKCS2(R5)		
8668	041676	001407			BEQ	76\$		
8669	041700	004737	045132		JSR	PC,GSTAT		
8670	041704	104173			ERROR	173		:DLT AFTER READ HEADER CMD
8671	041706	104401	056436		TYPE	,MSG18		:ABORTING BALANCE OF TESTS
8672	041712	000137	042706		JMP	\$EOP		:ABORT DRIVE
8673	041716				76\$:			
8674								
8675	041716	023737	001674	001350	CMP	RHTAB,FRCYL		:CHECK WORD 0 (CYL#) ONLY
8676	041724	001401			BEQ	74\$:BR IF SAME
8677	041726	104311			ERROR	311		:READ CYL WORD HEADER ERROR
8678	041730				74\$:			
8679								
8680								
8681	041730	023737	001352	012116	CMP	TOCYL,FC		:ALL CYL DONE?
8682	041736	001404			BEQ	6\$:BR IF YES
8683	041740	005337	001352		DEC	TOCYL		:ELSE DO ANOTHER
8684	041744	000137	040366		JMP	1\$		
8685	041750				6\$:			
8686	041750	004737	047466		JSR	PC,SWTST		:SEE IF SW 14 OR 8 IS SET

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T40 SEEK TO ALL CYLS FROM LAST CYL & READ HEADERS

SEQ 0167

```
8687 041754 000515 BR TST41 ;:GO TO NEXT TEST
8688 ;:RETURN HERE IF SW 14 IS SET OR
8689 ;:SW 8 WITH SWR <7:0> APPLY
8690 041756 8$:
8691
8692 041756 004737 045462 JSR PC,SUBCLR
8693 041762 104024 ERROR 24 ;:CERR AFTER SCRL
8694
8695 041764 013765 001352 000020 77$: MOV TOCYL,RKDC(R5) ;:CYL#
8696
8697 041772 012737 000017 003322 MOV #SEEK,HCS1
8698 042000 004737 043472 JSR PC,DOCMD ;:DO SEEK CMD & GET CONTR READY
8699 042004 104131 ERROR 131 ;:NO RDY AFTER SEEK CMD.
8700
8701 042006 013737 001426 003360 MOV T50000,TEMP1
8702 042014 004737 044176 JSR PC,FATT2 ;:FIND ATTN
8703 042020 104132 ERROR 132 ;:NO ATTN AFTER SEEK CMD
8704 042022 032737 100000 003322 BIT #CERR,HCS1
8705 042030 001401 BEQ 79$
8706 042032 104210 ERROR 210 ;:CERR AFTER SEEK CMD.
8707
8708 042034 004737 045462 79$: JSR PC,SUBCLR
8709 042040 104024 ERROR 24 ;:CERR AFTER SCLR
8710
8711 042042 023737 001352 012100 CMP TOCYL,LC ;:LAST CYL DONE?
8712 042050 001403 BEQ 78$ ;:BR IF YES
8713 042052 005237 001352 INC TOCYL ;:ELSE DO ANOTHER
8714 042056 000742 BR 77$
8715
8716 042060 004737 045462 78$: JSR PC,SUBCLR
8717 042064 104024 ERROR 24 ;:CERR AFTER SCLR
8718
8719 042066 005037 001176 CLR $ESCAPE
8720 042072 005737 001410 TST LPFLG
8721 042076 001402 BEQ 80$
8722 042100 000177 137004 JMP @SLPERR ;:SW 9 WAS SET.
8723 042104 000177 136776 JMP @SLPADR ;:SW 14 OR 8 WAS SET
8724
8725 042110 10$:
8726 042110 005237 001410 INC LPFLG
8727 042114 032777 001000 137016 BIT #SW9,@SWR ;:LOOP ON ERROR?
8728 042122 001315 BNE 8$ ;:YES, RECONDITION DRIVE
8729 042124 000137 040526 JMP 2$ ;:RETURN TO MAINLINE
8730
8731 042130 005237 001410 12$: INC LPFLG
8732 042134 032777 001000 136776 BIT #SW9,@SWR ;:LOOP ON ERROR?
8733 042142 001305 BNE 8$ ;:YES, RECONDITION DRIVE
8734 042144 000137 041014 JMP 3$ ;:RETURN TO MAINLINE
8735
8736 042150 005237 001410 14$: INC LPFLG
8737 042154 032777 001000 136756 BIT #SW9,@SWR ;:LOOP ON ERROR?
8738 042162 001275 BNE 8$ ;:YES, RECONDITION DRIVE
8739 042164 000137 041276 JMP 4$ ;:RETURN TO MAINLINE
8740
8741 042170 005237 001410 16$: INC LPFLG
8742 042174 032777 001000 136736 BIT #SW9,@SWR ;:LOOP ON ERROR?
```

8743 042202 001265
8744 042204 000137 041564

BNE 8\$:YES, RECONDITION DRIVE
JMP 5\$:RETURN TO MAINLINE

8745
8746 042210

CYLINV:

8747
8748
8749

:TEST 41 SFEK TO ALL INVALID CYLINDERS

8750
8751

: THIS TEST VERIFIES THAT 'INV ADDR' & 'SEEK INCOMPLETE' IS
: PRODUCED & THAT HEADS DO NOT MOVE OR UNLOAD IF AN ILLEGAL
: CYL IS SPECIFIED IN A SEEK.

8752
8753

: INVALID CYLS ARE 633 THRU 777 (8) FOR THE RK06
: & 1457 THRU 1777 FOR THE RK07.

8754
8755

8756
8757

8758
8759

:TST41: SCOPE

8760 042210 000004
8761 042212 012737 000001 001174

MOV #1,\$TIMES ;;DO 1 ITERATION
MOV #STACK,SP :RESTORE STK PTR

8762 042220 012706 001100

8763
8764 042224 004737 045462

JSR PC,SUBCLR
ERROR 24 :CERR AFTER SCLR

8765 042230 104024
8766
8767 042232 012737 000017 003322

MOV #SEEK,HCS1
JSR PC,DOCMD :DO SEEK CMD & GET CONTR READY
ERROR 131 :NO RDY AFTER SEEK CMD.

8768 042240 004737 043472
8769 042244 104131

8770
8771 042246 013737 001426 003360

MOV T50000,TEMP1
JSR PC,FATT2 :FIND ATTN
ERROR 132 :NO ATTN AFTER SEEK CMD

8772 042254 004737 044176

8773 042260 104132
8774 042262 032737 100000 003322

BIT #CERR,HCS1
BEQ 64\$
ERROR 210 :CERR AFTER SEEK CMD.

8775 042270 001401
8776 042272 104210

8777
8778 042274 004737 045462

64\$: JSR PC,SUBCLR
ERROR 24 :CERR AFTER SCLR

8779 042300 104024
8780

8781 042302 005000
8782 042304 005037 001350

CLR R0
CLR FRCYL :FROM CYL 0
MOV LCP1,TOCYL ;1ST INV CYL

8783 042310 013737 012102 001352
8784

8785 042316
8786 042316 104415

1\$: SCOP1
MOV #STACK,SP :RESTORE STK PTR

8787 042320 012706 001100
8788

8789 042324 004737 045462
8790 042330 104024

JSR PC,SUBCLR
ERROR 24 :CERR AFTER SCLR

8791
8792 042332 013737 001352 001360

MOV TOCYL,CALDIF
MOV TOCYL,RKDC(R5)

8793 042340 013765 001352 000020
8794 042346 012737 000017 003322

MOV #SEEK,HCS1
JSR PC,DOCMD :DO SEEK CMD & GET CONTR RDY
ERROR 131 :NO RDY AFTER SEEK CMD

8795 042354 004737 043472
8796 042360 104131

8797 042362 004737 044050
8798 042366 104245

JSR PC,TSTATN
ERROR 245 :NO ATTN AFTER SEEK TO INV CYL


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8799
8800 042370 032737 000040 003352 BIT #D.IDAE,HMR3
8801 042376 001001 BNE 2$
8802 042400 104246 ERROR 246 ;IDAE NOT SET AFTER SEEK TO INVALID ADDR
8803 042402 032737 000200 003352 2$: BIT #D.FLT,HMR3
8804 042410 001001 BNE 4$
8805 042412 104247 ERROR 247 ;FLT NOT SET AFTER SEEK TO INV ADDR
8806 042414 032737 020000 003350 4$: BIT #D.PIP,HMR2
8807 042422 001401 BEQ 5$
8808 042424 104250 ERROR 250 ;PIP SET AFTER SEEK TO INV ADDR
8809 042426 032737 040000 003350 5$: BIT #D.DSC,HMR2
8810 042434 001001 BNE 6$
8811 042436 104251 ERROR 251 ;DSC NOT SET AFTER SEEK TO INV ADDR
8812
8813 042440 005237 001462 6$: INC BYPCERR ;BYPASS CHECKING FOR CERR IN GSTAT1
8814 042444 012737 050340 003412 MOV #<D.DSC!D.SPIN!D.DRDY!D.VV!D.DRA>,E.A0 ;EXPECTED A0
8815 042452 012737 002240 003414 MOV #<D.SKI!D.FLT!D.IDAE>,E.B0
8816 042460 012737 001720 003416 MOV #<D.SPOK!D.CART!D.DOOR!D.BRHM!D.SSP>,E.A1
8817 042466 012737 000001 003420 MOV #1,E.B1
8818
8819 042474 004737 044310 JSR PC,CHKMSG ;CHECK MSGS A0,B0,A1,B1
8820 042500 000003 .WORD T.A2!T.B2!0 ;& MSGS SPECIFIED HERE
8821 042502 104252 ERROR 252 ;MSG A0 ERROR AFTER SEEK TO INV CYL
8822 042504 104253 ERROR 253 ;MSG B0 ERROR
8823 042506 104254 ERROR 254 ;MSG A1 ERROR
8824 042510 104255 ERROR 255 ;MSG B1 ERROR
8825 042512 023737 001362 001352 CMP CYLDIF,TOCYL
8826 042520 001401 BEQ 7$
8827 042522 104256 ERROR 256 ;CYL DIFF IN RKMR2 NOT=CYL DIF
8828 042524 023737 001364 001352 7$: CMP CYLADD,TOCYL
8829 042532 001401 BEQ 8$
8830 042534 104257 ERROR 257 ;CYL ADDR IN RKMR3 NOT=RKDC
8831
8832 042536 8$:
8833
8834 042536 012765 100000 000000 MOV #CCLR,RKCS1(R5)
8835 042544 013765 001222 000010 MOV $UNIT,RKCS2(R5) ;DRIVE#
8836 042552 012737 000013 003322 MOV #RECAL,HCS1
8837 042560 004737 043472 JSR PC,DOCMD ;DO RECAL CMD & GET CONTR RDY
8838 042564 104124 ERROR 124 ;RDY NOT FOUND AFTER RECAL CMD
8839
8840 042566 012765 100000 000000 MOV #CCLR,RKCS1(R5)
8841 042574 013765 001222 000010 MOV $UNIT,RKCS2(R5) ;DRIVE#
8842 042602 012737 000005 003322 MOV #CLEAR,HCS1
8843 042610 004737 043472 JSR PC,DOCMD ;DO DRIVE CLEAR CMD & GET CONTR RDY
8844 042614 104151 ERROR 151 ;NO RDY AFTER DRIVE CLEAR CMD
8845 042616 004737 044050 JSR PC,TSTATN ;TEST FOR ATTN
8846 042622 000401 BR 66$
8847 042624 104154 ERROR 154 ;ATTN NOT CLEARED AFTER DRIVE CLEAR CMD
8848 042626 66$:
8849
8850
8851 042626 004737 045132 JSR PC,GSTAT
8852 042632 032737 000040 003352 BIT #D.IDAE,HMR3 ;SEE IF IDAE IS CLEARED
8853 042640 001401 BEQ 65$ ;BR IF YES
8854 042642 104155 ERROR 155 ;IDAE NOT CLEARED AFTER RECAL CMD

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```
8868 .SBTTL END OF PASS ROUTINE
8869
8870 ::*****
8871 :*INCREMENT THE PASS NUMBER ($PASS)
8872 :*TYPE 'END PASS #XXXXX' (WHERE XXXXX IS A DECIMAL NUMBER)
8873 :*IF THERES A MONITOR GO TO IT
8874 :*IF THERE ISN'T JUMP TO ST5XY
8875
8876 042706 $EOP:
8877
8878 042706 000004 SCOPE
8879 042710 005037 001176 CLR $ESCAPE
8880 042714 012737 000001 001174 MOV #1,$TIMES
8881 042722 012706 001100 MOV #STACK,SP
8882 042726 005237 001220 INC $DEVCT ;:INCR COUNT FOR # OF DRIVES THAT ARE CHECKED
8883 042732 023737 003442 001220 CMP DRIVS,$DEVCT ;:ARE ALL DRIVES PRESINT TESTED?
8884 042740 001404 BEQ 1$ ;:BR IF YES
8885 042742 005037 001456 CLR BSERR ;:CLEAR BAD SECTOR ERROR FLAG
8886 042746 000137 011544 JMP NUDRV ;:IF NOT , TEST NEXT DRIVE PRESENT
8887 042752 005037 001456 1$: CLR BSERR ;:CLEAR BAD SECTOR ERROR FLAG
8888 042756 000401 BR $EOP1+2 ;:GO TO $EOP1+2
8889 042760 000004 $EOP1: SCOPE
8890 042762 005037 001102 CLR $TSTNM ;:ZERO THE TEST NUMBER
8891 042766 005037 001174 CLR $TIMES ;:ZERO THE NUMBER OF ITERATIONS
8892 042772 005237 001216 INC $PASS ;:INCREMENT THE PASS NUMBER
8893 042776 042737 100000 001216 BIC #100000,$PASS ;:DON'T ALLOW A NEG. NUMBER
8894 043004 005327 DEC (PC)+ ;:LOOP?
8895 043006 000001 $EOPCT: .WORD 1
8896 043010 003022 BGT $DOAGN ;:YES
8897 043012 012737 MOV (PC)+,@(PC)+ ;:RESTORE COUNTER
8898 043014 000001 $ENDCT: .WORD 1
8899 043016 043006 $EOPCT
8900 043020 104401 043065 TYPE $SENDMG ;:TYPE 'END PASS #'
8901 043024 013746 001216 MOV $PASS,-(SP) ;:SAVE $PASS FOR TYPEOUT
8902 043030 104405 TYPDS ;:GO TYPE--DECIMAL ASCII WITH SIGN
8903 043032 104401 043062 TYPE $ENULL ;:TYPE A NULL CHARACTER
8904 043036 013700 000042 $GET42: MOV @#42,R0 ;:GET MONITOR ADDRESS
8905 043042 001405 BEQ $DOAGN ;:BRANCH IF NO MONITOR
8906 043044 000005 RESET ;:CLEAR THE WORLD
8907 043046 004710 $ENDAD: JSR PC,(R0) ;:GO TO MONITOR
8908 043050 000240 NOP ;:SAVE ROOM
8909 043052 000240 NOP ;:FOR
8910 043054 000240 NOP ;:ACT11
8911 043056 $DOAGN:
8912 043056 000137 JMP @(PC)+ ;:RETURN
8913 043060 043102 $RTNAD: .WORD ST5XY
8914 043062 377 377 000 $ENULL: .BYTE -1,-1,0 ;:NULL CHARACTER STRING
8915 043065 015 042412 042116 $SENDMG: .ASCIZ <15><12>/END PASS #/
8916 043072 050040 051501 020123
8917 043100 000043
8918 043102 122737 000001 001230 ST5XY: CMPB #APTENV,$ENV ;:RUN UNDER APT ?
8919 043110 001007 BNE 2$ ;:BRANCH IF NOT
8920 043112 022737 000002 001216 CMP #2,$PASS ;:TWO PASSES DONE ?
8921 043120 101003 BHI 2$ ;:BRANCH IF NOT
8922 043122 005237 001102 1$: INC $TSTNM ;:CHANGE THE TEST NUMBER
8923 043126 000775 BR 1$ ;:LOOP WAIT FOR LOADING THE NEXT PRG
```

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SEQ 0172

8924 043130 000137 007744

2\$: JMP ST5 ;EXIT


```
8925 .SBTTL SUBROUTINES
8926
8927 ;SUBROUTINE TO CLEAR ALL FLAGS FROM DDUMP THRU DOTIM
8928 ;
8929
8930 043134 012700 003432 CLRFLG: MOV #DDUMP,R0
8931 043140 012701 177757 MOV #17,R1
8932 043144 005020 1$: CLR (R0)+
8933 043146 005201 INC R1
8934 043150 001375 BNE 1$
8935 043152 000207 RTS PC
8936
8937
8938 ;TYPE PROGRAM ID IF FTITLE=0
8939 ;
8940
8941 043154 005737 001344 TITLE: TST FTITLE
8942 043160 001024 BNE 1$
8943 043162 005237 001344 INC FTITLE
8944 043166 104401 054640 TYPE ,MSG1 ;PROGRAM ID
8945 .SBTTL GET VALUE FOR SOFTWARE SWITCH REGISTER
8946 043172 005737 000042 TST #42 ;:ARE WE RUNNING UNDER XXDP/ACT?
8947 043176 001012 BNE 64$ ;:BRANCH IF YES
8948 043200 123727 001230 000001 CMPB $ENV,#1 ;:ARE WE RUNNING UNDER APT?
8949 043206 001406 BEQ 64$ ;:BRANCH IF YES
8950 043210 023727 001140 000176 CMP SWR,#SWREG ;:SOFTWARE SWITCH REG SELECTED?
8951 043216 001005 BNE 65$ ;:BRANCH IF NO
8952 043220 104406 GTSWR ;:GET SOFT-SWR SETTINGS
8953 043222 000403 BR 65$
8954 043224 112737 000001 001134 64$: MOVB #1,$AUTOB ;:SET AUTO-MODE INDICATOR
8955 043232 65$:
8956 043232 000207 1$: RTS PC
8957
8958
8959 ;ROUTINE TO INPUT DRIVE NOS. TYPED IN & SET
8960 ;DRIVS, DRIV0-DRIV7 REGISTERS APPROPRIATELY
8961 ;
8962
8963 043234 104411 GDRVS: RDLIN
8964 043236 012600 MOV (SP)+,R0 ;GET STARTING ADDR OF ASCII STRING
8965 043240 012701 177770 MOV #8,R1 ;SET UP COUNT
8966 043244 112002 1$: MOVB (R0)+,R2 ;GET ASCII CHAR
8967 043246 042702 177400 BIC #177400,R2 ;MASK HI BYTE
8968 043252 012703 003444 MOV #DRIV0,R3 ;DRIVE FLAG ADDR
8969 043256 012704 000060 MOV #60,R4
8970
8971 043262 020402 2$: CMP R4,R2 ;WAS TYPED CHAR 0 THRU 7?
8972 043264 001415 BEQ 3$ ;BRANCH IF YES
8973 043266 005723 TST (R3)+ ;NO, INCREMENT DR FLAG ADDR
8974 043270 005204 INC R4
8975 043272 020427 000070 CMP R4,#70
8976 043276 001371 BNE 2$ ;S/B 0-7 OR TERMINATOR
8977 043300 005702 TST R2
8978 043302 001022 BNE 4$
8979 043304 020127 177770 CMP R1,#8.
8980 043310 001426 BEQ 6$ ;DEFAULT ALL DRIVES
```

```
8981 043312 005037 003472 7$: CLR SIZFLG ;BYPASS TEST 1 (SIZING)
8982 043316 000207 RTS PC ;FOUND TERMINATOR, EXIT
8983
8984 043320 005213 3$: INC @R3 ;SET UP FLAG FOR THE DRIVE
8985 043322 005237 003442 INC DRIVS ;INCREMENT TOTAL # DRIVES TO BE TESTED
8986 043326 112002 MOV#B (R0)+,R2 ;GET NEXT ASCII CHAR.
8987 043330 042702 177400 BIC #177400,R2 ;MASK
8988 043334 022702 000054 CMP #54,R2 ;IS IT A COMMA?
8989 043340 001407 BEQ 5$ ;YES, GO TO NEXT WORD.
8990 043342 005702 TST R2 ;NO, IS IT A TERMINATOR?
8991 043344 001001 BNE 4$ ;IF NOT, SOMETHING WRONG.
8992 043346 000761 BR 7$ ;FOUND TERMINATOR, EXIT
8993
8994 043350 104401 057145 4$: TYPE ,EM1 ;ONLY 0-7 ALLOWED.
8995 043354 000137 007134 JMP PRGSRT ;START ALL OVER
8996
8997 043360 005201 5$: INC R1 ;S/B NO MORE THAN 8 DIFF
8998 043362 001330 BNE 1$ ;DRIVES TYPED IN.
8999 043364 000771 BR 4$ ;IF MORE, HAVE ERROR.
9000
9001 043366 005237 003472 6$: INC SIZFLG ;DO TEST 1 (SIZING)
9002 043372 000207 RTS PC ;EXIT.
9003
9004
9005 ;ROUTINE TO INPUT RKBAS OR DEFAULT.
9006
9007
9008 043374 104412 GBA: RDOCT
9009 043376 012600 MOV (SP)+,R0 ;GET LOW ORDER FROM STACK
9010 043400 005700 TST R0
9011 043402 001403 BEQ 1$ ;BRANCH IF DEFAULT.
9012 043404 010037 001264 MOV R0,$BASE
9013 043410 000207 RTS PC
9014 043412 012737 177440 001264 1$: MOV #177440,$BASE ;DEFAULT VALUE
9015 043420 000207 RTS PC
9016
9017
9018 ;ROUTINE TO INPUT RKVEC OR DEFAULT
9019
9020
9021 043422 104412 GINT: RDOCT
9022 043424 012600 MOV (SP)+,R0 ;GET LOW ORDER FROM STACK
9023 043426 005700 TST R0
9024 043430 001405 BEQ 1$ ;BRANCH IF DEFAULT
9025 043432 010037 001314 MOV R0,RKVEC
9026 043436 004737 043454 2$: JSR PC,SETINT
9027 043442 000207 RTS PC
9028 043444 012737 000210 001314 1$: MOV #210,RKVEC ;DEFAULT VALUE
9029 043452 000771 BR 2$
9030
9031
9032 ;ROUTINE TO SETUP INTERRUPT VECTOR & PRIORITY
9033
9034
9035 043454 013700 001314 SETINT: MOV RKVEC,R0
9036 043460 012720 050214 MOV #INTER,(R0)+ ;INTER ADDR TO RKVEC
```



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9093 043674 062716 000002      1$:  ADD    #2,(SP)      ;SKIP OVER ERROR
9094 043700 016537 000034      003350  MOV    RKMR2(R5),HMR2
9095 043706 016537 000036      003352  MOV    RKMR3(R5),HMR3
9096 043714 004737 045050      JSR    PC,CKCERR      ;CHECK FOR SPECIAL CERR CONDITIONS
9097 043720 000207
9098
9099      ;STORE ALL RK611 REGISTERS IN HOLDING REGS
9100
9101
9102 043722 016537 000000      003322  HOLD:  MOV    RKCS1(R5),HCS1
9103 043730 016537 000010      003324  MOV    RKCS2(R5),HCS2
9104 043736 016537 000002      003326  MOV    RKWC(R5),HWC
9105 043744 016537 000004      003330  MOV    RKBA(R5),HBA
9106 043752 016537 000006      003332  MOV    RKDA(R5),HDA
9107 043760 016537 000012      003334  MOV    RKDS(R5),HDS
9108 043766 016537 000014      003336  MOV    RKER(R5),HER
9109 043774 016537 000016      003340  MOV    RKASOF(R5),HASOF
9110 044002 016537 000020      003342  MOV    RKDC(R5),HDC
9111 044010 016537 000026      003346  MOV    RKMR1(R5),HMR1
9112 044016 016537 000034      003350  MOV    RKMR2(R5),HMR2
9113 044024 016537 000036      003352  MOV    RKMR3(R5),HMR3
9114 044032 016537 000030      003354  MOV    RKECPS(R5),HPOS
9115 044040 016537 000032      003356  MOV    RKECPT(R5),HPAT
9116 044046 000207      RTS    PC
9117
9118
9119      ;ROUTINE TO CHECK FOR CORRECT ATTN
9120      ;RETURN IF ATTN NOT PRESENT (ERROR CONDITION)
9121      ;RETURN +2 IF ATTN PRESENT (SKIP OVER ERROR)
9122
9123 044050 010446      ;STATN: MOV    R4,-(SP)      ;SAV R4
9124 044052 013704 001222      MOV    $UNIT,R4
9125 044056 136437 003312      003341  BITB  ATTN(R4),HASOF+1
9126 044064 001404      BEQ    1$
9127 044066 012604      MOV    (SP)+,R4      ;RESTOR R4
9128 044070 062716 000002      ADD    #2,(SP)      ;INCR RET ADDR TO JUMP OVER ERROR.
9129 044074 000207      RTS    PC
9130 044076 012604      1$:  MOV    (SP)+,R4      ;RESTOR R4
9131 044100 000207      RTS    PC
9132
9133
9134      ;ROUTINE TO FIND ATTN WITHIN TIMES GREATER THAN 1 SEC
9135      ;ENTER WITH TIME IN SECONDS IN TEMP2
9136      ;RETURN IF NO ATTN (ERROR CONDITION)
9137      ;RETURN +2 IF ATTN FOUND
9138      ;STATUS IS OBTAINED BEFORE THE RETURN FOR EITHER CASE
9139
9140
9141 044102 010446      FATT1: MOV    R4,-(SP)      ;SAV R4
9142 044104 012737 177777      003360  3$:  MOV    #-1,TEMP1
9143 044112 013704 001222      MOV    $UNIT,R4
9144 044116 136465 003312      000017  1$:  BITB  ATTN(R4),RKASOF+1(R5) ;FIND CORRECT ATTN
9145 044124 001014      BNE   2$
9146 044126 005337 003360      DEC   TEMP1
9147 044132 001371      BNE   1$
9148 044134 005337 003362      DEC   TEMP2

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9149 044140 001361          BNE      3$
9150 044142 005065 000026    CLR      RKMR1(R5)      ;SELECT WORD 0
9151 044146 004737 045132    JSR      PC,GSTAT      ;GET LATEST STATUS
9152 044152 012604          MOV      (SP)+,R4      ;RESTOR R4
9153 044154 000207          RTS      PC
9154 044156 005065 000026    2$:    CLR      RKMR1(R5)
9155 044162 004737 045132    JSR      PC,GSTAT      ;GET STATUS AFTER ATTN SEEN
9156 044166 012604          MOV      (SP)+,R4      ;RESTOR R4
9157 044170 062716 000002    ADD      #2,(SP)       ;SKIP OVER ERROR
9158 044174 000207          RTS      PC
9159
9160          ;
9161          ;ROUTINE TO FIND ATTN WITHIN 1 SEC
9162          ;ENTER WITH COUNT IN TEMP1
9163          ;RETURN IF NO ATTN (ERROR)
9164          ;RETURN +2 IF ATTN FOUND
9165          ;STATUS IS OBTAINED BEFORE THE RETURN FOR EITHER CASE
9166          ;
9167          ;
9168 044176 010446          FATT2:  MOV      R4,-(SP)      ;SAV R4
9169 044200 013704 001222    2$:    MOV      $UNIT,R4
9170 044204 136465 003312 000017  BITB     ATTN(R4),RKASOF+1(R5) ;FIND CORRECT ATTN
9171 044212 001011          BNE      1$
9172 044214 005337 003360    DEC      TEMP1
9173 044220 001367          BNE      2$
9174 044222 005065 000026    CLR      RKMR1(R5)      ;SELECT WORD 0
9175 044226 004737 045132    JSR      PC,GSTAT      ;GET LATEST STATUS.
9176 044232 012604          MOV      (SP)+,R4      ;RESTOR R4
9177 044234 000207          RTS      PC
9178 044236 005065 000026    1$:    CLR      RKMR1(R5)
9179 044242 004737 045132    JSR      PC,GSTAT
9180 044246 012604          MOV      (SP)+,R4      ;RESTOR R4
9181 044250 062716 000002    ADD      #2,(SP)       ;SKIP OVER ERROR
9182 044254 000207          RTS      PC
9183          ;
9184          ;ENTER WITH A COUNT IN TEMP1
9185          ;THE DELAY IS APPROX 17 US/ITERATION + 12 US TO EXIT
9186          ;WHEN COUNT IS 0...BASED ON AN 11/05.
9187          ;
9188 044256 005737 003360    DLY:    TST      TEMP1      ;5.6 US
9189 044262 001403          BEQ      1$            ;2.5 US
9190 044264 005337 003360    DEC      TEMP1        ;6.8 US
9191 044270 000772          BR       DLY          ;2.5 US
9192 044272 000207          1$:    RTS      PC        ;3.8 US
9193          ;
9194          ;THIS ROUTINE TYPES BYPASSED DRIVE#. ENTER WITH DRIVE# IN R0
9195          ;
9196          ;
9197 044274 104401 056331    BYP:    TYPE     ,MSG14      ;BYPASS DRIVE
9198 044300 010046          MOV      R0,-(SP)      ;SAVE R0 FOR TYPEOUT
9199          ;TYPE DR#
9200 044302 104403          TYPOS    1            ;GO TYPE--OCTAL ASCII
9201 044304 001          .BYTE    1            ;TYPE 1 DIGIT(S)
9202 044305 000          .BYTE    0            ;SUPPRESS LEADING ZEROS
9203 044306 000207          RTS      PC
9204          ;
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9205 ; THIS ROUTINE READS ALL MSG A&B WORDS & CHECKS THEM AS REQ'D
9206 ;
9207 044310 017637 000000 001466 CHKMSG: MOV @ (SP),CHKFLG ;PASS MSGS TO BE TESTED
9208 044316 062716 000002 ADD #2,(SP) ;BUMP RETURN ADDR TO 1ST ERROR
9209 044322 004737 045166 JSR PC,GSTAT1 ;GET ALL ACTUAL DRIVE & CONTR STATUS
9210
9211 044326 053737 001222 003412 BIS $UNIT,E.A0 ;SET UNIT #
9212 044334 053737 001222 003416 BIS $UNIT,E.A1
9213 044342 053737 001222 003422 BIS $UNIT,E.A2
9214 044350 053737 001222 003426 BIS $UNIT,E.A3
9215 044356 053737 012114 003412 BIS E.DDT,E.A0 ;ADD EXP. DRV TYPE
9216
9217 044364 013746 003360 MOV TEMP1,-(SP) ;SAVE TEMP 1
9218
9219 044370 013737 003412 003360 MOV E.A0,TEMP1
9220 044376 004737 047362 JSR PC,SBPAR ;GET PARITY FOR MSG A0
9221 044402 013737 003360 003412 MOV TEMP1,E.A0
9222
9223 044410 013737 003416 003360 MOV E.A1,TEMP1
9224 044416 004737 047362 JSR PC,SBPAR ;GET PARITY FOR MSG A1
9225 044422 013737 003360 003416 MOV TEMP1,E.A1
9226
9227 044430 013737 003422 003360 MOV E.A2,TEMP1
9228 044436 004737 047362 JSR PC,SBPAR ;GET PARITY FOR MSG A2
9229 044442 013737 003360 003422 MOV TEMP1,E.A2
9230
9231 044450 013737 003414 003360 MOV E.B0,TEMP1
9232 044456 004737 047362 JSR PC,SBPAR ;GET PARITY FOR MSG B0
9233 044462 013737 003360 003414 MOV TEMP1,E.B0
9234
9235 044470 013737 003420 003360 MOV E.B1,TEMP1
9236 044476 004737 047362 JSR PC,SBPAR ;GET PARITY FOR MSG B1
9237 044502 013737 003360 003420 MOV TEMP1,E.B1
9238
9239 044510 013737 003424 003360 MOV E.B2,TEMP1
9240 044516 004737 047362 JSR PC,SBPAR ;GET PARITY FOR MSG B2
9241 044522 013737 003360 003424 MOV TEMP1,E.B2
9242
9243 044530 013737 003430 003360 MOV E.B3,TEMP1
9244 044536 004737 047362 JSR PC,SBPAR ;GET PARITY FOR MSG B3
9245 044542 013737 003360 003430 MOV TEMP1,E.B3
9246
9247 044550 012637 003360 MOV (SP)+,TEMP1 ;RESTORE TEMP 1
9248 044554 013737 001176 001172 MOV $ESCAPE,$TMP5 ;SAVE ESCAPE
9249
9250 044562 023737 003372 003412 CMP H.A0,E.A0 ;TEST MSG A0
9251 044570 001411 BEQ 2$ ;BR IF OK
9252 044572 012737 044604 001176 MOV #1,$ESCAPE ;ELSE SETUP ESCAPE
9253 044600 011646 MOV (SP),-(SP) ;COPY RET ADDR
9254 044602 000207 RTS PC ;& RETURN TO MAINLINE ERROR
9255
9256 044604 032777 001000 134326 1$: BIT #SW9,@SWR ;RET HERE FROM MAINLINE ERROR
9257 044612 001107 BNE 20$ ;& BR IF LOOP ON ERROR
9258 044614 062716 000002 2$: ADD #2,(SP) ;BUMP RET ADDR TO NEXT ERROR
9259
9260 044620 023737 003374 003414 CMP H.B0,E.B0 ;TEST MSG B0

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9261 044626 001411          BEQ      5$          ;BR IF OK
9262 044630 012737 044642 001176    MOV      #4$, $ESCAPE ;ELSE SETUP ESCAPE
9263 044636 011646          MOV      (SP), -(SP)  ;COPY RET ADDR
9264 044640 000207          RTS      PC          ;& RETURN TO MAINLINE ERROR
9265
9266 044642 032777 001000 134270 4$:  BIT      #SW9, @SWR   ;RETURN HERE FROM MAINLINE ERROR
9267 044650 001070          BNE      20$        ;& BR IF LOOP ON ERROR
9268 044652 062716 000002          5$:  ADD      #2, (SP)  ;BUMP RET ADDR TO NEXT ERROR
9269
9270 044656 023737 003376 003416    CMP      H.A1, E.A1  ;TEST MSG A1
9271 044664 001411          BEQ      8$          ;BR IF OK
9272 044666 012737 044700 001176    MOV      #7$, $ESCAPE
9273 044674 011646          MOV      (SP), -(SP)
9274 044676 000207          RTS      PC
9275
9276 044700 032777 001000 134232 7$:  BIT      #SW9, @SWR
9277 044706 001051          BNE      20$
9278 044710 062716 000002          8$:  ADD      #2, (SP)
9279
9280 044714 023737 003400 003420    CMP      H.B1, E.B1  ;TEST MSG B1
9281 044722 001411          BEQ      11$        ;BR IF OK
9282 044724 012737 044736 001176    MOV      #10$, $ESCAPE
9283 044732 011646          MOV      (SP), -(SP)
9284 044734 000207          RTS      PC
9285
9286 044736 032777 001000 134174 10$: BIT      #SW9, @SWR
9287 044744 001032          BNE      20$
9288 044746 062716 000002          11$: ADD      #2, (SP)
9289
9290 044752 032737 000001 001466 12$: BIT      #T.A2, CHKFLG ;TEST MSG A2?
9291 044760 001402          BEQ      13$        ;BR IF NO
9292 044762 004737 046122          JSR      PC, RCYLD  ;PUT INFO IN CYLDIF, DO NOT CHECK
9293
9294 044766 032737 000002 001466 13$: BIT      #T.B2, CHKFLG ;TEST MSG B2?
9295 044774 001402          BEQ      14$        ;BR IF NO
9296 044776 004737 046174          JSR      PC, RCYLA  ;PUT INFO IN CYLADD, DO NOT CHECK
9297
9298 045002 032737 000004 001466 14$: BIT      #T.B3, CHKFLG ;TEST MSG B3?
9299 045010 001404          BEQ      15$
9300 045012 004737 046232          JSR      PC, RSEC   ;PUT INFO IN SECTOR, DO NOT CHECK
9301 045016 004737 046270          JSR      PC, RHEAD  ;PUT INFO IN HEADA, DO NOT CHECK
9302
9303 045022 013737 001172 001176 15$: MOV      $TMP5, $ESCAPE ;RESTORE ESCAPE
9304 045030 000207          RTS      PC
9305
9306 045032 012706 001100          20$: MOV      #STACK, SP ;RESET STACK PTR
9307 045036 013737 001172 001176    MOV      $TMP5, $ESCAPE ;RESTORE ESCAPE
9308 045044 000177 134040          JMP      @SLPERR
9309
9310
9311
9312
9313
9314 045050 005737 001462          ; THIS ROUTINE CHECKS FOR CERTAIN ERROR CONDITIONS ONLY
9315 045054 001025          ; IE: IF NED, CTO OR MDS SET, MSG A&B ARE INVALID
9316 045056 032737 100000 003322    CKCERR: TST      BYPCERR
          BNE      4$
          BIT      #CERR, HCS1

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9317 045064 001001          BNE 1$          ;BR IF CERR
9318 045066 000207          RTS PC
9319
9320 045070 032737 004000 003322 1$: BIT #CTO,HCS1
9321 045076 001402          BEQ 2$          ;BR IF NOT CTO
9322 045100 104326          ERROR 326      ;CTO ERROR, MSG A&B INVALID
9323 045102 000207          RTS PC
9324
9325 045104 032737 010000 003324 2$: BIT #NED,HCS2
9326 045112 001401          BEQ 3$          ;BR IF NOT NED
9327 045114 104327          ERROR 327      ;NED ERROR, MSG A&B INVALID
9328
9329 045116 032737 001000 003324 3$: BIT #MDS,HCS2
9330 045124 001401          BEQ 4$
9331 045126 104330          ERROR 330      ;MDS ERROR, MSG A&B INVALID
9332
9333 045130 000207          4$: RTS PC
9334
9335
9336      ; THIS ROUTINE DOES THE SELECT DRIVE CMD TO GET STATUS
9337      ; IT THEN WAITS FOR CONTROLLER READY.
9338      ; IF RDY NOT RECEIVED BY A TIMEOUT, AN ERROR IS FLAGGED
9339
9340 045132 013746 003360      GSTAT: MOV TEMP1,-(SP) ;SAVE TEMP1
9341 045136 013765 001222 000010 MOV $UNIT,RKCS2(R5) ;CURRENT DRIVE #
9342 045144 012737 000001 003322 MOV #SELDRV,HCS1
9343 045152 004737 043472 JSR PC,DOCMD ;DR SELDRV (STATUS) CMD & GET CONTR RDY
9344 045156 104117          ERROR 117      ;RDY NOT SET BY END OF SELECT DRIVE CMD
9345 045160 012637 003360 MOV (SP)+,TEMP1 ;RESTOR TEMP1
9346 045164 000207          RTS PC
9347
9348
9349      ; THIS ROUTINE GETS STATUS OF ALL DRIVE REGISTERS (MSG A0-A3, B0-B3)
9350      ; & ALL CONTROLLER REGISTERS
9351
9352 045166 013746 003360      GSTAT1: MOV TEMP1,-(SP) ;SAVE TEMP 1
9353 045172 004737 043722 JSR PC,HOLD ;GET ALL CONTR REGS
9354 045176 012765 100000 000000 MOV #CCLR,RKCS1(R5) ;CLEAR CONTR
9355 045204 013765 001222 000010 MOV $UNIT,RKCS2(R5) ;CURRENT DRIVE #
9356 045212 012765 000003 000026 MOV #3,RKMR1(R5) ;SELECT WORD 3
9357 045220 004737 045416 JSR PC,GSTAT2
9358 045224 104117          ERROR 117      ;RDY NOT SET BY END OF SELECT DRV CMD
9359 045226 013737 003350 003406 MOV HMR2,H.A3 ;STORE MSG A3
9360 045234 013737 003352 003410 MOV HMR3,H.B3 ;STORE MSG B3
9361
9362 045242 012765 100000 000000 MOV #CCLR,RKCS1(R5)
9363 045250 013765 001222 000010 MOV $UNIT,RKCS2(R5)
9364 045256 012765 000002 000026 MOV #2,RKMR1(R5) ;SELECT WORD 2
9365 045264 004737 045416 JSR PC,GSTAT2
9366 045270 104117          ERROR 117      ;RDY NOT SET BY END OF SELECT DRV CMD
9367 045272 013737 003350 003402 MOV HMR2,H.A2 ;STORE MSG A2
9368 045300 013737 003352 003404 MOV HMR3,H.B2 ;STORE MSG B2
9369
9370 045306 012765 100000 000000 MOV #CCLR,RKCS1(R5)
9371 045314 013765 001222 000010 MOV $UNIT,RKCS2(R5)
9372 045322 012765 000001 000026 MOV #1,RKMR1(R5) ;SELECT WORD 1
```



```
9373 045330 004737 045416 JSR PC,GSTAT2
9374 045334 104117 ERROR 117 ;RDY NOT SET BY END OF SELECT DRV CMD
9375 045336 013737 003350 003376 MOV HMR2,H.A1 ;STORE MSG A1
9376 045344 013737 003352 003400 MOV HMR3,H.B1 ;STORE MSG B1
9377
9378 045352 012765 100000 000000 MOV #CCLR,RKCS1(R5)
9379 045360 013765 001222 000010 MOV $UNIT,RKCS2(R5)
9380 045366 004737 045416 JSR PC,GSTAT2
9381 045372 104117 ERROR 117 ;RDY NOT SET BY END OF SEL DRV CMD
9382 045374 013737 003350 003372 MOV HMR2,H.A0 ;STORE MSG A0
9383 045402 013737 003352 003374 MOV HMR3,H.B0 ;STORE MSG B0
9384
9385 045410 012637 003360 MOV (SP)+,TEMP1 ;RESTORE TEMP1
9386 045414 000207 RTS PC
9387
9388
9389 045416 012737 000001 003322 GSTAT2: MOV #SELDRV,HCS1
9390 045424 053737 001170 003322 BIS $TMP4,HCS1 ;ADD CDT IF RK07
9391 045432 013765 003322 000000 MOV HCS1,RKCS1(R5) ;GET STATUS
9392 045440 013737 001414 003360 MOV T10,TEMP1
9393 045446 004737 043634 JSR PC,FRDY1 ;FIND CONTR RDY & STORE DRIVE REGS ONLY
9394 045452 000207 RTS PC ;RET HERE IF NOT RDY
9395 045454 062716 000002 ADD #2,(SP) ;RET HERE IF OK
9396 045460 000207 RTS PC
9397
9398 ; THIS ROUTINE DOES A SUBSYSTEM CLEAR & WAITS FOR CONTROLLER READY
9399 ; IF RDY IS NOT RECEIVED BY THE END OF THE TIMEOUT, AN ERROR IS FLAGGED.
9400 ; THE ROUTINE THEN GETS CURRENT STATUS & CHECKS FOR CONTROLLER ERROR (CERR)
9401 ; RETURN IF CERR SET
9402 ; RETURN +2 IF CERR CLEAR
9403
9404 045462 012765 000040 000010 SUBCLR: MOV #SCLR,RKCS2(R5) ;SUBSYS CLEAR
9405 045470 013737 001414 003360 MOV T10,TEMP1
9406 045476 004737 043566 JSR PC,FRDY ;FIND RDY
9407 045502 104120 ERROR 120 ;RDY NOT SET BY END OF SCLR
9408 045504 013765 001222 000010 MOV $UNIT,RKCS2(R5) ;CURRENT DRIVE #
9409 045512 005065 000026 CLR RKMR1(R5) ;SELECT WORD 0
9410 045516 004737 045132 JSR PC,GSTAT ;GET STATUS
9411 045522 032737 100000 003322 BIT #CERR,HCS1 ;CHECK FOR CONT ERROR
9412 045530 001401 BEQ 1$
9413 045532 000207 RTS PC
9414 045534 062716 000002 1$: ADD #2,(SP) ;SKIP OVER ERROR
9415 045540 000207 RTS PC
9416
9417
9418 ; READ THE SECTOR COUNT IN RKMR3, RIGHT JUSTIFY IT & STORE IT IN 'SECTOR'
9419
9420 045542 012765 000003 000026 RDSEC: MOV #3,RKMR1(R5) ;WORD 3
9421 045550 004737 045132 JSR PC,GSTAT
9422 045554 013737 003352 001406 MOV HMR3,SECTOR
9423 045562 042737 177017 001406 BIC #^C<M.SECT>,SECTOR
9424 045570 006237 001406 ASR SECTOR ;RIGHT JUSTIFY
9425 045574 006237 001406 ASR SECTOR ;SECTOR
9426 045600 006237 001406 ASR SECTOR ;INFO
9427 045604 006237 001406 ASR SECTOR
9428 045610 000207 RTS PC
```

```
9429
9430
9431
9432      ;FIND SECTOR 0 IN 22 SECTOR FORMAT.
9433      ;ERROR FLAGGED IF NOT FOUND BY TIMEOUT
9434
9435      FS022:  MOV     TEMP1,-(SP)      ;SAVE TEMP1
9436      045612 013746 003360      003360  MOV     T5000,TEMP1      ;SETUP TIMEOUT
9437      045616 013737 001424      1$:     JSR     PC,RDSEC      ;READ SECTOR
9438      045624 004737 045542      TST     SECTOR          ;LOOK FOR SECTOR 0
9439      045630 005737 001406      BNE     2$
9440      045634 001005
9441      045636 004737 045542      JSR     PC,RDSEC
9442      045642 005737 001406      TST     SECTOR
9443      045646 001406      BEQ     3$              ;BR IF SAME TWICE
9444      045650 005337 003360      2$:     DEC     TEMP1
9445      045654 001363      BNE     1$              ;TRY AGAIN IF TIMEOUT NOT UP
9446      045656 012637 003360      MOV     (SP)+,TEMP1     ;ELSE RESTORE TEMP1
9447      045662 000207      RTS     PC              ;EXIT
9448      045664 012637 003360      3$:     MOV     (SP)+,TEMP1
9449      045670 062716 000002      ADD     #2,(SP)        ;SKIP OVER ERROR
9450      045674 000207      RTS     PC
9451
9452      ;
9453      ;FIND NEXT SECTOR IN 22 SECTOR FORMAT
9454      ;ERROR FLAGGED IF NOT FOUND BY TIMEOUT
9455
9456      FNS22:  MOV     TEMP1,-(SP)      ;SAVE TEMP 1
9457      045676 013746 003360      003360  MOV     T500,TEMP1     ;SETUP TIMEOUT
9458      045702 013737 001420      1$:     JSR     PC,RDSEC      ;READ SECTOR
9459      045710 004737 045542      CMP     PSEC,SECTOR
9460      045714 023737 001402      001406  BEQ     3$              ;BR IF SAME
9461      045722 001406      JSR     PC,RDSEC      ;ELSE TRY READ DIFFERENT TWICE
9462      045724 004737 045542      CMP     PSEC,SECTOR
9463      045730 023737 001402      001406  BNE     2$              ;BR IF DIFFERENT TWICE
9464      045736 001006      3$:     DEC     TEMP1
9465      045740 005337 003360      BNE     1$              ;ELSE TRY AGAIN IF TIME LEFT
9466      045744 001361      MOV     (SP)+,TEMP1     ;RESTORE TEMP 1
9467      045746 012637 003360      RTS     PC
9468      045752 000207      2$:     MOV     (SP)+,TEMP1     ;RESTORE TEMP 1
9469      045754 012637 003360      ADD     #2,(SP)        ;SKIP OVER ERROR
9470      045760 062716 000002      RTS     PC
9471
9472      ;
9473      ;READ THE CYL DIFF/OFFSET IN RKMR2, RIGHT JUSTIFY IT & STORE IT IN 'CYLDIF'
9474      RDCYLD: MOV     #2,RKMR1(R5)      ;WORD 2
9475      045766 012765 000002      000026  JSR     PC,GSTAT
9476      045774 004737 045132      MOV     HMR2,CYLDIF
9477      046000 013737 003350      001362  BIC     MASK1,CYLDIF
9478      046006 043737 012112      001362  ASR     CYLDIF          ;RIGHT JUSTIFY
9479      046014 006237 001362      ASR     CYLDIF          ;CYL DIFF/OFFSET
9480      046020 006237 001362      ASR     CYLDIF          ;INFO
9481      046024 006237 001362      ASR     CYLDIF
9482      046030 006237 001362      CMP     CYLDIF,MASK     ;CHK TO SEE IF RET IN COMPL. FORM
9483      046034 023737 001362      012110  BNE     1$              ;BR IF NOT
9484      046042 001002      CLR     CYLDIF          ;CLR IF YES
```



```
9485 046050 000207 1$: RTS PC
9486
9487 :READ THE CYL ADDR IN RKMR3, RIGHT JUSTIFY IT & STORE IT IN 'CYLADD'
9488
9489 046052 012765 000002 000026 RDCYLA: MOV #2,RKMR1(R5) ;WORD 2
9490 046060 004737 045132 JSR PC,GSTAT
9491 046064 013737 003352 001364 MOV HMR3,CYLADD
9492 046072 043737 012112 001364 BIC MASK1,CYLADD
9493 046100 006237 001364 ASR CYLADD ;RIGHT JUSTIFY
9494 046104 006237 001364 ASR CYLADD ;CYL ADDR
9495 046110 006237 001364 ASR CYLADD ;INFO
9496 046114 006237 001364 ASR CYLADD
9497 046120 000207 RTS PC
9498
9499 : READ THE CYL DIFF/OFFSET IN H.A2, RIGHT JUSTIFY IT & STORE IT IN 'CYLDIF'
9500
9501 046122 013737 003402 001362 RCYLD: MOV H.A2,CYLDIF
9502 046130 043737 012112 001362 BIC MASK1,CYLDIF
9503 046136 006237 001362 ASR CYLDIF ;RIGHT JUSTIFY
9504 046142 006237 001362 ASR CYLDIF
9505 046146 006237 001362 ASR CYLDIF
9506 046152 006237 001362 ASR CYLDIF
9507 046156 023737 001362 012110 CMP CYLDIF,MASK ;CHK TO SEE IF RET IN COMPL. FORM
9508 046164 001002 BNE 1$ ;BR IF NO
9509 046166 005037 001362 CLR CYLDIF ;ELSE CLEAR
9510 046172 000207 1$: RTS PC
9511
9512 : READ THE CYL ADDR IN H.B2, RIGHT JUSTIFY IT & STORE IT IN 'CYLADD'
9513
9514 046174 013737 003404 001364 RCYLA: MOV H.B2,CYLADD
9515 046202 043737 012112 001364 BIC MASK1,CYLADD
9516 046210 006237 001364 ASR CYLADD ;RIGHT JUSTIFY
9517 046214 006237 001364 ASR CYLADD
9518 046220 006237 001364 ASR CYLADD
9519 046224 006237 001364 ASR CYLADD
9520 046230 000207 RTS PC
9521
9522 : READ THE SECTOR COUNT IN H.B3, RIGHT JUSTIFY IT & STORE IT IN 'SECTOR'
9523
9524 046232 013737 003410 001406 RSEC: MOV H.B3,SECTOR
9525 046240 042737 177017 001406 BIC #^C<M.SECT>,SECTOR ;CLEAR UNWANTED INFO
9526 046246 006237 001406 ASR SECTOR ;RIGHT JUSTIFY
9527 046252 006237 001406 ASR SECTOR
9528 046256 006237 001406 ASR SECTOR
9529 046262 006237 001406 ASR SECTOR
9530 046266 000207 RTS PC
9531
9532 : READ THE HEAD ADDR IN H.B3, RIGHT IT & STORE IT IN 'HEAD A'
9533
9534 046270 013737 003410 001432 RHEAD: MOV H.B3,HEAD A
9535 046276 042737 170777 001432 BIC #^C<M.HEAD>,HEAD A ;CLEAR UNWANTED INFO
9536 046304 006237 001432 ASR HEAD A ;RIGHT JUSTIFY IT
9537 046310 000337 001432 SWAB HEAD A
9538 046314 000207 RTS PC
9539
9540 :FIND LIMIT DETECT ON SEEK IN RKMR3 BEFORE TIMEOUT
```

```
9541 ;RETURN IF NOT FOUND: ERROR
9542 ;RETURN+2 IF FOUND: SKIP OVER ERROR
9543 ;
9544 046316 005037 001460 003360 FLIM: CLR LIMERR ;LIMIT DETECT ERROR FLAG
9545 046322 012737 177777 000026 MOV #-1,TEMP1 ;SETUP TIMEOUT
9546 046330 012765 000001 000026 MOV #1,RKMR1(R5) ;WORD 1
9547 046336 004737 045132 1$: JSR PC,GSTAT
9548 046342 032737 020000 003352 BIT #D.LIMD,HMR3
9549 046350 001006 BNE 2$ ;EXIT IF SET
9550 046352 005337 003360 DEC TEMP1
9551 046356 001367 BNE 1$
9552 046360 005237 001460 INC LIMERR ;SET LIMIT DETECT FLAG
9553 046364 000207 RTS PC
9554 046366 062716 000002 2$: ADD #2,(SP) ;SKIP OVER ERROR
9555 046372 000207 RTS PC
9556 ;
9557 ;ROUTINE TO FIND HEADS HOME IN RKMR2 WORD 1 BEFORE TIMEOUT
9558 ;ENTER WITH TIME IN SECONDS IN TEMP2
9559 ;RETURN IF NOT FOUND
9560 ;RETURN+2 IF FOUND - SKIP OVER ERROR
9561 ;
9562 046374 012737 177777 003360 FHDHM: MOV #-1,TEMP1 ;ALL 1'S
9563 046402 012765 000001 000026 MOV #1,RKMR1(R5) ;WORD 1
9564 046410 004737 045132 1$: JSR PC,GSTAT
9565 046414 032737 000040 003350 BIT #D.HDHM,HMR2
9566 046422 001007 BNE 2$
9567 046424 005337 003360 DEC TEMP1
9568 046430 001367 BNE 1$
9569 046432 005337 003362 DEC TEMP2
9570 046436 001356 BNE FHDHM
9571 046440 000207 RTS PC
9572 046442 062716 000002 2$: ADD #2,(SP) ;SKIP OVER ERROR
9573 046446 000207 RTS PC
9574 ;
9575 ;ROUTINE TO FIND LOAD HEADS IN RKMR2 WORD 1 BEFORE TIMEOUT
9576 ;RETURN IF NOT FOUND
9577 ;RETURN+2 IF FOUND: SKIP OVER ERROR
9578 ;
9579 046450 012737 177777 003360 FLOAD: MOV #-1,TEMP1 ;SETUP TIMEOUT
9580 046456 012765 000001 000026 MOV #1,RKMR1(R5) ;WORD 1
9581 046464 004737 045132 1$: JSR PC,GSTAT
9582 046470 032737 010000 003350 BIT #D.LOAD,HMR2
9583 046476 001004 BNE 2$
9584 046500 005337 003360 DEC TEMP1
9585 046504 001367 BNE 1$
9586 046506 000207 RTS PC
9587 046510 062716 000002 2$: ADD #2,(SP) ;SKIP OVER ERROR
9588 046514 000207 RTS PC
9589 ;
9590 ;ROUTINE TO FIND SPOK BEFORE TIMEOUT
9591 ;ENTER WITH APPROX TIME IN TEMP2
9592 ;RETURN IF NOT CLEARED
9593 ;RETURN +2 IF CLEARED TO SKIP OVER ERROR
9594 ;
9595 046516 012737 177777 003360 FSPOK: MOV #-1,TEMP1 ;ALL 1'S
9596 046524 012765 000001 000026 MOV #1,RKMR1(R5) ;WORD 1
```



```
9597 046532 004737 045132 1$: JSR PC,GSTAT
9598 046536 032737 001000 003350 BIT #D.SPOK,HMR2 ;SEE IF SPOK CLEARED
9599 046544 001407 BEQ 2$
9600 046546 005337 003360 DEC TEMP1
9601 046552 001367 BNE 1$
9602 046554 005337 003362 DEC TEMP2
9603 046560 001356 BNE FSPOK
9604 046562 000207 RTS PC
9605 046564 062716 000002 2$: ADD #2,(SP) ;SKIP OVER ERROR
9606 046570 000207 RTS PC
9607
9608 ;FILL HEADER TABLE WITH 66 WORDS OF VALID HEADERS
9609 ;ENTER WITH CYL # IN 'CALADD'
9610 ;ENTER WITH HEAD # IN 'HEAD'
9611 ;ENTER WITH FORMAT IN 'FORMAT'
9612
9613 046572 010046 FHDTAB: MOV R0,-(SP) ;SAV R0
9614 046574 010146 MOV R1,-(SP) ;SAV R1
9615 046576 012700 001470 MOV #HDTAB,R0 ;HEADER WORD TABLE ADDR
9616 046602 005001 CLR R1 ;SECTOR COUNTER
9617 046604 013737 001430 001434 MOV HEAD,HD1
9618 046612 006337 001434 ASL HD1
9619 046616 006337 001434 ASL HD1
9620 046622 006337 001434 ASL HD1
9621 046626 006337 001434 ASL HD1
9622 046632 006337 001434 ASL HD1 ;SETUP HEAD # FOR WORD 2 OF HEADER
9623 046636 013737 001436 001440 MOV FORMAT,FMT1
9624 046644 000337 001440 SWAB FMT1
9625 046650 006337 001440 ASL FMT1 ;SETUP FORMAT FOR WORD 2 OF HEADER
9626
9627 046654 013720 001366 1$: MOV CALADD,(R0)+ ;HEADER WORD 1-CYL ADDR
9628 046660 010110 MOV R1,(R0) ;HEADER WORD 2-SECTOR NO
9629 046662 053710 001434 BIS HD1,(R0) ;
9630 046666 053710 001440 BIS FMT1,(R0) ; -HEAD NO
9631 046672 005737 001464 TST BYPFMT ; -FORMAT
9632 046676 001403 BEQ 2$ ;BR IF TRUE FORMAT
9633 046700 052710 140000 BIS #<BIT14!BIT15>,(R0) ;SET GOOD SECTOR FLAGS
9634 046704 000402 BR 3$
9635 046706 004737 046766 2$: JSR PC,SECFLG ;GET SECTOR FLAGS
9636
9637 046712 013737 001366 003360 3$: MOV CALADD,TEMP1
9638 046720 011037 003362 MOV (R0),TEMP2
9639 046724 043737 001366 003362 BIC CALADD,TEMP2
9640 046732 042037 003360 BIC (R0)+,TEMP1
9641 046736 053737 003360 003362 BIS TEMP1,TEMP2
9642 046744 013720 003362 MOV TEMP2,(R0)+ ;HEADER WORD 3-HEADER CHECK
9643
9644 046750 005201 INC R1 ;SECTOR CTR
9645 046752 020127 000026 CMP R1,#22. ;ALL 22 SECTORS DONE? (66 WORDS)
9646 046756 001336 BNE 1$ ;BR IF NO
9647
9648 046760 012601 MOV (SP)+,R1 ;RESTOR R1
9649 046762 012600 MOV (SP)+,R0 ;RESTOR R0
9650 046764 000207 RTS PC
9651
9652 ;
```

```
9653 ; THIS ROUTINE GETS INFORMATION FROM THE BAD SECTOR TABLE FILLED BY A PREVIOUS TEST
9654 ; & SETS BITS 14 & 15 APPROPRIATELY.
9655
9656 046766 010246 SECFLG: MOV R2,-(SP) ;SAVE R2
9657 046770 005737 001436 TST FORMAT
9658 046774 001016 BNE 1$ ;BR IF 20 SECTOR FORMAT
9659 046776 012702 002314 MOV #BSE22H+8.,R2
9660 047002 004737 047036 JSR PC,FLGTST ;GET HARDWARE DETECTED FLAG
9661 047006 052710 100000 BIS #BIT15,(R0) ;RETURN HERE IF GOOD SECTOR
9662
9663 047012 012702 054504 MOV #BSE22S+8.,R2 ;ELSE RETURN HERE
9664 047016 004737 047036 JSR PC,FLGTST ;GET SOFTWARE DETECTED FLAG
9665 047022 052710 040000 BIS #BIT14,(R0) ;RETURN HERE IF GOOD SECTOR
9666
9667 047026 012602 MOV (SP)+,R2 ;ELSE RETURN HERE
9668 047030 000207 RTS PC
9669
9670
9671 047032 012602 1$: MOV (SP)+,R2 ;RESTORE R2
9672 047034 000207 RTS PC
9673
9674
9675 ; THIS ROUTINE DOES THE ACTUAL SCANNING OF THE BAD SECTOR TABLES
9676 ; ENTER WITH THE ADDRESS OF TABLE (BSE22H, BSE22S, ETC) IN TEMP1
9677 ; RETURN IF NO COMPARE
9678 ; RETURN +4 IF COMPARE
9679
9680 047036 010346 FLGTST: MOV R3,-(SP) ;SAVE R3
9681
9682 047040 021227 177777 1$: CMP (R2),#-1 ;SEE IF ALL 1'S
9683 047044 001002 BNE 2$ ;BR IF NO
9684 047046 012603 MOV (SP)+,R3 ;RESTORE R3
9685 047050 000207 RTS PC
9686
9687 047052 022237 001366 2$: CMP (R2)+,CALADD ;SEE IF = CYL #, & ADV PTR TO TRACK/SECTOR WORD
9688 047056 001403 BEQ 3$
9689 047060 062702 000002 ADD #2,R2 ;GO TO NEXT CYL WORD IN TABLE
9690 047064 000765 BR 1$
9691
9692 047066 013703 001430 3$: MOV HEAD,R3 ;GET HEAD # FROM FHDTAB ROUTINE
9693 047072 000303 SWAB R3
9694 047074 050103 BIS R1,R3 ;ADD SECTOR # FROM FHDTAB ROUTINE
9695 047076 022203 CMP (R2)+,R3 ;SECTOR/HEAD COMPARE? & INCR TO NEXT CYL WORD
9696 047100 001401 BEQ 4$ ;BR IF YES
9697 047102 000756 BR 1$ ;TRY NEXT CYL
9698
9699 047104 012603 4$: MOV (SP)+,R3 ;RESTORE R3
9700 047106 062716 000004 ADD #4,(SP) ;INCREMENT RET ADDR
9701 047112 000207 RTS PC
9702
9703 ; THIS ROUTINE SORTS THE RHTAB TABLE FROM WHATEVER SECTOR IT BEGINS
9704 ; WITH AND RE-WITES THE INFO IN SRTTAB TABLE TO BEGIN WITH SECTOR 0
9705
9706 047114 010046 SORT: MOV R0,-(SP) ;SAVE R0
9707 047116 010146 MOV R1,-(SP) ;SAVE R1
9708 047120 004737 045542 JSR PC,RDSEC
```



```
9709 047124 062737 000001 001406      ADD    #1,SECTOR
9710 047132 004737 047222                JSR    PC,MULT6      ;MULT SECTOR BY 6
9711
9712 047136 012700 000204                MOV    #132,R0
9713 047142 163700 001406                SUB    SECTOR,R0    ;R0-SECTOR TO R0 = INDEX
9714 047146 010037 001406                MOV    R0,SECTOR
9715 047152 062737 001674 001406      ADD    #RHTAB,SECTOR ;SAVE INDEX
9716
9717 047160 062700 001674                ADD    #RHTAB,R0    ;INDEX TO BOT HALF OF RHTAB
9718 047164 012701 002100                MOV    #SRTTAB,R1  ;INDEX TO TOP HALF OF SRTTAB
9719
9720 047170 012021                1$:   MOV    (R0)+,(R1)+ ;PUT BOTTOM OF RHTAB TO TOP OF SRTTAB
9721 047172 020027 002100                CMP    R0,#RHTAB+132.
9722 047176 001374                BNE    1$
9723
9724 047200 012700 001674                2$:   MOV    #RHTAB,R0    ;PUT TOP OF RHTAB TO BOT OF SRTTAB
9725 047204 012021                MOV    (R0)+,(R1)+
9726 047206 020037 001406                CMP    R0,SECTOR
9727 047212 001374                BNE    2$
9728
9729 047214 012601                MOV    (SP)+,R1    ;RESTOR R1
9730 047216 012600                MOV    (SP)+,R0    ;RESTOR R0
9731 047220 000207                RTS    PC
9732
9733
9734                ;MULT BY 6. ENTER WITH DESIRED # IN 'SECTOR'
9735                ;
9736 047222 006337 001406      MULT6: ASL    SECTOR    ;2 X SECTOR
9737 047226 013746 001406      MOV    SECTOR,-(SP)
9738 047232 006337 001406      ASL    SECTOR    ;4 X SECTOR
9739 047236 062637 001406      ADD    (SP)+,SECTOR ;(4 X S)+(2 X S) = 6 X SECTOR
9740 047242 000207                RTS    PC
9741
9742                ;
```

CZR6HFO UNIBUS RK6 DR PT1
CZR6HF.P11 04-JAN-82 12:44

MACY11 30(1046) 04-JAN-82 13:01 G 15
GET VALUE FOR SOFTWARE SWITCH REGISTER PAGE 189

SEQ 0188

```
9743 ;ROUTINE TO TURN L OR P CLOCK INTERRUPT ON
9744 ;
9745 047244 005037 001376 CLKON: CLR TIMUP
9746 047250 005737 003466 TST PCLKF
9747 047254 001004 BNE 1$ ;BRANCH IF P-CLOCK PRESENT
9748 047256 012777 000100 132042 MOV #100,@LKS ;L-CLOCK, ENABLE INT
9749 047264 000207 RTS PC
9750 047266 012777 177777 132026 1$: MOV #-1,@PKSB ;P-CLOCK, ALL 1'S
```


CZR6HFO UNIBUS RK6 DR PT1
CZR6HF.P11 04-JAN-82 12:44

MACY11 30(1046) 04-JAN-82 13:01 H 15
GET VALUE FOR SOFTWARE SWITCH REGISTER PAGE 190

SEQ 0189

9751	047274	012777	000135	132016	MOV	#135,@PKS	:ENABLE INT, CT UP, REP INT
9752	047302	000207			RTS	PC	:LINE FREQ & RUN
9753							
9754					:	KW11-L & KW11-P INTERRUPT HANDLER	
9755					:		
9756	047304	005037	001376		CLOCK:	CLR	TIMUP
9757	047310	005337	001372			DEC	COUNT
9758	047314	001010				BNE	1\$
9759	047316	013737	001370	001372		MOV	HZ,COUNT


```
9773  
9774  
9775  
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9777  
9778  
9779  
9780  
9781  
9782  
9783  
9784 047362 010046  
9785 047364 010146  
9786 047366 012700 000021  
9787 047372 005001  
9788 047374 000241  
9789  
9790 047376 006137 003360  
9791 047402 103001  
9792 047404 005201  
9793 047406 005300  
9794 047410 001372  
9795  
9796 047412 032701 000001  
9797 047416 001003  
9798 047420 052737 100000 003360  
9799 047426 012601  
9800 047430 012600  
9801 047432 000207  
9802  
9803  
9804  
9805  
9806  
9807  
9808 047434 032777 001000 131476  
9809 047442 001406  
9810 047444 105737 001103  
9811 047450 001403  
9812 047452 013716 001110  
9813 047456 000002  
9814  
9815 047460 011637 001110  
9816 047464 000002  
9817  
9818  
9819  
9820  
9821  
9822  
9823  
9824  
9825  
9826  
9827 047466 005037 001176  
9828 047472 005037 001410
```

```
;  
; THIS ROUTINE GENERATES PARITY FOR THE EXPECTED MSGS  
; ENTER WITH THE EXPECTED WORD IN TEMP1  
; TEMP1 IS ROTATED LEFT 17 TIMES. EACH TIME THE CARRY BIT IS SET,  
; R1 IS INCREMENTED. AT THE END OF 17 ROTATES ( TEMP1 BACK TO ORIG),  
; R1 BIT 0 IS EXAMINED. IF IT IS SET, INDICATING AN ODD # OF 1'S,  
; THE PARITY BIT IS NOT SET IN B  
; IF IT IS NOT SET, INDICATING AN EVEN # OF 1'S ,THE PARITY BIT IS  
; SET IN TEMP1  
; SBPAR: MOV R0,-(SP) ;SAVE R0  
; MOV R1,-(SP) ;SAVE R1  
; MOV #17,,R0 ;SHIFT COUNTER  
; CLR R1 ;COUNT # OF 1'S IN TEMP1  
; CLC ;CLEAR CARRY  
1$: ROL TEMP1  
; BCC 2$ ;BR IF CARRY CLEAR  
; INC R1 ;COUNT # OF 1'S  
2$: DEC R0 ;SHIFT COUNTER  
; BNE 1$  
; BIT #BIT0,R1  
; BNE 3$ ;BR IF ODD # IN R0  
3$: BIS #M.PAR,TEMP1 ;SET PARITY BIT  
; MOV (SP)+,R1 ;RESTORE R1  
; MOV (SP)+,R0 ;RESTORE R0  
; RTS PC  
;  
; ROUTINE TO ENABLE LOOPING ON INTERMITTANT ERRORS  
; WHEN $LPERR SET BY OTHER THAN SCOPE ROUTINE  
; IE: MY LOOP MACRO  
; SCOP1$: BIT #SW9,@SWR ;LOOP ON ERROR?  
; BEQ 1$ ;BR IF NO  
; TSTB $ERFLG ;HAD ERROR?  
; BEQ 1$ ;BR IF NO  
; MOV $LPERR,(SP)  
; RTI  
1$: MOV (SP),$LPERR ;SET LOOP ADDR FOR TIGHT SCOPE LOOP  
; RTI  
;  
; CHECK FOR SW14 (LOOP ON TEST) OR SW8 (LOOP ON SPECIFIC TEST)  
; RETURN IF NEITHER SET  
; RETURN +2 IF EITHER SET  
;  
; THIS SUBROUTINE IS USED AT THE END OF ANY TEST THAT REQUIRES  
; RECONDITIONING OF THE DRIVE BEFORE LOOPING ON AN ERROR OR TEST  
; SWTST: CLR $ESCAPE  
; CLR LPFLG
```

```

9829 047476 032777 040000 131434 BIT #SW14,@SWR ;LOOP ON TEST?
9830 047504 001403 BEQ 3$ ;BR IF NO
9831 047506 062716 000002 1$: ADD #2,(SP)
9832 047512 000207 2$: RTS PC
9833
9834 047514 032777 000400 131416 3$: BIT #SW8,@SWR ;LOOP ON SPECIFIC TEST?
9835 047522 001773 BEQ 2$ ;BR IF NO
9836 047524 127737 131410 001102 CMPB @SWR,$STSTNM ;RIGHT TEST? SWR <7:0>
9837 047532 001765 BEQ 1$ ;BR IF YES
9838 047534 000207 RTS PC
9839
9840
9841 ; THIS ROUTINE IS ENTERED BY TYPING A CONTROL-C.
9842 ; IT IS USED TO ALLOW THE OPERATOR TO HALT THE CPU WHILE INSURING
9843 ; THAT HEADS ARE LOADED & FORMATTING IS VALID BEFORE ACTUALLY HALTING
9844 ; THE CPU.
9845
9846 047536 022626 STOP: CMP (SP)+,(SP)+ ;RESTORE STACK FROM INTERRUPT
9847
9848 047540 004737 045462 JSR PC,SUBCLR
9849 047544 104024 ERROR 24 ;CERR AFTER
9850
9851 047546 005737 003304 TST UNLD ;SEE IF HEADS UNLOADED
9852 047552 001431 BEQ 3$ ;BR IF NO
9853 047554 005737 000042 TST 42 ;SEE IF MANUAL OR AUTO MODE
9854 047560 001403 BEQ 1$ ;BR IF MANUAL MODE
9855 047562 104401 057041 TYPE ,MSG74 ;PGM ABORT PENDING
9856 047566 000402 BR 2$
9857 047570 104401 057070 1$: TYPE ,MSG75 ;HALT PENDING
9858 047574 2$:
9859
9860 047574 004737 045462 JSR PC,SUBCLR
9861 047600 104024 ERROR 24 ;CERR AFTER SCLR
9862
9863 047602 012737 000011 003322 MOV #SRTSPL,HCS1
9864 047610 004737 043472 JSR PC,DOCMD ;DO START SPINDLE CMD & GET CONTR RDY
9865 047614 104121 ERROR 121 ;RDY NOT FOUND AFTER ST SPIN CMD.
9866
9867 047616 013737 001420 003362 MOV T500,TEMP2 ;SETUP TIMEOUT
9868 047624 004737 044102 JSR PC,FATT1 ;FIND ATTN
9869 047630 104067 ERROR 67 ;NO ATTN AFTER ST SPIN CMD.
9870
9871 047632 005037 003304 CLR UNLD
9872
9873 047636 005737 003306 3$: TST BADHDR ;SEE IF HEADERS VALID
9874 047642 001460 BEQ 4$ ;BR IF YES
9875 047644 005237 003310 INC HPEND
9876
9877 047650 012765 100000 000000 MOV #CCLR,RKCS1(R5)
9878 047656 013765 001222 000010 MOV $UNIT,RKCS2(R5)
9879 047664 012737 000013 003322 MOV #RECAL,HCS1
9880 047672 004737 043472 JSR PC,DOCMD ;DO RECAL CMD & GET CONTR RDY
9881 047676 104124 ERROR 124 ;RDY NOT SET AFTER RECAL CMD
9882
9883 047700 012765 000001 000026 MOV #1,RKMR1(R5) ;SELECT WORD 1
9884 047706 004737 045132 JSR PC,GSTAT
    
```



```

9885 047712 032737 020000 003350      BIT      #D.RTZ,HMR2
9886 047720 001001                BNE      64$
9887 047722 104244                ERROR    244
9888 047724 013737 001414 003362 64$:    MOV      T10,TEMP2      ;RTZ NOT SET DURING RECAL CMD
9889 047732 004737 044102        JSR      PC,FATT1      ;SETUP TIMEOUT
9890 047736 104055                ERROR    55            ;FIND ATTN
9891                                ;NO ATTN AFTER RECAL CMD
9892 047740 012765 100000 000000    MOV      #CCLR,RKCS1(R5)
9893 047746 013765 001222 000010    MOV      $UNIT,RKCS2(R5) ;DRIVE#
9894 047754 012737 000005 003322    MOV      #CLEAR,HCS1
9895 047762 004737 043472        JSR      PC,DOCMD      ;DO DRIVE CLEAR CMD & GET CONTR RDY
9896 047766 104151                ERROR    151          ;NO RDY AFTER DRIVE CLEAR CMD
9897 047770 004737 044050        JSR      PC,TSTATN     ;TEST FOR ATTN
9898 047774 000401                BR       66$
9899 047776 104154                ERROR    154          ;ATTN NOT CLEARED AFTER DRIVE CLEAR CMD
9900 050000        66$:
9901
9902
9903 050000 000137 031366        JMP      FORM          ;WRITE VALID FORMATS
9904
9905 050004 005737 000042        4$:    TST      42
9906 050010 001410                BEQ      5$            ;SEE IF MANUAL OR AUTO MODE
9907 050012 104401 057112        TYPE    ,MSG76        ;BR IF MANUAL MODE
9908 050016 005037 043006        CLR      $EOPCT       ;PGM ABORTED
9909 050022 005037 001176        CLR      $ESCAPE      ;SET UP EOP TO EXIT TO MONITOR
9910 050026 000137 042760        JMP      $EOP1
9911                                ;ABORT PROGRAM
9912 050032 104401 057130        5$:    TYPE    ,MSG77        ;CPU HALTED
9913 050036 000000                HALT
9914 050040 000137 007744        JMP      ST5          ;START OVER IF CONTINUE PRESSED
9915
9916
9917                                ;CHECK IF HALT PENDING
9918                                ;RET IF YES
9919                                ;RET+4 IF NO
9920
9921 050044 005037 003306        HPEN:   CLR      BADHDR ;CLR VALID HALT FLAG
9922 050050 005737 003310                TST      HPEND        ;SEE IF HALT PENDING
9923 050054 001002                BNE      1$            ;BR IF YES
9924 050056 062716 000004        ADD      #4,(SP)      ;ELSE BUMP RET ADDR
9925 050062 000207        1$:    RTS      PC          ;& RET
9926
9927                                .SBTTL UNEXPECTED TIMEOUT HANDLER
9928
9929
9930                                ;THIS ROUTINE IS ENTERED IF THERE IS
9931                                ; A. NON EXISTANT MEMORY (NO SSYN)
9932                                ; B. BOUNDRY ERROR
9933                                ; C. STACK OVERFLOW
9934
9935
9936 050064 011600        BADTMO: MOV      (SP),R0      ;SAVE PC WHERE TIMEOUT OCCURRED.
9937 050066 005740                TST      -(R0)        ;GET PC BEFORE UPDATE
9938 050070 032777 020000 131042    BIT      #SW13,@SWR   ;INHIBIT ERROR TYP0UT?
9939 050076 001005                BNE      1$            ;YES, DON'T TYPE
9940 050100 104401 057306        TYPE    ,EM3          ;ABORT TESTS,UNEXP T.O. @ PC=

```

```
9941 050104 010046          MOV    R0,-(SP)          ;;SAVE R0 FOR TYPEOUT
9942                                ;;TYPE PC
9943 050106 104403          TYPOS                                ;;GO TYPE--OCTAL ASCII
9944 050110      006          .BYTE 6                  ;;TYPE 6 DIGIT(S)
9945 050111      000          .BYTE 0                  ;;SUPPRESS LEADING ZEROS
9946 050112 032777 001000 131020 1$: BIT    #SW9,@SWR          ;LOOP ON ERROR?
9947 050120 001403          BEQ    2$                  ;NO, BRANCH
9948 050122 022626          CMP    (SP)+,(SP)+       ;YES, RESTORE STACK
9949 050124 000177 130756          JMP    @SLPADR          ;GO TO STARTING ADDR OF TEST
9950                                ;THAT GAVE BAD TIMEOUT
9951 050130 032777 040000 131002 2$: BIT    #SW14,@SWR         ;LOOP ON TEST?
9952 050136 001401          BEQ    3$                  ;NO BRANCH
9953 050140 000002          RTI                                ;YES
9954
9955 050142 000000          3$:  HALT                  ;UNEXPECTED TIME OUT OCCURRED
9956                                ;AS INDICATED. YOU CAN LOOP ON
9957                                ;ERROR, LOOP ON TEST OR INHIBIT
9958                                ;ERROR TYPEOUT BY SETTING THOSE
9959                                ;SWITCHES.
9960
9961 050144 022626          CMP    (SP)+,(SP)+       ;RESTORE STACK
9962 050146 000137 042760          JMP    $EOP1           ;ABORT TESTS
9963
9964                                .SBTTL MEMORY CHECK ENABLE TRAP
9965
9966 050152 012737 050166 001176 MEMERR: MOV    #1$,$ESCAPE        ;LOAD ESCAPE
9967 050160 011637 001334          MOV    (SP),TRAPPC      ;STORE PC
9968 050164 104236          ERROR 236              ;UNEXP MEM PARITY TRAP
9969
9970 050166 005037 001176 1$: CLR    $ESCAPE
9971 050172 032777 001000 130740 BIT    #SW9,@SWR          ;CHECK IF LOOP ON ERROR
9972 050200 001001          BNE    2$                  ;YES, FORCE STACK AND TRY AGAIN
9973 050202 000002          RTI                        ;ELSE RETURN
9974
9975 050204 012706 001100 2$: MOV    #STACK,SP        ;INIT STACK
9976 050210 000177 130674          JMP    @SLPERR          ;LOOP ON ERROR
9977
9978                                .SBTTL RK06 INTERRUPT HANDLER
9979
9980 050214 011600          INTER: MOV    (SP),R0      ;SAVE PC WHERE INT OCCURRED.
9981 050216 005740          TST    -(R0)             ;GET PC BEFORE UPDATE.
9982 050220 104401 055774          TYPE    ,MSG6           ;INT AT PC=
9983 050224 010046          MOV    R0,-(SP)         ;SAVE R0 FOR TYPEOUT
9984                                ;TYPE PC
9985 050226 104403          TYPOS                                ;GO TYPE--OCTAL ASCII
9986 050230      006          .BYTE 6                  ;TYPE 6 DIGIT(S)
9987 050231      000          .BYTE 0                  ;SUPPRESS LEADING ZEROS
9988 050232 000002          RTI
9989
9990                                .SBTTL POWER DOWN AND UP ROUTINES
9991
9992                                ;POWER DOWN ROUTINE
9993
9994 050234 012737 050246 000024 $PWRDN: MOV    #$PWRUP,PWRVEC ;SET UP VECTOR
9995 050242 000000          HALT
9996 050244 000776          BR     .-2              ;HANG UP.
```

B
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```
9997
9998
9999
10000 050246 005037 050320
10001 050252 005237 050320
10002 050256 001375
10003 050260 012737 050234 000024
10004 050266 012737 000340 000026
10005 050274 012737 000340 000036
10006 050302 012706 001100
10007 050306 104401 056162
10008 050312 000005
10009 050314 000137 012214
10010
10011 050320 000000
10012

;POWER UP ROUTINE
$PWRUP: CLR $PWRCT ;WAIT LOOP FOR TTY
1$: INC $PWRCT ;WAIT FOR THE INCR
BNE 1$ ;OF WORD
MOV #PWRDN,PWRVEC ;SET POWER DOWN VECTOR
MOV #PR7,PWRVEC+2 ;PRIORITY 7
MOV #PR7,TRAPVEC+2 ;LOCKOUT ALL INTERRUPTS FOR TRAPS
MOV #STACK,SP ;INITIALIZE STACK
TYPE .MSG11 ;REPORT POWER FAIL
RESET
JMP PFSRT

$PWRCT: 0 ;WAIT COUNT FOR TTY
```

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Z

```
10013 .SBTTL SCOPE HANDLER ROUTINE
10014
10015 ::*****
10016 ::*THIS ROUTINE CONTROLS THE LOOPING OF SUBTESTS. IT WILL INCREMENT
10017 ::*AND LOAD THE TEST NUMBER($STNM) INTO THE DISPLAY REG.(DISPLAY<7:0>)
10018 ::*AND LOAD THE ERROR FLAG ($ERFLG) INTO DISPLAY<15:08>
10019 ::*THE SWITCH OPTIONS PROVIDED BY THIS ROUTINE ARE:
10020 ::*SW14=1 LOOP ON TEST
10021 ::*SW11=1 INHIBIT ITERATIONS
10022 ::*SW09=1 LOOP ON ERROR
10023 ::*SW08=1 LOOP ON TEST IN SWR<7:0>
10024 ::*CALL
10025 ::* SCOPE ;:SCOPE=IOT
10026
10027 $SCOPE:
10028 050322 104407 CKSWR ;:TEST FOR CHANGE IN SOFT-SWR
10029 050324 032777 040000 130606 1$: BIT #BIT14,@SWR ;:LOOP ON PRESENT TEST?
10030 050332 001114 BNE $OVER ;:YES IF SW14=1
10031 ;:*****START OF CODE FOR THE XOR TESTER*****
10032 050334 000416 $XTSTR: BR 6$ ;:IF RUNNING ON THE "XOR" TESTER CHANGE
10033 ;:THIS INSTRUCTION TO A "NOP" (NOP=240)
10034 050336 013746 000004 MOV @#ERRVEC,-(SP) ;:SAVE THE CONTENTS OF THE ERROR VECTOR
10035 050342 012737 050362 000004 MOV #5$,@#ERRVEC ;:SET FOR TIMEOUT
10036 050350 005737 177060 TST @#177060 ;:TIME OUT ON XOR?
10037 050354 012637 000004 MOV (SP)+,@#ERRVEC ;:RESTORE THE ERROR VECTOR
10038 050360 000463 BR $$VLAD ;:GO TO THE NEXT TEST
10039 050362 022626 5$: CMP (SP)+,(SP)+ ;:CLEAR THE STACK AFTER A TIME OUT
10040 050364 012637 000004 MOV (SP)+,@#ERRVEC ;:RESTORE THE ERROR VECTOR
10041 050370 000423 BR 7$ ;:LOOP ON THE PRESENT TEST
10042 050372 6$: ;:*****END OF CODE FOR THE XOR TESTER*****
10043 050372 032777 000400 130540 BIT #BIT08,@SWR ;:LOOP ON SPEC. TEST?
10044 050400 001404 BEQ 2$ ;:BR IF NO
10045 050402 127737 130532 001102 CMPB @SWR,$STNM ;:ON THE RIGHT TEST? SWR<7:0>
10046 050410 001465 BEQ $OVER ;:BR IF YES
10047 050412 105737 001103 2$: TSTB $ERFLG ;:HAS AN ERROR OCCURRED?
10048 050416 001421 BEQ 3$ ;:BR IF NO
10049 050420 123737 001115 001103 CMPB $ERMAX,$ERFLG ;:MAX. ERRORS FOR THIS TEST OCCURRED?
10050 050426 101015 BHI 3$ ;:BR IF NO
10051 050430 032777 001000 130502 BIT #BIT09,@SWR ;:LOOP ON ERROR?
10052 050436 001404 BEQ 4$ ;:BR IF NO
10053 050440 013737 001110 001106 7$: MOV $LPERR,$LPADR ;:SET LOOP ADDRESS TO LAST SCOPE
10054 050446 000446 BR $OVER
10055 050450 105037 001103 4$: CLRB $ERFLG ;:ZERO THE ERROR FLAG
10056 050454 005037 001174 CLR $TIMES ;:CLEAR THE NUMBER OF ITERATIONS TO MAKE
10057 050460 000415 BR 1$ ;:ESCAPE TO THE NEXT TEST
10058 050462 032777 004000 130450 3$: BIT #BIT11,@SWR ;:INHIBIT ITERATIONS?
10059 050470 001011 BNE 1$ ;:BR IF YES
10060 050472 005737 001216 TST $PASS ;:IF FIRST PASS OF PROGRAM
10061 050476 001406 BEQ 1$ ;: INHIBIT ITERATIONS
10062 050500 005237 001104 INC $ICNT ;:INCREMENT ITERATION COUNT
10063 050504 023737 001174 001104 CMP $TIMES,$ICNT ;:CHECK THE NUMBER OF ITERATIONS MADE
10064 050512 002024 BGE $OVER ;:BR IF MORE ITERATION REQUIRED
10065 050514 012737 000001 001104 1$: MOV #1,$ICNT ;:REINITIALIZE THE ITERATION COUNTER
10066 050522 013737 050600 001174 MOV $MXCNT,$TIMES ;:SET NUMBER OF ITERATIONS TO DO
10067 050530 105237 001102 $SVLAD: INCB $STNM ;:COUNT TEST NUMBERS
10068 050534 113737 001102 001214 MOVB $STNM,$TESTN ;:SET TEST NUMBER IN APT MAILBOX
```



```
10069 050542 011637 001106          MOV      (SP), $LPADR      ;;SAVE SCOPE LOOP ADDRESS
10070 050546 011637 001110          MOV      (SP), $LPERR     ;;SAVE ERROR LOOP ADDRESS
10071 050552 005037 001176          CLR      $ESCAPE         ;;CLEAR THE ESCAPE FROM ERROR ADDRESS
10072 050556 112737 000001 001115  MOVVB   #1, $SERMAX       ;;ONLY ALLOW ONE(1) ERROR ON NEXT TEST
10073 050564 013777 001102 130350 $OVER:  MOV      $STNM, @DISPLAY ;;DISPLAY TEST NUMBER
10074 050572 013716 001106          MOV      $LPADR, (SP)    ;;FUDGE RETURN ADDRESS
10075 050576 000002          RTI                      ;;FIXES PS
10076 050600 003720          $MXCNT: 2000.           ;;MAX. NUMBER OF ITERATIONS
10077                                     .SBTTL  ERROR HANDLER ROUTINE
10078
10079                                     ;;*****
10080                                     ;;*THIS ROUTINE WILL INCREMENT THE ERROR FLAG AND THE ERROR COUNT,
10081                                     ;;*SAVE THE ERROR ITEM NUMBER AND THE ADDRESS OF THE ERROR CALL
10082                                     ;;*AND GO TO TYPERR ON ERROR
10083                                     ;;*THE SWITCH OPTIONS PROVIDED BY THIS ROUTINE ARE:
10084                                     ;;*SW15=1      HALT ON ERROR
10085                                     ;;*SW13=1      INHIBIT ERROR TYPEOUTS
10086                                     ;;*SW10=1      BELL ON ERROR
10087                                     ;;*SW09=1      LOOP ON ERROR
10088                                     ;;*CALL
10089                                     ;;*      ERROR      N      ;;ERROR=EMT AND N=ERROR ITEM NUMBER
10090
10091 050602          $ERROR:
10092 050602 104407          CKSWR
10093 050604 105237 001103 7$:      INCB      $ERFLG      ;;TEST FOR CHANGE IN SOFT-SWR
10094 050610 001775          BEQ      7$             ;;SET THE ERROR FLAG
10095 050612 013777 001102 130322          MOV      $STNM, @DISPLAY ;;DON'T LET THE FLAG GO TO ZERO
10096 050620 032777 002000 130312          BIT      #BIT10, @SWR   ;;DISPLAY TEST NUMBER AND ERROR FLAG
10097 050626 001402          BEQ      1$             ;;BELL ON ERROR?
10098 050630 104401 001200          TYPE    , $BELL        ;;NO - SKIP
10099 050634 005237 001112          INC      $ERTTL        ;;RING BELL
10100 050640 011637 001116          MOV      (SP), $ERRPC   ;;COUNT THE NUMBER OF ERRORS
10101 050644 162737 000002 001116          SUB      #2, $ERRPC    ;;GET ADDRESS OF ERROR INSTRUCTION
10102 050652 117737 130240 001114          MOVVB   @ $ERRPC, $ITEMB ;;STRIP AND SAVE THE ERROR ITEM CODE
10103 050660 032777 020000 130252          BIT      #BIT13, @SWR   ;;SKIP TYPEOUT IF SET
10104 050666 001004          BNE     20$            ;;SKIP TYPEOUTS
10105 050670 004737 067604          JSR     PC, TYPERR     ;;GO TO USER ERROR ROUTINE
10106 050674 104401 001205          TYPE    , $CRLF
10107 050700          20$:
10108 050700 122737 000001 001230          CMPB   #APTENV, $ENV   ;;RUNNING IN APT MODE
10109 050706 001007          BNE     2$             ;;NO, SKIP APT ERROR REPORT
10110 050710 113737 001114 050722          MOVVB   $ITEMB, 21$    ;;SET ITEM NUMBER AS ERROR NUMBER
10111 050716 004737 051600          JSR     PC, $ATY4     ;;REPORT FATAL ERROR TO APT
10112 050722          21$:      .BYTE    0
10113 050723          .BYTE    0
10114 050724 000777          BR      22$           ;;APT ERROR LOOP
10115 050726 005777 130206          22$:      BR      2$             ;;HALT ON ERROR
10116 050732 100002          2$:      TST      @SWR      ;;SKIP IF CONTINUE
10117 050734 000000          BPL     3$             ;;HALT ON ERROR!
10118 050736 104407          HALT
10119 050740 032777 001000 130172          3$:      CKSWR
10120 050746 001402          BIT      #BIT09, @SWR  ;;TEST FOR CHANGE IN SOFT-SWR
10121 050750 013716 001110          BEQ     4$             ;;LOOP ON ERROR SWITCH SET?
10122 050754 005737 001176          MOV     $LPERR, (SP)   ;;BR IF NO
10123 050760 001402          TST     $ESCAPE       ;;FUDGE RETURN FOR LOOPING
10124 050762 013716 001176          BEQ     5$             ;;CHECK FOR AN ESCAPE ADDRESS
10124 050762 013716 001176          MOV     $ESCAPE, (SP) ;;BR IF NONE
10124 050762 013716 001176          MOV     $ESCAPE, (SP) ;;FUDGE RETURN ADDRESS FOR ESCAPE
```



```
10125 050766
10126 050766 022737 043046 000042
10127 050774 001001
10128 050776 000000
10129 051000
10130 051000 000002
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10133
10134
10135
10136
10137
10138
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10145
10146
10147
10148 051002 105737 001157
10149 051006 100002
10150 051010 000000
10151 051012 000430
10152 051014 010046
10153 051016 017600 000002
10154 051022 122737 000001 001230
10155 051030 001011
10156 051032 132737 000100 001231
10157 051040 001405
10158 051042 010037 051052
10159 051046 004737 051570
10160 051052 000000
10161 051054 132737 000040 001231
10162 051062 001003
10163 051064 112046
10164 051066 001005
10165 051070 005726
10166 051072 012600
10167 051074 062716 000002
10168 051100 000002
10169 051102 122716 000011
10170 051106 001430
10171 051110 122716 000200
10172 051114 001006
10173 051116 005726
10174 051120 104401
10175 051122 001205
10176 051124 105037 051332
10177 051130 000755
10178 051132 004737 051214
10179 051136 123726 001156
10180 051142 001350

5$:      CMP      #SENDAD,@#42      ;;ACT-11 AUTO-ACCEPT?
        BNE      6$                ;;BRANCH IF NO
        HALT                      ;;YES
6$:      RTI                        ;;RETURN
        .SBTTL   TYPE ROUTINE

*****
*ROUTINE TO TYPE ASCIZ MESSAGE. MESSAGE MUST TERMINATE WITH A 0 BYTE.
*THE ROUTINE WILL INSERT A NUMBER OF NULL CHARACTERS AFTER A LINE FEED.
*NOTE1:      $NULL CONTAINS THE CHARACTER TO BE USED AS THE FILLER CHARACTER.
*NOTE2:      $FILLS CONTAINS THE NUMBER OF FILLER CHARACTERS REQUIRED.
*NOTE3:      $FILLC CONTAINS THE CHARACTER TO FILL AFTER.
*
*CALL:
*1) USING A TRAP INSTRUCTION
*      TYPE      ,MESADR      ;;MESADR IS FIRST ADDRESS OF AN ASCIZ STRING
*OR
*      TYPE
*      MESADR
*
$TYPE:   TSTB     $TPFLG      ;;IS THERE A TERMINAL?
        BPL      1$          ;;BR IF YES
        HALT                      ;;HALT HERE IF NO TERMINAL
        BR       3$          ;;LEAVE
1$:      MOV      RO,-(SP)      ;;SAVE RO
        MOV      @2(SP),RO     ;;GET ADDRESS OF ASCIZ STRING
        CMPB     #APTENV,$ENV  ;;RUNNING IN APT MODE
        BNE     62$          ;;NO,GO CHECK FOR APT CONSOLE
        BITB     #APTSPOOL,$ENVM ;;SPOOL MESSAGE TO APT
        BEQ     62$          ;;NO,GO CHECK FOR CONSOLE
        MOV      RO,61$        ;;SETUP MESSAGE ADDRESS FOR APT
        JSR     PC,$ATY3      ;;SPOOL MESSAGE TO APT
61$:     .WORD    0            ;;MESSAGE ADDRESS
62$:     BITB     #APTCSUP,$ENVM ;;APT CONSOLE SUPPRESSED
        BNE     60$          ;;YES,SKIP TYPE OUT
2$:      MOVB     (RO)+,-(SP)   ;;PUSH CHARACTER TO BE TYPED ONTO STACK
        BNE     4$          ;;BR IF IT ISN'T THE TERMINATOR
        TST     (SP)+         ;;IF TERMINATOR POP IT OFF THE STACK
60$:     MOV      (SP)+,RO     ;;RESTORE RO
3$:      ADD      #2,(SP)      ;;ADJUST RETURN PC
        RTI                      ;;RETURN
4$:      CMPB     #HT,(SP)     ;;BRANCH IF <HT>
        BEQ     8$          ;;BRANCH IF NOT <CRLF>
        CMPB     #CRLF,(SP)
        BNE     5$          ;;POP <CR><LF> EQUIV
        TST     (SP)+         ;;TYPE A CR AND LF
5$:      CLR      $CHARCNT     ;;CLEAR CHARACTER COUNT
        BR       2$          ;;GET NEXT CHARACTER
6$:      JSR     PC,$TYPEC     ;;GO TYPE THIS CHARACTER
        CMPB     $FILLC,(SP)+  ;;IS IT TIME FOR FILLER CHARS.?
        BNE     2$          ;;IF NO GO GET NEXT CHAR.
```



```
10181 051144 013746 001154          MOV    $NULL,-(SP)      ;;GET # OF FILLER CHARS. NEEDED
10182                                     ;;AND THE NULL CHAR.
10183 051150 105366 000001      7$:   DECB    1(SP)      ;;DOES A NULL NEED TO BE TYPED?
10184 051154 002770                BLT    6$              ;;BR IF NO--GO POP THE NULL OFF OF STACK
10185 051156 004737 051214        JSR    PC,$TYPEC      ;;GO TYPE A NULL
10186 051162 105337 051332        DECB    $CHARCNT      ;;DO NOT COUNT AS A COUNT
10187 051166 000770                BR     7$              ;;LOOP
10188
10189                               ;HORIZONTAL TAB PROCESSOR
10190
10191 051170 112716 000040      8$:   MOVB    #' ,(SP)      ;;REPLACE TAB WITH SPACE
10192 051174 004737 051214      9$:   JSR    PC,$TYPEC      ;;TYPE A SPACE
10193 051200 132737 000007 051332  BITB    #7,$CHARCNT      ;;BRANCH IF NOT AT
10194 051206 001372                BNE    9$              ;;TAB STOP
10195 051210 005726                TST    (SP)+          ;;POP SPACE OFF STACK
10196 051212 000724                BR     2$              ;;GET NEXT CHARACTER
10197 051214
10198 051214 105777 127724      $TYPEC: TSTB    @STKS          ;;CHAR IN KYBD BUFFER?           :MJD001
10199 051220 100022                BPL    10$            ;;BR IF NOT                       :MJD001
10200 051222 017746 127720        MOV    @STKB,-(SP)      ;;GET CHAR                          :MJD001
10201 051226 042716 177600        BIC    #177600,(SP)     ;;STRIP EXTRANEIOUS BITS          :MJD001
10202 051232 122716 000023        CMPB   #$XOFF,(SP)     ;;WAS CHAR XOFF                    :MJD001
10203 051236 001012                BNE    102$           ;;BR IF NOT                          :MJD001
10204 051240
10205 051240 105777 127700      101$: TSTB    @STKS          ;;WAIT FOR CHAR                       :MJD001
10206 051244 100375                BPL    101$           ;;BR IF NOT                          :MJD001
10207 051246 117716 127674        MOVB   @STKB,(SP)      ;;GET CHAR                          :MJD001
10208 051252 042716 177600        BIC    #177600,(SP)     ;;STRIP IT                          :MJD001
10209 051256 122716 000021        CMPB   #$XON,(SP)     ;;WAS IT XON?                       :MJD001
10210 051262 001366                BNE    101$           ;;BR IF NOT                          :MJD001
10211 051264
10212 051264 005726      102$: TST    (SP)+          ;;FIX STACK                          :MJD001
10213 051266
10214 051266 105777 127656      10$:   TSTB    @STPS          ;;WAIT UNTIL PRINTER IS READY       :MJD001
10215 051272 100375                BPL    10$              ;;BR IF NOT                          :MJD001
10216 051274 116677 000002 127650  MOVB   2(SP),@STPB      ;;LOAD CHAR TO BE TYPED INTO DATA REG.
10217 051302 122766 000015 000002  CMPB   #CR,2(SP)        ;;IS CHARACTER A CARRIAGE RETURN?
10218 051310 001003                BNE    1$              ;;BRANCH IF NO
10219 051312 105037 051332        CLRB   $CHARCNT      ;;YES--CLEAR CHARACTER COUNT
10220 051316 000406                BR     $TYPEX          ;;EXIT
10221 051320 122766 000012 000002  1$:   CMPB   #LF,2(SP)     ;;IS CHARACTER A LINE FEED?
10222 051326 001402                BEQ    $TYPEX          ;;BRANCH IF YES
10223 051330 105227                INCB   (PC)+          ;;COUNT THE CHARACTER
10224 051332 000000      $CHARCNT: .WORD 0      ;;CHARACTER COUNT STORAGE
10225 051334 000207      $TYPEX: RTS    PC
10226
10227                               .SBTTL CONVERT BINARY TO DECIMAL AND TYPE ROUTINE
10228
10229                               ;*****
10230                               ;*THIS ROUTINE IS USED TO CHANGE A 16-BIT BINARY NUMBER TO A 5-DIGIT
10231                               ;*SIGNED DECIMAL (ASCII) NUMBER AND TYPE IT. DEPENDING ON WHETHER THE
10232                               ;*NUMBER IS POSITIVE OR NEGATIVE A SPACE OR A MINUS SIGN WILL BE TYPED
10233                               ;*BEFORE THE FIRST DIGIT OF THE NUMBER. LEADING ZEROS WILL ALWAYS BE
10234                               ;*REPLACED WITH SPACES.
10235                               ;*CALL:
10236                               ;*   MOV    NUM,-(SP)      ;;PUT THE BINARY NUMBER ON THE STACK
```



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10237      ;*      TYPDS      ;;GO TO THE ROUTINE
10238
10239      $TYPDS:
10240      051336 010046      MOV      R0,-(SP)      ;;PUSH R0 ON STACK
10241      051336 010146      MOV      R1,-(SP)      ;;PUSH R1 ON STACK
10242      051342 010246      MOV      R2,-(SP)      ;;PUSH R2 ON STACK
10243      051344 010346      MOV      R3,-(SP)      ;;PUSH R3 ON STACK
10244      051346 010546      MOV      R5,-(SP)      ;;PUSH R5 ON STACK
10245      051350 012746 020200      MOV      #20200,-(SP)  ;;SET BLANK SWITCH AND SIGN
10246      051354 016605 000020      MOV      20(SP),R5    ;;GET THE INPUT NUMBER
10247      051360 100004      BPL      1$           ;;BR IF INPUT IS POS.
10248      051362 005405      NEG      R5           ;;MAKE THE BINARY NUMBER POS.
10249      051364 112766 000055 000001      MOVB     #'-,1(SP)    ;;MAKE THE ASCII NUMBER NEG.
10250      051372 005000      1$:      CLR      R0           ;;ZERO THE CONSTANTS INDEX
10251      051374 012703 051552      MOV      #$DBLK,R3    ;;SETUP THE OUTPUT POINTER
10252      051400 112723 000040      MOVB     #' ,(R3)+    ;;SET THE FIRST CHARACTER TO A BLANK
10253      051404 005002      2$:      CLR      R2           ;;CLEAR THE BCD NUMBER
10254      051406 016001 051542      MOV      $DTBL(R0),R1 ;;GET THE CONSTANT
10255      051412 160105      3$:      SUB      R1,R5        ;;FORM THIS BCD DIGIT
10256      051414 002402      BLT      4$           ;;BR IF DONE
10257      051416 005202      INC      R2           ;;INCREASE THE BCD DIGIT BY 1
10258      051420 000774      BR       3$
10259      051422 060105      4$:      ADD      R1,R5        ;;ADD BACK THE CONSTANT
10260      051424 005702      TST      R2           ;;CHECK IF BCD DIGIT=0
10261      051426 001002      BNE      5$           ;;FALL THROUGH IF 0
10262      051430 105716      TSTB     (SP)         ;;STILL DOING LEADING 0'S?
10263      051432 100407      BMI      7$           ;;BR IF YES
10264      051434 106316      5$:      ASLB     (SP)         ;;MSD?
10265      051436 103003      BCC      6$           ;;BR IF NO
10266      051440 116663 000001 177777      MOVB     1(SP),-1(R3)  ;;YES--SET THE SIGN
10267      051446 052702 000060      BIS      #'0,R2       ;;MAKE THE BCD DIGIT ASCII
10268      051452 052702 000040      7$:      BIS      #' ,R2       ;;MAKE IT A SPACE IF NOT ALREADY A DIGIT
10269      051456 110223      MOVB     R2,(R3)+     ;;PUT THIS CHARACTER IN THE OUTPUT BUFFER
10270      051460 005720      TST      (R0)+        ;;JUST INCREMENTING
10271      051462 020027 000010      CMP      R0,#10       ;;CHECK THE TABLE INDEX
10272      051466 002746      BLT      2$           ;;GO DO THE NEXT DIGIT
10273      051470 003002      BGT      8$           ;;GO TO EXIT
10274      051472 010502      MOV      R5,R2        ;;GET THE LSD
10275      051474 000764      BR       6$           ;;GO CHANGE TO ASCII
10276      051476 105726      8$:      TSTB     (SP)+        ;;WAS THE LSD THE FIRST NON-ZERO?
10277      051500 100003      BPL      9$           ;;BR IF NO
10278      051502 116663 177777 177776      MOVB     -1(SP),-2(R3) ;;YES--SET THE SIGN FOR TYPING
10279      051510 105013      9$:      CLRB     (R3)         ;;SET THE TERMINATOR
10280      051512 012605      MOV      (SP)+,R5     ;;POP STACK INTO R5
10281      051514 012603      MOV      (SP)+,R3     ;;POP STACK INTO R3
10282      051516 012602      MOV      (SP)+,R2     ;;POP STACK INTO R2
10283      051520 012601      MOV      (SP)+,R1     ;;POP STACK INTO R1
10284      051522 012600      MOV      (SP)+,R0     ;;POP STACK INTO R0
10285      051524 104401 051552      TYPE     $DBLK        ;;NOW TYPE THE NUMBER
10286      051530 016666 000002 000004      MOV      2(SP),4(SP)  ;;ADJUST THE STACK
10287      051536 012616      MOV      (SP)+,(SP)
10288      051540 000002      RTI
10289      051542 023420      ;;RETURN TO USER
10290      051544 001750      $DTBL: 10000.
10291      051546 000144      1000.
10292      051550 000012      100.
      10.

```



```
10293 051552 000004          $DBLK: .BLKW 4
10294                          .SBTTL  APT COMMUNICATIONS ROUTINE
10295
10296                          ::*****
10297 051562 112737 000001 052026 $ATY1:  MOVB  #1,$FFLG          ;;TO REPORT FATAL ERROR
10298 051570 112737 000001 052024 $ATY3:  MOVB  #1,$MFLG          ;;TO TYPE A MESSAGE
10299 051576 000403
10300 051600 112737 000001 052026 $ATY4:  MOVB  #1,$FFLG          ;;TO ONLY REPORT FATAL ERROR
10301 051606
10302 051606 010046          MOV      R0,-(SP)          ;;PUSH R0 ON STACK
10303 051610 010146          MOV      R1,-(SP)          ;;PUSH R1 ON STACK
10304 051612 105737 052024          TSTB   $MFLG              ;;SHOULD TYPE A MESSAGE?
10305 051616 001450          BEQ     5$                ;;IF NOT: BR
10306 051620 122737 000001 001230  CMPB   #APTENV,$ENV      ;;OPERATING UNDER APT?
10307 051626 001031          BNE     3$                ;;IF NOT: BR
10308 051630 132737 000100 001231  BITB   #APTPOOL,$ENVM    ;;SHOULD SPOOL MESSAGES?
10309 051636 001425          BEQ     3$                ;;IF NOT: BR
10310 051640 017600 000004          MOV     @4(SP),R0         ;;GET MESSAGE ADDR.
10311 051644 062766 000002 000004  ADD     #2,4(SP)          ;;BUMP RETURN ADDR.
10312 051652 005737 001210          1$:   TST   $MSGTYPE      ;;SEE IF DONE W/ LAST XMISSION?
10313 051656 001375          BNE     1$                ;;IF NOT: WAIT
10314 051660 010037 001224          MOV     R0,$MSGAD        ;;PUT ADDR IN MAILBOX
10315 051664 105720          2$:   TSTB  (R0)+          ;;FIND END OF MESSAGE
10316 051666 001376          BNE     2$
10317 051670 163700 001224          SUB     $MSGAD,R0        ;;SUB START OF MESSAGE
10318 051674 006200          ASR     R0                ;;GET MESSAGE LNTH IN WORDS
10319 051676 010037 001226          MOV     R0,$MSGGLT       ;;PUT LENGTH IN MAILBOX
10320 051702 012737 000004 001210  MOV     #4,$MSGTYPE      ;;TELL APT TO TAKE MSG.
10321 051710 000413          BR      5$
10322 051712 017637 000004 051736 3$:   MOV     @4(SP),4$        ;;PUT MSG ADDR IN JSR LINKAGE
10323 051720 062766 000002 000004  ADD     #2,4(SP)          ;;BUMP RETURN ADDRESS
10324 051726 013746 177776          MOV     177776,-(SP)     ;;PUSH 177776 ON STACK
10325 051732 004737 051002          JSR     PC,$TYPE         ;;CALL TYPE MACRO
10326 051736 000000          4$:   .WORD  0
10327 051740          5$:
10328 051740 105737 052026          10$:  TSTB   $FFLG            ;;SHOULD REPORT FATAL ERROR?
10329 051744 001416          BEQ     12$              ;;IF NOT: BR
10330 051746 005737 001230          TST     $ENV             ;;RUNNING UNDER APT?
10331 051752 001413          BEQ     12$              ;;IF NOT: BR
10332 051754 005737 001210          11$:  TST     $MSGTYPE        ;;FINISHED LAST MESSAGE?
10333 051760 001375          BNE     11$              ;;IF NOT: WAIT
10334 051762 017637 000004 001212  MOV     @4(SP),$FATAL     ;;GET ERROR #
10335 051770 062766 000002 000004  ADD     #2,4(SP)          ;;BUMP RETURN ADDR.
10336 051776 005237 001210          INC     $MSGTYPE         ;;TELL APT TO TAKE ERROR
10337 052002 105037 052026          12$:  CLRB   $FFLG            ;;CLEAR FATAL FLAG
10338 052006 105037 052025          CLRB   $LFLG            ;;CLEAR LOG FLAG
10339 052012 105037 052024          CLRB   $MFLG            ;;CLEAR MESSAGE FLAG
10340 052016 012601          MOV     (SP)+,R1         ;;POP STACK INTO R1
10341 052020 012600          MOV     (SP)+,R0         ;;POP STACK INTO R0
10342 052022 000207          RTS     PC                ;;RETURN
10343 052024 000          SMFLG: .BYTE 0           ;;MESSG. FLAG
10344 052025 000          $LFLG: .BYTE 0           ;;LOG FLAG
10345 052026 000          $FFLG: .BYTE 0           ;;FATAL FLAG
10346          052030          .EVEN
10347          000200
10348          000001
APTSIZE=200
APTENV=001
```

10349 000100
10350 000040
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10376 052030 017646 000000
10377 052034 116637 000001 052253
10378 052042 112637 052255
10379 052046 062716 000002
10380 052052 000406
10381 052054 112737 000001 052253
10382 052062 112737 000006 052255
10383 052070 112737 000005 052252
10384 052076 010346
10385 052100 010446
10386 052102 010546
10387 052104 113704 052255
10388 052110 005404
10389 052112 062704 000006
10390 052116 110437 052254
10391 052122 113704 052253
10392 052126 016605 000012
10393 052132 005003
10394 052134 006105 1\$:
10395 052136 000404
10396 052140 006105 2\$:
10397 052142 006105
10398 052144 006105
10399 052146 010503
10400 052150 006103 3\$:
10401 052152 105337 052254
10402 052156 100016
10403 052160 042703 177770
10404 052164 001002

```
APTSPool=100
APTCSUP=040
.SBTTL BINARY TO OCTAL (ASCII) AND TYPE

*****
*THIS ROUTINE IS USED TO CHANGE A 16-BIT BINARY NUMBER TO A 6-DIGIT
*OCTAL (ASCII) NUMBER AND TYPE IT.
*$TYPOS---ENTER HERE TO SETUP SUPPRESS ZEROS AND NUMBER OF DIGITS TO TYPE
*CALL:
*   MOV     NUM,-(SP)      ;;NUMBER TO BE TYPED
*   TYPOS   ;;CALL FOR TYPEOUT
*   .BYTE  N              ;;N=1 TO 6 FOR NUMBER OF DIGITS TO TYPE
*   .BYTE  M              ;;M=1 OR 0
*                               ;;1=TYPE LEADING ZEROS
*                               ;;0=SUPPRESS LEADING ZEROS
*$TYPON----ENTER HERE TO TYPE OUT WITH THE SAME PARAMETERS AS THE LAST
*$TYPOS OR $TYPOC
*CALL:
*   MOV     NUM,-(SP)      ;;NUMBER TO BE TYPED
*   TYPON   ;;CALL FOR TYPEOUT
*$TYPOC---ENTER HERE FOR TYPEOUT OF A 16 BIT NUMBER
*CALL:
*   MOV     NUM,-(SP)      ;;NUMBER TO BE TYPED
*   TYPOC   ;;CALL FOR TYPEOUT
$TYPOS: MOV     @ (SP),-(SP)  ;;PICKUP THE MODE
        MOVVB  1(SP), $OFILL  ;;LOAD ZERO FILL SWITCH
        MOVVB  (SP)+, $OMODE+1 ;;NUMBER OF DIGITS TO TYPE
        ADD    #2, (SP)      ;;ADJUST RETURN ADDRESS
        BR     $TYPON
$TYPOC: MOVVB  #1, $OFILL    ;;SET THE ZERO FILL SWITCH
        MOVVB  #6, $OMODE+1  ;;SET FOR SIX(6) DIGITS
$TYPON: MOVVB  #5, $OCNT     ;;SET THE ITERATION COUNT
        MOV    R3,-(SP)      ;;SAVE R3
        MOV    R4,-(SP)      ;;SAVE R4
        MOV    R5,-(SP)      ;;SAVE R5
        MOVVB  $OMODE+1, R4  ;;GET THE NUMBER OF DIGITS TO TYPE
        NEG    R4
        ADD    #6, R4        ;;SUBTRACT IT FOR MAX. ALLOWED
        MOVVB  R4, $OMODE    ;;SAVE IT FOR USE
        MOVVB  $OFILL, R4   ;;GET THE ZERO FILL SWITCH
        MOV    12(SP), R5   ;;PICKUP THE INPUT NUMBER
        CLR    R3           ;;CLEAR THE OUTPUT WORD
1$:     ROL    R5           ;;ROTATE MSB INTO "C"
        BR     3$          ;;GO DO MSB
2$:     ROL    R5           ;;FORM THIS DIGIT
        ROL    R5
        ROL    R5
        MOV    R5, R3
3$:     ROL    R3           ;;GET LSB OF THIS DIGIT
        DECB  $OMODE        ;;TYPE THIS DIGIT?
        BPL   7$           ;;BR IF NO
        BIC   #177770, R3  ;;GET RID OF JUNK
        BNE   4$           ;;TEST FOR 0
4$:     ;
```



```
10405 052166 005704          TST      R4          ;;SUPPRESS THIS 0?
10406 052170 001403          BEQ      5$          ;;BR IF YES
10407 052172 005204          4$: INC      R4          ;;DON'T SUPPRESS ANYMORE 0'S
10408 052174 052703 000060    BIS      #'0,R3      ;;MAKE THIS DIGIT ASCII
10409 052200 052703 000040    5$: BIS      #' ,R3      ;;MAKE ASCII IF NOT ALREADY
10410 052204 110337 052250    MOVB     R3,8$       ;;SAVE FOR TYPING
10411 052210 104401 052250    TYPE     ,8$        ;;GO TYPE THIS DIGIT
10412 052214 105337 052252    7$: DECB    $OCNT     ;;COUNT BY 1
10413 052220 003347          BGT      2$          ;;BR IF MORE TO DO
10414 052222 002402          BLT      6$          ;;BR IF DONE
10415 052224 005204          INC      R4          ;;INSURE LAST DIGIT ISN'T A BLANK
10416 052226 000744          BR       2$          ;;GO DO THE LAST DIGIT
10417 052230 012605          6$: MOV      (SP)+,R5  ;;RESTORE R5
10418 052232 012604          MOV      (SP)+,R4  ;;RESTORE R4
10419 052234 012603          MOV      (SP)+,R3  ;;RESTORE R3
10420 052236 016666 000002 000004  MOV      2(SP),4(SP) ;;SET THE STACK FOR RETURNING
10421 052244 012616          MOV      (SP)+,(SP)
10422 052246 000002          RTI          ;;RETURN
10423 052250          8$: .BYTE    0          ;;STORAGE FOR ASCII DIGIT
10424 052251          .BYTE    0          ;;TERMINATOR FOR TYPE ROUTINE
10425 052252          .BYTE    0          ;;OCTAL DIGIT COUNTER
10426 052253          .BYTE    0          ;;ZERO FILL SWITCH
10427 052254 000000          .WORD    0          ;;NUMBER OF DIGITS TO TYPE
10428
10429
10430
10431          .SBTTL  TTY INPUT ROUTINE
10432          ;;*****
10432 052256 000000          .ENABL  LSB
10433 052260 000000          $TKCNT: .WORD    0          ;;NUMBER OF ITEMS IN QUEUE
10434 052262 000000          $TKQIN: .WORD    0          ;;INPUT POINTER
10435 052264 000001          $TKQOUT: .WORD    0         ;;OUTPUT POINTER
10436          052265          $TKQSRV: .BLKB   1         ;;TTY KEYBOARD QUEUE
10437          052266          $TKQEND=.
10438
10439          .EVEN
10440          ;*TK INITIALIZE ROUTINE
10441          ;*THIS ROUTINE WILL INITIALIZE THE TTY KEYBOARD INPUT QUEUE
10442          ;*SETUP THE INTERRUPT VECTOR AND TURN ON THE KEYBOARD INTERRUPT
10443          ;
10444          ;*CALL:
10445          ;*      JSR      PC,$TKINT
10446          ;*      RETURN
10447 052266 005037 052256          $TKINT: CLR      $TKCNT     ;;CLEAR COUNT OF ITEMS IN QUEUE
10448 052272 012737 052264 052260  MOV      #$TKQSRV,$TKQIN ;;MOVE THE STARTING ADDRESS OF THE
10449 052300 013737 052260 052262  MOV      $TKQIN,$TKQOUT  ;;QUEUE INTO THE INPUT & OUTPUT POINTERS.
10450 052306 012737 052336 000060  MOV      #$TKSRV,@TKVEC  ;;INITIALIZE THE KEYBOARD VECTOR
10451 052314 012737 000200 000062  MOV      #200,@TKVEC+2  ;;'BR' LEVEL 4
10452 052322 005777 126620          TST      @TKB          ;;CLEAR DONE FLAG
10453 052326 012777 000100 126610  MOV      #100,@TKS     ;;ENABLE TTY KEYBOARD INTERRUPT
10454 052334 000207          RTS      PC          ;;RETURN TO CALLER
10455
10456          ;*TK SERVICE ROUTINE
10457          ;*THIS ROUTINE WILL SERVICE THE TTY KEYBOARD INTERRUPT
10458          ;*BY READING THE CHARACTER FROM THE INPUT BUFFER AND PUTTING
10459          ;*IT IN THE QUEUE.
10460          ;*IF THE CHARACTER IS A "CONTROL-C" (^C) $TKINT IS CALLED AND
```

```
10461 ;*UPON RETURN EXIT IS MADE TO THE "CONTROL-C" RESTART ADDRESS (STOP)
10462 ;
10463 $TKSRV: MOVB @STKB,-(SP) ;;PICKUP THE CHARACTER
10464 BIC #^C177,(SP) ;;STRIP THE JUNK
10465 CMP (SP),#$XON ;;IS IT A RANDOM XON? ;RAN001
10466 BNE 30$ ;;BRANCH IF NO ;RAN001
10467 TST (SP)+ ;;CLEAN RANDOM XON OFF STACK ;RAN001
10468 RTI ;;RETURN ;RAN001
10469 30$:
10470 CMP (SP),#3 ;;IS IT A CONTROL C?
10471 BNE 1$ ;;BRANCH IF NO
10472 TYPE ,SCNTLC ;;TYPE A CONTROL-C (^C)
10473 JSR PC,$TKINT ;;INIT THE KEYBOARD
10474 TST (SP)+ ;;CLEAN UP STACK
10475 JMP STOP ;;CONTROL C RESTART
10476 1$: CMP (SP),#7 ;;IS IT A CONTROL G?
10477 BNE 2$ ;;BRANCH IF NO
10478 CMP #SWREG,SWR ;;IS SOFT-SWR SELECTED?
10479 BEQ 6$ ;;GO TO SWR CHANGE
10480
10481 2$:
10482 CMP #1,$TKCNT ;;IS THE QUEUE FULL?
10483 BNE 3$ ;;BRANCH IF NO
10484 TYPE ,SBELL ;;RING THE TTY BELL
10485 TST (SP)+ ;;CLEAN CHARACTER OFF OF STACK
10486 BR 5$ ;;EXIT
10487 3$: CMP (SP),#23 ;;IS IT A CONTROL-S?
10488 BNE 32$ ;;BRANCH IF NO
10489 CLR @STKS ;;DISABLE TTY KEYBOARD INTERRUPTS
10490 TST (SP)+ ;;CLEAN CHAR OFF STACK
10491 31$: TSTB @STKS ;;WAIT FOR A CHAR
10492 BPL 31$ ;;LOOP UNTIL ITS THERE
10493 MOVB @STKB,-(SP) ;;GET THE CHARACTER
10494 BIC #^C177,(SP) ;;MAKE IT 7-BIT ASCII
10495 CMP (SP)+,#21 ;;IS IT A CONTROL-Q?
10496 BNE 31$ ;;BRANCH IF NO
10497 MOV #100,@STKS ;;REENABLE TTY KEYBOARD INTERRUPTS
10498 RTI ;;RETURN
10499 32$: INC $TKCNT ;;COUNT THIS CHARACTER
10500 CMP (SP),#140 ;;IS IT UPPER CASE?
10501 BLT 4$ ;;BRANCH IF YES
10502 CMP (SP),#175 ;;IS IT A SPECIAL CHAR?
10503 BGT 4$ ;;BRANCH IF YES
10504 BIC #40,(SP) ;;MAKE IT UPPER CASE
10505 MOVB (SP)+,@STKQIN ;;AND PUT IT IN QUEUE
10506 INC $TKQIN ;;UPDATE THE POINTER
10507 CMP $TKQIN,$STKQEND ;;GO OFF THE END?
10508 BNE 5$ ;;BRANCH IF NO
10509 MOV #STKQSRST,$TKQIN ;;RESET THE POINTER
10510 5$: RTI ;;RETURN
10511
10512 ;*****
10513 ;*SOFTWARE SWITCH REGISTER CHANGE ROUTINE.
10514 ;*ROUTINE IS ENTERED FROM THE TRAP HANDLER, AND WILL
10515 ;*SERVICE THE TEST FOR CHANGE IN SOFTWARE SWITCH REGISTER TRAP
10516 ;*CALL WHEN OPERATING IN TTY INTERRUPT MODE.
```



```
10517 052566 022737 000176 001140 $CKSWR: CMP #SWREG,SWR ;;IS THE SOFT-SWR SELECTED
10518 052574 001124 BNE 15$ ;;EXIT IF NOT
10519 052576 105777 126342 TSTB @STKS ;;IS A CHAR WAITING?
10520 052602 100121 BPL 15$ ;;IF NOT, EXIT
10521 052604 117746 126336 MOVB @STKB,-(SP) ;;YES
10522 052610 042716 177600 BIC #^C177,(SP) ;;MAKE IT 7-BIT ASCII
10523 052614 021627 000007 CMP (SP),#7 ;;IS IT A CONTROL-G?
10524 052620 001300 BNE 2$ ;;IF NOT, PUT IT IN THE TTY QUEUE
10525 ;;AND EXIT
10526
10527
```

```
*****
;*CONTROL IS PASSED TO THIS POINT FROM EITHER THE TTY INTERRUPT SERVICE
;*ROUTINE OR FROM THE SOFTWARE SWITCH REGISTER TRAP CALL, AS A RESULT OF A
;*CONTROL-G BEING TYPED, AND THE SOFTWARE SWITCH REGISTER BEING SELECTED.
```

```
10530
10531 052622 123727 001134 000001 6$: CMPB $AUTOB,#1 ;;ARE WE RUNNING IN AUTO-MODE?
10532 052630 001674 BEQ 2$ ;;BRANCH IF YES
10533 052632 005726 TST (SP)+ ;;CLEAR CONTROL-G OFF STACK
10534 052634 004737 052266 JSR PC,$TKINT ;;FLUSH THE TTY INPUT QUEUE
10535 052640 005077 126300 CLR @STKS ;;DISABLE TTY KEYBOARD INTERRUPTS
10536 052644 112737 000001 001135 MOVB #1,$INTAG ;;SET INTERRUPT MODE INDICATOR
10537
10538 052652 104401 053510 $GTSWR: TYPE , $CNTLG ;;ECHO THE CONTROL-G (^G)
10539 052656 104401 053515 TYPE , $MSWR ;;TYPE CURRENT CONTENTS
10540 052662 013746 000176 MOV SWREG,-(SP) ;;SAVE SWREG FOR TYPEOUT
10541 052666 104402 TYPOC ;;GO TYPE--OCTAL ASCII(ALL DIGITS)
10542 052670 104401 053526 TYPE , $MNEW ;;PROMPT FOR NEW SWR
10543 052674 005046 19$: CLR -(SP) ;;CLEAR COUNTER
10544 052676 005046 CLR -(SP) ;;THE NEW SWR
10545 052700 105777 126240 7$: TSTB @STKS ;;CHAR THERE?
10546 052704 100375 BPL 7$ ;;IF NOT TRY AGAIN
10547
```

```
10548 052706 117746 126234 MOVB @STKB,-(SP) ;;PICK UP CHAR
10549 052712 042716 177600 BIC #^C177,(SP) ;;MAKE IT 7-BIT ASCII
10550
10551 052716 021627 000003 CMP (SP),#3 ;;IS IT A CONTROL-C?
10552 052722 001015 BNE 9$ ;;BRANCH IF NOT
10553 052724 104401 053476 TYPE , $CNTLC ;;YES, ECHO CONTROL-C (^C)
10554 052730 062706 000006 ADD #6,SP ;;CLEAN UP STACK
10555 052734 123727 001135 000001 CMPB $INTAG,#1 ;;REENABLE TTY KEYBOARD INTERRUPTS?
10556 052742 001003 BNE 8$ ;;BRANCH IF NO
10557 052744 012777 000100 126172 MOV #100,@STKS ;;ALLOW TTY KEYBOARD INTERRUPTS
10558 052752 000137 047536 8$: JMP STOP ;;CONTROL-C RESTART
10559
```

```
10560
10561 052756 021627 000025 9$: CMP (SP),#25 ;;IS IT A CONTROL-U?
10562 052762 001005 BNE 10$ ;;BRANCH IF NOT
10563 052764 104401 053503 TYPE , $CNTLU ;;YES, ECHO CONTROL-U (^U)
10564 052770 062706 000006 20$: ADD #6,SP ;;IGNORE PREVIOUS INPUT
10565 052774 000737 BR 19$ ;;LET'S TRY IT AGAIN
10566
```

```
10567
10568 052776 021627 000015 10$: CMP (SP),#15 ;;IS IT A <CR>?
10569 053002 001022 BNE 16$ ;;BRANCH IF NO
10570 053004 005766 000004 TST 4(SP) ;;YES, IS IT THE FIRST CHAR?
10571 053010 001403 BEQ 11$ ;;BRANCH IF YES
10572 053012 016677 000002 126120 MOV 2(SP),@SWR ;;SAVE NEW SWR
```

10573	053020	062706	000006		11\$:	ADD	#6,SP	::CLEAR UP STACK
10574	053024	104401	001205		14\$:	TYPE	,\$CRLF	::ECHO <CR> AND <LF>
10575	053030	123727	001135	000001		CMPB	,\$INTAG,#1	::RE-ENABLE TTY KBD INTERRUPTS?
10576	053036	001003				BNE	15\$::BRANCH IF NOT
10577	053040	012777	000100	126076		MOV	#100,@\$TKS	::RE-ENABLE TTY KBD INTERRUPTS
10578	053046	000002			15\$:	RTI		::RETURN
10579	053050	004737	051214		16\$:	JSR	PC,\$TYPEC	::ECHO CHAR
10580	053054	021627	000060			CMP	(SP),#60	::CHAR < 0?
10581	053060	002420				BLT	18\$::BRANCH IF YES
10582	053062	021627	000067			CMP	(SP),#67	::CHAR > 7?
10583	053066	003015				BGT	18\$::BRANCH IF YES
10584	053070	042726	000060			BIC	#60,(SP)+	::STRIP-OFF ASCII
10585	053074	005766	000002			TST	2(SP)	::IS THIS THE FIRST CHAR
10586	053100	001403				BEQ	17\$::BRANCH IF YES
10587	053102	006316				ASL	(SP)	::NO, SHIFT PRESENT
10588	053104	006316				ASL	(SP)	:: CHAR OVER TO MAKE
10589	053106	006316				ASL	(SP)	:: ROOM FOR NEW ONE.
10590	053110	005266	000002		17\$:	INC	2(SP)	::KEEP COUNT OF CHAR
10591	053114	056616	177776			BIS	-2(SP),(SP)	::SET IN NEW CHAR
10592	053120	000667				BR	7\$::GET THE NEXT ONE
10593	053122	104401	001204		18\$:	TYPE	,\$QUES	::TYPE ?<CR><LF>
10594	053126	000720				BR	20\$::SIMULATE CONTROL-U
10595						.DSABL	LSB	

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```
::*****  
*THIS ROUTINE WILL INPUT A SINGLE CHARACTER FROM THE TTY  
*CALL:  
* RDCHR ::GET A CHARACTER FROM THE QUEUE  
* RETURN HERE ::CHARACTER IS ON THE STACK  
* ::WITH PARITY BIT STRIPPED OFF  
:
```

10606	053130	011646			\$RDCHR:	MOV	(SP),-(SP)	::PUSH DOWN THE PC AND
10607	053132	016666	000004	000002		MOV	4(SP),2(SP)	::THE PS
10608	053140	005066	000004			CLR	4(SP)	::GET READY FOR A CHARACTER
10609	053144	005046				CLR	-(SP)	::PUT NEW PS ON STACK
10610	053146	012746	053154			MOV	#64\$,-(SP)	::PUT NEW PC ON STACK
10611	053152	000002				RTI		::POP NEW PC AND PS
10612	053154				64\$:			
10613	053154	005737	052256		1\$:	TST	\$TKCNT	::WAIT ON A CHARACTER
10614	053160	001775				BEQ	1\$	
10615	053162	005337	052256			DEC	\$TKCNT	::DECREMENT THE COUNTER
10616	053166	117766	177070	000004		MOVB	@\$TKQOUT,4(SP)	::GET ONE CHARACTER
10617	053174	005237	052262			INC	\$TKQOUT	::UPDATE THE POINTER
10618	053200	023727	052262	052265		CMP	\$TKQOUT,\$\$TKQEND	::DID IT GO OFF OF THE END?
10619	053206	001003				BNE	2\$::BRANCH IF NO
10620	053210	012737	052264	052262		MOV	\$\$TKQSRT,\$TKQOUT	::RESET THE POINTER
10621	053216	000002			2\$:	RTI		::RETURN

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```
::*****  
*THIS ROUTINE WILL INPUT A STRING FROM THE TTY  
*CALL:  
* RDLIN ::INPUT A STRING FROM THE TTY  
* RETURN HERE ::ADDRESS OF FIRST CHARACTER WILL BE ON THE STACK  
* ::TERMINATOR WILL BE A BYTE OF ALL 0'S
```


10629	053220	010346			SRDLIN:	MOV	R3,-(SP)	::SAVE R3
10630	053222	005046				CLR	-(SP)	::CLEAR THE RUBOUT KEY
10631	053224	012703	053454		1\$:	MOV	#\$TTYIN,R3	::GET ADDRESS
10632	053230	022703	053476		2\$:	CMP	#\$TTYIN+22,R3	::BUFFER FULL?
10633	053234	101456				BLOS	4\$::BR IF YES
10634	053236	104410				RDCHR		::GO READ ONE CHARACTER FROM THE TTY
10635	053240	112613				MOVB	(SP)+,(R3)	::GET CHARACTER
10636	053242	122713	000177		10\$:	CMPB	#177,(R3)	::IS IT A RUBOUT
10637	053246	001022				BNE	5\$::BR IF NO
10638	053250	005716				TST	(SP)	::IS THIS THE FIRST RUBOUT?
10639	053252	001007				BNE	6\$::BR IF NO
10640	053254	112737	000134	053452		MOVB	#'\,9\$::TYPE A BACK SLASH
10641	053262	104401	053452			TYPE	,9\$	
10642	053266	012716	177777			MOV	#-1,(SP)	::SET THE RUBOUT KEY
10643	053272	005303			6\$:	DEC	R3	::BACKUP BY ONE
10644	053274	020327	053454			CMP	R3,\$TTYIN	::STACK EMPTY?
10645	053300	103434				BLO	4\$::BR IF YES
10646	053302	111337	053452			MOVB	(R3),9\$::SETUP TO TYPEOUT THE DELETED CHAR.
10647	053306	104401	053452			TYPE	,9\$::GO TYPE
10648	053312	000746				BR	2\$::GO READ ANOTHER CHAR.
10649	053314	005716			5\$:	TST	(SP)	::RUBOUT KEY SET?
10650	053316	001406				BEQ	7\$::BR IF NO
10651	053320	112737	000134	053452		MOVB	#'\,9\$::TYPE A BACK SLASH
10652	053326	104401	053452			TYPE	,9\$	
10653	053332	005016				CLR	(SP)	::CLEAR THE RUBOUT KEY
10654	053334	122713	000025		7\$:	CMPB	#25,(R3)	::IS CHARACTER A CTRL U?
10655	053340	001003				BNE	8\$::BR IF NO
10656	053342	104401	053503			TYPE	,\$CNTLU	::TYPE A CONTROL 'U'
10657	053346	000726				BR	1\$::GO START OVER
10658	053350	122713	000022		8\$:	CMPB	#22,(R3)	::IS CHARACTER A '^R'?
10659	053354	001011				BNE	3\$::BRANCH IF NO
10660	053356	105013				CLRB	(R3)	::CLEAR THE CHARACTER
10661	053360	104401	001205			TYPE	,\$CRLF	::TYPE A 'CR' & 'LF'
10662	053364	104401	053454			TYPE	,\$TTYIN	::TYPE THE INPUT STRING
10663	053370	000717				BR	2\$::GO PICKUP ANOTHER CHACTER
10664	053372	104401	001204		4\$:	TYPE	,\$QUES	::TYPE A '?'
10665	053376	000712				BR	1\$::CLEAR THE BUFFER AND LOOP
10666	053400	111337	053452		3\$:	MOVB	(R3),9\$::ECHO THE CHARACTER
10667	053404	104401	053452			TYPE	,9\$	
10668	053410	122723	000015			CMPB	#15,(R3)+	::CHECK FOR RETURN
10669	053414	001305				BNE	2\$::LOOP IF NOT RETURN
10670	053416	105063	177777			CLRB	-1(R3)	::CLEAR RETURN (THE 15)
10671	053422	104401	001206			TYPE	,\$LF	::TYPE A LINE FEED
10672	053426	005726				TST	(SP)+	::CLEAN RUBOUT KEY FROM THE STACK
10673	053430	012603				MOV	(SP)+,R3	::RESTORE R3
10674	053432	011646				MOV	(SP),-(SP)	::ADJUST THE STACK AND PUT ADDRESS OF THE
10675	053434	016666	000004	000002		MOV	4(SP),2(SP)	::FIRST ASCII CHARACTER ON IT
10676	053442	012766	053454	000004		MOV	#\$TTYIN,4(SP)	
10677	053450	000002				RTI		::RETURN
10678	053452	000			9\$:	.BYTE	0	::STORAGE FOR ASCII CHAR. TO TYPE
10679	053453	000				.BYTE	0	::TERMINATOR
10680	053454	000022				.BLKB	22	::RESERVE 22 BYTES FOR TTY INPUT
10681	053476	041536	005015	000		.\$CNTLC:	.\$ASCIIZ /^C/<15><12>	::CONTROL 'C'
10682	053503	136	006525	000012		.\$CNTLU:	.\$ASCIIZ /^U/<15><12>	::CONTROL 'U'
10683	053510	043536	005015	000		.\$CNTLG:	.\$ASCIIZ /^G/<15><12>	::CONTROL 'G'
10684	053515	015	051412	051127		.\$MSWR:	.\$ASCIIZ <15><12>/SWR = /	


```
10685 053522 036440 000040
10686 053526 020040 042516 020127 $MNEW: .ASCIZ / NEW = /
10687 053534 020075 000
10688 053540
10689 .EVEN
10690 .SBTTL READ AN OCTAL NUMBER FROM THE TTY
10691
10692 ::*****
10693 ::*THIS ROUTINE WILL READ AN OCTAL (ASCII) NUMBER FROM THE TTY AND
10694 ::*CHANGE IT TO BINARY.
10695 ::*THE INPUT CHARACTERS WILL BE CHECKED TO INSURED THEY ARE LEGAL
10696 ::*OCTAL DIGITS. IF AN ILLEGAL CHARACTER IS READ A "?" WILL BE TYPED
10697 ::*FOLLOWED BY A CARRIAGE RETURN-LINE FEED. THE COMPLETE NUMBER MUST
10698 ::*THEN BE RETYPED. THE INPUT IS TERMINATED BY TYPING A CARRIAGE RETURN.
10699 ::*CALL:
10700 ::* RDOCT ::READ AN OCTAL NUMBER
10701 ::* RETURN HERE ::LOW ORDER BITS ARE ON TOP OF THE STACK
10702 ::* ::HIGH ORDER BITS ARE IN $HIOCT
10703 053540 011646 000004 000002 $RDOCT: MOV (SP),-(SP) ::PROVIDE SPACE FOR THE
10704 053542 016666 MOV 4(SP),2(SP) ::INPUT NUMBER
10705 053550 010046 MOV R0,-(SP) ::PUSH R0 ON STACK
10706 053552 010146 MOV R1,-(SP) ::PUSH R1 ON STACK
10707 053554 010246 MOV R2,-(SP) ::PUSH R2 ON STACK
10708 053556 104411 1$: RDLIN ::READ AN ASCIZ LINE
10709 053560 012600 MOV (SP)+,R0 ::GET ADDRESS OF 1ST CHARACTER
10710 053562 010037 053666 MOV R0,$$ ::AND SAVE IT
10711 053566 005001 CLR R1 ::CLEAR DATA WORD
10712 053570 005002 CLR R2
10713 053572 112046 2$: MOV (R0)+,-(SP) ::PICKUP THIS CHARACTER
10714 053574 001420 BEQ 3$ ::IF ZERO GET OUT
10715 053576 122716 000060 CMPB #'0,(SP) ::MAKE SURE THIS CHARACTER
10716 053602 003026 BGT 4$ ::IS AN OCTAL DIGIT
10717 053604 122716 000067 CMPB #'7,(SP)
10718 053610 002423 BLT 4$
10719 053612 006301 ASL R1 ::*2
10720 053614 006102 ROL R2
10721 053616 006301 ASL R1 ::*4
10722 053620 006102 ROL R2
10723 053622 006301 ASL R1 ::*8
10724 053624 006102 ROL R2
10725 053626 042716 177770 BIC #'C7,(SP) ::STRIP THE ASCII JUNK
10726 053632 062601 ADD (SP)+,R1 ::ADD IN THIS DIGIT
10727 053634 000756 BR 2$ ::LOOP
10728 053636 005726 3$: TST (SP)+ ::CLEAN TERMINATOR FROM STACK
10729 053640 010166 000012 MOV R1,12(SP) ::SAVE THE RESULT
10730 053644 010237 053676 MOV R2,$HIOCT
10731 053650 012602 MOV (SP)+,R2 ::POP STACK INTO R2
10732 053652 012601 MOV (SP)+,R1 ::POP STACK INTO R1
10733 053654 012600 MOV (SP)+,R0 ::POP STACK INTO R0
10734 053656 000002 RTI ::RETURN
10735 053660 005726 4$: TST (SP)+ ::CLEAN PARTIAL FROM STACK
10736 053662 105010 CLRB (R0) ::SET A TERMINATOR
10737 053664 104401 TYPE ::TYPE UP THRU THE BAD CHAR.
10738 053666 000000 5$: .WORD 0
10739 053670 104401 001204 TYPE $QUES ::'"' 'CR' & 'LF'
10740 053674 000730 BR 1$ ::TRY AGAIN
```



```
10741 053676 000000 $HIOCT: .WORD 0 ::HIGH ORDER BITS GO HERE
10742 .SBTTL DOUBLE LENGTH BINARY TO OCTAL ASCII CONVERT ROUTINE
10743
10744 ::*****
10745 ::*THIS ROUTINE WILL CONVERT A 32-BIT UNSIGNED BINARY NUMBER TO AN
10746 ::*UNSIGNED OCTAL ASCII NUMBER.
10747 ::*CALL
10748 ::* MOV #PNTR,-(SP) ::POINTER TO LOW WORD OF BINARY NUMBER
10749 ::* JSR PC,@#$DB20 ::CALL THE ROUTINE
10750 ::* RETURN ::THE ADDRESS OF THE FIRST ASCII CHAR. IS ON THE STACK
10751
10752
10753 053700 104413 $DB20: SAVREG ::SAVE ALL REGISTERS
10754 053702 016601 000002 MOV 2(SP),R1 ::PICKUP THE POINTER TO LOW WORD
10755 053706 012705 054017 MOV #SOCTVL+13.,R5 ::POINTER TO DATA TABLE
10756 053712 012704 000014 MOV #12.,R4 ::DO ELEVEN CHARACTERS
10757 053716 012703 177770 MOV #^7,R3 ::MASK
10758 053722 012100 MOV (R1)+,R0 ::LOWER WORD
10759 053724 012101 MOV (R1)+,R1 ::HIGH WORD
10760 053726 005002 CLR R2 ::TERMINATOR
10761 053730 110245 1$: MOV R2,-(R5) ::PUT CHARACTER IN DATA TABLE
10762 053732 010002 MOV R0,R2 ::GET THIS DIGIT
10763 053734 005304 DEC R4 ::COUNT THIS CHARACTER
10764 053736 003007 BGT 3$ ::BR IF NOT THE LAST DIGIT
10765 053740 001405 BEQ 2$ ::BR IF IT IS THE LAST DIGIT
10766 053742 005205 INC R5 ::ALL DIGITS DONE-ADJUST POINTER FOR FIRST
10767 053744 010566 000002 MOV R5,2(SP) ::ASCII CHAR. & PUT IT ON THE STACK
10768 053750 104414 RESREG ::RESTORE ALL REGISTERS
10769 053752 000207 RTS PC ::RETURN TO USER
10770 053754 006203 2$: ASR R3 ::POSITION THE MASK FOR THE LAST DIGIT
10771 053756 006001 3$: ROR R1 ::POSITION THE BINARY NUMBER FOR
10772 053760 006000 ROR R0 :: THE NEXT OCTAL DIGIT
10773 053762 006001 ROR R1
10774 053764 006000 ROR R0
10775 053766 006001 ROR R1
10776 053770 006000 ROR R0
10777 053772 040302 BIC R3,R2 ::MASK OUT ALL JUNK
10778 053774 062702 000060 ADD #'0,R2 ::MAKE THIS CHAR. ASCII
10779 054000 000753 BR 1$ ::GO PUT IT IN THE DATA TABLE
10780 054002 000016 $OCTVL: .BLKB 14. ::RESERVE DATA TABLE
10781 .SBTTL SINGLE LENGTH BINARY TO DECIMAL ASCII ROUTINE
10782
10783 ::*****
10784 ::*THIS ROUTINE WILL CONVERT A 16-BIT UNSIGNED BINARY NUMBER TO AN
10785 ::*UNSIGNED DECIMAL ASCII NUMBER.
10786 ::*CALL
10787 ::* MOV NUMBER,-(SP) ::PUT BINARY NUMBER ON THE STACK
10788 ::* JSR PC,@#$SB2D ::CALL
10789 ::* RETURN ::ADDRESS OF THE 1ST ASCII CHAR.IS ON THE STACK
10790
10791
10792 054020 016637 000002 054050 $$SB2D: MOV 2(SP),1$ ::SAVE BINARY NUMBER
10793 054026 012746 054050 MOV #1$,-(SP) ::SET POINTER
10794 054032 004737 054054 JSR PC,@#$DB2D ::CALL DOUBLE LENGTH CONVERT
10795 054036 062716 000005 ADD #5,(SP) ::ONLY ALLOW FIVE CHARACTERS
10796 054042 012666 000002 MOV (SP)+,2(SP) ::PICKUP POINTER
```

```

10797 054046 000207          RTS      PC          ;;RETURN
10798 054050 000000 000000 1$:      .WORD    0,0
10799          .SBTTL  DOUBLE LENGTH BINARY TO DECIMAL ASCII CONVERT ROUTINE
10800
10801          ;*****
10802          ;*THIS ROUTINE WILL CONVERT A 32-BIT BINARY NUMBER TO AN UNSIGNED
10803          ;*DECIMAL (ASCII) NUMBER. THE SIGN OF THE BINARY NUMBER MUST BE
10804          ;*POSITIVE.
10805          ;*CALL
10806          ;*      MOV      #PNTR,-(SP)      ;;POINTER TO LOW WORD OF BINARY NUMBER
10807          ;*      JSR      PC,@#$DB2D
10808          ;*      RETURN
10809          ;*      ;:THE FIRST ADDRESS OF ASCIZ
10810          ;*      ;:IS ON THE STACK
10811
10812 054054 104413          $DB2D:  SAVREG      ;;SAVE REGISTERS
10813 054056 016602 000002  MOV      2(SP),R2      ;;PICKUP THE DATA POINTER
10814 054062 012700 054234  MOV      #$DECVL,R0    ;;GET ADDRESS OF '$DECVL' STRING
10815 054066 010066 000002  MOV      R0,2(SP)      ;;PUT ADDRESS OF ASCIZ STRING ON STACK
10816 054072 012201          MOV      (R2)+,R1      ;;PICKUP THE BINARY NUMBER
10817 054074 012202          MOV      (R2)+,R2
10818 054076 012737 000012 054152  MOV      #10,,4$      ;;SET UP TO DO 10 CONVERSIONS
10819 054104 012704 054164  MOV      #STNPWR,R4    ;;ADDRESS OF TEN POWER
10820 054110 012705 054166  MOV      #STNPWR+2,R5
10821 054114 005003          1$:      CLR      R3          ;;CLEAR PARTIAL
10822 054116 161401          2$:      SUB      (R4),R1      ;;SUBTRACT TEN POWER
10823 054120 005602          SBC      R2
10824 054122 161502          SUB      (R5),R2
10825 054124 002402          BLT      3$          ;;BR IF TEN POWER TO LARGE
10826 054126 005203          INC      R3          ;;ADD 1 TO PARTIAL
10827 054130 000772          BR       2$          ;;LOOP
10828 054132 062401          3$:      ADD      (R4)+,R1      ;;RESTORE SUBTRACTED VALUE
10829 054134 005502          ADC      R2
10830 054136 062402          ADD      (R4)+,R2
10831 054140 022525          CMP      (R5)+,(R5)+  ;;MOVE TO NEXT TEN POWER
10832 054142 052703 000060  BIS      #0,R3          ;;CHANGE PARTIAL TO ASCII
10833 054146 110320          MOV      R3,(R0)+     ;;SAVE IT
10834 054150 005327          DEC      (PC)+        ;;DONE?
10835 054152 000000          4$:      .WORD    0
10836 054154 001357          BNE      1$          ;;BR IF NO
10837 054156 105020          CLRB    (R0)+        ;;TERMINATOR
10838 054160 104414          RESREG      ;;RESTORE REGISTERS
10839 054162 000207          RTS      PC          ;;RETURN
10840 054164 145000          $TNPWR: 145000      ;;1.0E09
10841 054166 035632          35632
10842 054170 160400          160400      ;;1.0E08
10843 054172 002765          2765
10844 054174 113200          113200      ;;1.0E07
10845 054176 000230          230
10846 054200 041100          041100      ;;1.0E06
10847 054202 000017          17
10848 054204 103240          103240      ;;1.0E05
10849 054206 000001          1
10850 054210 023420          23420      ;;1.0E04
10851 054212 000000          0
10852 054214 001750          1750      ;;1.0E03

```



```
10853 054216 000000 0
10854 054220 000144 144 ::1.0E02
10855 054222 000000 0
10856 054224 000012 12 ::1.0E01
10857 054226 000000 0
10858 054230 000001 1 ::1.0E00
10859 054232 000000 0
10860 054234 000014 $DECVL: .BLKB 12. ::RESERVE STORAGE FOR ASCIZ STRING
10861 .SBTTL TYPE NUMERICAL ASCIZ STRING SUPPRESS LEADING ZEROS
10862
10863 ::*****
10864 ::*THIS ROUTINE IS USED TO TYPE AN ASCIZ NUMBER SUPPRESSING THE
10865 ::*LEADING NUMBERS.
10866 ::*CALL
10867 ::* MOV #NUMADR,-(SP) ::FIRST ADDRESS OF ASCIZ STRING
10868 ::* JSR PC,@#$SUPRS
10869
10870
10871 054250 010046 $SUPRS: MOV R0,-(SP) ::SAVE R0
10872 054252 016600 000004 MOV 4(SP),R0 ::PICKUP THE POINTER
10873 054256 105710 1$: TSTB (R0) ::TERMINATEOR?
10874 054260 001403 BEQ 2$ ::BR IF YES
10875 054262 122720 000060 CMPB #'0,(R0)+ ::IS THIS AN ASCII '0' ?
10876 054266 001773 BEQ 1$ ::BR IF YES
10877 054270 005300 2$: DEC R0 ::BACKUP BY '1'
10878 054272 010037 054300 MOV R0,3$ ::SAVE FOR TYPING
10879 054276 104401 TYPE ::GO TYPE
10880 054300 000000 3$: .WORD 0 ::ASCIZ POINTER GOES HERE
10881 054302 012600 MOV (SP)+,R0 ::RESTORE R0
10882 054304 012616 MOV (SP)+,(SP) ::RESTORE THE STACK
10883 054306 000207 RTS PC ::RETURN
10884 .SBTTL SAVE AND RESTORE R0-R5 ROUTINES
10885
10886 ::*****
10887 ::*SAVE R0-R5
10888 ::*CALL:
10889 ::* SAVREG
10890 ::*UPON RETURN FROM $SAVREG THE STACK WILL LOOK LIKE:
10891 ::*
10892 ::*TOP---(+16)
10893 ::* +2---(+18)
10894 ::* +4---R5
10895 ::* +6---R4
10896 ::* +8---R3
10897 ::*+10---R2
10898 ::*+12---R1
10899 ::*+14---R0
10900
10901 054310 $SAVREG:
10902 054310 010046 MOV R0,-(SP) ::PUSH R0 ON STACK
10903 054312 010146 MOV R1,-(SP) ::PUSH R1 ON STACK
10904 054314 010246 MOV R2,-(SP) ::PUSH R2 ON STACK
10905 054316 010346 MOV R3,-(SP) ::PUSH R3 ON STACK
10906 054320 010446 MOV R4,-(SP) ::PUSH R4 ON STACK
10907 054322 010546 MOV R5,-(SP) ::PUSH R5 ON STACK
10908 054324 016646 000022 MOV 22(SP),-(SP) ::SAVE PS OF MAIN FLOW
```

```
10909 054330 016646 000022      MOV    22(SP),-(SP)    ;;SAVE PC OF MAIN FLOW
10910 054334 016646 000022      MOV    22(SP),-(SP)    ;;SAVE PS OF CALL
10911 054340 016646 000022      MOV    22(SP),-(SP)    ;;SAVE PC OF CALL
10912 054344 000002                RTI
10913
10914      ;*RESTORE R0-R5
10915      ;*CALL:
10916      ;*   RESREG
10917 054346      $RESREG:
10918 054346 012666 000022      MOV    (SP)+,22(SP)    ;;RESTORE PC OF CALL
10919 054352 012666 000022      MOV    (SP)+,22(SP)    ;;RESTORE PS OF CALL
10920 054356 012666 000022      MOV    (SP)+,22(SP)    ;;RESTORE PC OF MAIN FLOW
10921 054362 012666 000022      MOV    (SP)+,22(SP)    ;;RESTORE PS OF MAIN FLOW
10922 054366 012605                MOV    (SP)+,R5        ;;POP STACK INTO R5
10923 054370 012604                MOV    (SP)+,R4        ;;POP STACK INTO R4
10924 054372 012603                MOV    (SP)+,R3        ;;POP STACK INTO R3
10925 054374 012602                MOV    (SP)+,R2        ;;POP STACK INTO R2
10926 054376 012601                MOV    (SP)+,R1        ;;POP STACK INTO R1
10927 054400 012600                MOV    (SP)+,R0        ;;POP STACK INTO R0
10928 054402 000002                RTI
10929      .SBTTL TRAP DECODER
10930
10931      ;*****
10932      ;*THIS ROUTINE WILL PICKUP THE LOWER BYTE OF THE "TRAP" INSTRUCTION
10933      ;*AND USE IT TO INDEX THROUGH THE TRAP TABLE FOR THE STARTING ADDRESS
10934      ;*OF THE DESIRED ROUTINE. THEN USING THE ADDRESS OBTAINED IT WILL
10935      ;*GO TO THAT ROUTINE.
10936
10937 054404 010046      $TRAP: MOV    R0,-(SP)        ;;SAVE R0
10938 054406 016600 000002      MOV    2(SP),R0        ;;GET TRAP ADDRESS
10939 054412 005740                TST    -(R0)            ;;BACKUP BY 2
10940 054414 111000                MOV    (R0),R0         ;;GET RIGHT BYTE OF TRAP
10941 054416 006300                ASL    R0               ;;POSITION FOR INDEXING
10942 054420 016000 054440      MOV    $TRPAD(R0),R0   ;;INDEX TO TABLE
10943 054424 000200                RTS    R0               ;;GO TO ROUTINE
10944
10945      ;;THIS IS USE TO HANDLE THE "GETPRI" MACRO
10946
10947
10948 054426 011646      $TRAP2: MOV   (SP),-(SP)    ;;MOVE THE PC DOWN
10949 054430 016666 000004 000002  MOV   4(SP),2(SP)    ;;MOVE THE PSW DOWN
10950 054436 000002                RTI                    ;;RESTORE THE PSW
10951
10952      .SBTTL TRAP TABLE
10953
10954      ;*THIS TABLE CONTAINS THE STARTING ADDRESSES OF THE ROUTINES CALLED
10955      ;*BY THE "TRAP" INSTRUCTION.
10956
10957      :   ROUTINE
10958      :   -----
10959 054440 054426      $TRPAD: .WORD  $TRAP2
10960 054442 051002      $TYPE  ;;CALL=TYPE    TRAP+1(104401) TTY TYPEOUT ROUTINE
10961 054444 052054      $TYPOC ;;CALL=TYPOC   TRAP+2(104402) TYPE OCTAL NUMBER (WITH LEADING ZEROS)
10962 054446 052030      $TYPOS ;;CALL=TYPOS   TRAP+3(104403) TYPE OCTAL NUMBER (NO LEADING ZEROS)
10963 054450 052070      $TYPON ;;CALL=TYPON   TRAP+4(104404) TYPE OCTAL NUMBER (AS PER LAST CALL)
10964 054452 051336      $TYPDS ;;CALL=TYPDS   TRAP+5(104405) TYPE DECIMAL NUMBER (WITH SIGN)
```


10965					
10966	054454	052656	\$GTSWR	::CALL=GTSWR	TRAP+6(104406) GET SOFT-SWR SETTING
10967					
10968	054456	052566	\$CKSWR	::CALL=CKSWR	TRAP+7(104407) TEST FOR CHANGE IN SOFT-SWR
10969	054460	053130	\$RDCHR	::CALL=RDCHR	TRAP+10(104410) TTY TYPEIN CHARACTER ROUTINE
10970	054462	053220	\$RDLIN	::CALL=RDLIN	TRAP+11(104411) TTY TYPEIN STRING ROUTINE
10971	054464	053540	\$RDOCT	::CALL=RDOCT	TRAP+12(104412) READ AN OCTAL NUMBER FROM TTY
10972	054466	054310	\$SAVREG	::CALL=SAVREG	TRAP+13(104413) SAVE R0-R5 ROUTINE
10973	054470	054346	\$RESREG	::CALL=RESREG	TRAP+14(104414) RESTORE R0-R5 ROUTINE
10974	054472	047434	\$SCOP1\$::CALL=SCOP1	TRAP+15(104415) INTERNAL LOOP ON ERROR
10975					

Address	Offset	Value	Value	Value	Description
10976					
10977					
10978					.SBTTL SERVICE MSGS
10979	054474	000062			.EVEN
10980					BSE22S: .BLKW 50. ;22 SECTOR SOFTWARE INFO
10981					
10982	054640	005015	045522	033060	MSG1: .ASCII <CR><LF>/RK06\07 DRV TST PRT 1/
10983	054646	030134	020067	051104	
10984	054654	020126	051524	020124	
10985	054662	051120	020124	061	
10986	054667	015	041412	051132	.ASCII <CR><LF>/CZR6HFO/<CR><LF>
10987	054674	044066	030106	005015	
10988	054702	005015	025011	025052	.ASCII <CR><LF>/ *** CAUTION ***/<CR><LF>
10989	054710	041440	052501	044524	
10990	054716	047117	025040	025052	
10991	054724	005015			
10992	054726	005015	044124	051511	.ASCII <CR><LF>/THIS PROGRAM SHOULD BE HALTED ONLY BY TYPING CONTROL-C/
10993	054734	050040	047522	051107	
10994	054742	046501	051440	047510	
10995	054750	046125	020104	042502	
10996	054756	044040	046101	042524	
10997	054764	020104	047117	054514	
10998	054772	041040	020131	054524	
10999	055000	044520	043516	041440	
11000	055006	047117	051124	046117	
11001	055014	041455			
11002	055016	005015	052117	042510	.ASCII <CR><LF>/OTHERWISE, CARTRIDGE FORMATTING AND,OR THE DRIVE/
11003	055024	053522	051511	026105	
11004	055032	041440	051101	051124	
11005	055040	042111	042507	043040	
11006	055046	051117	040515	052124	
11007	055054	047111	020107	047101	
11008	055062	026104	051117	052040	
11009	055070	042510	042040	044522	
11010	055076	042526			
11011	055100	005015	040515	020131	.ASCII <CR><LF>/MAY BE LEFT IN AN UNDETERMINED STATE/<CR><LF>
11012	055106	042502	046040	043105	
11013	055114	020124	047111	040440	
11014	055122	020116	047125	042504	
11015	055130	042524	046522	047111	
11016	055136	042105	051440	040524	
11017	055144	042524	005015		
11018	055150	005015	047111	052111	.ASCII <CR><LF>/INITIALLY, DRIVES TO BE TESTED SHOULD HAVE: /<CR><LF>
11019	055156	040511	046114	026131	
11020	055164	042040	044522	042526	
11021	055172	020123	047524	041040	
11022	055200	020105	042524	052123	
11023	055206	042105	051440	047510	
11024	055214	046125	020104	040510	
11025	055222	042526	006472	012	
11026	055227	015	040412	020056	.ASCII <CR><LF>/A. HEADS MANUALLY LOADED/
11027	055234	044040	040505	051504	
11028	055242	046440	047101	040525	
11029	055250	046114	020131	047514	
11030	055256	042101	042105		
11031	055262	005015	027102	020040	.ASCII <CR><LF>/B. CORRECT PORT SELECTED/

11032	055270	047503	051122	041505	
11033	055276	020124	047520	052122	
11034	055304	051440	046105	041505	
11035	055312	042524	104		
11036	055315	015	041412	020056	.ASCII <CR><LF>/C. WRITE LOCK DISABLED/
11037	055322	053440	044522	042524	
11038	055330	046040	041517	020113	
11039	055336	044504	040523	046102	
11040	055344	042105			
11041	055346	005015	027104	020040	.ASCII <CR><LF>/D. DRIVE READY INDICATOR ON/<CR><LF>
11042	055354	051104	053111	020105	
11043	055362	042522	042101	020131	
11044	055370	047111	044504	040503	
11045	055376	047524	020122	047117	
11046	055404	005015			
11047	055406	005015	051104	053111	.ASCII <CR><LF>/DRIVES NOT TO BE TESTED MUST HAVE BOTH/
11048	055414	051505	047040	052117	
11049	055422	052040	020117	042502	
11050	055430	052040	051505	042524	
11051	055436	020104	052515	052123	
11052	055444	044040	053101	020105	
11053	055452	047502	044124		
11054	055456	005015	047520	052122	.ASCIIZ <CR><LF>/PORTS DESELECTED/<CR><LF>
11055	055464	020123	042504	042523	
11056	055472	042514	052103	042105	
11057	055500	005015	000		
11058	055503	015	052012	020117	MSG2: .ASCII <CR><LF>/TO TEST DRIVE 0, REMOVE XXDP MEDIA,/
11059	055510	042524	052123	042040	
11060	055516	044522	042526	030040	
11061	055524	020054	042522	047515	
11062	055532	042526	054040	042130	
11063	055540	020120	042515	044504	
11064	055546	026101			
11065	055550	005015	046103	040505	.ASCII <CR><LF>/CLEAR LOC 40, & HIT CONT. ./
11066	055556	020122	047514	020103	
11067	055564	030064	020054	020046	
11068	055572	044510	020124	047503	
11069	055600	052116	020056	056	
11070	055605	015	044412	020106	.ASCII <CR><LF>/IF DRIVE 0 ISN'T TO BE TESTED JUST HIT CONT. ./<CR><LF>
11071	055612	051104	053111	020105	
11072	055620	020060	051511	023516	
11073	055626	020124	047524	041040	
11074	055634	020105	042524	052123	
11075	055642	042105	045040	051525	
11076	055650	020124	044510	020124	
11077	055656	047503	052116	020056	
11078	055664	006456	012		
11079	055667	015	042012	044522	MSG3: .ASCIIZ <CR><LF>/DRIVE(S) TO BE TESTED: /
11080	055674	042526	051450	020051	
11081	055702	047524	041040	020105	
11082	055710	042524	052123	042105	
11083	055716	020072	000		
11084	055721	015	041012	051525	MSG4: .ASCIIZ <CR><LF>/BUS ADDR (177440): /
11085	055726	040440	042104	020122	
11086	055734	030450	033467	032064	
11087	055742	024460	020072	000	

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11088	055747	015	041412	047117	MSG5:	.ASCIZ <CR><LF>/CONTR ADDR (210): /
11089	055754	051124	040440	042104		
11090	055762	020122	031050	030061		
11091	055770	035051	000040			
11092	055774	005015	047111	051124	MSG6:	.ASCIZ <CR><LF>/INTR AT PC= /
11093	056002	040440	020124	041520		
11094	056010	000075				
11095	056012	005015	051104	053111	MSG7:	.ASCIZ <CR><LF>/DRIVE 0 WILL NOT BE TESTED/
11096	056020	020105	020060	044527		
11097	056026	046114	047040	052117		
11098	056034	041040	020105	042524		
11099	056042	052123	042105	000		
11100	056047	015	052012	051505	MSG8:	.ASCIZ <CR><LF>/TEST 16 TAKES 2 TO 4 MIN./<CR><LF>
11101	056054	020124	033061	052040		
11102	056062	045501	051505	031040		
11103	056070	052040	020117	020064		
11104	056076	044515	027116	005015		
11105	056104	000				
11106	056105	015	041012	050131	MSG9:	.ASCIZ <CR><LF>/BYPASSING TEST 16/<CR><LF>
11107	056112	051501	044523	043516		
11108	056120	052040	051505	020124		
11109	056126	033061	005015	000		
11110	056133	015	005012	044527	MSG10:	.ASCIZ <CR><LF><LF>/WILL TEST DRIVE(S): /
11111	056140	046114	052040	051505		
11112	056146	020124	051104	053111		
11113	056154	024105	024523	000072		
11114	056162	005015	050012	053517	MSG11:	.ASCIZ <CR><LF><LF>/POWER UP RESTART TO TEST 1/<CR><LF>
11115	056170	051105	052440	020120		
11116	056176	042522	052123	051101		
11117	056204	020124	047524	052040		
11118	056212	051505	020124	006461		
11119	056220	000012				
11120	056222	005015	040520	045503	MSG12:	.ASCIZ <CR><LF>/PACK BEING FORMATTED/<CR><LF>
11121	056230	041040	044505	043516		
11122	056236	043040	051117	040515		
11123	056244	052124	042105	005015		
11124	056252	000				
11125	056253	015	047012	020117	MSG13:	.ASCII <CR><LF>/NO L OR P CLOCKS/
11126	056260	020114	051117	050040		
11127	056266	041440	047514	045503		
11128	056274	123				
11129	056275	015	040412	046114		.ASCIZ <CR><LF>/ALL TIMING TESTS BYPASSED/
11130	056302	052040	046511	047111		
11131	056310	020107	042524	052123		
11132	056316	020123	054502	040520		
11133	056324	051523	042105	000		
11134	056331	015	041012	050131	MSG14:	.ASCIZ <CR><LF>/BYPASSING DRIVE /
11135	056336	051501	044523	043516		
11136	056344	042040	044522	042526		
11137	056352	000040				
11138	056354	005015	042012	044522	MSG15:	.ASCIZ <CR><LF><LF>/DRIVE /
11139	056362	042526	000040			
11140	056366	005015	051104	053111	MSG16:	.ASCIZ <CR><LF>/DRIVE SERIAL #/
11141	056374	020105	042523	044522		
11142	056402	046101	021440	000		
11143	056407	015	041412	051101	MSG17:	.ASCIZ <CR><LF>/CARTRIDGE SERIAL NO./

11144	056411	051124	042111	042507	
11145	056422	051440	051105	040511	
11146	056430	020114	047516	000056	
11147	056436	005015	040412	047502	MSG18: .ASCIZ <CR><LF><LF>/ABORTING BAL OF TESTS/<CR><LF><LF>
11148	056444	052122	047111	020107	
11149	056452	040502	020114	043117	
11150	056460	052040	051505	051524	
11151	056466	005015	000012		
11152	056472	005015	040412	046114	MSG19: .ASCIZ <CR><LF><LF>/ALL DRIVES TESTED/<CR><LF><LF>
11153	056500	042040	044522	042526	
11154	056506	020123	042524	052123	
11155	056514	042105	005015	000012	
11156	056522	005015	047515	044504	MSG20: .ASCII <CR><LF>/MODIFIED VERSION OF FORMAT PACK TEST FOR MODULE TESTING/
11157	056530	044506	042105	053040	
11158	056536	051105	044523	047117	
11159	056544	047440	020106	047506	
11160	056552	046522	052101	050040	
11161	056560	041501	020113	042524	
11162	056566	052123	043040	051117	
11163	056574	046440	042117	046125	
11164	056602	020105	042524	052123	
11165	056610	047111	107		
11166	056613	015	052012	020117	.ASCIZ <CR><LF>/TO RESTORE HEADERS ON CYL 0 & 1, ALL TRACKS/
11167	056620	042522	052123	051117	
11168	056626	020105	042510	042101	
11169	056634	051105	020123	047117	
11170	056642	041440	046131	030040	
11171	056650	023040	030440	020054	
11172	056656	046101	020114	051124	
11173	056664	041501	051513	000	
11174	056671	015	041012	050131	MSG21: .ASCIZ <CR><LF>/BYPASSING TESTS 36,40,41 FOR MODULE TESTING/<CR><LF>
11175	056676	051501	044523	043516	
11176	056704	052040	051505	051524	
11177	056712	031440	026066	030064	
11178	056720	032054	020061	047506	
11179	056726	020122	047515	052504	
11180	056734	042514	052040	051505	
11181	056742	044524	043516	005015	
11182	056750	000			
11183	056751	015	043012	051117	MSG22: .ASCIZ <CR><LF>/FORMATTING FINISHED/<CR><LF>
11184	056756	040515	052124	047111	
11185	056764	020107	044506	044516	
11186	056772	044123	042105	005015	
11187	057000	000			
11188	057001	015	052012	051505	MSG23: .ASCIZ <CR><LF>/TEST 16 TAKES 10 TO 12 MIN./<CR><LF>
11189	057006	020124	033061	052040	
11190	057014	045501	051505	030440	
11191	057022	020060	047524	030440	
11192	057030	020062	044515	027116	
11193	057036	005015	000		
11194	057041	015	050012	046507	MSG74: .ASCIZ <CR><LF>/PGM ABORT PENDING.../
11195	057046	040440	047502	052122	
11196	057054	050040	047105	044504	
11197	057062	043516	027056	000056	
11198	057070	005015	040510	052114	MSG75: .ASCIZ <CR><LF>/HALT PENDING.../
11199	057076	050040	047105	044504	

11200 057104 043516 027056 000056
11201 057112 005015 043520 020115
11202 057120 041101 051117 042524
11203 057126 000104
11204 057130 005015 050103 020125
11205 057136 040510 052114 042105
11206 057144 000
11207
11208
11209
11210
11211 057145 015 042412 051122
11212 057152 020054 047117 054514
11213 057160 030040 052040 051110
11214 057166 020125 020067 046101
11215 057174 047514 042527 026104
11216 057202 052040 054522 040440
11217 057210 040507 047111 005015
11218 057216 000
11219 057217 123 046105 041505
11220 057224 042524 020104 051104
11221 057232 053111 020105 020043
11222 057240 047111 051040 041513
11223 057246 031123 041440 047101
11224 057254 047516 020124 042502
11225 057262 051040 040505 020104
11226 057270 040502 045503 044440
11227 057276 020116 045522 051115
11228 057304 000062
11229 057306 005015 041101 051117
11230 057314 020124 042524 052123
11231 057322 027123 027056 047125
11232 057330 054105 020120 044524
11233 057336 042515 047440 052125
11234 057344 040440 020124 041520
11235 057352 000075
11236 057354 040506 040524 020114
11237 057362 051105 047522 006522
11238 057370 012
11239 057371 101 047502 052122
11240 057376 047111 020107 040502
11241 057404 040514 041516 020105
11242 057412 043117 052040 051505
11243 057420 051524 005015
11244 057424 042510 042101 050040
11245 057432 051517 052111 047511
11246 057440 020116 040503 047116
11247 057446 052117 041040 020105
11248 057454 042504 042524 046522
11249 057462 047111 042105 000
11250 057467 115 051504 051440
11251 057474 052105 044440 020116
11252 057502 045522 051503 000062
11253 057510 043125 020105 042523
11254 057516 020124 047111 051040
11255 057524 041513 031123 000

MSG76: .ASCIZ <CR><LF>/PGM ABORTED/

MSG77: .ASCIZ <CR><LF>/CPU HALTED/

.SBTTL ERR MSGS

EM1: .ASCIZ <CR><LF>/ERR, ONLY 0 THRU 7 ALLOWED, TRY AGAIN/<CR><LF>

EM2: .ASCIZ /SELECTED DRIVE # IN RKCS2 CANNOT BE READ BACK IN RKMR2/

EM3: .ASCIZ <CR><LF>/ABORT TESTS...UNEXP TIME OUT AT PC=/

EM4: .ASCII /FATAL ERROR/<CR><LF>

.ASCII /ABORTING BALANCE OF TESTS/<CR><LF>

.ASCIZ /HEAD POSITION CANNOT BE DETERMINED/

EM5: .ASCIZ /MDS SET IN RKCS2/

EM6: .ASCIZ /UFE SET IN RKCS2/

11256	057531	104	040522	044440	EM7:	.ASCIZ /DRA IN RKDS & NED IN RKCS2 RESET; WRONG PORT SELECTED?/
11257	057536	020116	045522	051504		
11258	057544	023040	047040	042105		
11259	057552	044440	020116	045522		
11260	057560	051503	020062	042522		
11261	057566	042523	035524	053440		
11262	057574	047522	043516	050040		
11263	057602	051117	020124	042523		
11264	057610	042514	052103	042105		
11265	057616	000077				
11266	057620	051104	020126	054524	EM8:	.ASCIZ /DRV TYPE NOT SET IN RKMR2/
11267	057626	042520	047040	052117		
11268	057634	051440	052105	044440		
11269	057642	020116	045522	051115		
11270	057650	000062				
11271	057652	051104	053111	020105	EM9:	.ASCIZ /DRIVE NOT PRESENT BUT TYPED BY OPERATOR/
11272	057660	047516	020124	051120		
11273	057666	051505	047105	020124		
11274	057674	052502	020124	054524		
11275	057702	042520	020104	054502		
11276	057710	047440	042520	040522		
11277	057716	047524	000122			
11278	057722	041101	051117	020124	EM10:	.ASCIZ /ABORT TESTS...CANNOT REF CONTR REG/
11279	057730	042524	052123	027123		
11280	057736	027056	040503	047116		
11281	057744	052117	051040	043105		
11282	057752	041440	047117	051124		
11283	057760	051040	043505	000		
11284	057765	104	040522	044440	EM11:	.ASCIZ /DRA IN RKDS & NED IN RKCS2 BOTH SET/
11285	057772	020116	045522	051504		
11286	060000	023040	047040	042105		
11287	060006	044440	020116	045522		
11288	060014	051503	020062	047502		
11289	060022	044124	051440	052105		
11290	060030	000				
11291	060031	103	047117	051124	EM12:	.ASCIZ /CONTR NOT READY IN RKCS1/
11292	060036	047040	052117	051040		
11293	060044	040505	054504	044440		
11294	060052	020116	045522	051503		
11295	060060	000061				
11296	060062	047516	040440	052124	EM13:	.ASCIZ /NO ATTN IN RKASOF/
11297	060070	020116	047111	051040		
11298	060076	040513	047523	000106		
11299	060104	051127	047117	020107	EM14:	.ASCIZ /WRONG ATTN IN RKASOF/
11300	060112	052101	047124	044440		
11301	060120	020116	045522	051501		
11302	060126	043117	000			
11303	060131	104	042122	020131	EM15:	.ASCIZ /DRDY NOT CLEARED IN RKMR2/
11304	060136	047516	020124	046103		
11305	060144	040505	042522	020104		
11306	060152	047111	051040	046513		
11307	060160	031122	000			
11308	060163	104	041523	047040	EM16:	.ASCIZ /DSC NOT SET IN RKMR2/
11309	060170	052117	051440	052105		
11310	060176	044440	020116	045522		
11311	060204	051115	000062			

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SEQ 0220

11312	060210	051515	020107	030101	EM17:	.ASCIZ	/MSG A0 ERROR/
11313	060216	042440	051122	051117			
11314	060224	000					
11315	060225	115	043523	041040	EM18:	.ASCIZ	/MSG B0 ERROR/
11316	060232	020060	051105	047522			
11317	060240	000122					
11318	060242	051515	020107	030501	EM19:	.ASCIZ	/MSG A1 ERROR/
11319	060250	042440	051122	051117			
11320	060256	000					
11321	060257	115	043523	041040	EM20:	.ASCIZ	/MSG B1 ERROR/
11322	060264	020061	051105	047522			
11323	060272	000122					
11324	060274	042503	051122	051440	EM21:	.ASCIZ	/CERR SET IN PKCS1/
11325	060302	052105	044440	020116			
11326	060310	045522	051503	000061			
11327	060316	046122	020123	047111	EM22:	.ASCIZ	/RLS IN RKCS2 SET CERR IN RKCS1/
11328	060324	051040	041513	031123			
11329	060332	051440	052105	041440			
11330	060340	051105	020122	047111			
11331	060346	051040	041513	030523			
11332	060354	000					
11333	060355	125	042506	044440	EM23:	.ASCIZ	/UFE IN RKCS2 SET (SACK) AFTER RLS IN RKCS2 SENT/
11334	060362	020116	045522	051503			
11335	060370	020062	042523	020124			
11336	060376	051450	041501	024513			
11337	060404	040440	052106	051105			
11338	060412	051040	051514	044440			
11339	060420	020116	045522	051503			
11340	060426	020062	042523	052116			
11341	060434	000					
11342	060435	126	020126	047516	EM24:	.ASCIZ	/VV NOT SET IN RKMR2/
11343	060442	020124	042523	020124			
11344	060450	047111	051040	046513			
11345	060456	031122	000				
11346	060461	104	053122	052040	EM25:	.ASCIZ	/DRV TYPE SET IN RKMR2/
11347	060466	050131	020105	042523			
11348	060474	020124	047111	051040			
11349	060502	046513	031122	000			
11350	060507	104	052104	051440	EM26:	.ASCIZ	/DDT SET IN RKDS/
11351	060514	052105	044440	020116			
11352	060522	045522	051504	000			
11353	060527	104	054524	020105	EM27:	.ASCIZ	/DTYE SET IN RKER/
11354	060534	042523	020124	047111			
11355	060542	051040	042513	000122			
11356	060550	052104	042531	047040	EM28:	.ASCIZ	/DTYE NOT SET IN RKER AFTER WRONG CDT IN RKCS1/
11357	060556	052117	051440	052105			
11358	060564	044440	020116	045522			
11359	060572	051105	040440	052106			
11360	060600	051105	053440	047522			
11361	060606	043516	041440	052104			
11362	060614	044440	020116	045522			
11363	060622	051503	000061				
11364	060626	052104	042531	044440	EM29:	.ASCIZ	/DTYE IN RKER DID NOT SET CERR IN RKCS1/
11365	060634	020116	045522	051105			
11366	060642	042040	042111	047040			
11367	060650	052117	051440	052105			

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SEQ 0221

11368	060656	041440	051105	020122	
11369	060664	047111	051040	041513	
11370	060672	030523	000		
11371	060675	103	042055	050040	EM30: .ASCIZ /C-D PAR ERR SET IN RKMR3/
11372	060702	051101	042440	051122	
11373	060710	051440	052105	044440	
11374	060716	020116	045522	051115	
11375	060724	000063			
11376	060726	026504	020103	040520	EM31: .ASCIZ /D-C PAR SET IN RKCS1/
11377	060734	020122	042523	020124	
11378	060742	047111	051040	041513	
11379	060750	030523	000		
11380	060753	106	052114	047040	EM32: .ASCIZ /FLT NOT SET IN RKMR3/
11381	060760	052117	051440	052105	
11382	060766	044440	020116	045522	
11383	060774	051115	000063		
11384	061000	026503	020104	040520	EM33: .ASCIZ /C-D PAR ERR NOT SET IN RKMR3/
11385	061006	020122	051105	020122	
11386	061014	047516	020124	042523	
11387	061022	020124	047111	051040	
11388	061030	046513	031522	000	
11389	061035	104	041455	050040	EM34: .ASCIZ /D-C PAR NOT SET IN RKCS1/
11390	061042	051101	047040	052117	
11391	061050	051440	052105	044440	
11392	061056	020116	045522	051503	
11393	061064	000061			
11394	061066	026504	020103	040520	EM35: .ASCIZ /D-C PAR IN RKCS1 DID NOT SET CERR IN RKCS1/
11395	061074	020122	047111	051040	
11396	061102	041513	030523	042040	
11397	061110	042111	047040	052117	
11398	061116	051440	052105	041440	
11399	061124	051105	020122	047111	
11400	061132	051040	041513	030523	
11401	061140	000			
11402	061141	103	046131	040440	EM36: .ASCIZ /CYL ADDR IN B2 NOT SAME AS RKDC/
11403	061146	042104	020122	047111	
11404	061154	041040	020062	047516	
11405	061162	020124	040523	042515	
11406	061170	040440	020123	045522	
11407	061176	041504	000		
11408	061201	103	046131	042040	EM37: .ASCIZ /CYL DIFF IN A2 NOT SAME AS RKDC/
11409	061206	043111	020106	047111	
11410	061214	040440	020062	047516	
11411	061222	020124	040523	042515	
11412	061230	040440	020123	045522	
11413	061236	041504	000		
11414	061241	103	046131	042040	EM38: .ASCIZ /CYL DIFF IN RKMR2 NOT SAME AS 'CYL DIFF'/
11415	061246	043111	020106	047111	
11416	061254	051040	046513	031122	
11417	061262	047040	052117	051440	
11418	061270	046501	020105	051501	
11419	061276	023440	054503	020114	
11420	061304	044504	043106	000047	
11421	061312	054503	020114	044504	EM39: .ASCIZ /CYL DIFF & OFST IN A2 NOT =0/
11422	061320	043106	023040	047440	
11423	061326	051506	020124	047111	

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SEQ 0222

11424	061334	040440	020062	047516	
11425	061342	020124	030075	000	
11426	061347	103	046131	040440	EM40: .ASCIZ /CYL ADDR IN B2 NOT =0/
11427	061354	042104	020122	047111	
11428	061362	041040	020062	047516	
11429	061370	020124	030075	000	
11430	061375	103	046131	040440	EM41: .ASCIZ /CYL ADDR IN B2 DID NOT REMAIN =0/
11431	061402	042104	020122	047111	
11432	061410	041040	020062	044504	
11433	061416	020104	047516	020124	
11434	061424	042522	040515	047111	
11435	061432	036440	000060		
11436	061436	044514	044515	020124	EM42: .ASCIZ /LIMIT DET NOT SET IN B1/
11437	061444	042504	020124	047516	
11438	061452	020124	042523	020124	
11439	061460	047111	041040	000061	
11440	061466	042510	042101	040440	EM43: .ASCIZ /HEAD ADDR IN B3 NOT =0/
11441	061474	042104	020122	047111	
11442	061502	041040	020063	047516	
11443	061510	020124	030075	000	
11444	061515	110	040505	020104	EM44: .ASCIZ /HEAD DECODE IN B3 INCORRECT/
11445	061522	042504	047503	042504	
11446	061530	044440	020116	031502	
11447	061536	044440	041516	051117	
11448	061544	042522	052103	000	
11449	061551	104	044522	042526	EM45: .ASCII /DRIVE READY IN RKMR2 NOT SET BY 1 SEC FROM FWD/
11450	061556	051040	040505	054504	
11451	061564	044440	020116	045522	
11452	061572	051115	020062	047516	
11453	061600	020124	042523	020124	
11454	061606	054502	030440	051440	
11455	061614	041505	043040	047522	
11456	061622	020115	053506	104	
11457	061627	015	044412	020116	.ASCIZ <CR><LF>/IN RTZ PORTION OF START SPIN CMD/
11458	061634	052122	020132	047520	
11459	061642	052122	047511	020116	
11460	061650	043117	051440	040524	
11461	061656	052122	051440	044520	
11462	061664	020116	046503	000104	
11463	061672	051515	020107	031101	EM46: .ASCIZ /MSG A2 ERR/
11464	061700	042440	051122	000	
11465	061705	115	043523	041040	EM47: .ASCIZ /MSG B2 ERR/
11466	061712	020062	051105	000122	
11467	061720	051515	020107	031502	EM48: .ASCIZ /MSG B3 ERR/
11468	061726	042440	051122	000	
11469	061733	106	042127	047040	EM49: .ASCIZ /FWD NOT SET IN RKMR2 IN RTZ PORTION OF START SPIN CMD/
11470	061740	052117	051440	052105	
11471	061746	044440	020116	045522	
11472	061754	051115	020062	047111	
11473	061762	051040	055124	050040	
11474	061770	051117	044524	047117	
11475	061776	047440	020106	052123	
11476	062004	051101	020124	050123	
11477	062012	047111	041440	042115	
11478	062020	000			
11479	062021	106	042127	047040	EM50: .ASCIZ /FWD NOT SET IN RKMR2 FROM START SPIN CMD/

11480	062026	052117	051440	052105
11481	062034	044440	020116	045522
11482	062042	051115	020062	051106
11483	062050	046517	051440	040524
11484	062056	052122	051440	044520
11485	062064	020116	046503	000104
11486	062072	053506	020104	047516
11487	062100	020124	046103	040505
11488	062106	042522	020104	047111
11489	062114	051040	046513	031122
11490	062122	041040	020131	020065
11491	062130	042523	020103	043117
11492	062136	046440	052117	047511
11493	062144	020116	051106	046517
11494	062152	051440	040524	052122
11495	062160	051440	044520	020116
11496	062166	046503	000104	
11497	062172	030062	051440	041505
11498	062200	043040	051117	040515
11499	062206	020124	047516	020124
11500	062214	042523	020124	047111
11501	062222	051040	046513	031122
11502	062230	000		
11503	062231	123	041505	030040
11504	062236	047040	052117	043040
11505	062244	052517	042116	041040
11506	062252	020131	030065	046440
11507	062260	000123		
11508	062262	044504	043106	051440
11509	062270	041505	047040	052117
11510	062276	043040	052517	042116
11511	062304	041040	020131	020063
11512	062312	051515	000	
11513	062315	101	052124	020116
11514	062322	047516	020124	046103
11515	062330	040505	042522	020104
11516	062336	047111	051040	040513
11517	062344	047523	000106	
11518	062350	047125	054105	020120
11519	062356	042515	020115	040520
11520	062364	020122	051124	050101
11521	062372	000		
11522	062373	127	042503	040040
11523	062400	041440	046131	032040
11524	062406	030461	020054	051124
11525	062414	020113	026062	051440
11526	062422	041505	031040	000
11527	062427	015	051412	042520
11528	062434	042105	047440	020113
11529	062442	047111	051040	046513
11530	062450	031122	047040	052117
11531	062456	036440	020060	054502
11532	062464	052040	046511	047505
11533	062472	052125	000	
11534	062475	114	046511	042040
11535	062502	052105	047040	052117

EM51: .ASCIZ /FWD NOT CLEARED IN RKMR2 BY 5 SEC OF MOTION FROM START SPIN CMD/

EM52: .ASCIZ /20 SEC FORMAT NOT SET IN RKMR2/

EM53: .ASCIZ /SEC 0 NOT FOUND BY 50 MS/

EM54: .ASCIZ /DIFF SEC NOT FOUND BY 3 MS/

EM55: .ASCIZ /ATTN NOT CLEARED IN RKASOF/

EM56: .ASCIZ /UNEXP MEM PAR TRAP/

EM57: .ASCIZ /WCE @ CYL 411, TRK 2, SEC 2/

EM58: .ASCIZ <CR><LF>/SPEED OK IN RKMR2 NOT =0 BY TIMEOUT/

EM59: .ASCIZ /LIM DET NOT SET IN RKMR3/

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SEQ 0224

11536	062510	051440	052105	044440		
11537	062516	020116	045522	051115		
11538	062524	000063				
11539	062526	042510	042101	020123	EM60:	.ASCIZ /HEADS HOME NOT SET IN RKMR2/
11540	062534	047510	042515	047040		
11541	062542	052117	051440	052105		
11542	062550	044440	020116	045522		
11543	062556	051115	000062			
11544	062562	047514	042101	044040	EM61:	.ASCIZ /LOAD HEADS NOT SET IN RKMR2/
11545	062570	040505	051504	047040		
11546	062576	052117	051440	052105		
11547	062604	044440	020116	045522		
11548	062612	051115	000062			
11549	062616	046104	020124	042523	EM63:	.ASCIZ /DLT SET IN RKCS2/
11550	062624	020124	047111	051040		
11551	062632	041513	031123	000		
11552	062637	115	043523	041040	EM64:	.ASCIZ /MSG B3 HEAD REG NOT =C/
11553	062644	020063	042510	042101		
11554	062652	051040	043505	047040		
11555	062660	052117	036440	000060		
11556	062666	042522	042101	044040	EM65:	.ASCIZ /READ HEADER ERR/
11557	062674	040505	042504	020122		
11558	062702	051105	000122			
11559	062706	054503	020114	042101	EM66:	.ASCIZ /CYL ADDR IN RKMR3 INCORRECT/
11560	062714	051104	044440	020116		
11561	062722	045522	051115	020063		
11562	062730	047111	047503	051122		
11563	062736	041505	000124			
11564	062742	042522	042101	047111	EM67:	.ASCIZ /READING CYL 0 HEADERS ON CYL 1/
11565	062750	020107	054503	020114		
11566	062756	020060	042510	042101		
11567	062764	051105	020123	047117		
11568	062772	041440	046131	030440		
11569	063000	000				
11570	063001	122	040505	044504	EM68:	.ASCIZ /READING CYL 1 HEADERS ON CYL 0/
11571	063006	043516	041440	046131		
11572	063014	030440	044040	040505		
11573	063022	042504	051522	047440		
11574	063030	020116	054503	020114		
11575	063036	000060				
11576	063040	046101	043511	020116	EM69:	.ASCIZ /ALIGN CART USED/
11577	063046	040503	052122	052440		
11578	063054	042523	000104			
11579	063060	047125	054105	020120	EM70:	.ASCIZ /UNEXP ATTN/
11580	063066	052101	047124	000		
11581	063073	104	041523	051440	EM71:	.ASCIZ /DSC SET IN RKMR2/
11582	063100	052105	044440	020116		
11583	063106	045522	051115	000062		
11584	063114	047506	046522	052101	EM72:	.ASCIZ /FORMAT TEST BYPASSED/
11585	063122	052040	051505	020124		
11586	063130	054502	040520	051523		
11587	063136	042105	000			
11588	063141	103	047524	051440	EM73:	.ASCIZ /CTO SET IN RKCS1/
11589	063146	052105	044440	020116		
11590	063154	045522	051503	000061		
11591	063162	052122	020132	047516	EM74:	.ASCIZ /RTZ NOT SET IN RKMR2/

11592	063170	020124	042523	020124	
11593	063176	047111	051040	046513	
11594	063204	031122	000		
11595	063207	111	040504	020105	EM75: .ASCIZ /IDAE NOT SET IN RKMR3/
11596	063214	047516	020124	042523	
11597	063222	020124	047111	051040	
11598	063230	046513	031522	000	
11599	063235	120	050111	051440	EM76: .ASCIZ /PIP SET IN RKMR2/
11600	063242	052105	044440	020116	
11601	063250	045522	051115	000062	
11602	063256	040506	046125	020124	EM77: .ASCIZ /FAULT NOT =0 IN RKMR3/
11603	063264	047516	020124	030075	
11604	063272	044440	020116	045522	
11605	063300	051115	000063		
11606	063304	054503	020114	044504	EM78: .ASCIZ /CYL DIFF IN RKMR2 DID NOT REMAIN = 1 IN SEEK TO SELF/
11607	063312	043106	044440	020116	
11608	063320	045522	051115	020062	
11609	063326	044504	020104	047516	
11610	063334	020124	042522	040515	
11611	063342	047111	036440	030440	
11612	063350	044440	020116	042523	
11613	063356	045505	052040	020117	
11614	063364	042523	043114	000	
11615	063371	116	042105	051440	EM79: .ASCIZ /NED SET IN RKCS2/
11616	063376	052105	044440	020116	
11617	063404	045522	051503	000062	
11618	063412	047125	047514	042101	EM80: .ASCIZ /UNLOAD NOT SET IN RKMR2/
11619	063420	047040	052117	051440	
11620	063426	052105	044440	020116	
11621	063434	045522	051115	000062	
11622	063442	050123	047111	047040	EM81: .ASCIZ /SPIN NOT SET IN RKMR2/
11623	063450	052117	051440	052105	
11624	063456	044440	020116	045522	
11625	063464	051115	000062		
11626	063470	052122	020132	047516	EM82: .ASCIZ /RTZ NOT SET IN RKMR2/
11627	063476	020124	042523	020124	
11628	063504	047111	051040	046513	
11629	063512	031122	000		
11630	063515	122	040505	020104	EM83: .ASCIZ /READ HEADER ERR WORD 0 (CYL#)/
11631	063522	042510	042101	051105	
11632	063530	042440	051122	020040	
11633	063536	053440	051117	020104	
11634	063544	020060	041450	046131	
11635	063552	024443	000		
11636	063555	106	051117	040515	EM84: .ASCIZ /FORMAT IN RKMR3 NOT SET/
11637	063562	020124	047111	051040	
11638	063570	046513	031522	047040	
11639	063576	052117	051440	052105	
11640	063604	000			
11641	063605	111	046114	040440	EM85: .ASCIZ /ILL ADDR IN RKMR3 NOT =0/
11642	063612	042104	020122	047111	
11643	063620	051040	046513	031522	
11644	063626	047040	052117	036440	
11645	063634	000060			
11646	063636	044127	046111	020105	EM86: .ASCIZ /WHILE WAITING FOR CONTR RDY OR AFTER CONTR RDY REC'D/
11647	063644	040527	052111	047111	

11648	063652	020107	047506	020122	
11649	063660	047503	052116	020122	
11650	063666	042122	020131	051117	
11651	063674	040440	052106	051105	
11652	063702	041440	047117	051124	
11653	063710	051040	054504	051040	
11654	063716	041505	042047	000	
11655	063723	103	047101	047516	EM87: .ASCIZ /CANNOT READ BSE INFO/
11656	063730	020124	042522	042101	
11657	063736	041040	042523	044440	
11658	063744	043116	000117		
11659	063750	047516	042040	044522	EM88: .ASCII /NO DRIVES FOUND ON BUS/<CR><LF>
11660	063756	042526	020123	047506	
11661	063764	047125	020104	047117	
11662	063772	041040	051525	005015	
11663	064000	042523	052524	020120	.ASCIZ /SETUP CORRECTLY & PRESS 'CONT'/<CR><LF>
11664	064006	047503	051122	041505	
11665	064014	046124	020131	020046	
11666	064022	051120	051505	020123	
11667	064030	041447	047117	023524	
11668	064036	005015	000		
11669	064041	116	020117	051104	EM89: .ASCII /NO DRIVES FOUND IN DEVICE MAP (\$DEVN)/<CR><LF>
11670	064046	053111	051505	043040	
11671	064054	052517	042116	044440	
11672	064062	020116	042504	044526	
11673	064070	042503	046440	050101	
11674	064076	024040	042044	053105	
11675	064104	024515	005015		
11676	064110	042523	052524	020120	.ASCIZ /SETUP CORRECTLY & RESTART/<CR><LF>
11677	064116	047503	051122	041505	
11678	064124	046124	020131	020046	
11679	064132	042522	052123	051101	
11680	064140	006524	000012		
11681	064144	042104	020124	047516	EM90: .ASCIZ /DDT NOT SET IN RKDS/
11682	064152	020124	042523	020124	
11683	064160	047111	051040	042113	
11684	064166	000123			
11685					
11686					.SBTTL DATA HEADERS
11687					
11688	064170	042524	052123	047040	DH1: .ASCIZ /TEST NO. PC/
11689	064176	027117	020040	041520	
11690	064204	000			
11691	064205	122	046513	030522	DH2: .ASCIZ /RKMR1 RKMR2 RKMR3 RKER RKDS RKCS1 RKCS2/
11692	064212	051011	046513	031122	
11693	064220	051011	046513	031522	
11694	064226	051011	042513	004522	
11695	064234	045522	051504	051011	
11696	064242	041513	030523	051011	
11697	064250	041513	031123	000	
11698	064255	122	053513	004503	DH3: .ASCIZ /RKWC RKBA RKDA RKASOF RKDC RKECPS RKECPT/
11699	064262	045522	040502	051011	
11700	064270	042113	004501	045522	
11701	064276	051501	043117	051011	
11702	064304	042113	004503	045522	
11703	064312	041505	051520	051011	

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11704	064320	042513	050103	000124		
11705	064326	051106	046517	041440	DH6:	.ASCIZ /FROM CYL TO CYL CYL DIFF/
11706	064334	046131	020040	047524		
11707	064342	041440	046131	020040		
11708	064350	054503	020114	044504		
11709	064356	043106	000			
11710	064361	127	042510	020116	DH8:	.ASCIZ /WHEN DRIVE UNLOADED/
11711	064366	051104	053111	020105		
11712	064374	047125	047514	042101		
11713	064402	042105	000			
11714	064405	101	052106	051105	DH9:	.ASCIZ /AFTER START SPIN CMD REC'D BY DRIVE/
11715	064412	051440	040524	052122		
11716	064420	051440	044520	020116		
11717	064426	046503	020104	042522		
11718	064434	023503	020104	054502		
11719	064442	042040	044522	042526		
11720	064450	000				
11721	064451	101	020124	047105	DH10:	.ASCIZ /AT END OF HEAD LOADING/
11722	064456	020104	043117	044040		
11723	064464	040505	020104	047514		
11724	064472	042101	047111	000107		
11725	064500	043101	042524	020122	DH11:	.ASCIZ /AFTER START SPIN CMD & FWD SET/
11726	064506	052123	051101	020124		
11727	064514	050123	047111	041440		
11728	064522	042115	023040	043040		
11729	064530	042127	051440	052105		
11730	064536	000				
11731	064537	101	020124	047111	DH12:	.ASCIZ /AT INNER LIM FROM START SPIN CMD/
11732	064544	042516	020122	044514		
11733	064552	020115	051106	046517		
11734	064560	051440	040524	052122		
11735	064566	051440	044520	020116		
11736	064574	046503	000104			
11737	064600	051106	046517	047440	DH13:	.ASCIZ /FROM OUTER LIM TO CYL 0 DURING LOADING/
11738	064606	052125	051105	046040		
11739	064614	046511	052040	020117		
11740	064622	054503	020114	020060		
11741	064630	052504	044522	043516		
11742	064636	046040	040517	044504		
11743	064644	043516	000			
11744	064647	101	052106	051105	DH14:	.ASCIZ /AFTER SEEK WITH BAD PAR/
11745	064654	051440	042505	020113		
11746	064662	044527	044124	041040		
11747	064670	042101	050040	051101		
11748	064676	000				
11749	064677	101	052106	051105	DH16:	.ASCIZ /AFTER LOADING HEAD REG & SEEK CMD/
11750	064704	046040	040517	044504		
11751	064712	043516	044040	040505		
11752	064720	020104	042522	020107		
11753	064726	020046	042523	045505		
11754	064734	041440	042115	000		
11755	064741	101	052106	051105	DH17:	.ASCIZ /AFTER RECAL CMD/
11756	064746	051040	041505	046101		
11757	064754	041440	042115	000		
11758	064761	101	052106	051105	DH18:	.ASCIZ /AFTER UNLOAD CMD/
11759	064766	052440	046116	040517		

11816	065442	051440	041505	040440		
11817	065450	052106	051105	052440		
11818	065456	046116	040517	000104		
11819	065464	043101	042524	020122	DH38:	.ASCIZ /AFTER LIM DET/
11820	065472	044514	020115	042504		
11821	065500	000124				
11822	065502	043101	042524	020122	DH39:	.ASCIZ /AFTER WRITE HEADER CMD/
11823	065510	051127	052111	020105		
11824	065516	042510	042101	051105		
11825	065524	041440	042115	000		
11826	065531	127	051117	021504	DH40:	.ASCIZ /WORD# HEADER WAS SHOULD BE/
11827	065536	044011	040505	042504		
11828	065544	020122	040527	020123		
11829	065552	051440	047510	046125		
11830	065560	020104	042502	000		
11831	065565	104	051125	047111	DH41:	.ASCIZ /DURING RECAL CMD/
11832	065572	020107	042522	040503		
11833	065600	020114	046503	000104		
11834	065606	047117	051440	041505	DH42:	.ASCIZ /ON SEC 0,2,4,6,8 CYL 410 TRK 2/
11835	065614	030040	031054	032054		
11836	065622	033054	034054	020040		
11837	065630	054503	020114	030464		
11838	065636	020060	051124	020113		
11839	065644	000062				
11840	065646	047506	046522	052101	DH44:	.ASCIZ /FORMAT & ALL R-W TESTS WILL BE BYPASSED/
11841	065654	023040	040440	046114		
11842	065662	051040	053455	052040		
11843	065670	051505	051524	053440		
11844	065676	046111	020114	042502		
11845	065704	041040	050131	051501		
11846	065712	042523	000104			
11847	065716	042502	040503	051525	DH45:	.ASCIZ /BECAUSE OF LIMIT DETECT ERROR ON PREVIOUS TEST/
11848	065724	020105	043117	046040		
11849	065732	046511	052111	042040		
11850	065740	052105	041505	020124		
11851	065746	051105	047522	020122		
11852	065754	047117	050040	042522		
11853	065762	044526	052517	020123		
11854	065770	042524	052123	000		
11855	065775	103	052517	042114	DH46:	.ASCIZ /COULD NOT READ BSE INFO ON PREV TEST/
11856	066002	047040	052117	051040		
11857	066010	040505	020104	051502		
11858	066016	020105	047111	047506		
11859	066024	047440	020116	051120		
11860	066032	053105	052040	051505		
11861	066040	000124				
11862	066042	043101	042524	020122	DH48:	.ASCIZ /AFTER SEEK CMD TO INV CYL/
11863	066050	042523	045505	041440		
11864	066056	042115	052040	020117		
11865	066064	047111	020126	054503		
11866	066072	000114				
11867	066074	051515	020107	023101	DH49:	.ASCIZ /MSG A&B IN RKMR2 & RKMR3 RESP., ARE INVALID/
11868	066102	020102	047111	051040		
11869	066110	046513	031122	023040		
11870	066116	051040	046513	031522		
11871	066124	051040	051505	027120		

11872	066132	020054	051101	020105	
11873	066140	047111	040526	044514	
11874	066146	000104			
11875	066150	043101	042524	020122	DH51: .ASCIZ /AFTER SEEK TO SELF CMD/
11876	066156	042523	045505	052040	
11877	066164	020117	042523	043114	
11878	066172	041440	042115	000	
11879	066177	105	050130	041440	DH52: .ASCIZ /EXP CYL# CYL HEADER WAS/
11880	066204	046131	004443	054503	
11881	066212	020114	042510	042101	
11882	066220	051105	053440	051501	
11883	066226	000			
11884	066227	117	020116	042523	DH53: .ASCIZ /ON SEC 10,12,14,16,18,20 CYL 410 TRK 2/
11885	066234	020103	030061	030454	
11886	066242	026062	032061	030454	
11887	066250	026066	034061	031054	
11888	066256	020060	054503	020114	
11889	066264	030464	020060	051124	
11890	066272	020113	000062		
11891					.SBTTL ERR OUTPUT DATA
11892					
11893					
11894	066276	001214	001116		DT1: .EVEN
11895	066302	003346	003350	003352	\$TESTN,\$ERRPC
11896	066310	003336	003334	003322	HMR1,HMR2,HMR3,HER,HDS,HCS1,HCS2
11897	066316	003324			
11898	066320	003326	003330	003332	HWC,HBA,HDA,HASOF,HDC,HPOS,HPAT
11899	066326	003340	003342	003354	
11900	066334	003356			
11901	066336	001214	001116	001350	DT4: \$TESTN,\$ERRPC,FRCYL,TOCYL,CALDIF
11902	066344	001352	001360		
11903	066350	003346	003350	003352	HMR1,HMR2,HMR3,HER,HDS,HCS1,HCS2
11904	066356	003336	003334	003322	
11905	066364	003324			
11906	066366	003326	003330	003332	HWC,HBA,HDA,HASOF,HDC,HPOS,HPAT
11907	066374	003340	003342	003354	
11908	066402	003356			
11909	066404	001214	001116	001402	DT6: \$TESTN,\$ERRPC,PSEC,ESEC
11910	066412	001404			
11911	066414	003346	003350	003352	HMR1,HMR2,HMR3,HER,HDS,HCS1,HCS2
11912	066422	003336	003334	003322	
11913	066430	003324			
11914	066432	003326	003330	003332	HWC,HBA,HDA,HASOF,HDC,HPOS,HPAT
11915	066440	003340	003342	003354	
11916	066446	003356			
11917	066450	001214	001116	001442	DT7: \$TESTN,\$ERRPC,WDCNT,HDWD,TEMP1
11918	066456	001454	003360		
11919	066462	003346	003350	003352	HMR1,HMR2,HMR3,HER,HDS,HCS1,HCS2
11920	066470	003336	003334	003322	
11921	066476	003324			
11922	066500	003326	003330	003332	HWC,HBA,HDA,HASOF,HDC,HPOS,HPAT
11923	066506	003340	003342	003354	
11924	066514	003356			
11925	066516	001214	001116	001352	DT8: \$TESTN,\$ERRPC,TOCYL,FRCYL,CALDIF
11926	066524	001350	001360		
11927	066530	003346	003350	003352	HMR1,HMR2,HMR3,HER,HDS,HCS1,HCS2

11928	066536	003336	003334	003322		
11929	066544	003324				
11930	066546	003326	003330	003332		HWC ,HBA ,HDA ,HASOF ,HDC ,HPOS ,HPAT
11931	066554	003340	003342	003354		
11932	066562	003356				
11933	066564	001214	001116	001352	DT9:	\$TESTN,\$ERRPC ,TOCYL ,RHTAB
11934	066572	001674				
11935	066574	003346	003350	003352		HMR1 ,HMR2 ,HMR3 ,HER ,HDS ,HCS1 ,HCS2
11936	066602	003336	003334	003322		
11937	066610	003324				
11938	066612	003326	003330	003332		HWC ,HBA ,HDA ,HASOF ,HDC ,HPOS ,HPAT
11939	066620	003340	003342	003354		
11940	066626	003356				
11941	066630	001214	001116	001350	DT10:	\$TESTN,\$ERRPC ,FRCYL ,RHTAB
11942	066636	001674				
11943	066640	003346	003350	003352		HMR1 ,HMR2 ,HMR3 ,HER ,HDS ,HCS1 ,HCS2
11944	066646	003336	003334	003322		
11945	066654	003324				
11946	066656	003326	003330	003332		HWC ,HBA ,HDA ,HASOF ,HDC ,HPOS ,HPAT
11947	066664	003340	003342	003354		
11948	066672	003356				
11949	066674	001214	001334		DT11:	\$TESTN ,TRAPPC
11950	066700	001214	001116	003412	DT13:	\$TESTN,\$ERRPC ,E .A0 ,E .B0 ,E .A1 ,E .B1 ,H .A0 ,H .B0 ,H .A1 ,H .B1
11951	066706	003414	003416	003420		
11952	066714	003372	003374	003376		
11953	066722	003400				
11954	066724	003346	003350	003352		HMR1 ,HMR2 ,HMR3 ,HER ,HDS ,HCS1 ,HCS2
11955	066732	003336	003334	003322		
11956	066740	003324				
11957	066742	003326	003330	003332		HWC ,HBA ,HDA ,HASOF ,HDC ,HPOS ,HPAT
11958	066750	003340	003342	003354		
11959	066756	003356				
11960						
11961	066760	001214	001116	003412	DT14:	\$TESTN,\$ERRPC ,E .A0 ,E .B0 ,E .A1 ,E .B1 ,E .A2 ,E .B2
11962	066766	003414	003416	003420		
11963	066774	003422	003424			
11964	067000	003372	003374	003376		H .A0 ,H .B0 ,H .A1 ,H .B1 ,H .A2 ,H .B2
11965	067006	003400	003402	003404		
11966	067014	003346	003350	003352		HMR1 ,HMR2 ,HMR3 ,HER ,HDS ,HCS1 ,HCS2
11967	067022	003336	003334	003322		
11968	067030	003324				
11969	067032	003326	003330	003332		HWC ,HBA ,HDA ,HASOF ,HDC ,HPOS ,HPAT
11970	067040	003340	003342	003354		
11971	067046	003356				
11972						
11973	067050	001214	001116	003412	DT15:	\$TESTN,\$ERRPC ,E .A0 ,E .B0 ,E .A1 ,E .B1 ,E .A2 ,E .B2 ,E .B3
11974	067056	003414	003416	003420		
11975	067064	003422	003424	003430		
11976	067072	003372	003374	003376		H .A0 ,H .B0 ,H .A1 ,H .B1 ,H .A2 ,H .B2 ,H .B3
11977	067100	003400	003402	003404		
11978	067106	003410				
11979	067110	003346	003350	003352		HMR1 ,HMR2 ,HMR3 ,HER ,HDS ,HCS1 ,HCS2
11980	067116	003336	003334	003322		
11981	067124	003324				
11982	067126	003326	003330	003332		HWC ,HBA ,HDA ,HASOF ,HDC ,HPOS ,HPAT
11983	067134	003340	003342	003354		

Line	Code	Value	DF	Format
11984	067142	003356		
11985				
11986				
11987				
11988	067144	000003	DF1:	3
11989	067146	002 000		.BYTE 2,0
11990	067150	064205		DH2
11991	067152	007 000		.BYTE 7,0
11992	067154	064255		DH3
11993	067156	007 000		.BYTE 7,0
11994				
11995	067160	000005	DF2:	5
11996	067162	000 000		.BYTE 0,0
11997	067164	066074		DH49
11998	067166	000 000		.BYTE 0,0
11999	067170	064170		DH1
12000	067172	002 000		.BYTE 2,0
12001	067174	064205		DH2
12002	067176	007 000		.BYTE 7,0
12003	067200	064255		DH3
12004	067202	007 000		.BYTE 7,0
12005				
12006	067204	000001	DF3:	1
12007	067206	002 000		.BYTE 2,0
12008	067210	000003	DF4:	3
12009	067212	002 000		.BYTE 2,0
12010	067214	064205		DH2
12011	067216	007 000		.BYTE 7,0
12012	067220	064255		DH3
12013	067222	007 000		.BYTE 7,0
12014				
12015	067224	000004	DF5:	4
12016	067226	000 000		.BYTE 0,0
12017	067230	064170		DH1
12018	067232	002 000		.BYTE 2,0
12019	067234	064205		DH2
12020	067236	007 000		.BYTE 7,0
12021	067240	064255		DH3
12022	067242	007 000		.BYTE 7,0
12023				
12024	067244	000005	DF6:	5
12025	067246	000 000		.BYTE 0,0
12026	067250	064170		DH1
12027	067252	002 000		.BYTE 2,0
12028	067254	064326		DH6
12029	067256	003 000		.BYTE 3,0
12030	067260	064205		DH2
12031	067262	007 000		.BYTE 7,0
12032	067264	064255		DH3
12033	067266	007 000		.BYTE 7,0
12034				
12035				
12036				
12037	067270	000004	DF10:	4
12038	067272	000 000		.BYTE 0,0
12039	067274	064170		DH1

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SEQ 0233

12040	067276	002	000		.BYTE 2,0
12041	067300	064205			DH2
12042	067302	007	000		.BYTE 7,0
12043	067304	064255			DH3
12044	067306	007	000		.BYTE 7,0
12045					
12046	067310	000005		DF12:	5
12047	067312	000	000		.BYTE 0,0
12048	067314	064170			DH1
12049	067316	002	000		.BYTE 2,0
12050	067320	065375			DH36
12051	067322	002	000		.BYTE 2,0
12052	067324	064205			DH2
12053	067326	007	000		.BYTE 7,0
12054	067330	064255			DH3
12055	067332	007	000		.BYTE 7,0
12056					
12057	067334	000004		DF14:	4
12058	067336	002	000		.BYTE 2,0
12059	067340	065531			DH40
12060	067342	003	000		.BYTE 3,0
12061	067344	064205			DH2
12062	067346	007	000		.BYTE 7,0
12063	067350	064255			DH3
12064	067352	007	000		.BYTE 7,0
12065					
12066					
12067	067354	000004		DF15:	4
12068	067356	000	000		.BYTE 0,0
12069	067360	064170			DH1
12070	067362	002	000		.BYTE 2,0
12071	067364	064205			DH2
12072	067366	007	000		.BYTE 7,0
12073	067370	064255			DH3
12074	067372	007	000		.BYTE 7,0
12075					
12076	067374	000004		DF16:	4
12077	067376	000	000		.BYTE 0,0
12078	067400	064170			DH1
12079	067402	002	000		.BYTE 2,0
12080	067404	064205			DH2
12081	067406	007	000		.BYTE 7,0
12082	067410	064255			DH3
12083	067412	007	000		.BYTE 7,0
12084					
12085	067414	000005		DF17:	5
12086	067416	000	000		.BYTE 0,0
12087	067420	065646			DH44
12088	067422	000	000		.BYTE 0,0
12089	067424	064170			DH1
12090	067426	002	000		.BYTE 2,0
12091	067430	064205			DH2
12092	067432	007	000		.BYTE 7,0
12093	067434	064255			DH3
12094	067436	007	000		.BYTE 7,0
12095	067440	000007		DF20:	7

12096	067442	000	000	.BYTE	0.0
12097	067444	064170		DH1	
12098	067446	002	000	.BYTE	2.0
12099	067450	065210		DH28	
12100	067452	000	000	.BYTE	0.0
12101	067454	065256		DH32	
12102	067456	004	000	.BYTE	4.0
12103	067460	065220		DH29	
12104	067462	004	000	.BYTE	4.0
12105	067464	064205		DH2	
12106	067466	007	000	.BYTE	7.0
12107	067470	064255		DH3	
12108	067472	007	000	.BYTE	7.0
12109	067474	000004	DF21:	4	
12110	067476	002	000	.BYTE	2.0
12111	067500	066177		DH52	
12112	067502	002	000	.BYTE	2.0
12113	067504	064205		DH2	
12114	067506	007	000	.BYTE	7.0
12115	067510	064255		DH3	
12116	067512	007	000	.BYTE	7.0
12117	067514	000007	DF22:	7	
12118	067516	000	000	.BYTE	0.0
12119	067520	064170		DH1	
12120	067522	002	000	.BYTE	2.0
12121	067524	065210		DH28	
12122	067526	000	000	.BYTE	0.0
12123	067530	065256		DH32	
12124	067532	006	000	.BYTE	6.0
12125	067534	065220		DH29	
12126	067536	006	000	.BYTE	6.0
12127	067540	064205		DH2	
12128	067542	007	000	.BYTE	7.0
12129	067544	064255		DH3	
12130	067546	007	000	.BYTE	7.0
12131					
12132	067550	000007	DF23:	7	
12133	067552	000	000	.BYTE	0.0
12134	067554	064170		DH1	
12135	067556	002	000	.BYTE	2.0
12136	067560	065210		DH28	
12137	067562	000	000	.BYTE	0.0
12138	067564	065256		DH32	
12139	067566	007	000	.BYTE	7.0
12140	067570	065220		DH29	
12141	067572	007	000	.BYTE	7.0
12142	067574	064205		DH2	
12143	067576	007	000	.BYTE	7.0
12144	067600	064255		DH3	
12145	067602	007	000	.BYTE	7.0


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12146  
12147  
12148  
12149  
12150  
12151  
12152  
12153  
12154  
12155  
12156 067604 104413  
12157 067606 113700 001114  
12158 067612 042700 177400  
12159 067616 005300  
12160 067620 006300  
12161 067622 006300  
12162 067624 006300  
12163 067626 062700 003474  
12164 067632 012037 067646  
12165 067636 001404  
12166 067640 104401 001205  
12167 067644 104401  
12168 067646 000000  
12169 067650 012037 067664  
12170 067654 001404  
12171 067656 104401 001205  
12172 067662 104401  
12173 067664 000000  
12174 067666 012001  
12175 067670 001455  
12176 067672 005004  
12177 067674 012000  
12178 067676 012002  
12179 067700 001446  
12180 067702 005104  
12181 067704 104401 001205  
12182 067710 112003  
12183 067712 105720  
12184 067714 005703  
12185 067716 001407  
12186 067720 013146  
12187 067722 104402  
12188 067724 005303  
12189 067726 001403  
12190 067730 104401 070060  
12191 067734 000771  
12192 067736 005302  
12193 067740 003431  
12194 067742 104401 001205  
12195 067746 005760 000002  
12196 067752 001404  
12197 067754 005104  
12198 067756 001002  
12199 067760 104401 070060  
12200 067764 012037 067772  
12201 067770 104401
```

```
*****  
:SBTTL TYPE ERR ROUTINE  
:*ENTRY JSR PC,TYP ERR  
:*RETURN RTS PC  
:*  
:*THIS ROUTINE USES THE "ITEM CONTROL BYTE" ($ITEMB) TO DETERMINE WHICH  
:*ERR IS TO BE REPORTED. IT THEN USES THE "ERR TABLE" ($ERRTB)  
:*ENTRY TO DEFINE WHAT INFORMATION IS TO BE REPORTED CONCERNING  
:*THE ERR.  
*****  
TYPERR: SAVREG  
MOV $ITEMB,R0 ;ENTER ERR NUMBER  
BIC #177400,R0 ;CLEAR SIGN EXTENSION  
DEC R0 ;FORM INDEX FOR ERR TABLE  
ASL R0  
ASL R0  
ASL R0  
1$: ADD #SERRTB,R0 ;FORM ADDRESS OF ERR ENTRY  
MOV (R0)+,2$ ;GET EM POINTER  
BEQ 3$ ;BRANCH IF THERE ISN'T ONE  
TYPE ,SCLF ;TYPE CARRIAGE RETURN LINE FEED  
TYPE ;TYPE ERR MSG (EM)  
2$: .WORD 0 ;EM POINTER GOES HERE  
3$: MOV (R0)+,4$ ;GET DH POINTER  
BEQ 5$ ;BRANCH IF THERE ISN'T ONE  
TYPE ,SCLF ;TYPE CR-LF  
TYPE ;TYPE DATA HEADER  
4$: .WORD 0 ;DH POINTER GOES HERE  
5$: MOV (R0)+,R1 ;GET DT POINTER  
BEQ 20$ ;BRANCH IF THERE ARE NONE  
CLR R4 ;SET INDENT SWITCH  
MOV (R0)+,R0 ;GET DF POINTER  
MOV (R0)+,R2 ;STORE NUMBER OF DH'S  
BEQ 17$ ;DH NUM IS 0-BRANCH  
COM R4 ;NO INDENT  
TYPE ,SCLF  
10$: MOV (R0)+,R3 ;GET & STORE NUMBER OF DATA WORDS  
TSTB (R0)+ ;BUMP PAST FORMAT WORD  
TST R3 ;TEST IF ANY DATA FOR THIS HEADER  
BEQ 14$ ;NO - SKIP DATA PRINT  
11$: MOV @R1+,-(SP) ;PUT FIRST DATA WORD ON STACK  
TYPOC ;TYPE IT  
DEC R3 ;MORE DATA WORDS  
BEQ 14$ ;NO-BRANCH  
TYPE ,SPACE2 ;TYPE SEPARATORS  
BR 11$ ;LOOP  
14$: DEC R2 ;MORE DH'S?  
BLE 20$ ;NO-BRANCH  
TYPE ,SCLF  
TST 2(R0) ;ONLY A DH IN THIS REQUEST?  
BEQ 15$ ;YES-BRANCH BYPASS INDENT  
COM R4 ;INDENT?  
BNE 15$ ;NO-BRANCH  
TYPE ,SPACE2 ;YES-TYPE SPACES  
15$: MOV (R0)+,16$ ;GET NEXT DH POINTER  
TYPE ;TYPE DH
```

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SEQ 0236

12202	067772	000000			16\$:	.WORD	0		:DH POINTER GOES HERE
12203	067774	105710				TSTB	(R0)		:TYPE A DT?
12204	067776	001003				BNE	21\$:YES-BRANCH
12205	070000	062700	000002			ADD	#2,R0		:INCREMENT DF POINTER
12206	070004	000754				BR	14\$:SEE IF END OF DF BLOCK
12207	070006	104401	001205		21\$:	TYPE	,\$CRLF		
12208	070012	005704				TST	R4		:INDENT?
12209	070014	001335				BNE	10\$:NO-BRANCH
12210	070016	104401	070060		17\$:	TYPE	,SPACE2		:YES-TYPE SPACES
12211	070022	000732				BR	10\$:LOOP
12212	070024	104414			20\$:	RESREG			
12213									
12214	070026	032777	010000	111104		BIT	#SW12,\$SWR		:ABORT DRV AFTER 20 ERRS?
12215	070034	001410				BEQ	25\$:BR IF NO
12216	070036	023727	001103	000024		CMP	\$ERFLG,#20.		:ELSE SEE IF 20 ERRS
12217	070044	001004				BNE	25\$:BR IF NO
12218	070046	012706	001100			MOV	#STACK,SP		:ELSE RESTORE STK
12219	070052	000137	042706			JMP	\$EOP		:AND DROP DRIVE
12220	070056	000207			25\$:	RTS	PC		
12221	070060	020040	000		SPACE2:	.ASCIZ/	/		:2 SPACES


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12222      ; ODT-11 -- V005A
12223
12224      ; DEC-11-UODPA-A-LA
12225
12226      ; COPYRIGHT 1969,1970,1972
12227      ; DIGITAL EQUIPMENT CORPORATION
12228      ; MAYNARD, MASSACHUSETTS 01754
12229      ; .ENABL ABS,AMA
12230      ; .EVEN
12231      ; .+.60
12232      R0      =      %0      ; REGISTER
12233      R1      =      %1      ; NAMING
12234      R2      =      %2      ; CONVENTIONS
12235      R3      =      %3
12236      R4      =      %4
12237      R5      =      %5
12238      SP     =      %6
12239      PC     =      %7
12240      ST     =      177776   ;STATUS REGISTER
12241      ;
12242      O.TVEC =      14      ;TRT VECTOR LOCATION
12243      O.STM  =      340     ;PRIORITY MASK - STATUS REGISTER
12244      O.TBT  =      20      ;T-BIT MASK - STATUS REGISTER
12245      TRT   =      000003   ;TRT INSTRUCTION
12246      RTT   =      000006   ;RTT INSTRUCTION
12247      ;
12248      ; R5 IS USUALLY CONSIDERED SAFE, THE CURRENT ADDRESS WORD
12249      ; RESIDES IN IT. AFTER A BREAKPOINT, IT IS SET TO ZERO, AND SEARCH
12250      ; OPERATIONS LEAVE IT RANDOMLY FILLED. OTHERWISE, IT SHOULD NOT
12251      ; BE USED EXCEPT FOR JSR'S AND THE CURRENT ADDRESS POINTER (CAD).
12252      ;
12253      O.RDB  =      177562   ;R DATA BUFFER
12254      O.RCSR =      177560   ;R C/SR
12255      O.TDB  =      177566   ;T DATA BUFFER
12256      O.TCSR =      177564   ;T C/SR
12257      ;
12258      ;
12259      ; INITIALIZE ODT
12260      ; USE O.ODT FOR A NORMAL ENTRY
12261      ; USE O.ODT+2 TO RESTART ODT - WIPING OUT ALL BREAKPOINTS
12262      ; USE O.ODT+4 TO RE-ENTER (I.E. - FAKE A BREAKPOINT)
12263      ;
12264      O.ODT: BR      O.STRT   ;NORMAL ENTRY
12265      BR      O.RST      ;RESTART
12266      O.ENTR: MOV    ST,O.UST ;RE-ENTER -- SAVE STATUS
12267      MOV    O.TVEC+2,ST ;SET UP LOCAL STATUS
12268      MOV    PC,O.UPC    ;FAKE THE PC
12269      JMP    O.BK1
12270      ;
12271      O.STRT: MOV    #O.URO,SP ;SET UP STACK
12272      MOV    SP,O.USP    ;FAKE THE SAVED STACK
12273      BR      O.RST1    ;CLEAR BREAKPOINT TABLES
12274      O.RST: JSR    O.O.SVR ;SAVE REGISTERS
12275      MOV    O.UIN,@O.ADR1 ;REMOVE THE BREAKPOINT
12276      MOV    O.PRI,R4   ;GET ODT PRIORITY
12277      RORB   R4         ;SHIFT

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12278 070226 106004          RORB R4          ; INTO
12279 070230 106004          RORB R4          ; POSITION
12280 070232 110437 177776    MOVB R4,ST       ;STORE IN STATUS
12281 070236 000127          O.RST1: JMP      (PC)+
12282 070240 000403          BR      0.45
12283 070242 012737 000002 071232  MOV      #RTI,O.RTIT ;SET TO RTI IF 11/20 OR /05
12284 070250 105037 072151 0.45:  CLRB   O.P       ;DISALLOW PROCEED
12285 070254 012737 000340 000016  MOV      #O.STM,O.TVEC+2 ;STATUS WORD TO TRT VECTOR + 2
12286 070262 012737 071312 00C014  MOV      #O.BRK,O.TVEC  ;PC TO TRT VECTOR
12287 070270 000447          BR      O.RALL    ;CLEAR BREAKPOINT TABLES
12288
12289          ; SPECIAL NAME HANDLER
12290          ; DEPENDS UPON THE EXPLICIT ORDER OF THE TWO TABLES O.TL AND O.URO
12291
12292 070272 004537 071752  O.REGT: JSR      5,O.GET ;SPECIAL NAME, GET ONE MORE CHARACTER
12293 070276 012704 072175          MOV      #O.TL,R4     ;TABLE START ADDRESS
12294 070302 120024          O.RSP:  CMPB   R0,(R4)+ ;IS THIS THE CORRECT CHARACTER?
12295 070304 001413          BEQ      O.SP        ;JUMP IF YES
12296 070306 022704 072203          CMP      #O.TL+O.LG,R4 ;IS THE SEARCH DONE?
12297 070312 101373          BHI      O.RSP       ;BRANCH IF NOT
12298 070314 042700 177770          BIC      #177770,R0   ;MASK OFF OCTAL
12299 070320 010004          MOV      R0,R4
12300 070322 006304          O.SP1:  ASL      R4
12301 070324 062704 070104          ADD      #O.URO,R4    ;GENERATE ADDRESS
12302 070330 005202          INC      R2           ;SET FOUND FLAG
12303 070332 000444          BR      O.SCAN      ;GO FIND NEXT CHARACTER
12304 070334 162704 072166  O.SP:   SUB      #O.TL-7,R4 ;CORRECT CONSTANT
12305 070340 000770          BR      O.SP1
12306
12307          ;
12308          ; _ HANDLER - OPEN INDEXED ON THE PC
12309 070342 004737 072076  O.ORPC: JSR      PC,O.TCLS
12310 070346 010502          MOV      R5,R2       ;CURRENT ADDRESS IN R2
12311 070350 061202          ADD      @R2,R2      ;COMPUTE
12312 070352 006202          ASR      R2          ;MOVE ONE BIT TO CARRY
12313 070354 103421          BCS     O.ERR        ;ERR IF ODD NUMBER
12314 070356 006302          ASL      R2          ;RESTORE WORD
12315 070360 005722          TST     (R2)+        ; AND INCREMENT BY TWO
12316 070362 010205          MOV      R2,R5       ;UPDATE CAD
12317 070364 000137 070636          JMP     O.OP2        ;GO FINISH UP
12318
12319          ;
12320          ; B HANDLER - SET AND REMOVE BREAKPOINTS
12321 070370 005702          O.BKPT: TST     R2     ;IF NO NUMBER TYPED
12322 070372 001406          BEQ     O.RALL      ; REMOVE BREAKPOINT
12323 070374 006204          ASR     R4          ;CHECK IF ODD
12324 070376 103410          BCS     O.ERR        ;JUMP IF ODD
12325 070400 006304          ASL     R4          ;RESTORE ONE BIT
12326 070402 010437 070136          MOV     R4,O.ADR1   ;SET A BREAKPOINT
12327 070406 000412          BR     O.DCD
12328 070410 012737 072212 070136  O.RALL: MOV     #O.TRTC,O.ADR1 ;CLEAR BREAKPOINT
12329 070416 000406          BR     O.DCD
12330
12331          ;
12332          ; CMD DECODER - ODT11
12333          ;
12333          ; REGISTERS R0-R4 MAY BE USED,
```



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12334 ; REGISTER R5 WILL BE CONSIDERED SAFE
12335 ;
12336 070420 052705 000001 0.ERR: BIS #1,R5 ;CLOSE EVERYTHING
12337 070424 012700 000077 MOV #?,R0 ; ? TO BE TYPED
12338 070430 004537 072030 JSR 5,O.FTYP ; OUTPUT ?
12339 070434 004537 072130 O.DCD: JSR 5,O.CRLS ;TYPE <CR><LF>*
12340 070440 005004 O.DCD1: CLR R4 ; R4 CONTAINS THE CONVERTED OCTAL
12341 070442 005002 CLR R2 ; R2 IS THE NUMBER FOUND FLAG
12342 070444 004537 071752 O.SCAN: JSR 5,O.GET ;GET A CHAR, RETURN IN R0
12343 070450 022700 000060 CMP #0,R0 ;COMPARE WITH ASCII 0
12344 070454 101013 BHI 0,CLGL ;CHECK LEGALITY IF NON-NUMERIC
12345 070456 022700 000067 CMP #7,R0 ;COMPARE WITH ASCII 7
12346 070462 103410 BLO 0,CLGL ;CHECK LEGALITY IF NOT OCTAL
12347 070464 042700 177770 BIC #177770,R0 ;CONVERT TO BCD
12348 070470 006304 ASL R4 ; MAKE ROOM
12349 070472 006304 ASL R4 ; IN
12350 070474 006304 ASL R4 ; R4
12351 070476 060004 ADD R0,R4 ;PACK THREE BITS IN R4
12352 070500 005202 INC R2 ;R2 HAS NUMERIC FLAG
12353 070502 000760 BR 0,SCAN ; AND TRY AGAIN
12354 070504 005001 O.CLGL: CLR R1 ;CLEAR INDEX
12355 070506 120061 072161 O.LGL1: CMPB R0,O.LGCH(R1) ;DO THE CODES MATCH?
12356 070512 001405 BEQ 0,LGL2 ;JUMP IF YES
12357 070514 005201 INC R1 ; SET INDEX FOR NEXT SEARCH
12358 070516 020127 000014 CMP R1,#O.CLGT ;IS THE SEARCH DONE?
12359 070522 103336 BHS 0,ERR ; OOPS!
12360 070524 000770 BR 0,LGL1 ;RE-LOOP
12361 070526 006301 O.LGL2: ASL R1 ;MULTIPLY BY TWO
12362 070530 000171 070534 JMP @O.LGDR(R1) ;GO TO PROPER ROUTINE
12363 ;
12364 070534 070564 O.LGDR: O.WRD ; / OPEN WORD
12365 070536 070616 O.CRET ; CARRIAGE RETURN CLOSE
12366 070540 070272 O.REGT ; $ REGISTER OPS
12367 070542 071126 O.GO ; G GO TO ADDRESS K
12368 070544 070630 O.OP1 ; <LF> MODIFY, CLOSE, OPEN NEXT
12369 070546 070342 O.ORPC ; * OPEN RELATED, INDEX - PC
12370 070550 070662 O.BACK ; * OPEN PREVIOUS
12371 070552 070672 O.OFST ; O OFFSET
12372 070554 070750 O.WSCH ; W SEARCH WORD
12373 070556 070744 O.EFF ; E SEARCH EFFECTIVE ADDRESS
12374 070560 070370 O.BKPT ; B BREAKPOINTS
12375 070562 071234 O.PROC ; P PROCEED
12376 000030 O.LGL = -O.LGDR ;LGL MUST EQUAL 2X CHLGT ALWAYS
12377 ;
12378 ; PROCESS / - OPEN WORD
12379 ;
12380 070564 005702 O.WRD: TST R2 ;GET VALUE IF R2 IS NON-ZERO
12381 070566 001410 BEQ 0,WRDA ;SKIP OTHERWISE
12382 070570 010405 MOV R4,R5 ; PUT VALUE IN CAD
12383 070572 006205 O.WRD1: ASR R5 ;MOVE ONE BIT TO CARRY
12384 070574 103711 O.ERR2: BCS 0,ERR ;JUMP IF ODD ADDRESS
12385 070576 006305 ASL R5 ;RESTORE THE CARRY BIT
12386 070600 011500 MOV @R5,R0 ;GET CONTENTS OF WORD
12387 070602 004537 071666 JSR 5,O.CADV ;GO GET AND TYPE OUT @CAD
12388 070606 000714 BR 0,DCD1 ;GO BACK TO DECODER
12389 070610 042705 000001 O.WRDA: BIC #1,R5 ;CLEAR CLOSED BIT
```

```
12390 070614 000766          BR      0.WRD1          ;GO BACK TO MAIN-LINE
12391
12392          ; PROCESS CARRIAGE RETURN
12393
12394 070616 004737 072076  O.CRET: JSR      PC,0.TCLS          ;CLOSE LOCATION
12395 070622 052705 000001          BIS      #1,R5          ;CLOSE EVERYTHING
12396 070626 000702          BR      0.DCD          ;RETURN TO DECODER
12397
12398          ; PROCESS <LF>, OPEN NEXT WORD
12399
12400 070630 004737 072076  O.OP1: JSR      PC,0.TCLS          ;CLOSE PRESENT CELL
12401 070634 005725          TST      (R5)+          ;GENERATE NEW ADDRESS
12402 070636 004537 072122  O.OP2: JSR      5,0.CRLF          ;<CR><LF>
12403 070642 010500          MOV      R5,R0          ;NUMBER TO TYPE
12404 070644 004537 071666          JSR      5,0.CADV          ; TYPE OUT ADDRESS
12405 070650 012700 000057          MOV      #1,R0          ;TYPE A /
12406 070654 004537 072030          JSR      5,0.FTYP          ;
12407 070660 000744          BR      0.WRD1          ;GO PROCESS IT
12408
12409          ; PROCESS ^, OPEN PREVIOUS WORD
12410
12411 070662 004737 072076  O.BACK: JSR      PC,0.TCLS          ;GENERATE NEW ADDRESS
12412 070666 005745          TST      -(R5)          ;GO DO THE REST
12413 070670 000762          BR
12414
12415          ; PROCESS 0, COMPUTE OFFSET
12416
12417 070672 006205  O.OFST: ASR      R5          ;GET LOW ORDER BIT
12418 070674 103737          BCS      0.ERR2          ;ERR IF CLOSED
12419 070676 006305          ASL      R5          ;RESTORE WORD
12420 070700 012700 000040          MOV      #1,R0          ;TYPE ONE BLANK
12421 070704 004537 072030          JSR      5,0.FTYP          ; AS A SEPARATOR
12422 070710 160504          SUB      R5,R4          ;COMPUTE
12423 070712 005304          DEC      R4
12424 070714 005304          DEC      R4          ; 16 BIT OFFSET
12425 070716 010400          MOV      R4,R0          ;TYPE A
12426 070720 010402          MOV      R4,R2          ;SAVE R4
12427 070722 004537 071666          JSR      5,0.CADV          ;NUMBER IN R0 - WORD MODE
12428 070726 010200          MOV      R2,R0
12429 070730 006200          ASR      R0          ;DIVIDE BY TWO
12430 070732 103402          BCS      0.OF1          ;BRANCH IF ODD
12431 070734 004537 071666          JSR      5,0.CADV          ;NUMBER IN R0 - BYTE MODE
12432 070740 000137 070440  O.OF1: JMP      0.DCD1          ;ALL DONE
12433
12434          ; SEARCHES - $MSK HAS THE MASK
12435          ; $MSK+2 HAS THE FWA
12436          ; $MSK+4 HAS THE LWA
12437
12438 070744 005201  O.EFF: INC      R1          ;SET EFFECTIVE SEARCH
12439 070746 000401          BR      0.WDS
12440 070750 005001  O.WSCH: CLR      R1          ;SET WORD SEARCH
12441 070752 005702  O.WDS: TST      R2          ;CHECK FOR OBJECT FOUND
12442 070754 001621  O.ERR1: BEQ      0.ERR          ;ERR IF NO OBJECT
12443 070756 013702 070132          MOV      0.MSK+2,R2          ;SET ORIGIN
12444 070762 013705 070130          MOV      0.MSK,R5          ;SET MASK
12445 070766 005105          COM      R5          ;AND COMPLEMENT IT
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12446 070770 020237 070134      0.WDS2:  CMP      R2,0.MSK+4      ; IS THE SEARCH ALL DONE?
12447 070774 101217                BHI      0.DCD                ; YES
12448 070776 011200                MOV      @R2,R0              ; GET OBJECT
12449 071000 005701                TST      R1                  ; NO
12450 071002 001027                BNE      0.EFF1              ; BRANCH IF EFFECTIVE SEARCH
12451 071004 010046                MOV      R0,-(SP)            ; EXCLUSIVE OR
12452 071006 010403                MOV      R4,R3              ; IS DONE
12453 071010 040400                BIC      R4,R0
12454 071012 042603                BIC      (SP)+,R3           ; IN A VERY
12455 071014 050003                BIS      R0,R3              ; FANCY MANNER HERE
12456 071016 040503                BIC      R5,R3              ; AND RESULT WITH MASK
12457 071020 001016                0.WDS3: BNE      0.WDS4              ; RE-LOOP IF NO MATCH
12458 071022 010446                MOV      R4,-(SP)            ; REGISTERS R2,R4, AND R5 ARE SAFE
12459 071024 004537 072122        JSR      5,0.CRLF            ; TYPE <CR,LF>
12460 071030 010200                MOV      R2,R0              ; GET READY TO TYPE
12461 071032 004537 071666        JSR      5,0.CADV            ; TYPE ADDRESS
12462 071036 012700 000057        MOV      #1,R0              ; SLASH TO R0
12463 071042 004537 072030        JSR      5,0.FTYP            ; TYPE IT
12464 071046 011200                MOV      @R2,R0              ; GET CONTENTS
12465 071050 004537 071666        JSR      5,0.CADV            ; TYPE CONTENTS
12466 071054 012604                MOV      (SP)+,R4           ; RESTORE R4
12467 071056 005722                0.WDS4: TST      (R2)+        ; INCREMENT TO NEXT CELL AND
12468 071060 000743                BR       0.WDS2              ; RETURN
12469 071062 020004                0.EFF1: CMP      R0,R4        ; IS (X)=K?
12470 071064 001755                BEQ      0.WDS3              ; TYPE IF EQUAL
12471 071066 010003                MOV      R0,R3              ; (X) TO R3
12472 071070 060203                ADD      R2,R3              ; (X)+X
12473 071072 005203                INC      R3
12474 071074 005203                INC      R3                  ; (X)+X+2
12475 071076 020304                CMP      R3,R4              ; IS (X)+X+2=K?
12476 071100 001747                BEQ      0.WDS3              ; BRANCH IF EQUAL
12477 071102 042700 177400        BIC      #177400,R0          ; WIPE OUT EXTRANEIOUS BITS
12478 071106 110000                MOV      R0,R0              ; EXTEND SIGN
12479 071110 000257                CCC
12480 071112 006300                ASL      R0                  ; MULTIPLY BY TWO
12481 071114 005200                INC      R0                  ; ADD TWO
12482 071116 005200                INC      R0
12483 071120 060200                ADD      R2,R0              ; ADD PC
12484 071122 020004                CMP      R0,R4              ; IS THE RESULT A PROPER REL. BRANCH?
12485 071124 000735                BR       0.WDS3
12486
12487      ; PROCESS G - GO
12488
12489 071126 105037 072151      0.GO:  CLR      0.P            ; DISALLOW PROCEED
12490 071132 006204                ASR      R4                  ; CHECK LOW ORDER BIT
12491 071134 103617                BCS      0.ERR?             ; ERR IF ODD NUMBER
12492 071136 006304                ASL      R4                  ; RESTORE WORD
12493 071140 010437 070122        MOV      R4,0.UPC           ; SET UP NEW PC
12494 071144 112737 000340 177776  MOV      #0,STM,ST         ; SET HIGH PRIORITY
12495 071152 004537 071620        JSR      5,0.RSTT          ; RESTORE TELETYPE
12496 071156 105037 072150        0.TBIT: CLR      0.T            ; CLEAR BOTH
12497 071162 042737 000020 070124  BIC      #0.TBT,0.UST       ; T-BIT FLAGS
12498 071170 017737 176742 070142  MOV      @0.ADR1,0.UIN      ; SAVE INSTRUCTION
12499 071176 013777 072212 176732  MOV      0.TRTC,@0.ADR1    ; REPLACE WITH TRAP
12500 071204 012600                0.G02: MOV      (SP)+,R0      ; RESTORE
12501 071206 012601                MOV      (SP)+,R1          ; R0
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12502 071210 012602          MOV      (SP)+,R2      ; THRU
12503 071212 012603          MOV      (SP)+,R3
12504 071214 012604          MOV      (SP)+,R4      ;
12505 071216 012605          MOV      (SP)+,R5      ; R5
12506 071220 012606          MOV      (SP)+,SP      ; AND SP
12507 071222 013746 070124    MOV      O.UST,-(SP)   ; AND STATUS
12508 071226 013746 070122    MOV      O.UPC,-(SP)  ; AND PC
12509 071232 000006          O.RTIT: RTT          ;CHANGED TO RTI FOR 11/20 AND /05
12510
12511          ; PROCESS P - PROCEED
12512          ; ONLY ALLOWED AFTER A BREAKPOINT
12513
12514 071234 105737 072151    O.PROC: TSTB  O.P      ;CHECK LEGALITY OF PROCEED
12515 071240 001645          BEQ      O.ERR1      ;NOT LEGAL
12516 071242 105037 072151    CLRB     O.P          ;CLEAR PROCEED FLAG
12517 071246 005702          TST     R2           ;WAS COUNT SPECIFIED?
12518 071250 001402          BEQ     O.PR1        ;NO
12519 071252 010437 070140    MOV     R4,O.CT      ;YES, PUT AWAY COUNT
12520 071256 112737 000340 177776 O.PR1: MOVB  #O.STM,ST  ;FORCE HIGH PRIORITY
12521 071264 004537 071620    JSR     5,O.RSTT     ;RESTORE TTY
12522 071270 112737 000340 177776 O.C1:  MOVB  #O.STM,ST  ;SET HIGH PRIORITY
12523 071276 105237 072150    INCB   O.T           ;SET T-BIT FLAG
12524 071302 052737 000020 070124  BIS    #O.TBT,O.UST ;SET T-BIT
12525 071310 000735          BR      O.G02
12526
12527          ; BREAKPOINT HANDLER
12528          ; A TRT BREAKPOINT CAUSES O.BRK TO BE ENTERED, WHICH SAVES
12529          ; VARIOUS ODDS AND ENDS, FINDS OUT IF THE BREAKPOINT WAS LEGAL,
12530          ; AND GIVES CONTROL TO THE CMD DECODER
12531
12532 071312 012637 070122    O.BRK:  MOV    (SP)+,O.UPC ;PRIORITY IS 7 UPON ENTRY
12533 071316 012637 070124    MOV    (SP)+,O.UST      ;SAVE STATUS AND PC
12534 071322 004037 071530    O.BK1:  JSR    O,O.SVR    ;SAVE VARIOUS REGISTERS
12535 071326 105737 072150    TSTB   O.T             ;CHECK FOR T-BIT SET
12536 071332 001311          BNE    O.TBIT         ;JUMP IF SET
12537 071334 013777 070142 176574  MOV    O.UIN,@O.ADR1   ;REMOVE BREAKPOINTS
12538 071342 105737 070126    TSTB   O.PRI          ;CHECK IF PRIORITY
12539 071346 100003          BPL    O.BK2          ; IS AS SAME AS USER PGM
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12543 071362 000257          CCC          ;CLEAR CARRY
12544 071364 106005          RORB R5     ;SHIFT LOW ORDER BITS
12545 071366 106005          RORB R5     ; INTO
12546 071370 106005          RORB R5     ; HIGH ORDER
12547 071372 106005          RORB R5     ; POSITION
12548 071374 110537 177776    0.BK3: MOV B R5,ST ;PUT THE STATUS AWAY WHERE IT BELONGS
12549 071400 013705 070122    MOV O.UPC,R5 ;GET PC, IT POINTS TO THE TRT
12550 071404 005745          TST -(R5)   ;SUBTRACT TWO
12551 071406 010537 070122    MOV R5,O.UPC ;FROM THE USER'S PC
12552 071412 020537 070136    CMP R5,O.ADR1 ;COMPARE WITH LIST
12553 071416 001417          BEQ O.B2   ;JUMP IF FOUND
12554 071420 004537 071566    JSR 5,O.SVTT ;SAVE TELETYPE STATUS
12555 071424 004537 072122    JSR 5,O.CRLF
12556 071430 012704 072154    MOV #O.BD,R4 ;ERR, NOTHING FOUND
12557 071434 012703 072155    MOV #O.BD+1,R3
12558 071440 004537 072014    JSR 5,O.TYPE ;OUTPUT 'BE' FOR BAD ENTRY
12559 071444 010500          MOV R5,R0
12560 071446 042737 000020 070124 BIC #O.TBT,O.UST ;CLEAR OUT ANY POSSIBLE FAKE T-BIT
12561 071454 000420          BR O.B3    ; AND CONTINUE
12562 071456 005337 070140    0.B2: DEC O.CT
12563 071462 003302          BGT O.C1   ;JUMP IF REPEAT
12564 071464 012737 000001 070140 MOV #1,O.CT ;RESET COUNT TO 1
12565 071472 105237 072151    INCB O.P   ;ALLOW PROCEED
12566 071476 004537 071566    JSR 5,O.SVTT ;SAVE TELETYPE STATUS, R4 IS SAFE
12567 071502 012700 000102    MOV #'B',R0
12568 071506 004537 072030    JSR 5,O.FTYP ;TYPE 'B'
12569 071512 013700 070136    MOV O.ADR1,R0 ;GET ADDRESS OF BREAK
12570 071516 004537 071666    0.B3: JSR 5,O.CADV ;TYPE ADDRESS
12571 071522 005005          CLR R5     ;CLEAR CAD
12572 071524 000137 070434    JMP O.DCD  ;GO TO DECODER
12573          ;
12574          ; SAVE REGISTERS R0-R6 IN INTERNAL STACK
12575          ;
12576 071530 012637 072146    0.SVR: MOV (SP)+,O.XXX ;PICK REGISTER FROM STACK AND SAVE
12577 071534 010637 070120    MOV SP,O.USP ;SAVE USER STACK ADDRESS
12578 071540 012706 070120    MOV #O.USP,SP ;SET TO INTERNAL STACK
12579 071544 010546          MOV R5,-(SP) ;SAVE
12580 071546 010446          MOV R4,-(SP) ; REGISTERS
12581 071550 010346          MOV R3,-(SP) ;1
12582 071552 010246          MOV R2,-(SP) ; THRU
12583 071554 010146          MOV R1,-(SP) ; 5
12584 071556 013746 072146    MOV O.XXX,-(SP) ;PUT SAVED REGISTER ON STACK
12585 071562 005746          TST -(SP)
12586 071564 000200          RTS R0
12587          ;
12588          ; SAVE TELETYPE STATUS
12589          ;
12590 071566 113737 177560 072152 0.SVTT: MOV B O.RCSR,O.CSR1 ;SAVE R C/SR
12591 071574 113737 177564 072153    MOV B O.TCSR,O.CSR2 ;SAVE T C/SR
12592 071602 105037 177560    CLRB O.RCSR ;CLEAR ENABLE AND MAINTENANCE
12593 071606 105037 177564    CLRB O.TCSR ; BITS IN BOTH C/SR
12594 071612 004537 072122    JSR 5,O.CRLF ;TYPE <CR,LF>
12595 071616 000205          RTS R5
12596          ;
12597          ; RESTORE TELETYPE STATUS
12598          ;
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12599 071620 004537 072122 O.RSTT: JSR 5,0.CRLF ;<CR,LF> BEFORE RESTORING
12600 071624 105737 177564 TSTB 0.TCSR ;WAIT READY ON PRINTER
12601 071630 100375 BPL -4
12602 071632 032737 004000 177560 BIT #4000,0.RCSR ;CHECK BUSY FLAG ON READER
12603 071640 001403 BEQ 0.RSE1 ;SKIP READY LOOP IF NOT BUSY
12604 071642 105737 177560 TSTB 0.RCSR ;WAIT READY
12605 071646 100375 BPL -4 ; ON READER
12606 071650 113737 072152 177560 O.RSE1: MOV 0.CSR1,0.RCSR ;RESTORE
12607 071656 113737 072153 177564 MOV 0.CSR2,0.TCSR ; THE STATUS REGISTERS
12608 071664 000205 RTS R5
12609
12610 ; TYPE OUT CONTENTS OF WORD OR BYTE WITH ONE TRAILING SPACE
12611 ; WORD IS IN R0
12612
12613 071666 010246 O.CADV: MOV R2,-(SP) ;SAVE R2
12614 071670 012704 072211 MOV #0.BUF+6,R4 ;BUFFER START ADDRESS
12615 071674 012746 000060 MOV #'0,-(SP) ;CONSTANT ASCII 0
12616 071700 010002 O.SPC: MOV R0,R2 ; GET
12617 071702 042702 177770 BIC #177770,R2 ; OCTAL CHARACTER
12618 071706 061602 ADD @SP,R2 ;CONVERT TO ASCII
12619 071710 110244 MOV R2,-(R4) ;STORE IN BUFFER
12620 071712 006200 ASR R0 ;SHIFT THIS MESS
12621 071714 006200 ASR R0 ; RIGHT
12622 071716 006200 ASR R0 ; THREE WHOLE PLACES
12623 071720 020427 072204 CMP R4,#0.BUF+1 ;DONE?
12624 071724 101365 BHI 0.SPC ; NO
12625 071726 042700 177776 BIC #177776,R0 ;GET LAST BIT
12626 071732 062600 ADD (SP)+,R0 ;CONVERT TO ASCII
12627 071734 110044 MOV R0,-(R4) ;AND PUT IT AWAY
12628 071736 012703 072211 MOV #0.BUF+6,R3 ;LWA
12629 071742 004537 072014 JSR 5,0.TYPE ;TYPE WHOLE STRING OF CHARACTERS
12630 071746 012602 MOV (SP)+,R2 ;RESTORE R2
12631 071750 000205 RTS R5
12632
12633 ; GENERAL CHARACTER INPUT ROUTINE
12634 ; CHARACTER INPUT GOES TO R0
12635
12636 071752 105737 177560 O.GET: TSTB 0.RCSR ;WAIT FOR
12637 071756 100375 BPL -4 ; INPUT FROM KEYBOARD
12638 071760 113700 177562 MOV 0.RDB,R0 ;GET A CHARACTER
12639 071764 004537 072030 JSR 5,0.FIYP ;ECHO CHARACTER
12640 071770 042700 177600 BIC #177600,R0 ;STRIP OFF PARITY FROM CHARACTER
12641 071774 001766 BEQ 0.GET ;IGNORE NULLS
12642 071776 122700 000040 CMPB #40,R0 ;CHECK FOR SPACES
12643 072002 001763 BEQ 0.GET ;IGNORE NULLS
12644 072004 122700 000073 CMPB #' ;,R0 ;CHECK FOR SEMI-COLON
12645 072010 001760 BEQ 0.GET ;IGNORE THEM IF FOUND
12646 072012 000205 RTS R5
12647
12648 ; GENERAL CHARACTER OUTPUT ROUTINE
12649 ; ADDRESS OF FIRST BYTE IN R4,
12650 ; ADDRESS OF LAST BYTE IN R3, (R3)>(R4)
12651
12652 072014 020304 O.TYPE: CMP R3,R4 ;CHECK FOR COMPLETION
12653 072016 103426 BLO 0.TYP1 ; EXIT WHEN DONE
12654 072020 112400 MOV (R4)+,R0 ;GET A CHARACTER
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12655 072022 004537 072030      JSR    5,O.FTYP      ;TYPE ONE CHARACTER
12656 072026 000772              BR      0.TYPE       ;LOOP UNTIL DONE
12657
12658      ; TYPE ONLY ONE CHARACTER (CONTAINED IN R0)
12659
12660 072030 105737 177564      O.FTYP: TSTB    0.TCSR      ;CHECK STATUS
12661 072034 100375              BPL    -4            ;WAIT UNTIL READY
12662 072036 110037 177566      MOVB   R0,0.TDB     ;TYPE ONE CHARACTER
12663 072042 120037 000045      CMPB   R0,@#45     ;IS CHAR TO BE FILLED?
12664 072046 001012              BNE    0.TYP1       ;NO
12665 072050 113746 000044      MOVB   @#44,-(SP)   ;YES, INIT THE COUNT
12666 072054 105737 177564      O.TYP2: TSTB    0.TCSR      ;CHECK STATUS
12667 072060 100375              BPL    0.TYP2       ;WAIT UNTIL READY
12668 072062 105037 177566      CLRB   0.TDB        ;GENERATE NULL FILLER
12669 072066 105316              DECB   @SP
12670 072070 003371              BGT    0.TYP2       ;GENERATE NULL FILLER
12671 072072 005726              TST   (SP)+         ;POP STACK
12672 072074 000205      O.TYP1: RTS      R5
12673
12674      ; CLOSE WORD OR BYTE AND EXIT,
12675      ; UPON ENTERING, R2 HAS NUMERIC FLAG, R4 HAS CONTENTS
12676
12677 072076 006205      O.TCLS: ASR     R5        ;GET LOW ORDER BIT
12678 072100 103405              BCS    0.TC          ;JUMP IF ALREADY CLOSED
12679 072102 006305              ASL    R5
12680 072104 005702              TST   R2
12681 072106 001401              BEQ    0.CLS1        ;IF NO NUMBER WAS TYPED THERE IS
12682 072110 010415              MOV    R4,@R5       ;NO CHANGE TO THE OPEN CELL
12683 072112 000207      O.CLS1: RTS      PC        ;STORE WORD
12684 072114 005746      O.TC:   TST     -(SP)     ;POP EXTRA CELL FROM STACK
12685 072116 000137 070420      JMP    0.ERR        ;AND SCREAM BLOODY MURDER
12686
12687      ; O.CRLF - TYPE <CR,LF>
12688      ; O.CRLS - TYPE <CR,LF>*
12689
12690 072122 012703 072157      O.CRLF: MOV     #0.CR+1,R3 ;LWA <CR,LF>
12691 072126 000402              BR     0.CRS
12692 072130 012703 072160      O.CRLS: MOV     #0.CR+2,R3 ;LWA <CR,LF>*
12693 072134 012704 072156      O.CRS:  MOV     #0.CR,R4  ;FWA
12694 072140 004537 072014      JSR    5,O.TYPE     ;TYPE SOMETHING
12695 072144 000205      RTS     R5
12696
12697 072146 000000      O.XXX: .WORD    0        ;TEMPORARY STORAGE
12698 072150 000          O.T:   .BYTE    0        ; T-BIT FLAG
12699 072151 000          O.P:   .BYTE    0        ;PROCEED FLAG = 0 IF PROCEED NOT ALLOWED
12700                                ; = 1 IF PROCEED ALLOWED
12701 072152 000          O.CSR1: .BYTE   0        ;SAVE CELL - R C/SR
12702 072153 000          O.CSR2: .BYTE   0        ;SAVE CELL - T C/SR
12703
12704
12705 072154 042502      O.BD:  .EVEN    'BE
12706
12707 072156 015          O.CR:  .BYTE    015      ; <CR>
12708 072157 012          .BYTE    012      ; <LF>
12709 072160 052          .BYTE    '*        ; *
12710
```


BADTMO	050064	3919	9936#															
BAI	= 000020	1346#	5726	5877														
BA16	= 000400	1332#																
BA17	= 001000	1333#																
BIT0	= 000001	1275#	1329	1361	1380	2532	3958	9796										
BIT00	= 000001	1265#	1275															
BIT01	= 000002	1264#	1274															
BIT02	= 000004	1263#	1273															
BIT03	= 000010	1262#	1272															
BIT04	= 000020	1261#	1271															
BIT05	= 000040	1260#	1270															
BIT06	= 000100	1259#	1269															
BIT07	= 000200	1258#	1268															
BIT08	= 000400	1257#	1267	10043														
BIT09	= 001000	1256#	1266	10051	10119													
BIT1	= 000002	1274#	1362	2533														
BIT10	= 002000	1255#	1334	1352	1371	1403	1417	1431	1444	1458	5307	10096						
BIT11	= 004000	1254#	1335	1353	1372	1389	1404	1418	1432	1445	1459	5309	10058					
BIT12	= 010000	1253#	1336	1354	1373	1405	1419	1433	1446	1460								
BIT13	= 020000	1252#	1337	1355	1374	1390	1406	1420	1434	1447	1461	10103						
BIT14	= 040000	1251#	1338	1356	1375	1391	1407	1421	1435	1448	1462	1472	9633	9665				
		10029																
BIT15	= 100000	1250#	1339	1340	1357	1376	1392	1408	1475	4839	9633	9661						
BIT2	= 000004	1273#	1363	1382	2534													
BIT3	= 000010	1272#	1345	1364	1383													
BIT4	= 000020	1271#	1346	1365	1384	1397	1425	1452	6592									
BIT5	= 000040	1270#	1347	1366	1385	1398	1412	1426	1439	1453								
BIT6	= 000100	1269#	1330	1348	1367	1386	1399	1413	1427	1440	1454							
BIT7	= 000200	1268#	1331	1349	1368	1387	1400	1414	1428	1441	1455	3948						
BIT8	= 000400	1267#	1332	1350	1369	1388	1401	1415	1429	1442	1456							
BIT9	= 001000	1266#	1333	1351	1370	1402	1416	1430	1443	1457	5303							
BPTVEC	= 000014	1282#																
BSE	= 000200	1368#																
BSERR	001456	2446#	6869*	6875*	7144	8885*	8887*											
BSE22H	002304	2460#	6821	6872	6891	9659												
BSE22S	054474	6882	9663	10979#														
BYP	044274	4035	4063	4067	4082	4143	4170	4174	4178	4182	9197#							
BYPCER	001462	2449#	3946*	4194*	4918*	4953*	5521*	5602*	6917*	6998*	8813*	9314						
BYPFMT	001464	2450#	6007*	6097*	6324*	6414*	9631											
BYPT16	001340	2394#	3676*	3681*	3686*	3691*	3696*	3700*	4974									
BYT16	007014	1489	3680#															
BYT16A	007034	1493	3685#															
CALADD	001366	2407#	6013*	6330*	7156*	7186*	7208	7292	7310	7347	7354*	7365*	9627	9637				
		9639	9687															
CALDIF	001360	2404#	5075*	5090	5121*	5151	6123*	6439*	6635*	7190*	7752*	8088*	8438*	8792*				
		11901	11925															
CCLR	= 100000	1340#	4632	4657	4684	4697	4770	4849	4886	4997	5034	5082	5096	5128				
		5157	5208	5245	5348	5354	5370	5434	5543	5573	5639	6178	6444	6500				
		6696	6939	6969	7035	7267	7583	7801	7926	8137	8262	8487	8612	8834				
		8840	8856	9354	9362	9370	9378	9877	9892									
CCYL	001354	2402#	4987*	5069*	5070	5076	5115*	5116	5122	7878*	8214*	8564*						
CDT	= 002000	1334#	4051	4153	4233	4235	4514											
CERR	= 100000	1339#	4012	4127	4462	4537	4584	5409	5741	5795	5886	5930	6024	6060				
		6153	6223	6267	6341	6377	6470	6534	6576	6671	6754	6832	7080	7172				
		7242	7317	7380	7443	7456	7558	7628	7678	7776	7845	7901	7970	8018				
		8112	8181	8237	8306	8354	8416	8462	8531	8587	8656	8704	8774	9316				

		5885	5943	5974	6023	6072	6236	6340	6389	6446	6491	6547	6831	6970
		7029	7171	7316	7469	7550	7641	7858	7983	8194	8319	8544	8669	8851
		9151	9155	9175	9179	9340#	9410	9421	9475	9490	9547	9564	9581	9597
		9884												
GSTAT1	045166	9209	9352#											
GSTAT2	045416	9357	9365	9373	9380	9389#								
GTSWR =	104406	8952	10966#											
HASOF	003340	2492#	9109*	9125	11898	11906	11914	11922	11930	11938	11946	11957	11969	11982
HBA	003330	2488#	9105*	11898	11906	11914	11922	11930	11938	11946	11957	11969	11982	
HCS1	003322	2485#	4004*	4005*	4006	4012	4119*	4120*	4121	4127	4297*	4428*	4462	4537
		4562	4581	4584	4605*	4659*	4787*	4851*	4888*	4921*	4999*	5036*	5077*	5098*
		5123*	5159*	5210*	5247*	5286*	5350*	5356*	5402*	5409	5436*	5526*	5545*	5570*
		5641*	5677*	5737*	5741	5792*	5795	5882*	5886	5893*	5927*	5930	5949*	6020*
		6024	6057*	6060	6127*	6153	6180*	6220*	6223	6260*	6267	6337*	6341	6374*
		6377	6441*	6470	6502*	6531*	6534	6569*	6576	6647*	6671	6698*	6747*	6754
		6828*	6832	6922*	6941*	6966*	7037*	7073*	7080	7106*	7168*	7172	7213*	7242
		7269*	7313*	7317	7373*	7380	7436*	7443	7453*	7456	7532*	7558	7585*	7625*
		7628	7671*	7678	7756*	7776	7803*	7842*	7845	7880*	7901	7928*	7967*	7970
		8011*	8018	8092*	8112	8139*	8178*	8181	8216*	8237	8264*	8303*	8306	8347*
		8354	8409*	8416	8442*	8462	8489*	8528*	8531	8566*	8587	8614*	8653*	8656
		8697*	8704	8767*	8774	8794*	8836*	8842*	9044*	9045	9055*	9056	9102*	9316
		9320	9342*	9389*	9390*	9391	9411	9863*	9879*	9894*	11895	11903	11911	11919
HCS2	003324	11927	11935	11943	11954	11966	11979							
		2486#	4053	4055	4059	4071	4156	4158	4162	4165	4448	4465	9103*	9325
		9329	11895	11903	11911	11919	11927	11935	11943	11954	11966	11979		
HDA	003332	2489#	9106*	11898	11906	11914	11922	11930	11938	11946	11957	11969	11982	
HDB	003344	2494#												
HDC	003342	2493#	9110*	11898	11906	11914	11922	11930	11938	11946	11957	11969	11982	
HDS	003334	2490#	4057	4160	4488	4496	4523	4531	9107*	11895	11903	11911	11919	11927
		11935	11943	11954	11966	11979								
HDTAB	001470	2456#	5727	5735*	6009	6084	6326	6401	7162	7308	9615			
HDWD	001454	2444#	5849*	5850	5986*	5987	6086*	6088	6247*	6249	6403*	6405	11917	
HD1	001434	2434#	9617*	9618*	9619*	9620*	9621*	9622*	9629	9629				
HEAD	001430	2432#	5718*	5729*	5730	5731*	5733	5774*	5775	5778*	5787*	5788	5789*	5939*
		5840	5841*	5846	5857*	5858	6014*	6331*	7157*	7164*	7165	7166*	7179*	7180
		7187*	7209*	7210	7211*	7305*	7306	7307*	7344*	7345	7351	7353*	7428*	7429
		9617	9692											
HEADA	001432	2433#	4361	5271	5334	9534*	9535*	9536*	9537*					
HER	003336	2491#	4049	4151	4499	4534	9108*	11895	11903	11911	11919	11927	11935	11943
		11954	11966	11979										
HMR1	003346	2495#	9111*	11895	11903	11911	11919	11927	11935	11943	11954	11966	11979	
HMR2	003350	2496#	4014	4129	4332	4386	4419	4432	4485	4493	4520	4528	4610	4690
		4704	4731	4757	4773	4857	5005	5216	5578	5896	5952	6447	6974	8806
		8809	9089*	9094*	9112*	9359	9367	9375	9382	9476	9565	9582	9598	9885
HMR3	003352	11895	11903	11911	11919	11927	11935	11943	11954	11966	11979			
		2497#	4559	4575	4578	5366	8800	8803	8852	9090*	9095*	9113*	9360	9368
		9376	9383	9422	9491	9548	11895	11903	11911	11919	11927	11935	11943	11954
		11966	11979											
HOLD	043722	9074	9078	9102#	9353									
HPAT	003356	2499#	9115*	11898	11906	11914	11922	11930	11938	11946	11957	11969	11982	
HPEN	050044	7402	7480	9921#										
HPEND	003310	2467#	3846*	7357	7361*	9875*	9922							
HPOS	003354	2498#	9114*	11898	11906	11914	11922	11930	11938	11946	11957	11969	11982	
HT	= 000011	1188#	10169	10227										
HVRC	= 000400	1369#												
HWC	003326	2487#	9104*	11898	11906	11914	11922	11930	11938	11946	11957	11969	11982	

O.CRLF	072122	12402	12459	12555	12594	12599	12690#		
O.CRLS	072130	12339	12692#						
O.CRS	072134	12691	12693#						
O.CSR1	072152	12590*	12606	12701#					
O.CSR2	072153	12591*	12607	12702#					
O.CT	070140	12519*	12562*	12564*	12761#				
O.C1	071270	12522#	12563						
O.DCD	070434	12327	12329	12339#	12396	12447	12572		
O.DCD1	070440	12340#	12388	12432					
O.EFF	070744	12373	12438#						
O.EFF1	071062	12450	12469#						
O.ENTR	070150	12266#							
O.ERR	070420	12313	12324	12336#	12359	12384	12442	12685	
O.ERR1	070754	12442#	12515						
O.ERR2	070574	12384#	12418	12491					
O.FTYP	072030	12338	12406	12421	12463	12568	12639	12655	12660#
O.GET	071752	12292	12342	12636#	12641	12643	12645		
O.GO	071126	12367	12489#						
O.GO2	071204	12500#	12525						
O.LG =	000006	12296	12731#						
O.LGCH	072161	12355	12711#	12723					
O.LGDR	070534	12362	12364#	12376					
O.LGL =	000030	12376#							
O.LGL1	070506	12355#	12360						
O.LGL2	070526	12356	12361#						
O.MSK	070130	12443	12444	12446	12753#				
O.ODT	070144	1495	12264#	12742					
O.OFST	070672	12371	12417#						
O.OF1	070740	12430	12432#						
O.OP1	070630	12368	12400#						
O.OP2	070636	12317	12402#	12413					
O.ORPC	070342	12309#	12369						
O.P	072151	12284*	12489*	12514	2516*	12565*	12699#		
O.PRI	070126	12276	12538	12542	12752#				
O.PROC	071234	12375	12514#						
O.PR1	071256	12518	12520#						
O.RALL	070410	12287	12322	12328#					
O.RCSR=	177560	12254#	12590	12592*	12602	12604	12606*	12636	
O.RDB =	177562	12253#	12638						
O.REGT	070272	12292#	12366						
O.RSE1	071650	12603	12606#						
O.RSP	070302	12294#	12297						
O.RST	070206	12265	12274#						
O.RSTT	071620	12495	12521	12599#					
O.RST1	070236	12273	12281#						
O.RTIT	071232	12283*	12509#						
O.SCAN	070444	12303	12342#	12353					
O.SP	070334	12295	12304#						
O.SPC	071700	12616#	12624						
O.SP1	070322	12300#	12305						
O.STM =	000340	12243#	12285	12494	12520	12522			
O.STRT	070174	12264	12271#						
O.SVR	071530	12274	12534	12576#					
O.SVTT	071566	12554	12566	12590#					
O.T	072150	12496*	12523*	12535	12698#				
O.TBIT	071156	12496#	12536						

RLS = 000010	1345#	4459												
RSEC = 046232	9300	9524#												
RTT = 000006	12246#													
SAVREG= 104413	10753	10812	10972#	12156										
SBPAR 047362	9220	9224	9228	9232	9236	9240	9244	9784#						
SCLR = 000040	1347#	3985	3999	4114	9404									
SCOP1 = 104415	3996	4111	4422	4453	4504	4567	4842	4912	4990	5060	5107	5200	5276	
	5396	5720	5779	5919	6047	6210	6364	6638	7197	7297	7523	7613	7742	
	7831	7870	7956	8078	8167	8206	8292	8428	8517	8556	8642	8786	10974#	
SCOP1\$ 047434	9808#	10974												
SDC = ***** U	1175	1494	12222											
SEC 001374	2413#	4686*	4700*	4727*	4753*	9760*								
SECFLG 046766	9635	9656#												
SECNT 001400	2415#	5474*	5496*	5917*	6046*	6363*								
SECTOR 001406	2418#	5484	5493	9422*	9423*	9424*	9425*	9426*	9427*	9438	9441	9459	9462	
	9524*	9525*	9526*	9527*	9528*	9529*	9709*	9713	9714*	9715*	9726	9736*	9737	
	9738*	9739*												
SEEK = 000017	1322#	4921	5077	5123	5286	5402	5526	5570	6127	6260	6569	6647	6747	
	6922	6966	7073	7213	7373	7436	7532	7671	7756	7880	8011	8092	8216	
	8347	8409	8442	8566	8697	8767	8794							
SELDRV= 000001	1315#	4004	4119	4512	4514	5893	5949	9342	9389					
SETINT 043454	3841	9026	9035#											
SIZFLG 003472	2561#	3842*	3989	8981*	9001*									
SKI = 000002	1362#													
SORT 047114	6080	6397	9706#											
SPACE2 070060	12190	12199	12210	12221#										
SRTSPL= 000011	1319#	4605	5677	7106	9863									
SRTTAB 002100	2458#	6083	6400	9718										
ST = 177776	12240#	12266	12267*	12280*	12494*	12520*	12522*	12548*						
STACK = 001100	1183#	3703	3730	3894	3945	3997	4107	4112	4209	4290	4326	4376	4415	
	4423	4444	4454	4479	4505	4555	4568	4600	4836	4843	4913	4972	4991	
	5061	5108	5196	5201	5277	5387	5397	5468	5515	5710	5721	5780	5873	
	5920	6002	6048	6118	6211	6319	6365	6433	6629	6639	6813	6911	7135	
	7198	7298	7512	7524	7614	7736	7743	7832	7871	7957	8067	8079	8168	
	8207	8293	8401	8429	8518	8557	8643	8762	8787	8881	9306	9975	10006	
	12218													
START 007120	1487	3699#												
START1 007552	3794#													
STKLMT= 177774	1194#													
STOP 047536	7363	9846#	10475	10558										
ST2 007622	3802	3813#												
ST3 007666	3803	3831#												
ST4 007720	3815	3836	3839#											
ST5 007744	3822	3844#	3983	4047	8924	9914								
ST5XY 043102	8913	8918#												
SUBCLR 045462	4294	4303	4328	4380	4425	4446	4456	4481	4507	4557	4570	4602	4784	
	4793	4800	4845	4915	4993	5063	5110	5203	5279	5389	5399	5470	5517	
	5604	5674	5723	5782	5875	5922	6004	6050	6119	6213	6257	6271	6321	
	6367	6435	6525	6565	6580	6631	6641	6742	6758	6766	6815	6851	6913	
	7000	7068	7084	7092	7103	7149	7200	7300	7368	7384	7392	7447	7526	
	7616	7666	7682	7690	7745	7834	7873	7959	8006	8022	8030	8081	8170	
	8209	8295	8342	8358	8366	8404	8420	8431	8520	8559	8645	8692	8708	
	8716	8764	8778	8789	9404#	9848	9860							
SVAL = 100000	1392#													
SWR 001140	2297#	3728	3750*	3752	3758*	3765*	4813	4818	5687	6302	6307	6611	6616	
	6779	6785	7116	7417	7423	7489	7494	7703	7709	8041	8046	8051	8056	

	8377	8382	8387	8392	8727	8732	8737	8742	8950	9256	9266	9276	9286
	9808	9829	9834	9836	9938	9946	9951	9971	10029	10043	10045	10051	10058
	10096	10103	10115	10119	10478	10517	10572*	12214					
SWREG = 000176	1485#	3758	8950	10478	10517	10540							
SWTST = 047466	4778	5664	6253	6561	6733	7060	7405	7483	7658	8000	8336	8686	9827#
SW0 = 000001	1247#												
SW00 = 000001	1237#	1247											
SW01 = 000002	1236#	1246											
SW02 = 000004	1235#	1245											
SW03 = 000010	1234#	1244											
SW04 = 000020	1233#	1243											
SW05 = 000040	1232#	1242											
SW06 = 000100	1231#	1241											
SW07 = 000200	1230#	1240											
SW08 = 000400	1229#	1239											
SW09 = 001000	1228#	1238											
SW1 = 000002	1246#												
SW10 = 002000	1227#												
SW11 = 004000	1226#												
SW12 = 010000	1225#	12214											
SW13 = 020000	1224#	9938											
SW14 = 040000	1223#	9829	9951										
SW15 = 100000	1222#												
SW2 = 000004	1245#												
SW3 = 000010	1244#												
SW4 = 000020	1243#												
SW5 = 000040	1242#												
SW6 = 000100	1241#												
SW7 = 000200	1240#												
SW8 = 000400	1239#	9834											
SW9 = 001000	1238#	4813	4818	5687	6302	6307	6611	6616	6779	6785	7116	7417	7423
	7489	7494	7703	7709	8041	8046	8051	8056	8377	8382	8387	8392	8727
	8732	8737	8742	9256	9266	9276	9286	9808	9946	9971			
TBITVE= 000014	1280#												
TEMP1 003360	2501#	3986*	4000*	4007*	4009*	4014*	4015*	4016	4115*	4122*	4124*	4129*	4130*
	4131	4515*	5332*	5333*	5334	5405*	5848*	5850	5985*	5987	6087*	6088	6148*
	6248*	6249	6264*	6404*	6405	6467*	6573*	6667*	6751*	7077*	7238*	7377*	7440*
	7554*	7675*	7772*	7897*	8015*	8108*	8233*	8351*	8413*	8458*	8583*	8701*	8771*
	9046*	9057*	9072*	9087*	9142*	9146*	9172*	9188	9190*	9217	9219*	9221	9223*
	9225	9227*	9229	9231*	9233	9235*	9237	9239*	9241	9243*	9245	9247*	9340
	9345*	9352	9385*	9392*	9405*	9435	9436*	9443*	9445*	9447*	9456	9457*	9464*
	9466*	9468*	9545*	9550*	9562*	9567*	9579*	9584*	9595*	9600*	9637*	9640*	9641
	9790*	9798*	11917										
TEMP2 003362	2502#	4306*	4633*	4796*	4860*	5008*	5219*	5371*	5606*	5611*	5681*	6817*	6854*
	6855	6881*	7002*	7007*	7110*	8857*	9148*	9569*	9602*	9638*	9639*	9641*	9642
	9867*	9888*											
TEMP3 003364	2503#	5070*	5072	5116*	5118	6818*	6857	6878*	6879	7749*	7751	8085*	8087
	8435*	8437*	8438										
TEMP4 003366	2504#	5071*	5072*	5074*	5075	5117*	5118*	5120*	5121	6821*	6822	6864	6882*
	6883	7750*	7751*	7752	8086*	8087*	8088	8436*	8437				
TEMP5 003370	2505#	4839*	4954*	6823*	6824	6865*	6866	6884*	6885				
TIMUP 001376	2414#	4692	4706	4733	4759	9745*	9756*	9762*					
TITLE 043154	3796	3834	8941#										
TKVEC = 000060	1287#	10450*	10451*										
TOCYL 001352	2401#	4986*	5068	5069	5083	5085	5135	5137	5168*	5169	5176*	6011*	6013
	6122*	6328*	6330	6438*	6634*	6645	6721	6725	6728*	6745	6761	6763*	7071

		7087	7089*	7154*	7156	7192*	7356*	7371	7387	7389*	7434*	7475	7520*	7530
		7608	7621	7647	7654*	7669	7685	7687*	7739*	7750	7754	7826	7838	7864
		7995	7997*	8009	8025	8027*	8075*	8086	8090	8162	8174	8200	8331	8333*
		8345	8361	8363*	8425*	8436	8440	8512	8524	8550	8681	8683*	8695	8711
		8713*	8783*	8792	8793	8825	8828	8862*	8863	11901	11925	11933		
TPVEC =	000064	1288#												
TRAPPC	001334	2391#	9967*	11949										
TRAPVE=	000034	1286#	3736*	3737*	3768*	10005*								
TRT =	000003	12245#	12738											
TRTVEC=	000014	1281#												
TSTATN	044050	4300	4615	4662	4711	4738	4764	4790	4891	4924	5039	5080	5101	5126
		5162	5250	5289	5359	5439	5529	5548	5600	5644	6183	6505	6701	6925
		6944	6996	7040	7272	7588	7806	7931	8142	8267	8492	8617	8797	8845
		9123#	9897											
TST1	010134	3870	3892#											
TST10	012550	4379	4413#											
TST11	012646	4420	4433	4442#										
TST12	012754	4466	4477#											
TST13	013250	4538	4553#											
TST14	013414	4585	4598#											
TST15	014736	4779	4834#											
TST16	015550	4958	4970#											
TST17	017012	5175	5194#											
TST2	010316	3921	3943#											
TST20	020032	5385#												
TST21	020376	5460	5466#											
TST22	020522	5513#												
TST23	021602	5665	5708#											
TST24	022522	5859	5871#											
TST25	023302	6000#												
TST26	023756	6116#												
TST27	025052	6254	6317#											
TST3	011152	4105#												
TST30	025526	6431#												
TST31	026626	6562	6627#											
TST32	027526	6734	6811#											
TST33	030164	6870	6889	6909#										
TST34	031366	7061	7131#											
TST35	033436	7406	7484	7510#										
TST36	034514	7659	7734#											
TST37	036366	8001	8065#											
TST4	011554	4141	4207#											
TST40	040252	8337	8399#											
TST41	042210	8687	8760#											
TST5	012122	4257	4267	4288#										
TST6	012214	4324#												
TST7	012412	4362	4374#											
TYPDS =	104405	8902	10964#											
TYPE =	104401	3800	3816	3818	3820	3835	3877	3952	3962	3991	4027	4073	4215	4237
		4383	4404	4405	4976	4981	4983	5597	5744	5798	5810	5889	5933	5945
		6027	6063	6074	6226	6238	6344	6380	6391	6537	6549	6890	6894	6895
		6993	7138	7152	7175	7320	7459	7471	7514	7517	7631	7643	7848	7860
		7973	7985	8184	8196	8309	8321	8534	8546	8659	8671	8900	8903	8944
		8994	9197	9855	9857	9907	9912	9940	9982	10007	10098	10106	10174	10285
		10411	10472	10484	10538	10539	10542	10553	10563	10574	10593	10641	10647	10652
		10656	10661	10662	10664	10667	10671	10737	10739	10879	10960#	12166	12167	12171

CZR6HFO UNIBUS RK6 DR PT1
CZR6HF.P11 04-JAN-82 12:44

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CROSS REFERENCE TABLE -- MACRO NAMES

SEQ 0276

.\$RDDE	1#		
.\$RDOC	1#	1137#	10689
.\$READ	1#	1137#	10428
.\$R2AZ	1#		
.\$SAVE	1#	1137#	10884
.\$SB2D	1#	1137#	10781
.\$SB2O	1#		
.\$SCOP	1#	1137#	10013
.\$SIZE	1#		
.\$SUPR	1#	1137#	10861
.\$STRAP	1#	1137#	10929
.\$TYPB	1#		
.\$TYPD	1#	1137#	10227
.\$TYPE	1#	1137#	10131
.\$TYPO	1#	1137#	10351
.\$40CA	1#		
.\$1170	1#		

. ABS. 072214 000

ERRORS DETECTED: 0

CZR6HF,CZR6HF.LST/SOL/CRF/NL:TOC=SYSMAC.SML,CZR6HF.P11
RUN-TIME: 24 31 3 SECONDS
RUN-TIME RATIO: 175/59=2.9
CORE USED: 43K (86 PAGES)