

PDT-11/LSI-11

TRAPS TEST
CVKADCO

AH-8200C-MC

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FICHE 1 OF 1

DEC 1978

digital

MADE IN USA

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15 IDENTIFICATION
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17 PRODUCT CODE: AC-8198C-MC
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19 PRODUCT NAME: CVKADC0 LSI-11 TRAPS TEST
20
21 DATE CREATED: AUG, 1978
22
23 MAINTAINER: DIAGNOSTIC GROUP
24
25 AUTHOR: AL LOSCHAK
26 REVISOR: M. MCNALLY JUNE 1976
27 J. RICH JULY 1978
28
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1. ABSTRACT

THIS IS A TEST OF ALL OPERATIONS AND INSTRUCTIONS THAT CAUSE TRAPS, ODDITIES OF REGISTER 6, INTERRUPTS, THE RESET AND WAIT INSTRUCTIONS.

2. REQUIREMENTS

2.1 EQUIPMENT

PDT-11 OR LSI-11 STANDARD COMPUTER WITH AN SLU UNIT AND 4K OF MEMORY. THE CLOCK MUST BE DISABLED.

2.2 STORAGE

2.2.1 PROGRAM STORAGE - THE ROUTINE USES 4K MEMORY

3. LOADING PROCEDURE

3.1 METHOD

PROCEDURE FOR NORMAL ABSOLUTE TAPES SHOULD BE FOLLOWED.

70 4. SETUP PROCEDURE
71
72 THE PROGRAM STARTS AT 200.
73 IF IT IS DESIRED TO RESET THE PASS COUNT BACK TO ZERO
74 START AT LOCATION 210.
75 THE CLOCK MUST BE DISABLED FOR THIS PROGRAM TO RUN CORRECTLY.
76
77 4.1 SWITCH REGISTER BITS
78
79 THE OPERATOR HAS THE FOLLOWING OPTIONS BY SETTING
80 THE SOFTWARE SWITCH REGISTER (LOCATION 422)
81
82 BIT 6=1 (100 OCTAL) TO SUPPRESS TESTING EIS AND FIS OPCODES
83 (70000-75037) FOR RESERVED INSTRUCTION TRAPPING
84 IN THE LAST TEST OF THIS DIAGNOSTIC.
85
86 BIT 5=1 (40 OCTAL) IF WE WANT TO SUPPRESS 'END OF PASS' TYPEOUT
87
88 BIT 4=1 (20 OCTAL) TO SUPPRESS TESTING OPCODES 75400-76777 FOR
89 RESERVED INSTRUCTIONS TRAPS IN THE LAST TEST OF THIS DIAGNOSTIC.
90
91 BIT 3=1 (10 OCTAL) TO SUPPRESS TESTING OPCODES 170000-177777 FOR
92 RESERVED INSTRUCTIONS TRAPS IN THE LAST TEST OF
93 THIS DIAGNOSTIC.
94
95 BIT 2=1 (4 OCTAL) TO SUPPRESS TESTING OPCODES 76030-76057 (DIS RESERVED OPCODE SPACE)
96 NOR EIS OPCODES FOR RESERVED INSTRUCTION TRAPS IN THE LAST
97 TEST OF THIS DIAGNOSTIC
98
99 BIT 1=1 (2 OCTAL) IF THERE IS NO I/O DEVICE IN THE ADDRESS SPACE
100 160000-167777. IF THIS BIT IS SET, THIS ADDRESS AREA WILL BE
101 CHECKED BY TEST 70, EXPECTING IT TO ALL TRAP, OR ALL NOT.
102 NOTE: THIS BIT WOULD NORMALLY BE SET WHEN RUNNING ON TIM, SINCE
103 IT CANNOT HAVE PERIPHERALS IN THIS ADDRESS AREA.
104
105 4.2 APT -- CPU OPTIONS
106
107 WHEN RUNNING UNDER APT, THE DON'T SIZE BIT SHOULD BE SET
108 (\$ENVM=200, OR 240 TO INHIBIT ALL TYPEOUTS) AND THE CPU OPTIONS
109 WORD (\$CPUOP) SHOULD BE SETUP TO FLAG THE PRESENCE OR ABSENCE
110 OF EIS/FIS OR DIS. TO INDICATE EIS/FIS PRESENT, SET CPU OPTIONS
111 BITS 6,7 (OCTAL 300). TO INDICATE DIS PRESENT, SET CPU OPTIONS
112 BIT 5 (OCTAL 40). THESE BITS INHIBIT TRAP TESTING OF THEIR
113 RESPECTIVE OPCODES. THE SAME THING CAN BE ACCOMPLISHED USING
114 THE SWITCH REGISTER AS DESCRIBED IN SECTION 4.1; HOWEVER, THE
115 DON'T SIZE BIT SHOULD STILL BE SET.
116
117 4.3 PROGRAM AND/OR OPERATOR ACTION
118
119 LOAD PROGRAM INTO MEMORY. (BOTTOM 4K)
120 SET THE DESIRED SWITCH REGISTER BITS, IF ANY.
121 LOAD ADDRESS 200.
122 START.
123 THE PROGRAM WILL SIZE FOR EIS/FIS AND FOR DIS. TO INHIBIT
124 SIZING, SET THE DON'T SIZE BIT (BIT 7 OF \$ENVM (ODD BYTE),
125 I.E. \$ENV,\$ENVM=100000)

126 THE PROGRAM WILL PRINT END OF PASS AFTER THE 1ST ITERATION AND
127 THEN PRINT IT EVERY 15 TIMES; APPROXIMATELY 2 MINUTES.
128

129
130 5. OPERATION PROCEDURE

131
132 5.2 SUBROUTINE ABSTRACTS

133
134 5.2.1 TRAPCATCHER
135 -----
136

137
138 THIS IS A SERIES OF INSTRUCTIONS DESIGNED TO DETECT AND
139 ISOLATE UNEXPECTED TRAPS AND INTERRUPTS, THAT OCCUR IN THE
140 TRAP AND INTERRUPT VECTOR AREA OF MEMORY.
141

142 THE PRINCIPLE OF THIS ROUTINE IS: THE VECTOR ENTRANCE
143 ADDRESS POINTS TO THE NEXT SEQUENTIAL WORD WHICH WILL CON-
144 TAIN A HALT (000000) (THIS LOCATION IS ALSO THE STATUS
145 WORD FOR THAT VECTOR ENTRANCE. BUT THIS WILL HAVE NO EFFECT
146 ON IT.
147

148 IF A HALT OCCURS IN THE TRAP OR INTERRUPT VECTOR AREA,
149 REGISTER SIX SHOULD BE EXAMINED TO DETERMINE ITS CONTENTS,
150 THEN USE REGISTER SIX CONTENTS AS AN ADDRESS TO DETERMINE
151 THE LOCATION THE PROGRAM WAS AT, WHEN THE INTERRUPT OR
152 TRAP OCCURRED. (MEMORY AS SPECIFIED BY R6 CONTAINS THE
153 PC OF THE INSTRUCTION FOLLOWING THE INSTRUCTION WHERE THE
154 TRAP OCCURRED.
155 ALSO THE CONTENTS OF '\$TESTN' CONTAIN THE TEST NUMBER
156 THAT IT WAS DOING BEFORE IT TRAPPED.
157

158 5.3 PROGRAM AND/OR OPERATOR ACTION

159
160 5.3.1 LOADING AND STARTING AT 200 STARTS THE TEST. IF
161 AN ERROR IS DETECTED, THERE WILL BE A HALT.

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6. ERRORS

6.1 ALL ERRORS WILL CAUSE A HALT.
THE PC+2 OF THE HALT INSTRUCTION IS PRINTED
ON THE CONSOLE DEVICE BY THE LSI-11'S U-ODT.

6.1.1 THE PROGRAM CHECKS TO SEE THAT THE P.C. DOESN'T JUMP
WITHIN THE TESTS, BY A SEQUENCE COUNT CALLED '\$TSTN'
THIS TEST IS A SEQUENTIAL INCREMENT AND COMPARE COUNT.

```
EX:      CODE
INC      @#$TESTN      ;UPDATE TEST NUMBER
CMP      #N,@#$TESTN  ;SEQUENCE ERROR?
BNE      SOME LOCATION ;BRANCH TO ERROR HALT ON SEQ ERROR
IMPORTANT
```

IF AN ERROR IS DETECTED ;IT COULD BE BECAUSE OF TWO REASONS.
A) WRONG TEST NUMBER
B) ERROR IN THE PRESENT TEST.

```
////////////////////////////////////  
THE TEST SEQUENCE LOCATION 'TESTN' SHOULD BE CHECKED FIRST  
TO SEE IF IT MATCHES THE PRESENT TEST.  
IF IT DOESN'T MATCH ; THEN THE CONTENTS OF THIS LOCATION  
TELL YOU WHICH TEST IT WAS DOING BEFORE IT HALTED.  
////////////////////////////////////
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6.2 ERROR RECOVERY
ON TRAP ERRORS - RESTART AT STARTING ADDRESS

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7. RESTRICTIONS
7.1 STARTING RESTRICTION

NONE

7.2 OPERATIONAL RESTRICTION

NONE

8. MISCELLANEOUS
THERE IS A TEST THAT WILL CHECK THAT ODD ADDRESSING
WILL IGNORE BIT '0'

8.1 EXECUTION TIME

FOR ONE PASS APROXIMATELY 8 SECONDS; THEN IT TYPES
'END OF PASS' APROXIMATELY EVERY 2 MINUTES.

9. PROGRAM DESCRIPTION

THIS PROGRAM CHECKS THAT ON ALL TRAP OPERATIONS REGISTER
6 IS DECREMENTED THE CORRECT AMOUNT, THAT THE CORRECT
PC IS SAVED ON THE STACK, THAT THE OLD CONDITION CODES AND
PRIORITY ARE PLACED ON THE STACK AND THAT THE NEW STATUS AND
CONDITION CODES ARE CORRECT. BOTH THE 'TRAP' AND 'EMT'
TRAP INSTRUCTIONS ARE TESTED TO SEE THAT ALL COMBINATIONS WILL
TRAP. CHECKED ALSO ARE THE RTT AND THE RTI INSTRUCTIONS AND THAT ALL
RESTRICTED INSTRUCTIONS WILL TRAP. VERIFICATION OF THE 'BPT' INSTRUCTION (00003)
WHICH IS USED FOR SOFTWARE DEBUG ROUTINES: ODT, DDT, IS DONE.
ALSO, THE TRACE BIT IS CHECKED TO SEE IF IT CAUSES A TRAP.
SPECIAL CHECKS ARE MADE TO SEE IF BUS
ERROR TRAPS OCCUR ON NON-EXISTENT MEMORY.

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.LIST BIN,LOC

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000006
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104400
104000
000003
000004
000004
000014
000030
000020
000034
177564
177560
177564
177566
000240
000240
000007
000010
004700
000100
000404
000402

.NLIST MD,CND,MC
.LIST ME

PC=%7
SP=%6
HLT=HALT
TRAP=104400
EMT=104000
TRT=3
ITRAP5=4
RTRAP5=4
RTRAP4=14
RTRAP3=30
RTRAP2=20
RTRAP1=34
TTCSR=177564
TRCSR=177560
TPS=177564
TPB=177566
BELL=240
NOP=240
TRAPA=000007
RTRAP=10
ILLA=004700
ILLB=100
\$STNM=\$TESTN
\$ERROR=\$FATAL
.ENABL ABS

:RESERVED INST AND ILLEGAL ADDRESSES
:FOR TRACE TRAP
:FOR EMULATOR TRAP
:FOR IOT TRAP
:FOR TRAP INST

```
264
265
266      000400
267      .SBTTL      .=400      ACT11 HOOKS
268
269      ;*****
270      ;HOOKS REQUIRED BY ACT11
271      $SVPC=.      ;SAVE PC
272      .=46
273      000046      013376      $ENDAD      ;;1)SET LOC.46 TO ADDRESS OF $ENDAD IN .$EOP
274      000052      000052      .=52
275      000052      000000      .WORD      0      ;;2)SET LOC.52 TO ZERO
276      000400      000400      .=$SVPC      ;; RESTORE PC
277      .SBTTL      APT MAILBOX-ETABLE
278
279      ;*****
280      .EVEN
281      000400      $MAIL:      ;;APT MAILBOX
282      000400      000000      $MSGTY: .WORD      AMSGTY      ;;MESSAGE TYPE CODE
283      000402      000000      $FATAL: .WORD      AFATAL      ;;FATAL ERROR NUMBER
284      000404      000000      $TESTN: .WORD      ATESTN      ;;TEST NUMBER
285      000406      000000      $PASS: .WORD      APASS      ;;PASS COUNT
286      000410      000000      $DEVCT: .WORD      ADEVCT      ;;DEVICE COUNT
287      000412      000000      $UNIT: .WORD      AUNIT      ;;I/O UNIT NUMBER
288      000414      000000      $MSGAD: .WORD      AMSGAD      ;;MESSAGE ADDRESS
289      000416      000000      $MSGLG: .WORD      AMSGLG      ;;MESSAGE LENGTH
290      000420      $ETABLE:      ;;APT ENVIRONMENT TABLE
291      000420      000      $ENV: .BYTE      AENV      ;;ENVIRONMENT BYTE
292      000421      000      $ENVM: .BYTE      AENVM      ;;ENVIRONMENT MODE BITS
293      000422      000000      $SWREG: .WORD      ASWREG      ;;APT SWITCH REGISTER
294      000424      000000      $USWR: .WORD      AUSWR      ;;USER SWITCHES
295      000426      000000      $CPUOP: .WORD      ACPUOP      ;;CPU TYPE,OPTIONS
296      *      BITS 15-11=CPU TYPE
297      *      11/04=01,11/05=02,11/20=03,11/40=04,11/45=05
298      *      11/70=06,PDQ=07,Q=10
299      *      BIT 10=REAL TIME CLOCK
300      *      BIT 9=FLOATING POINT PROCESSOR
301      *      BIT 8=MEMORY MANAGEMENT
302      000430      000      $MAMS1: .BYTE      AMAMS1      ;;HIGH ADDRESS,M.S. BYTE
303      000431      000      $MTYP1: .BYTE      AMTYP1      ;;MEM. TYPE,BLK#1
304      *      MEM.TYPE BYTE -- (HIGH BYTE)
305      *      900 NSEC CORE=001
306      *      300 NSEC BIPOLAR=002
307      *      500 NSEC MOS=003
308      000432      000000      $MADR1: .WORD      AMADR1      ;;HIGH ADDRESS,BLK#1
309      *      MEM.LAST ADDR.=3 BYTES,THIS WORD AND LOW OF "TYPE" ABOVE
310      000434      000      $MAMS2: .BYTE      AMAMS2      ;;HIGH ADDRESS,M.S. BYTE
311      000435      000      $MTYP2: .BYTE      AMTYP2      ;;MEM.TYPE,BLK#2
312      000436      000000      $MADR2: .WORD      AMADR2      ;;MEM.LAST ADDRESS,BLK#2
313      000440      000      $MAMS3: .BYTE      AMAMS3      ;;HIGH ADDRESS,M.S.BYTE
314      000441      000      $MTYP3: .BYTE      AMTYP3      ;;MEM.TYPE,BLK#3
315      000442      000000      $MADR3: .WORD      AMADR3      ;;MEM.LAST ADDRESS,BLK#3
316      000444      000      $MAMS4: .BYTE      AMAMS4      ;;HIGH ADDRESS,M.S.BYTE
317      000445      000      $MTYP4: .BYTE      AMTYP4      ;;MEM.TYPE,BLK#4
318      000446      000000      $MADR4: .WORD      AMADR4      ;;MEM.LAST ADDRESS,BLK#4
319      000450      $ETEND:
```

```
320 .MEXIT
321 .SBTTL APT PARAMETER ELOCK
322
323 ;*****
324 ;SET LOCATIONS 24 AND 44 AS REQUIRED FOR APT
325 ;*****
326 000450 .SX= ;;SAVE CURRENT LOCATION
327 000024 =24 ;;SET POWER FAIL TO POINT TO START OF PROGRAM
328 000024 200 ;;FOR APT START UP
329 000044 =44 ;;POINT TO APT INDIRECT ADDRESS PNTR.
330 000044 $APTHDR ;;POINT TO APT HEADER BLOCK
331 000450 =.SX ;;RESET LOCATION COUNTER
332 ;*****
333 ;SETUP APT PARAMETER BLOCK AS DEFINED IN THE APT-PDP11 DIAGNOSTIC
334 ;INTERFACE SPEC.
335
336 000450 $APTHD:
337 000450 000000 $HIBTS: .WORD 0 ;;TWO HIGH BITS OF 18 BIT MAILBOX ADDR.
338 000452 000400 $MBADR: .WORD $MAIL ;;ADDRESS OF APT MAILBOX (BITS 0-15)
339 000454 000010 $STMT: .WORD 10 ;;RUN TIM OF LONGEST TEST
340 000456 000015 $PASTM: .WORD 15 ;;RUN TIME IN SECS. OF 1ST PASS ON 1 UNIT (QUICK VERIFY)
341 000460 000000 $UNITM: .WORD 0 ;;ADDITIONAL RUN TIME (SECS) OF A PASS FOR EACH ADDITIONAL UNIT
342 000462 000024 .WORD $ETEND-$MAIL/2 ;;LENGTH MAILBOX-ETABLE(WORDS)
```

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344
345      000200      000200      . =200
346      000200      000167      000276      JMP      START
347      000210      000210      . =210
348      000210      005037      000406      CLR      @#$PASS      ;CLEAR THE PASS COUNT
349      000214      000167      000262      JMP      START
350      000500      000500      . =500
351      000500      000000      000000      BUFF:   00000
352      000502      012767      013714      177314      START:  MOV      #PWRDWN,24      ;SET UP THE POWER DOWN VECTOR
353      000510      012767      000340      177310      MOV      #340,26      ;SET UP POWER DOWN PRIORITY
354      000516      012706      000500      MOV      #BUFF,SP      ;SET STACK POINTER
355      000522      105737      177565      TSTB    @#TPS+1      ;TEST ODD BYTE OF CONSOLE IN CASE ON A
356      ;PDT-11. THIS WILL RESWAP COMM AND CONSOLE
357      ;ADDRESSES BACK TO NORMAL IN CASE ON APT.
358      000526      004567      013120      JSR      R5,TYPE      ;TYPE THE TITLE
359      000532      014024      TITLE
360      000534      105767      177661      TSTB    $ENVM      ;DON'T SIZE BIT SET?
361      000540      100402      BMI     BEGIN      ;BRANCH IF YES
362      000542      004767      012662      JSR      PC,SIZE      ;SIZE IF ALLOWED
363
364
365      000546      012737      177777      013426      BEGIN:  MOV      #-1,@#PASSPT
366      000554      012702      000400      RESTRT: MOV      # $MSGTY,%2
367      000560      005067      177614      CLR     $MSGTY
368      000564      005067      177614      CLR     $TSTNM
369      000570      005067      177606      CLR     $ERROR
370      000574      000167      000026      JMP     TST1
371      000600      000000      K1:     0
372      000602      000000      K2:     0
373      000604      000000      K3:     0
374      000606      000000      K4:     0
375      000610      000000      K5:     0
376      000612      000000      K6:     0
377      000614      052525      K7:     052525
378      000616      052400      K10:    052400
379      000620      000000      K11:    0
380      000622      000000      K12:    0
381      000624      000000      HERE:   0
  
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382
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386 000626 005237 000404
387 000632 022737 000001 000404
388 000640 001124
389 000642 005006
390 000644 112667 177754
391 000650 020627 000002
392 000654 001405
393 000656 012737 000001 000402
394 000664 005212
395 000666 000000
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399 000670 012706 001000
400 000674 114667 177724
401 000700 020627 000776
402 000704 001405
403 000706 012737 000002 000402
404 000714 005212
405 000716 000000
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409 000720 005006
410 000722 112626
411 000724 020627 000004
412 000730 001405
413 000732 012737 000003 000402
414 000740 005212
415 000742 000000
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419 000744 005006
420 000746 005004
421 000750 122624
422 000752 020627 000002
423 000756 001405
424 000760 012737 000004 000402
425 000766 005212
426 000770 000000
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430 000772 005006
431 000774 005004
432 000776 122426
433 001000 020627 000002
434 001004 001405
435 001006 012737 000005 000402
436 001014 005212
437 001016 000000

;*****
;TEST 1 AUTO INCREMENT/DECREMENT OF R6 FOR WORD AND BYTES
;*****
TST1: INC @#$TESTN ;UPDATE TEST NUMBER
      CMP #1,@#$TESTN ;SEQUENCE ERROR?
      BNE TST2-12 ;BR TO ERROR HALT ON SEQ ERROR
R6TST: CLR %6
      MOVB (6)+,HERE ;SIX SHOULD INCREMENT BY TWO
      CMP %6,#2
      BEQ 1$
      MOV #1,@#$FATAL ;MOVE TO MAILBOX # ***** 1 *****
      INC (R2) ;SET MSGTYP TO FATAL ERROR
      HALT ;R6 DID NOT AUTO INCREMENT BY TWO
          ; TO SCOPE REPLACE HALT W/ 240
          ; AND REPLACE NEXT INST W/ 764

1$: MOV #1000,%6
   MOVB -(6),HERE ;SHOULD DECREMENT BY TWO
   CMP %6,#776
   BEQ 2$
   MOV #2,@#$FATAL ;MOVE TO MAILBOX # ***** 2 *****
   INC (R2) ;SET MSGTYP TO FATAL ERROR
   HALT ;R6 DID NOT AUTO DECREMENT BY 2
          ; TO SCOPE REPLACE HALT W/ 240
          ; AND REPLACE NEXT INST W/ 750

2$: CLR %6
   MOVB (6)+,(6)+ ;DOUBLES AUTO INCREMENT OF R6
   CMP %6,#4
   BEQ 3$
   MOV #3,@#$FATAL ;MOVE TO MAILBOX # ***** 3 *****
   INC (R2) ;SET MSGTYP TO FATAL ERROR
   HALT ;WRONG AUTO INCREMENT OF R6
          ; TO SCOPE REPLACE HALT W/ 240
          ; AND REPLACE NEXT INST W/ 736

3$: CLR %6
   CLR %4
   CMPB (6)+,(4)+ ;TEST INCREMENT OF R6
   CMP %6,#2
   BEQ 4$
   MOV #4,@#$FATAL ;MOVE TO MAILBOX # ***** 4 *****
   INC (R2) ;SET MSGTYP TO FATAL ERROR
   HALT ;WRONG INCREMENT OF R6
          ; TO SCOPE REPLACE HALT W/ 240
          ; AND REPLACE NEXT INST W/ 723

4$: CLR %6
   CLR %4
   CMPB (4)+,(6)+ ;TEST INCREMENT OF R6
   CMP %6,#2
   BEQ 5$
   MOV #5,@#$FATAL ;MOVE TO MAILBOX # ***** 5 *****
   INC (R2) ;SET MSGTYP TO FATAL ERROR
   HALT ;WRONG INCREMENT OF R6
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001124 005237 000404
001130 022737 000002 000404
001136 001137
001140 012767 123456 177442
001146 012767 050505 177424
001154 012705 000600
001160 012706 000610
001164 112625
001166 022767 050456 177404
001174 001405
001176 012737 000011 000402
001204 005212
001206 000000

001210 012767 123456 177372 1\$:
001216 012767 050505 177354
001224 012705 000600
001230 012706 000612
001234 114625
001236 026727 177336 050456
001244 001405
001246 012737 000012 000402
001254 005212
001256 000000

001260 012767 123456 177312 2\$:
001266 012767 050505 177314
001274 012705 000600
001300 012706 000610
001304 112526
001306 022767 050456 177274
001314 001405
001316 012737 000013 000402
001324 005212
001326 000000

001330 012767 123456 177242 3\$:
001336 012767 050505 177244
001344 012705 000601
001350 012706 000610
001354 112526
001356 026727 177226 050647
001364 001405
001366 012737 000014 000402
001374 005212
001376 000000

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*****  
:TEST 2 TRANSFER OF BYTE USING R6  
*****  
TST2:  INC @#$TESTN      ;UPDATE TEST NUMBER  
        CMP #2,@#$TESTN  ;SEQUENCE ERROR?  
        BNE TST3-12 ;BR TO ERROR HALT ON SEQ ERROR  
        MOV #123456,K5  
        MOV #050505,K1  
        MOV #K1,%5      ;%5=(050505)K1  
        MOV #K5,%6      ;%6=(123456)K5  
        MOVB (6)+,(5)+  ;LOW .BYTE OF R6 TO R5  
        CMP #050456,K1  
        BEQ 1$  
        MOV #11,@#$FATAL ;MOVE TO MAILBOX # ***** 11 *****  
        INC (R2)        ;SET MSGTYP TO FATAL ERROR  
        HALT           ;FALSE TRANSFER OF .BYTE  
                       ; TO SCOPE REPLACE HALT W/ 240  
                       ; AND REPLACE NEXT INST W/ 753  
  
1$:    MOV #123456,K5  
        MOV #050505,K1  
        MOV #K1,%5      ;%5(050505)K1  
        MOV #K6,%6      ;%6(123456)K5  
        MOVB -(6),(5)+  ;LOW .BYTE OF R6 TO R5 (DECREMENT)  
        CMP K1,#050456  
        BEQ 2$  
        MOV #12,@#$FATAL ;MOVE TO MAILBOX # ***** 12 *****  
        INC (R2)        ;SET MSGTYP TO FATAL ERROR  
        HALT           ;FALSE R6 .BYTE TRANSFER  
                       ; TO SCOPE REPLACE HALT W/ 240  
                       ; AND REPLACE NEXT INST W/ 727  
  
2$:    MOV #123456,K1  
        MOV #050505,K5  
        MOV #K1,%5      ;(123456)  
        MOV #K5,%6      ;(050505)  
        MOVB (5)+,(6)+  ;LOW OF R5 TO LOW OF R6  
        CMP #050456,K5  
        BEQ 3$  
        MOV #13,@#$FATAL ;MOVE TO MAILBOX # ***** 13 *****  
        INC (R2)        ;SET MSGTYP TO FATAL ERROR  
        HALT           ;FALSE R6 .BYTE TRANSFER  
                       ; TO SCOPE REPLACE HALT W/ 240  
                       ; AND REPLACE NEXT INST W/ 703  
  
3$:    MOV #123456,K1  
        MOV #050505,K5  
        MOV #K1+1,%5    ;123456  
        MOV #K5,%6      ;050505  
        MOVB (5)+,(6)+  ;HIGH OF R5 TO LOW OF R6  
        CMP K5,#050647  
        BEQ 4$  
        MOV #14,@#$FATAL ;MOVE TO MAILBOX # ***** 14 *****  
        INC (R2)        ;SET MSGTYP TO FATAL ERROR  
        HALT           ;FALSE R6 .BYTE TRANSFER
```


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BYTE OPERATION WITH SEQUENTIAL ODD/EVEN ADDRESS

SEQ 0017

599	001646	001405				BEQ	6\$		
600	001650	012737	000024	000402		MOV	#24,@#FATAL	:MOVE TO MAILBOX # ***** 24 *****	
601	001656	005212				INC	(R2)	:SET MSGTYP TO FATAL ERROR	
602	001660	000000				HALT		:HIGH TO LOW IN SAME .WORD FAILED	
603								: TO SCOPE REPLACE HALT W/ 240	
604								: AND REPLACE NEXT INST W/ 700	
605									
606	001662	126767	176730	176725	6\$:	CMPB	K10,K7+1		
607	001670	001005				BNE	TST4		
608	001672	012737	000025	000402		MOV	#25,@#FATAL	:MOVE TO MAILBOX # ***** 25 *****	
609	001700	005212				INC	(R2)	:SET MSGTYP TO FATAL ERROR	
610	001702	000000				HALT		:.EVEN TO ODD FAILED,OR WRONG \$TESTN,OR WRONG \$TESTN	
611								: TO SCOPE REPLACE HALT W/ 240	
612								: AND REPLACE NEXT INST W/ 667	


```
715
716
717 :*****
718 :TEST 7 THAT PROPER P.C. IS SAVED
719 :*****
719 002224 005237 000404 TST7: INC @#$TESTN ;UPDATE TEST NUMBER
720 002230 022737 000007 000404 CMP #7,@#$TESTN ;SEQUENCE ERROR?
721 002236 001012 BNE TST10-12 ;BR TO ERROR HALT ON SEQ ERROR
722 002240 012706 000500 MOV #BUFF,SP ;STACK POINTER SETUP
723 002244 012767 002254 175536 MOV #RETC,RTRAP ;RETURN FROM TRAP POINTER
724 002252 000007 INSTC: TRAPA ;TRAP ON THIS INSTRUCTION
725 002254 022767 002254 176212 RETC: CMP #,BUFF-4 ;CHECK FOR INCREMENTED P.C.
726 002262 001405 BEQ TST10
727 002264 012737 000040 000402 MOV #40,@#$FATAL ;MOVE TO MAILBOX # ***** 40 *****
728 002272 005212 INC (R2) ;SET MSGTYP TO FATAL ERROR
729 002274 000000 HALT ;INCORRECT P.C.,OR WRONG $TESTN
730 ; TO SCOPE REPLACE HALT W/ 240
731 ; AND REPLACE NEXT INST W/ 760
732 :*****
733 :TEST 10 THAT 'OLD' STATUS AND PRIORITY ARE PLACED ON STACK
734 :*****
735 002276 005237 000404 TST10: INC @#$TESTN ;UPDATE TEST NUMBER
736 002302 022737 000010 000404 CMP #10,@#$TESTN ;SEQUENCE ERROR?
737 002310 001044 BNE TST11-12 ;BR TO ERROR HALT ON SEQ ERROR
738 002312 012706 000500 MOV #BUFF,SP ;SET UP
739 002316 012767 002340 175464 MOV #RETD,RTRAP ;SET UP
740 002324 005067 010756 CLR STATUS ;CLEAR STATUS AND PRIORITY
741 002330 106467 010752 MTPS STATUS
742 002334 000257 CCC
743 002336 000007 TRAPA ;TRAP
744 002340 026727 176132 000000 RETD: CMP BUFF-2,#0 ;TEST THAT OLD STATUS WENT TO STACK
745 002346 001405 BEQ 1$
746 002350 012737 000041 000402 MOV #41,@#$FATAL ;MOVE TO MAILBOX # ***** 41 *****
747 002356 005212 INC (R2) ;SET MSGTYP TO FATAL ERROR
748 002360 000000 HALT ;INCORRECT STATUS
749 ; TO SCOPE REPLACE HALT W/ 240
750 ; AND REPLACE NEXT INST W/ 753
751 002362 012706 000500 1$: MOV #BUFF,SP ;SET UP
752 002366 012767 002412 175414 MOV #RETE,RTRAP ;SET UP
753 002374 012767 000357 010704 MOV #357,STATUS ;SET PRIORITY
754 002402 106467 010700 MTPS STATUS
755 002406 000277 SCC ;SET STATUS
756 002410 000007 TRAPA ;TRAP
757 002412 026727 176060 000357 RETE: CMP BUFF-2,#357 ;COMPARES STATUS ON STACK
758 002420 001405 BEQ TST11
759 002422 012737 000042 000402 MOV #42,@#$FATAL ;MOVE TO MAILBOX # ***** 42 *****
760 002430 005212 INC (R2) ;SET MSGTYP TO FATAL ERROR
761 002432 000000 HALT ;INCORRECT STATUS ON STACK,OR WRONG $TESTN
762 ; TO SCOPE REPLACE HALT W/ 240
763 ; AND REPLACE NEXT INST W/ 726
```

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764  
765  
766  
767  
768 002434 005237 000404  
769 002440 022737 000011 000404  
770 002446 001125  
771 002450 012706 000500  
772 002454 012767 002470 175326  
773 002462 005067 175324  
774 002466 000007  
775 002470  
776 002470 100005  
777 002472 012737 000043 000402  
778 002500 005212  
779 002502 000000  
780  
781  
782 002504  
783 002504 001005  
784 002506 012737 000044 000402  
785 002514 005212  
786 002516 000000  
787  
788  
789 002520  
790 002520 102005  
791 002522 012737 000045 000402  
792 002530 005212  
793 002532 000000  
794  
795  
796 002534  
797 002534 103005  
798 002536 012737 000046 000402  
799 002544 005212  
800 002546 000000  
801  
802  
803 002550 106767 010532  
804 002554 032767 000340 010524  
805 002562 001405  
806 002564 012737 000047 000402  
807 002572 005212  
808 002574 000000  
809  
810  
811 002576 012706 000500  
812 002602 012767 002620 175200  
813 002610 012767 000357 175174  
814 002616 000007  
815 002620  
816 002620 100405  
817 002622 012737 000050 000402  
818 002630 005212  
819 002632 000000
```

```
*****  
;TEST 11 THAT 'NEW' STATUS IS CORRECT  
*****  
TST11: INC @#$TESTN ;UPDATE TEST NUMBER  
CMP #11,@#$TESTN ;SEQUENCE ERROR?  
BNE RSTP1 ;BR TO ERROR HALT ON SEQ ERROR  
MOV #BUFF,SP  
MOV #RETF,RTRAP  
CLR RTRAP+2 ;CLEAR FUTURE PRIORITY AND CC  
TRAPA  
  
RETF: BPL 1$  
MOV #43,@#$FATAL ;MOVE TO MAILBOX # ***** 43 *****  
INC (R2) ;SET MSGTYP TO FATAL ERROR  
HALT ;C NOT CLEARED  
; TO SCOPE REPLACE HALT W/ 240  
; AND REPLACE NEXT INST W/ 761  
  
1$: BNE 2$  
MOV #44,@#$FATAL ;MOVE TO MAILBOX # ***** 44 *****  
INC (R2) ;SET MSGTYP TO FATAL ERROR  
HALT ;Z NOT CLEARED  
; TO SCOPE REPLACE HALT W/ 240  
; AND REPLACE NEXT INST W/ 753  
  
2$: BVC 3$  
MOV #45,@#$FATAL ;MOVE TO MAILBOX # ***** 45 *****  
INC (R2) ;SET MSGTYP TO FATAL ERROR  
HALT ;V NOT CLEARED  
; TO SCOPE REPLACE HALT W/ 240  
; AND REPLACE NEXT INST W/ 745  
  
3$: BCC 4$  
MOV #46,@#$FATAL ;MOVE TO MAILBOX # ***** 46 *****  
INC (R2) ;SET MSGTYP TO FATAL ERROR  
HALT ;C NOT CLEARED  
; TO SCOPE REPLACE HALT W/ 240  
; AND REPLACE NEXT INST W/ 737  
  
4$: MFPS STATUS  
BIT #340,STATUS ;TEST PRIORITY  
BEQ 5$  
MOV #47,@#$FATAL ;MOVE TO MAILBOX # ***** 47 *****  
INC (R2) ;SET MSGTYP TO FATAL ERROR  
HALT ;PRIORITY NOT ZERO  
; TO SCOPE REPLACE HALT W/ 240  
; AND REPLACE NEXT INST W/ 724  
  
5$: MOV #BUFF,SP  
MOV #RETG,RTRAP  
MOV #357,RTRAP+2 ;SET NEW 'CC' AND PRIORITY  
TRAPA ;TRAP HERE  
  
RETG: BMI 1$  
MOV #50,@#$FATAL ;MOVE TO MAILBOX # ***** 50 *****  
INC (R2) ;SET MSGTYP TO FATAL ERROR  
HALT ;N NOT SET
```

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820 ; TO SCOPE REPLACE HALT W/ 240
821 ; AND REPLACE NEXT INST W/ 705
822 002634 001405 1$: BEQ 2$
823 002634 012737 000051 000402 MOV #51,@#$FATAL ;MOVE TO MAILBOX # ***** 51 *****
824 002636 012737 000051 000402 INC (R2) ;SET MSGTYP TO FATAL ERROR
825 002644 005212 ;Z NOT SET
826 002646 000000 HALT ; TO SCOPE REPLACE HALT W/ 240
827 ; AND REPLACE NEXT INST W/ 677
828
829 002650 2$: BVS 3$
830 002650 102405 000052 000402 MOV #52,@#$FATAL ;MOVE TO MAILBOX # ***** 52 *****
831 002652 012737 000052 000402 INC (R2) ;SET MSGTYP TO FATAL ERROR
832 002660 005212 ;V NOT SET
833 002662 000000 HALT ; TO SCOPE REPLACE HALT W/ 240
834 ; AND REPLACE NEXT INST W/ 671
835
836 002664 3$: BCS 4$
837 002664 103405 000053 000402 MOV #53,@#$FATAL ;MOVE TO MAILBOX # ***** 53 *****
838 002666 012737 000053 000402 INC (R2) ;SET MSGTYP TO FATAL ERROR
839 002674 005212 ;C NOT SET
840 002676 000000 HALT ; TO SCOPE REPLACE HALT W/ 240
841 ; AND REPLACE NEXT INST W/ 663
842
843 002700 106767 010402 4$: MFPS STATUS
844 002704 016706 010376 MOV STATUS,SP
845 002710 042706 000017 BIC #17,SP
846 002714 022706 000340 CMP #340,SP
847 002720 001405 BEQ RST1
848 002722
849 002722 012737 000054 000402 RSTP1: MOV #54,@#$FATAL ;MOVE TO MAILBOX # ***** 54 *****
850 002730 005212 INC (R2) ;SET MSGTYP TO FATAL ERROR
851 002732 000000 HALT ;PRIORITY WAS CHANGED,OR WRONG $TESTN
852 ; TO SCOPE REPLACE HALT W/ 240
853 ; AND REPLACE NEXT INST W/ 645
854 002734 012767 000012 175046 RST1: MOV #12,10
855 002742 005067 175044 CLR 12

```



```
856
857
858 :*****
859 :TEST 12 THAT A TRAP OCCURES FOR A 'TRAP' INSTRUCTION
860 :*****
860 002746 005237 000404 TST12: INC @#$TESTN ;UPDATE TEST NUMBER
861 002752 022737 000012 000404 CMP #12,@#$TESTN ;SEQUENCE ERROR?
862 002760 001006 BNE TST13-12 ;BR TO ERROR HALT ON SEQ ERROR
863 002762 012706 000500 MOV #BUFF,SP ;STACK POINTER SETUP
864 002766 012767 003010 175040 MOV #RETA1,RTRAP1 ;RETURN LOCATION
865 002774 104400 TRAP ;RESERVED INSTRUCTION, SHOULD TRAP
866 002776 012737 000055 000402 MOV #55,@#$FATAL ;MOVE TO MAILBOX # ***** 55 *****
867 003004 005212 INC (R2) ;SET MSGTYP TO FATAL ERROR
868 003006 000000 HALT ;DID NOT TRAP,OR WRONG $TESTN
869 ; TO SCOPE REPLACE HALT W/ 240
870 ; AND REPLACE NEXT INST W/ 764
871 003010 RETA1:
872 :*****
873 :TEST 13 DECREMENT OF STACK POINTER ON A TRAP OPERATION
874 :*****
875 003010 005237 000404 TST13: INC @#$TESTN ;UPDATE TEST NUMBER
876 003014 022737 000013 000404 CMP #13,@#$TESTN ;SEQUENCE ERROR?
877 003022 001011 BNE TST14-12 ;BR TO ERROR HALT ON SEQ ERROR
878 003024 012706 000500 MOV #BUFF,SP ;STACK POINTER SETUP
879 003030 012767 003040 174776 MOV #RETB1,RTRAP1 ;RETURN POINTER
880 003036 104400 TRAP ;RESERVED INSTRUCTION
881 003040 020627 000474 RETB1: CMP SP,#BUFF-4 ;TEST DECREMENT OF SP
882 003044 001405 BEQ TST14
883 003046 012737 000056 000402 MOV #56,@#$FATAL ;MOVE TO MAILBOX # ***** 56 *****
884 003054 005212 INC (R2) ;SET MSGTYP TO FATAL ERROR
885 003056 000000 HALT ;NOT DECREMENTED TWO WORDS,OR WRONG $TESTN
886 ; TO SCOPE REPLACE HALT W/ 240
887 ; AND REPLACE NEXT INST W/ 761
888 :*****
889 :TEST 14 THAT PROPER P.C. IS SAVED
890 :*****
891 003060 005237 000404 TST14: INC @#$TESTN ;UPDATE TEST NUMBER
892 003064 022737 000014 000404 CMP #14,@#$TESTN ;SEQUENCE ERROR?
893 003072 001012 BNE TST15-12 ;BR TO ERROR HALT ON SEQ ERROR
894 003074 012706 000500 MOV #BUFF,SP ;STACK POINTER SETUP
895 003100 012767 003110 174726 MOV #RETC1,RTRAP1 ;RETURN FROM TRAP POINTER
896 003106 104400 TRAP ;TRAP ON THIS INSTRUCTION
897 003110 022767 003110 175356 RETC1: CMP #,BUFF-4 ;CHECK INCREMENTED P.C.
898 003116 001405 BEQ TST15
899 003120 012737 000057 000402 MOV #57,@#$FATAL ;MOVE TO MAILBOX # ***** 57 *****
900 003126 005212 INC (R2) ;SET MSGTYP TO FATAL ERROR
901 003130 000000 HALT ;INCORRECT P.C.,OR WRONG $TESTN
902 ; TO SCOPE REPLACE HALT W/ 240
903 ; AND REPLACE NEXT INST W/ 760
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904
905
906
907
908 003132 005237 000404
909 003136 022737 000015 000404
910 003144 001043
911 003146 012706 000500
912 003152 012767 003174 174654
913 003160 005067 010122
914 003164 106467 010116
915 003170 000257
916 003172 104400
917 003174 026727 175276 000000 RETD1:
918 003202 001405
919 003204 012737 000060 000402
920 003212 005212
921 003214 000000
922
923
924 003216 012706 000500 1$:
925 003222 012767 003244 174604
926 003230 012767 000357 010050
927 003236 106467 010044
928 003242 104400
929 003244 026727 175226 000357 RETE1:
930 003252 001405
931 003254 012737 000061 000402
932 003262 005212
933 003264 000000
934
935

```

```

:*****
:TEST 15 THAT 'OLD' STATUS AND PRIORITY ARE PLACED ON STACK
:*****
TST15: INC @#$TESTN ;UPDATE TEST NUMBER
      CMP #15,@#$TESTN ;SEQUENCE ERROR?
      BNE TST16-12 ;BR TO ERROR HALT ON SEQ ERROR
      MOV #BUFF,SP ;SET UP
      MOV #RETD1,RTRAP1 ;SET UP
      CLR STATUS ;CLEAR STATUS AND PRIORITY
      MTPS STATUS
      CCC
      TRAP ;TRAP
RETD1: CMP BUFF-2,#0 ;TEST THAT OLD STATUS WENT TO STACK
      BEQ 1$
      MOV #60,@#$FATAL ;MOVE TO MAILBOX # ***** 60 *****
      INC (R2) ;SET MSGTYP TO FATAL ERROR
      HALT ;INCORRECT STATUS
      ; TO SCOPE REPLACE HALT W/ 240
      ; AND REPLACE NEXT INST W/ 753
1$: MOV #BUFF,SP ;SET UP
     MOV #RETE1,RTRAP1 ;SET UP
     MOV #357,STATUS ;SET PRIORITY
     MTPS STATUS
     TRAP ;SET CC
RETE1: CMP BUFF-2,#357 ;COMPARES STATUS ON STACK
      BEQ TST16
      MOV #61,@#$FATAL ;MOVE TO MAILBOX # ***** 61 *****
      INC (R2) ;SET MSGTYP TO FATAL ERROR
      HALT ;INCORRECT STATUS ON STACK
      ; TO SCOPE REPLACE HALT W/ 240
      ; AND REPLACE NEXT INST W/ 727

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936
937
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940 003266 005237 000404
941 003272 022737 000016 000404
942 003300 001125
943 003302 012706 000500
944 003306 012767 003322 174520
945 003314 005067 174516
946 003320 104400
947 003322
948 003322 100005
949 003324 012737 000062 000402
950 003332 005212
951 003334 000000
952
953
954 003336
955 003336 001005
956 003340 012737 000063 000402
957 003346 005212
958 003350 000000
959
960
961 003352
962 003352 102005
963 003354 012737 000064 000402
964 003362 005212
965 003364 000000
966
967
968 003366
969 003366 103005
970 003370 012737 000065 000402
971 003376 005212
972 003400 000000
973
974
975 003402 106767 007700
976 003406 032767 000340 007672
977 003414 001405
978 003416 012737 000066 000402
979 003424 005212
980 003426 000000
981
982
983 003430 012706 000500
984 003434 012767 003452 174372
985 003442 012767 000357 174366
986 003450 104400
987 003452
988 003452 100405
989 003454 012737 000067 000402
990 003462 005212
991 003464 000000

:*****
:TEST 16 THAT 'NEW' STATUS IS CORRECT
:*****
TST16: INC @#$TESTN ;UPDATE TEST NUMBER
        CMP #16,@#$TESTN ;SEQUENCE ERROR?
        BNE TST17-12 ;BR TO ERROR HALT ON SEQ ERROR
        MOV #BUFF,SP
        MOV #RETF1,RTRAP1
        CLR RTRAP1+2 ;CLEAR FUTURE PRIORITY AND CC
        TRAP

RETF1: BPL 1$
        MOV #62,@#$FATAL ;MOVE TO MAILBOX # ***** 62 *****
        INC (R2) ;SET MSGTYP TO FATAL ERROR
        HALT ;C NOT CLEARED
        ; TO SCOPE REPLACE HALT W/ 240
        ; AND REPLACE NEXT INST W/ 761

1$: BNE 2$
        MOV #63,@#$FATAL ;MOVE TO MAILBOX # ***** 63 *****
        INC (R2) ;SET MSGTYP TO FATAL ERROR
        HALT ;Z NOT CLEARED
        ; TO SCOPE REPLACE HALT W/ 240
        ; AND REPLACE NEXT INST W/ 753

2$: BVC 3$
        MOV #64,@#$FATAL ;MOVE TO MAILBOX # ***** 64 *****
        INC (R2) ;SET MSGTYP TO FATAL ERROR
        HALT ;V NOT CLEARED
        ; TO SCOPE REPLACE HALT W/ 240
        ; AND REPLACE NEXT INST W/ 745

3$: BCC 4$
        MOV #65,@#$FATAL ;MOVE TO MAILBOX # ***** 65 *****
        INC (R2) ;SET MSGTYP TO FATAL ERROR
        HALT ;C NOT CLEARED
        ; TO SCOPE REPLACE HALT W/ 240
        ; AND REPLACE NEXT INST W/ 737

4$: MFPS STATUS
        BIT #340,STATUS ;TEST PRIORITY
        BEQ 5$
        MOV #66,@#$FATAL ;MOVE TO MAILBOX # ***** 66 *****
        INC (R2) ;SET MSGTYP TO FATAL ERROR
        HALT ;PRIORITY NOT ZERO
        ; TO SCOPE REPLACE HALT W/ 240
        ; AND REPLACE NEXT INST W/ 724

5$: MOV #BUFF,SP
        MOV #RETG1,RTRAP1
        MOV #357,RTRAP1+2 ;SET NEW 'CC' AND PRIORITY
        TRAP ;TRAP HERE

RETG1: BMI 1$
        MOV #67,@#$FATAL ;MOVE TO MAILBOX # ***** 67 *****
        INC (R2) ;SET MSGTYP TO FATAL ERROR
        HALT ;N NOT SET
```



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1025
1026
1027
1028
1029 003566 005237 000404
1030 003572 022737 000017 000404
1031 003600 001011
1032 003602 012767 104400 000012
1033 003610 012767 003636 174216
1034 003616 012706 000500
1035 003622 104400
1036 003624
1037 003624 012737 000074 000402
1038 003632 005212
1039 003634 000000
1040
1041
1042 003636 005267 177760
1043 003642 022767 104777 177752
1044 003650 103362
1045 003652 012767 000036 174154
1046 003660 005067 174152
1047
1048
1049
1050 003664 005237 000404
1051 003670 022737 000020 000404
1052 003676 001006
1053 003700 012706 000500
1054 003704 012767 003726 174106
1055 003712 000004
1056 003714 012737 000075 000402
1057 003722 005212
1058 003724 000000
1059
1060
1061 003726
1062
1063
1064
1065 003726 005237 000404
1066 003732 022737 000021 000404
1067 003740 001011
1068 003742 012706 000500
1069 003746 012767 003756 174044
1070 003754 000004
1071 003756 020627 000474
1072 003762 001405
1073 003764 012737 000076 000402
1074 003772 005212
1075 003774 000000
1076
1077

:*****
:TEST 17 THAT ALL COMBINATION OF 'TRAP' WILL TRAP
:*****
TST17: INC @#$TESTN ;UPDATE TEST NUMBER
        CMP #17,@#$TESTN ;SEQUENCE ERROR?
        BNE RB1AA ;BR TO ERROR HALT ON SEQ ERROR
        MOV #TRAP,RB1 ;INITIALIZE BASE TRAP INSTRUCTION
        MOV #RA1,34 ;RETURN FROM TRAP TO RA1
RC1:    MOV #BUFF,SP ;SET UP STACK POINTER
RB1:    TRAP ;TRAP INST WILL BE MODIFIED TO TRAP+377
RB1AA:  MOV #74,@#$FATAL ;MOVE TO MAILBOX # ***** 74 *****
        INC (R2) ;SET MSGTYP TO FATAL ERROR
        HALT ;PREVIOUS INST FAILED TO TRAP,OR WRONG $TESTN
        ; TO SCOPE REPLACE HALT W/ 240
        ; AND REPLACE NEXT INST W/ 761
RA1:    INC RB1
        CMP #104777,RB1 ;TRAP+377 TO UPPER LIMIT
        BHIS RC1 ;HAVE WE TESTED ALL
        MOV #36,34
        CLR 36
:*****
:TEST 20 THAT A TRAP OCCURES ON AN 'IOT' INSTRUCTION
:*****
TST20: INC @#$TESTN ;UPDATE TEST NUMBER
        CMP #20,@#$TESTN ;SEQUENCE ERROR?
        BNE TST21-12 ;BR TO ERROR HALT ON SEQ ERROR
        MOV #BUFF,SP ;STACK POINTER SETUP
        MOV #RETA2,RTRAP2 ;RETURN LOCATION
        IOT ;RESERVE INSTRUCTION, SHOULD TRAP
        MOV #75,@#$FATAL ;MOVE TO MAILBOX # ***** 75 *****
        INC (R2) ;SET MSGTYP TO FATAL ERROR
        HALT ;IOT DID NOT TRAP,OR WRONG $TESTN
        ; TO SCOPE REPLACE HALT W/ 240
        ; AND REPLACE NEXT INST W/ 764
RETA2:
:*****
:TEST 21 DECREMENT OF STACK POINTER ON A TRAP OPERATION
:*****
TST21: INC @#$TESTN ;UPDATE TEST NUMBER
        CMP #21,@#$TESTN ;SEQUENCE ERROR?
        BNE TST22-12 ;BR TO ERROR HALT ON SEQ ERROR
        MOV #BUFF,SP ;STACK POINTER SETUP
        MOV #RETB2,RTRAP2 ;RETURN POINTER
        IOT ;RESERVED INSTRUCTION
RETB2: CMP SP,#BUFF-4 ;TEST DECREMENT OF SP
        BEQ TST22
        MOV #76,@#$FATAL ;MOVE TO MAILBOX # ***** 76 *****
        INC (R2) ;SET MSGTYP TO FATAL ERROR
        HALT ;NOT DECREMENTED TWO WORDS,OR WRONG $TESTN
        ; TO SCOPE REPLACE HALT W/ 240
        ; AND REPLACE NEXT INST W/ 761
```

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1078
1079
1080      ;*****
1081      ;TEST 22 THAT PROPER P.C. IS SAVED
1082      ;*****
1082 003776 005237 000404      TST22: INC @#$TESTN      ;UPDATE TEST NUMBER
1083 004002 022737 000022 000404      CMP #22,@#$TESTN      ;SEQUENCE ERROR?
1084 004010 001012      BNE TST23-12      ;BR TO ERROR HALT ON SEQ ERROR
1085 004012 012706 000500      MOV #BUFF,SP      ;STACK POINTER SETUP
1086 004016 012767 004026 173774      MOV #RETC2,RTRAP2      ;RETURN FROM TRAP POINTER
1087 004024 000004      IOT      ;TRAP ON THIS INSTRUCTION
1088 004026 022767 004026 174440      RETC2: CMP #,BUFF-4      ;CHECK FOR INCREMENTED P.C.
1089 004034 001405      BEQ TST23
1090 004036 012737 000077 000402      MOV #77,@#$FATAL      ;MOVE TO MAILBOX # ***** 77 *****
1091 004044 005212      INC (R2)      ;SET MSGTYP TO FATAL ERROR
1092 004046 000000      HALT      ;INCORRECT P.C.,OR WRONG $TESTN
1093      ; TO SCOPE REPLACE HALT W/ 240
1094      ; AND REPLACE NEXT INST W/ 760
1095      ;*****
1096      ;TEST 23 THAT 'OLD' STATUS AND PRIORITY ARE PLACED ON STACK
1097      ;*****
1098 004050 005237 000404      TST23: INC @#$TESTN      ;UPDATE TEST NUMBER
1099 004054 022737 000023 000404      CMP #23,@#$TESTN      ;SEQUENCE ERROR?
1100 004062 001044      BNE TST24-12      ;BR TO ERROR HALT ON SEQ ERROR
1101 004064 012706 000500      MOV #BUFF,SP      ;SET UP
1102 004070 012767 004112 173722      MOV #RETD2,RTRAP2      ;SET UP
1103 004076 005067 007204      CLR STATUS      ;CLEAR STATUS AND PRIORITY
1104 004102 106467 007200      MTPS STATUS
1105 004106 000257      CCC
1106 004110 000004      IOT      ;TRAP
1107 004112 026727 174360 000000      RETD2: CMP BUFF-2,#0      ;TEST THAT OLD STATUS WENT TO STACK
1108 004120 001405      BEQ 1$
1109 004122 012737 000100 000402      MOV #100,@#$FATAL      ;MOVE TO MAILBOX # ***** 100 *****
1110 004130 005212      INC (R2)      ;SET MSGTYP TO FATAL ERROR
1111 004132 000000      HALT      ;INCORRECT STATUS
1112      ; TO SCOPE REPLACE HALT W/ 240
1113      ; AND REPLACE NEXT INST W/ 753
1114 004134 012706 000500      1$: MOV #BUFF,SP      ;SET UP
1115 004140 012767 004164 173652      MOV #RETE2,RTRAP2      ;SET UP
1116 004146 012767 000357 007132      MOV #357,STATUS      ;SET PRIORITY
1117 004154 106467 007126      MTPS STATUS
1118 004160 000277      SCC      ;SET CC
1119 004162 000004      IOT      ;TRAP
1120 004164 026727 174306 000357      RETE2: CMP BUFF-2,#357      ;COMPARES STATUS ON STACK
1121 004172 001405      BEQ TST24
1122 004174 012737 000101 000402      MOV #101,@#$FATAL      ;MOVE TO MAILBOX # ***** 101 *****
1123 004202 005212      INC (R2)      ;SET MSGTYP TO FATAL ERROR
1124 004204 000000      HALT      ;INCORRECT STATUS ON STACK,OR WRONG $TESTN
1125      ; TO SCOPE REPLACE HALT W/ 240
1126      ; AND REPLACE NEXT INST W/ 726
```

```
1127
1128
1129
1130
1131 004206 005237 000404
1132 004212 022737 000024 000404
1133 004220 001125
1134 004222 012706 000500
1135 004226 012767 004242 173564
1136 004234 005067 173562
1137 004240 000004
1138 004242
1139 004242 100005
1140 004244 012737 000102 000402
1141 004252 005212
1142 004254 000000
1143
1144
1145 004256
1146 004256 001005
1147 004260 012737 000103 000402
1148 004266 005212
1149 004270 000000
1150
1151
1152 004272
1153 004272 102005
1154 004274 012737 000104 000402
1155 004302 005212
1156 004304 000000
1157
1158
1159 004306
1160 004306 103005
1161 004310 012737 000105 000402
1162 004316 005212
1163 004320 000000
1164
1165
1166 004322 106767 006760
1167 004326 032767 000340 006752
1168 004334 001405
1169 004336 012737 000106 000402
1170 004344 005212
1171 004346 000000
1172
1173
1174 004350 012706 000500
1175 004354 012767 004372 173436
1176 004362 012767 000357 173432
1177 004370 000004
1178 004372
1179 004372 100405
1180 004374 012737 000107 000402
1181 004402 005212
1182 004404 000000

;*****
;TEST 24 THAT 'NEW' STATUS IS CORRECT
;*****
TST24: INC @#$TESTN ;UPDATE TEST NUMBER
        CMP #24,@#$TESTN ;SEQUENCE ERROR?
        BNE STP ;BR TO ERROR HALT ON SEQ ERROR
        MOV #BUFF,SP
        MOV #RET2,RTRAP2
        CLR RTRAP2+2 ;CLEAR FUTURE PRIORITY AND CC
        IOT
RET2:
        BPL 1$
        MOV #102,@#$FATAL ;MOVE TO MAILBOX # ***** 102 *****
        INC (R2) ;SET MSGTYP TO FATAL ERROR
        HALT ;C NOT CLEARED
        ; TO SCOPE REPLACE HALT W/ 240
        ; AND REPLACE NEXT INST W/ 761
1$:
        BNE 2$
        MOV #103,@#$FATAL ;MOVE TO MAILBOX # ***** 103 *****
        INC (R2) ;SET MSGTYP TO FATAL ERROR
        HALT ;Z NOT CLEARED
        ; TO SCOPE REPLACE HALT W/ 240
        ; AND REPLACE NEXT INST W/ 753
2$:
        BVC 3$
        MOV #104,@#$FATAL ;MOVE TO MAILBOX # ***** 104 *****
        INC (R2) ;SET MSGTYP TO FATAL ERROR
        HALT ;V NOT CLEARED
        ; TO SCOPE REPLACE HALT W/ 240
        ; AND REPLACE NEXT INST W/ 745
3$:
        BCC 4$
        MOV #105,@#$FATAL ;MOVE TO MAILBOX # ***** 105 *****
        INC (R2) ;SET MSGTYP TO FATAL ERROR
        HALT ;C NOT CLEARED
        ; TO SCOPE REPLACE HALT W/ 240
        ; AND REPLACE NEXT INST W/ 737
4$:
        MFPS STATUS
        BIT #340,STATUS ;TEST PRIORITY
        BEQ 5$
        MOV #106,@#$FATAL ;MOVE TO MAILBOX # ***** 106 *****
        INC (R2) ;SET MSGTYP TO FATAL ERROR
        HALT ;PRIORITY NOT ZERO
        ; TO SCOPE REPLACE HALT W/ 240
        ; AND REPLACE NEXT INST W/ 724
5$:
        MOV #BUFF,SP
        MOV #RETG2,RTRAP2
        MOV #357,RTRAP2+2 ;SET NEW 'CC' AND PRIORITY
        IOT ;TRAP HERE
RETG2:
        BMI 1$
        MOV #107,@#$FATAL ;MOVE TO MAILBOX # ***** 107 *****
        INC (R2) ;SET MSGTYP TO FATAL ERROR
        HALT ;N NOT SET
```

```
1183                                     : TO SCOPE REPLACE HALT W/ 240
1184                                     : AND REPLACE NEXT INST W/ 705
1185 004406                               1$: BEQ      2$
1186 004406 001405                        MOV      #110,@#$FATAL :MOVE TO MAILBOX # ***** 110 *****
1187 004410 012737 000110 000402        INC      (R2)       :SET MSGTYP TO FATAL ERROR
1188 004416 005212                        HALT                    :Z NOT SET
1189 004420 000000                        : TO SCOPE REPLACE HALT W/ 240
1190                                     : AND REPLACE NEXT INST W/ 677
1191
1192 004422                               2$: BVS      3$
1193 004422 102405                        MOV      #111,@#$FATAL :MOVE TO MAILBOX # ***** 111 *****
1194 004424 012737 000111 000402        INC      (R2)       :SET MSGTYP TO FATAL ERROR
1195 004432 005212                        HALT                    :V NOT SET
1196 004434 000000                        : TO SCOPE REPLACE HALT W/ 240
1197                                     : AND REPLACE NEXT INST W/ 671
1198
1199 004436                               3$: BCS      4$
1200 004436 103405                        MOV      #112,@#$FATAL :MOVE TO MAILBOX # ***** 112 *****
1201 004440 012737 000112 000402        INC      (R2)       :SET MSGTYP TO FATAL ERROR
1202 004446 005212                        HALT                    :C NOT SET
1203 004450 000000                        : TO SCOPE REPLACE HALT W/ 240
1204                                     : AND REPLACE NEXT INST W/ 663
1205
1206 004452 106767 006630                 4$: MFPS    STATUS
1207 004456 016706 006624                 MOV      STATUS,SP
1208 004462 042706 000017                 BIC      #17,SP
1209 004466 022706 000340                 CMP      #340,SP
1210 004472 001405                        BEQ      STPA
1211 004474                                     STP:
1212 004474 012737 000113 000402        MOV      #113,@#$FATAL :MOVE TO MAILBOX # ***** 113 *****
1213 004502 005212                        INC      (R2)       :SET MSGTYP TO FATAL ERROR
1214 004504 000000                        HALT                    :PRIORITY WAS CHANGED,OR WRONG $TESTN
1215                                     : TO SCOPE REPLACE HALT W/ 240
1216                                     : AND REPLACE NEXT INST W/ 645
1217 004506 012767 000022 173304 STPA: MOV      #22,20
1218 004514 005067 173302                 CLR      22
```



```

1267
1268
1269
1270
1271 004704 005237 000404
1272 004710 022737 000030 000404
1273 004716 001044
1274 004720 012706 000500
1275 004724 012767 004746 173076
1276 004732 005067 006350
1277 004736 106467 006344
1278 004742 000257
1279 004744 104000
1280 004746 026727 173524 000000 RETD3:
1281 004754 001405
1282 004756 012737 000117 000402
1283 004764 005212
1284 004766 000000
1285
1286
1287 004770 012706 000500 1$:
1288 004774 012767 005020 173026
1289 005002 012767 000357 006276
1290 005010 106467 006272
1291 005014 000277
1292 005016 104000
1293 005020 026727 173452 000357 RETE3:
1294 005026 001405
1295 005030 012737 000120 000402
1296 005036 005212
1297 005040 000000
1298
1299

```

```

;*****
;TEST 30 THAT 'OLD' STATUS AND PRIORITY ARE PLACED ON STACK
;*****
TST30: INC @#$TESTN ;UPDATE TEST NUMBER
        CMP #30,@#$TESTN ;SEQUENCE ERROR?
        BNE TST31-12 ;BR TO ERROR HALT ON SEQ ERROR
        MOV #BUFF,SP ;SET UP
        MOV #RETD3,RTRAP3 ;SET UP
        CLR STATUS ;CLEAR STATUS AND PRIORITY
        M'PS STATUS
        CCC
        EMT ;TRAP
RETD3: CMP BUFF-2,#0 ;TEST THAT OLD STATUS WENT TO STACK
        BEQ 1$
        MOV #117,@#$FATAL ;MOVE TO MAILBOX # ***** 117 *****
        INC (R2) ;SET MSGTYP TO FATAL ERROR
        HALT ;INCORRECT STATUS
        ; TO SCOPE REPLACE HALT W/ 240
        ; AND REPLACE NEXT INST W/ 753
1$: MOV #BUFF,SP ;SET UP
        MOV #RETE3,RTRAP3 ;SET UP
        MOV #357,STATUS ;SET PRIORITY
        MTPS STATUS
        SCC ;SET CC
        EMT ;TRAP
RETE3: CMP BUFF-2,#357 ;COMPARES STATUS ON STACK
        BEQ TST31
        MOV #120,@#$FATAL ;MOVE TO MAILBOX # ***** 120 *****
        INC (R2) ;SET MSGTYP TO FATAL ERROR
        HALT ;INCORRECT STATUS ON STACK,OR WRONG $TESTN
        ; TO SCOPE REPLACE HALT W/ 240
        ; AND REPLACE NEXT INST W/ 726

```

```
1300
1301
1302
1303
1304 005042 005237 000404
1305 005046 022737 000031 000404
1306 005054 001125
1307 005056 012706 000500
1308 005062 012767 005076 172740
1309 005070 005067 172736
1310 005074 104000
1311 005076
1312 005076 100005
1313 005100 012737 000121 000402
1314 005106 005212
1315 005110 000000
1316
1317
1318 005112
1319 005112 001005
1320 005114 012737 000122 000402
1321 005122 005212
1322 005124 000000
1323
1324
1325 005126
1326 005126 102005
1327 005130 012737 000123 000402
1328 005136 005212
1329 005140 000000
1330
1331
1332 005142
1333 005142 103005
1334 005144 012737 000124 000402
1335 005152 005212
1336 005154 000000
1337
1338
1339 005156 106767 006124
1340 005162 032767 000340 006116
1341 005170 001405
1342 005172 012737 000125 000402
1343 005200 005212
1344 005202 000000
1345
1346
1347 005204 012706 000500
1348 005210 012767 005226 172612
1349 005216 012767 000357 172606
1350 005224 104000
1351 005226
1352 005226 100405
1353 005230 012737 000126 000402
1354 005236 005212
1355 005240 000000

:*****
:TEST 31 THAT 'NEW' STATUS IS CORRECT
:*****
TST31: INC @#$TESTN ;UPDATE TEST NUMBER
        CMP #31,@#$TESTN ;SEQUENCE ERROR?
        BNE TST32-12 ;BR TO ERROR HALT ON SEQ ERROR
        MOV #BUFF,SP
        MOV #RET3,RTRAP3
        CLR RTRAP3+2 ;CLEAR FUTURE PRIORITY AND CC
        EMT
RET3: ;TEST FOR 'C' CLEARED
        BPL 1$
        MOV #121,@#$FATAL ;MOVE TO MAILBOX # ***** 121 *****
        INC (R2) ;SET MSGTYP TO FATAL ERROR
        HALT ;C NOT CLEARED
        ; TO SCOPE REPLACE HALT W/ 240
        ; AND REPLACE NEXT INST W/ 761
1$:
        BNE 2$
        MOV #122,@#$FATAL ;MOVE TO MAILBOX # ***** 122 *****
        INC (R2) ;SET MSGTYP TO FATAL ERROR
        HALT ;Z NOT CLEARED
        ; TO SCOPE REPLACE HALT W/ 240
        ; AND REPLACE NEXT INST W/ 753
2$:
        BVC 3$
        MOV #123,@#$FATAL ;MOVE TO MAILBOX # ***** 123 *****
        INC (R2) ;SET MSGTYP TO FATAL ERROR
        HALT ;V NOT CLEARED
        ; TO SCOPE REPLACE HALT W/ 240
        ; AND REPLACE NEXT INST W/ 745
3$:
        BCC 4$
        MOV #124,@#$FATAL ;MOVE TO MAILBOX # ***** 124 *****
        INC (R2) ;SET MSGTYP TO FATAL ERROR
        HALT ;C NOT CLEARED
        ; TO SCOPE REPLACE HALT W/ 240
        ; AND REPLACE NEXT INST W/ 737
4$:
        MFPS STATUS
        BIT #340,STATUS ;TEST PRIORITY
        BEQ 5$
        MOV #125,@#$FATAL ;MOVE TO MAILBOX # ***** 125 *****
        INC (R2) ;SET MSGTYP TO FATAL ERROR
        HALT ;PRIORITY NOT ZERO
        ; TO SCOPE REPLACE HALT W/ 240
        ; AND REPLACE NEXT INST W/ 724
5$:
        MOV #BUFF,SP
        MOV #RET3,RTRAP3
        MOV #357,RTRAP3+2 ;SET NEW 'CC' AND PRIORITY
        EMT ;TRAP HERE
RET3:
        BMI 1$
        MOV #126,@#$FATAL ;MOVE TO MAILBOX # ***** 126 *****
        INC (R2) ;SET MSGTYP TO FATAL ERROR
        HALT ;N NOT SET
```


1389
1390
1391
1392
1393 005342 005237 000404
1394 005346 022737 000032 000404
1395 005354 001011
1396 005356 012767 104000 000012
1397 005364 012767 005412 172436
1398 005372 012706 000500
1399 005376 104000
1400 005400
1401 005400 012737 000133 000402
1402 005406 005212
1403 005410 000000
1404
1405
1406 005412 005267 177760
1407 005416 022767 104377 177752
1408 005424 103362
1409 005426 012767 000032 172374
1410 005434 005067 172372
1411
1412
1413
1414 005440 005237 000404
1415 005444 022737 000033 000404
1416 005452 001006
1417 005454 012706 000500
1418 005460 012767 005502 172326
1419 005466 000003
1420 005470 012737 000134 000402
1421 005476 005212
1422 005500 000000
1423
1424
1425 005502

```
*****  
:TEST 32 THAT ALL COMBINATION OF EMT WILL CAUSE A TRAP  
*****  
TST32: INC @#$TESTN :UPDATE TEST NUMBER  
CMP #32,@#$TESTN :SEQUENCE ERROR?  
BNE RBBB :BR TO ERROR HALT ON SEQ ERROR  
MOV #EMT,RB :INITIALIZE BASE EMT INSTRUCTION  
MOV #RA,30 :RETURN FROM TRAP TO RA  
RC: MOV #BUFF,SP :SET UP STACK POINTER  
RB: EMT :TRAP INST. WILL BE MODIFIED TO EMT+377  
RBBB: MOV #133,@#$FATAL :MOVE TO MAILBOX # ***** 133 *****  
INC (R2) :SET MSGTYP TO FATAL ERROR  
HALT :PREVIOUS INST FAILED TO TRAP,OR WRONG $TESTN  
: TO SCOPE REPLACE HALT W/ 240  
: AND REPLACE NEXT INST W/ 761  
RA: INC RB  
CMP #104377,RB :EMT+377 TO EMT?  
BHIS RC :HAVE WE TESTED ALL  
MOV #32,30  
CLR 32 :HALT  
*****  
:TEST 33 THAT A TRAP OCCURES ON AN 'BPT' INSTRUCTION  
*****  
TST33: INC @#$TESTN :UPDATE TEST NUMBER  
CMP #33,@#$TESTN :SEQUENCE ERROR?  
BNE TST34-12 :BR TO ERROR HALT ON SEQ ERROR  
MOV #BUFF,SP :STACK POINTER SETUP  
MOV #RETA4,RTRAP4 :RETURN LOCATION  
TRT :RESERVED INSTRUCTION, SHOULD TRAP  
MOV #134,@#$FATAL :MOVE TO MAILBOX # ***** 134 *****  
INC (R2) :SET MSGTYP TO FATAL ERROR  
HALT :DID NOT TRAP,OR WRONG $TESTN  
: TO SCOPE REPLACE HALT W/ 240  
: AND REPLACE NEXT INST W/ 764
```

RETA4:

1426
1427
1428
1429
1430 005502 005237 000404
1431 005506 022737 000034 000404
1432 005514 001011
1433 005516 012706 000500
1434 005522 012767 005532 172264
1435 005530 000003
1436 005532 020627 000474
1437 005536 001405
1438 005540 012737 000135 000402
1439 005546 005212
1440 005550 000000
1441
1442
1443
1444
1445
1446 005552 005237 000404
1447 005556 022737 000035 000404
1448 005564 001012
1449 005566 012706 000500
1450 005572 012767 005602 172214
1451 005600 000003
1452 005602 022767 005602 172664
1453 005610 001405
1454 005612 012737 000136 000402
1455 005620 005212
1456 005622 000000
1457
1458

:TEST 34 DECREMENT OF STACK POINTER ON A TRAP OPERATION

TST34: INC @#\$TESTN ;UPDATE TEST NUMBER
CMP #34,@#\$TESTN ;SEQUENCE ERROR?
BNE TST35-12 ;BR TO ERROR HALT ON SEQ ERROR
MOV #BUFF,SP ;STACK POINTER SETUP
MOV #RETB4,RTRAP4 ;RETURN POINTER
TRT ;RESERVED INSTRUCTION
RETB4: CMP SP,#BUFF-4 ;TEST DECREMENT OF SP
BEQ TST35
MOV #135,@#\$FATAL ;MOVE TO MAILBOX # ***** 135 *****
INC (R2) ;SET MSGTYP TO FATAL ERROR
HALT ;NOT DECREMENTED TWO WORDS,OR WRONG \$TESTN
; TO SCOPE REPLACE HALT W/ 240
; AND REPLACE NEXT INST W/ 761

:TEST 35 THAT PROPER P.C. IS SAVED

TST35: INC @#\$TESTN ;UPDATE TEST NUMBER
CMP #35,@#\$TESTN ;SEQUENCE ERROR?
BNE TST36-12 ;BR TO ERROR HALT ON SEQ ERROR
MOV #BUFF,SP ;STACK POINTER SETUP
MOV #RETC4,RTRAP4 ;RETURN FROM TRAP POINTER
TRT ;TRAP ON THIS INSTRUCTION
RETC4: CMP #,BUFF-4 ;CHECK FOR INCREMENTED P.C.
BEQ TST36
MOV #136,@#\$FATAL ;MOVE TO MAILBOX # ***** 136 *****
INC (R2) ;SET MSGTYP TO FATAL ERROR
HALT ;INCORRECT P.C.,OR WRONG \$TESTN
; TO SCOPE REPLACE HALT W/ 240
; AND REPLACE NEXT INST W/ 760

```
1459
1460
1461
1462
1463 005624 005237 000404
1464 005630 022737 000036 000404
1465 005636 001044
1466 005640 012706 000500
1467 005644 012767 005666 172142
1468 005652 005067 005430
1469 005656 106467 005424
1470 005662 000257
1471 005664 000003
1472 005666 026727 172604 000000 RETD4:
1473 005674 001405
1474 005676 012737 000137 000402
1475 005704 005212
1476 005706 000000
1477
1478
1479 005710 012706 000500 1$:
1480 005714 012767 005740 172072
1481 005722 012767 000357 005356
1482 005730 106467 005352
1483 005734 000277
1484 005736 000003
1485 005740 026727 172532 000357 RETE4:
1486 005746 001405
1487 005750 012737 000140 000402
1488 005756 005212
1489 005760 000000
1490
1491
```

:TEST 36 THAT 'OLD' STATUS AND PRIORITY ARE PLACED ON STACK

TST36: INC @#\$TESTN ;UPDATE TEST NUMBER
CMP #36,@#\$TESTN ;SEQUENCE ERROR?
BNE TST37-12 ;BR TO ERROR HALT ON SEQ ERROR
MOV #BUFF,SP ;SET UP
MOV #RETD4,RTRAP4 ;SET UP
CLR STATUS ;CLEAR STATUS AND PRIORITY
MTPS STATUS
CCC
TRT ;TRAP
RETD4: CMP BUFF-2,#0 ;TEST THAT OLD STATUS WENT TO STACK
BEQ 1\$
MOV #137,@#\$FATAL ;MOVE TO MAILBOX # ***** 137 *****
(R2) ;SET MSGTYP TO FATAL ERROR
HALT ;INCORRECT STATUS
; TO SCOPE REPLACE HALT W/ 240
; AND REPLACE NEXT INST W/ 753
1\$: MOV #BUFF,SP ;SET UP
MOV #RETE4,RTRAP4 ;SET UP
MOV #357,STATUS ;SET PRIORITY
MTPS STATUS
SCC ;SET-SET CC
TRT ;TRAP
RETE4: CMP BUFF-2,#357 ;COMPARES STATUS ON STACK
BEQ TST37
MOV #140,@#\$FATAL ;MOVE TO MAILBOX # ***** 140 *****
(R2) ;SET MSGTYP TO FATAL ERROR
HALT ;INCORRECT STATUS ON STACK,OR WRONG \$TESTN
; TO SCOPE REPLACE HALT W/ 240
; AND REPLACE NEXT INST W/ 726

```
1492
1493
1494
1495
1496 005762 005237 000404
1497 005766 022737 000037 000404
1498 005774 001125
1499 005776 012706 000500
1500 006002 012767 006016 172004
1501 006010 005067 172002
1502 006014 000003
1503 006016
1504 006016 100005
1505 006020 012737 000141 000402
1506 006026 005212
1507 006030 000000
1508
1509
1510 006032
1511 006032 001005
1512 006034 012737 000142 000402
1513 006042 005212
1514 006044 000000
1515
1516
1517 006046
1518 006046 102005
1519 006050 012737 000143 000402
1520 006056 005212
1521 006060 000000
1522
1523
1524 006062
1525 006062 103005
1526 006064 012737 000144 000402
1527 006072 005212
1528 006074 000000
1529
1530
1531 006076 106767 005204
1532 006102 032767 000340 005176
1533 006110 001405
1534 006112 012737 000145 000402
1535 006120 005212
1536 006122 000000
1537
1538
1539 006124 012706 000500
1540 006130 012767 006146 171656
1541 006136 012767 000357 171652
1542 006144 000003
1543 006146
1544 006146 100405
1545 006150 012737 000146 000402
1546 006156 005212
1547 006160 000000
```

:TEST 37 THAT 'NEW' STATUS IS CORRECT

```
TST37: INC @#$TESTN ;UPDATE TEST NUMBER
        CMP #37,@#$TESTN ;SEQUENCE ERROR?
        BNE RSTP2 ;BR TO ERROR HALT ON SEQ ERROR
        MOV #BUFF,SP
        MOV #RETF4,RTRAP4
        CLR RTRAP4+2 ;CLEAR FUTURE PRIORITY AND CC
        TRT

RETF4:
        BPL 1$
        MOV #141,@#$FATAL ;MOVE TO MAILBOX # ***** 141 *****
        INC (R2) ;SET MSGTYP TO FATAL ERROR
        HALT ;C NOT CLEARED
        ; TO SCOPE REPLACE HALT W/ 240
        ; AND REPLACE NEXT INST W/ 761

1$:
        BNE 2$
        MOV #142,@#$FATAL ;MOVE TO MAILBOX # ***** 142 *****
        INC (R2) ;SET MSGTYP TO FATAL ERROR
        HALT ;Z NOT CLEARED
        ; TO SCOPE REPLACE HALT W/ 240
        ; AND REPLACE NEXT INST W/ 753

2$:
        BVC 3$
        MOV #143,@#$FATAL ;MOVE TO MAILBOX # ***** 143 *****
        INC (R2) ;SET MSGTYP TO FATAL ERROR
        HALT ;V NOT CLEARED
        ; TO SCOPE REPLACE HALT W/ 240
        ; AND REPLACE NEXT INST W/ 745

3$:
        BCC 4$
        MOV #144,@#$FATAL ;MOVE TO MAILBOX # ***** 144 *****
        INC (R2) ;SET MSGTYP TO FATAL ERROR
        HALT ;C NOT CLEARED
        ; TO SCOPE REPLACE HALT W/ 240
        ; AND REPLACE NEXT INST W/ 737

4$:
        MFPS STATUS
        BIT #340,STATUS ;TEST PRIORITY
        BEQ 5$
        MOV #145,@#$FATAL ;MOVE TO MAILBOX # ***** 145 *****
        INC (R2) ;SET MSGTYP TO FATAL ERROR
        HALT ;PRIORITY NOT ZERO
        ; TO SCOPE REPLACE HALT W/ 240
        ; AND REPLACE NEXT INST W/ 724

5$:
        MOV #BUFF,SP
        MOV #RETF4,RTRAP4
        MOV #357,RTRAP4+2 ;SET NEW 'CC' AND PRIORITY
        TRT ;TRAP HERE

RETF4:
        BMI 1$
        MOV #146,@#$FATAL ;MOVE TO MAILBOX # ***** 146 *****
        INC (R2) ;SET MSGTYP TO FATAL ERROR
        HALT ;N NOT SET
```



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1548                                     : TO SCOPE REPLACE HALT W/ 240
1549                                     : AND REPLACE NEXT INST W/ 705
1550 006162                               1$:
1551 006162 001405                       BEQ      2$
1552 006164 012737 000147 000402        MOV      #147,@#$FATAL
1553 006172 005212                       INC      (R2)
1554 006174 000000                       HALT
1555                                     : Z NOT SET
1556                                     : TO SCOPE REPLACE HALT W/ 240
1557                                     : AND REPLACE NEXT INST W/ 677
1557 006176                               2$:
1558 006176 102405                       BVS      3$
1559 006200 012737 000150 000402        MOV      #150,@#$FATAL
1560 006206 005212                       INC      (R2)
1561 006210 000000                       HALT
1562                                     : V NOT SET
1563                                     : TO SCOPE REPLACE HALT W/ 240
1564                                     : AND REPLACE NEXT INST W/ 671
1564 006212                               3$:
1565 006212 103405                       BCS      4$
1566 006214 012737 000151 000402        MOV      #151,@#$FATAL
1567 006222 005212                       INC      (R2)
1568 006224 000000                       HALT
1569                                     : C NOT SET
1570                                     : TO SCOPE REPLACE HALT W/ 240
1571                                     : AND REPLACE NEXT INST W/ 663
1571 006226 106767 005054                4$:  MFPS   STATUS
1572 006232 016706 005050                MOV     STATUS,SP
1573 006236 042706 000017                BIC     #17,SP
1574 006242 022706 000340                CMP     #340,SP
1575 006246 001405                       BEQ     RST2
1576 006250                               RSTP2:
1577 006250 012737 000152 000402        MOV     #152,@#$FATAL
1578 006256 005212                       INC     (R2)
1579 006260 000000                       HALT
1580                                     : MOVE TO MAILBOX # ***** 152 *****
1581                                     : SET MSGTYP TO FATAL ERROR
1582                                     : PRIORITY WAS CHANGED,OR WRONG $TESTN
1583 006262 012767 000016 171524 RST2:  MOV     #16,14
1584 006270 005067 171522                 CLR     16

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1585
1586 ;PDP-11 ILLEGAL AND ADDRESS INSTRUCTION TEST
1587 ;ALL INSTRUCTIONS THAT ARE RESERVED
1588 ;SHOULD TRAP TO LOCATION 4, AND THE
1589 ;PC THAT POINTS TO THE TRAPPING INSTRUCTION
1590 ;SHOULD BE PLACED ON THE STACK
1591
1592 ;*****
1593 ;TEST 40 THAT A TRAP OCCURS ON AN ILLEGAL INSTRUCTION
1594 ;*****
1595 006274 005237 000404 TST40: INC @#$TESTN ;UPDATE TEST NUMBER
1596 006300 022737 000040 000404 CMP #40,@#$TESTN ;SEQUENCE ERROR?
1597 006306 001006 BNE TST41-12 ;BR TO ERROR HALT ON SEQ ERROR
1598 006310 012706 000500 MOV #BUFF,SP ;STACK POINTER SETUP
1599 006314 012767 006336 171462 MOV #RETA5,RTRAP5 ;RETURN LOCATION
1600 006322 000100 JMP %0 ;ILLEGAL INSTRUCTION, SHOULD TRAP
1601 006324 012737 000153 000402 MOV #153,@#$FATAL ;MOVE TO MAILBOX # ***** 153 *****
1602 006332 005212 INC (R2) ;SET MSGTYP TO FATAL ERROR
1603 006334 000000 HALT ;DID NOT TRAP,OR WRONG $TESTN
1604 ; TO SCOPE REPLACE HALT W/ 240
1605 ; AND REPLACE NEXT INST W/ 764
1606 006336 RETA5:
1607 ;*****
1608 ;TEST 41 DECREMENT OF STACK POINTER ON A TRAP OPERATION
1609 ;*****
1610 006336 005237 000404 TST41: INC @#$TESTN ;UPDATE TEST NUMBER
1611 006342 022737 000041 000404 CMP #41,@#$TESTN ;SEQUENCE ERROR?
1612 006350 001011 BNE TST42-12 ;BR TO ERROR HALT ON SEQ ERROR
1613 006352 012706 000500 MOV #BUFF,SP ;STACK POINTER SETUP
1614 006356 012767 006366 171420 MOV #RETB5,RTRAP5 ;RETURN POINTER
1615 006364 000100 JMP %0 ;RESERVED INSTRUCTION
1616 006366 020627 000474 RETB5: CMP SP,#BUFF-4 ;TEST DECREMENT OF SP
1617 006372 001405 BEQ TST42
1618 006374 012737 000154 000402 MOV #154,@#$FATAL ;MOVE TO MAILBOX # ***** 154 *****
1619 006402 005212 INC (R2) ;SET MSGTYP TO FATAL ERROR
1620 006404 000000 HALT ;NOT DECREMENTED TWO WORDS,OR WRONG $TESTN
1621 ; TO SCOPE REPLACE HALT W/ 240
1622 ; AND REPLACE NEXT INST W/ 761
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006406 005237 000404
006412 022737 000042 000404
006420 001012
006422 012706 000500
006426 012767 006436 171350
006434 000100
006436 022767 006436 172030
006444 001405
006446 012737 000155 000402
006454 005212
006456 000000

006460 005237 000404
006464 022737 000043 000404
006472 001044
006474 012706 000500
006500 012767 006522 171276
006506 005067 004574
006512 106467 004570
006516 000257
006520 000100
006522 026727 171750 000000
006530 001405
006532 012737 000156 000402
006540 005212
006542 000000

006544 012706 000500
006550 012767 006574 171226
006556 012767 000357 004522
006564 106467 004516
006570 000277
006572 000100
006574 026727 171676 000357
006602 001405
006604 012737 000157 000402
006612 005212
006614 000000

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*****  
:TEST 42 THAT PROPER P.C. IS SAVED  
*****  
TST42: INC @#$TESTN ;UPDATE TEST NUMBER  
CMP #42,@#$TESTN ;SEQUENCE ERROR?  
BNE TST43-12 ;BR TO ERROR HALT ON SEQ ERROR  
MOV #BUFF,SP ;STACK POINTER SETUP  
MOV #RETC5,RTRAP5 ;RETURN FROM TRAP POINTER  
JMP %0 ;TRAP ON THIS INSTRUCTION  
RETC5: CMP #,BUFF-4 ;CHECK FOR INCREMENTED P.C.  
BEQ TST43  
MOV #155,@#$FATAL ;MOVE TO MAILBOX # ***** 155 *****  
INC (R2) ;SET MSGTYP TO FATAL ERROR  
HALT ;INCORRECT P.C.,OR WRONG $TESTN  
; TO SCOPE REPLACE HALT W/ 240  
; AND REPLACE NEXT INST W/ 760  
*****  
:TEST 43 THAT 'OLD' STATUS AND PRIORITY ARE PLACED ON STACK  
*****  
TST43: INC @#$TESTN ;UPDATE TEST NUMBER  
CMP #43,@#$TESTN ;SEQUENCE ERROR?  
BNE TST44-12 ;BR TO ERROR HALT ON SEQ ERROR  
MOV #BUFF,SP ;SET UP  
MOV #RETD5,RTRAP5 ;SET UP  
CLR STATUS ;CLEAR STATUS AND PRIORITY  
MTPS STATUS  
CCC  
JMP %0 ;TRAP  
RETD5: CMP BUFF-2,#0 ;TEST THAT OLD STATUS WENT TO STACK  
BEQ 1$  
MOV #156,@#$FATAL ;MOVE TO MAILBOX # ***** 156 *****  
INC (R2) ;SET MSGTYP TO FATAL ERROR  
HALT ;INCORRECT STATUS  
; TO SCOPE REPLACE HALT W/ 240  
; AND REPLACE NEXT INST W/ 753  
1$: MOV #BUFF,SP ;SET UP  
MOV #RETE5,RTRAP5 ;SET UP  
MOV #357,STATUS ;SET PRIORITY  
MTPS STATUS  
SCC ;SET CC  
JMP %0 ;TRAP  
RETE5: CMP BUFF-2,#357 ;COMPARES STATUS ON STACK  
BEQ TST44  
MOV #157,@#$FATAL ;MOVE TO MAILBOX # ***** 157 *****  
INC (R2) ;SET MSGTYP TO FATAL ERROR  
HALT ;INCORRECT STATUS ON STACK,OR WRONG $TESTN  
; TO SCOPE REPLACE HALT W/ 240  
; AND REPLACE NEXT INST W/ 726
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1672
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1676 006616 005237 000404
1677 006622 022737 000044 000404
1678 006630 001123
1679 006632 012706 000500
1680 006636 012767 006652 171140
1681 006644 005067 171136
1682 006650 000100
1683 006652
1684 006652 100005
1685 006654 012737 000160 000402
1686 006662 005212
1687 006664 000000
1688
1689
1690 006666
1691 006666 001005
1692 006670 012737 000161 000402
1693 006676 005212
1694 006700 000000
1695
1696
1697 006702
1698 006702 102005
1699 006704 012737 000162 000402
1700 006712 005212
1701 006714 000000
1702
1703
1704 006716
1705 006716 103005
1706 006720 012737 000163 000402
1707 006726 005212
1708 006730 000000
1709
1710
1711 006732 106767 004350
1712 006736 032767 000357 004342
1713 006744 001405
1714 006746 012737 000164 000402
1715 006754 005212
1716 006756 000000
1717
1718
1719 006760 012706 000500
1720 006764 012767 007002 171012
1721 006772 012767 000357 171006
1722 007000 000100
1723 007002
1724 007002 100405
1725 007004 012737 000165 000402
1726 007012 005212
1727 007014 000000
```

:TEST 44 THAT 'NEW' STATUS IS CORRECT

TST44: INC @#\$TESTN ;UPDATE TEST NUMBER
CMP #44,@#\$TESTN ;SEQUENCE ERROR?
BNE TST45-12 ;BR TO ERROR HALT ON SEQ ERROR
MOV #BUFF,SP
MOV #RETF5,RTRAP5
CLR RTRAP5+2 ;CLEAR FUTURE PRIORITY AND CC
JMP %0

RETF5: BPL 1\$
MOV #160,@#\$FATAL ;MOVE TO MAILBOX # ***** 160 *****
INC (R2) ;SET MSGTYP TO FATAL ERROR
HALT ;C NOT CLEARED
; TO SCOPE REPLACE HALT W/ 240
; AND REPLACE NEXT INST W/ 761

1\$: BNE 2\$
MOV #161,@#\$FATAL ;MOVE TO MAILBOX # ***** 161 *****
INC (R2) ;SET MSGTYP TO FATAL ERROR
HALT ;Z NOT CLEARED
; TO SCOPE REPLACE HALT W/ 240
; AND REPLACE NEXT INST W/ 753

2\$: BVC 3\$
MOV #162,@#\$FATAL ;MOVE TO MAILBOX # ***** 162 *****
INC (R2) ;SET MSGTYP TO FATAL ERROR
HALT ;V NOT CLEARED
; TO SCOPE REPLACE HALT W/ 240
; AND REPLACE NEXT INST W/ 745

3\$: BCC 4\$
MOV #163,@#\$FATAL ;MOVE TO MAILBOX # ***** 163 *****
INC (R2) ;SET MSGTYP TO FATAL ERROR
HALT ;C NOT CLEARED
; TO SCOPE REPLACE HALT W/ 240
; AND REPLACE NEXT INST W/ 737

4\$: MFPS STATUS
BIT #357,STATUS ;TEST PRIORITY
BEQ 5\$
MOV #164,@#\$FATAL ;MOVE TO MAILBOX # ***** 164 *****
INC (R2) ;SET MSGTYP TO FATAL ERROR
HALT ;PRIORITY NOT ZERO
; TO SCOPE REPLACE HALT W/ 240
; AND REPLACE NEXT INST W/ 724

5\$: MOV #BUFF,SP
MOV #RETF5,RTRAP5
MOV #357,RTRAP5+2 ;SET NEW 'CC' AND PRIORITY
JMP %0 ;TRAP HERE

RETF5: BMI 1\$
MOV #165,@#\$FATAL ;MOVE TO MAILBOX # ***** 165 *****
INC (R2) ;SET MSGTYP TO FATAL ERROR
HALT ;N NOT SET

```
1728 ; TO SCOPE REPLACE HALT W/ 240
1729 ; AND REPLACE NEXT INST W/ 705
1730 007016 1$:
1731 007016 001405 BEQ 2$
1732 007020 012737 000166 000402 MOV #166,@#$FATAL ;MOVE TO MAILBOX # ***** 166 *****
1733 007026 005212 INC (R2) ;SET MSGTYP TO FATAL ERROR
1734 007030 000000 HALT ;Z NOT SET
1735 ; TO SCOPE REPLACE HALT W/ 240
1736 ; AND REPLACE NEXT INST W/ 677
1737 007032 2$:
1738 007032 102405 BVS 3$
1739 007034 012737 000167 000402 MOV #167,@#$FATAL ;MOVE TO MAILBOX # ***** 167 *****
1740 007042 005212 INC (R2) ;SET MSGTYP TO FATAL ERROR
1741 007044 000000 HALT ;V NOT SET
1742 ; TO SCOPE REPLACE HALT W/ 240
1743 ; AND REPLACE NEXT INST W/ 671
1744 007046 3$:
1745 007046 103405 BCS 4$
1746 007050 012737 000170 000402 MOV #170,@#$FATAL ;MOVE TO MAILBOX # ***** 170 *****
1747 007056 005212 INC (R2) ;SET MSGTYP TO FATAL ERROR
1748 007060 000000 HALT ;C NOT SET
1749 ; TO SCOPE REPLACE HALT W/ 240
1750 ; AND REPLACE NEXT INST W/ 663
1751 007062 106767 004220 4$: MFPS STATUS
1752 007066 016706 004214 MOV STATUS,SP
1753 007072 022706 000357 CMP #357,SP
1754 007076 001405 BEQ TST45
1755 007100 012737 000171 000402 MOV #171,@#$FATAL ;MOVE TO MAILBOX # ***** 171 *****
1756 007106 005212 INC (R2) ;SET MSGTYP TO FATAL ERROR
1757 007110 000000 HALT ;PRIORITY WAS CHANGED,OR WRONG $TESTN
1758 ; TO SCOPE REPLACE HALT W/ 240
1759 ; AND REPLACE NEXT INST W/ 647
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1792
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1796 007224 005237 000404
1797 007230 022737 000047 000404
1798 007236 001012
1799 007240 012706 000500
1800 007244 012767 007254 170532
1801 007252 004000
1802 007254 022767 007254 171212
1803 007262 001405
1804 007264 012737 000174 000402
1805 007272 005212
1806 007274 000000
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1813 007276 005237 000404
1814 007302 022737 000050 000404
1815 007310 001044
1816 007312 012706 000500
1817 007316 012767 007340 170460
1818 007324 005067 003756
1819 007330 106467 003752
1820 007334 000257
1821 007336 004000
1822 007340 026727 171132 000000
1823 007346 001405
1824 007350 012737 000175 000402
1825 007356 005212
1826 007360 000000
1827
1828
1829 007362 012706 000500
1830 007366 012767 007412 170410
1831 007374 012767 000357 003704
1832 007402 106467 003700
1833 007406 000277
1834 007410 004000
1835 007412 026727 171060 000357
1836 007420 001405
1837 007422 012737 000176 000402
1838 007430 005212
1839 007432 000000
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1846 007434 005237 000404
1847 007440 022737 000051 000404

;*****
;TEST 47 THAT PROPER P.C. IS SAVED
;*****
TST47: INC @#$TESTN ;UPDATE TEST NUMBER
        CMP #47,@#$TESTN ;SEQUENCE ERROR?
        BNE TST50-12 ;BR TO ERROR HALT ON SEQ ERROR
        MOV #BUFF,SP ;STACK POINTER SETUP
        MOV #RETK,RTRAP5 ;RETURN FROM TRAP POINTER
INSTK: JSR %0,%0 ;TRAP ON THIS INSTRUCTION
RETK: CMP #INSTK+2,BUFF-4 ;CHECK FOR INCREMENTED P.C.
        BEQ TST50
        MOV #174,@#$FATAL ;MOVE TO MAILBOX # ***** 174 *****
        INC (R2) ;SET MSGTYP TO FATAL ERROR
        HALT ;INCORRECT P.C.,OR WRONG $TESTN
        ; TO SCOPE REPLACE HALT W/ 240
        ; AND REPLACE NEXT INST W/ 760

;*****
;TEST 50 THAT 'OLD' STATUS AND PRIORITY ARE PLACED ON STACK
;*****
TST50: INC @#$TESTN ;UPDATE TEST NUMBER
        CMP #50,@#$TESTN ;SEQUENCE ERROR?
        BNE TST51-12 ;BR TO ERROR HALT ON SEQ ERROR
        MOV #BUFF,SP ;SET UP
        MOV #RETL,RTRAP5 ;SET UP
        CLR STATUS ;CLEAR STATUS AND PRIORITY
        MTPS STATUS
        CCC
        JSR %0,%0 ;TRAP
RETL: CMP BUFF-2,#0 ;TEST THAT OLD STATUS WENT TO STACK
        BEQ 1$
        MOV #175,@#$FATAL ;MOVE TO MAILBOX # ***** 175 *****
        INC (R2) ;SET MSGTYP TO FATAL ERROR
        HALT ;INCORRECT STATUS
        ; TO SCOPE REPLACE HALT W/ 240
        ; AND REPLACE NEXT INST W/ 753
1$: MOV #BUFF,SP ;SET UP
        MOV #RETM,RTRAP5 ;SET UP
        MOV #357,STATUS ;SET PRIORITY
        MTPS STATUS
        SCC ;SET CC
        JSR %0,%0 ;TRAP
RETM: CMP BUFF-2,#357 ;COMPARES STATUS ON STACK
        BEQ TST51
        MOV #176,@#$FATAL ;MOVE TO MAILBOX # ***** 176 *****
        INC (R2) ;SET MSGTYP TO FATAL ERROR
        HALT ;INCORRECT STATUS ON STACK,OR WRONG $TESTN
        ; TO SCOPE REPLACE HALT W/ 240
        ; AND REPLACE NEXT INST W/ 726

;*****
;TEST 51 THAT 'NEW' STATUS IS CORRECT
;*****
TST51: INC @#$TESTN ;UPDATE TEST NUMBER
        CMP #51,@#$TESTN ;SEQUENCE ERROR?
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1848	007446	001122			BNE	STP1		:BR TO ERROR HALT ON SEQ ERROR
1849	007450	012706	000500		MOV	#BUFF,SP		
1850	007454	012767	007470	170322	MOV	#RETN,RTRAP5		
1851	007462	005067	170320		CLR	RTRAP5+2		:CLEAR FUTURE PRIORITY AND CC
1852	007466	004000			JSR	%0,%0		
1853	007470			RETN:				:TEST FOR 'C' CLEARED
1854	007470	100005			BPL	1\$		
1855	007472	012737	000177	000402	MOV	#177,@#\$FATAL		:MOVE TO MAILBOX # ***** 177 *****
1856	007500	005212			INC	(R2)		:SET MSGTYP TO FATAL ERROR
1857	007502	000000			HALT			:C NOT CLEARED
1858								: TO SCOPE REPLACE HALT W/ 240
1859								: AND REPLACE NEXT INST W/ 761
1860	007504			1\$:				
1861	007504	001005			BNE	2\$		
1862	007506	012737	000200	000402	MOV	#200,@#\$FATAL		:MOVE TO MAILBOX # ***** 200 *****
1863	007514	005212			INC	(R2)		:SET MSGTYP TO FATAL ERROR
1864	007516	000000			HALT			:Z NOT CLEARED
1865								: TO SCOPE REPLACE HALT W/ 240
1866								: AND REPLACE NEXT INST W/ 753
1867	007520			2\$:				
1868	007520	102005			BVC	3\$		
1869	007522	012737	000201	000402	MOV	#201,@#\$FATAL		:MOVE TO MAILBOX # ***** 201 *****
1870	007530	005212			INC	(R2)		:SET MSGTYP TO FATAL ERROR
1871	007532	000000			HALT			:V NOT CLEARED
1872								: TO SCOPE REPLACE HALT W/ 240
1873								: AND REPLACE NEXT INST W/ 745
1874	007534			3\$:				
1875	007534	103005			BCC	4\$		
1876	007536	012737	000202	000402	MOV	#202,@#\$FATAL		:MOVE TO MAILBOX # ***** 202 *****
1877	007544	005212			INC	(R2)		:SET MSGTYP TO FATAL ERROR
1878	007546	000000			HALT			:C NOT CLEARED
1879								: TO SCOPE REPLACE HALT W/ 240
1880								: AND REPLACE NEXT INST W/ 737
1881	007550	106767	003532		MFPS	STATUS		
1882	007554	016700	003526	4\$:	MOV	STATUS,%0		:TEMP STORAGE
1883	007560	001405			BEQ	5\$		
1884	007562	012737	000203	000402	MOV	#203,@#\$FATAL		:MOVE TO MAILBOX # ***** 203 *****
1885	007570	005212			INC	(R2)		:SET MSGTYP TO FATAL ERROR
1886	007572	000000			HALT			:PRIORITY NOT ZERO
1887								: TO SCOPE REPLACE HALT W/ 240
1888								: AND REPLACE NEXT INST W/ 725
1889	007574	012706	000500		MOV	#BUFF,SP		
1890	007600	012767	007616	170176	MOV	#RETO,RTRAP5		
1891	007606	012767	000357	170172	MOV	#357,RTRAP5+2		:SET NEW 'CC' AND PRIORITY
1892	007614	004000			JSR	%0,%0		:TRAP HERE
1893	007616			RETO:				
1894	007616	100405			BMI	1\$		
1895	007620	012737	000204	000402	MOV	#204,@#\$FATAL		:MOVE TO MAILBOX # ***** 204 *****
1896	007626	005212			INC	(R2)		:SET MSGTYP TO FATAL ERROR
1897	007630	000000			HALT			:N NOT SET
1898								: TO SCOPE REPLACE HALT W/ 240
1899								: AND REPLACE NEXT INST W/ 706
1900	007632			1\$:				
1901	007632	001405			BEQ	2\$		
1902	007634	012737	000205	000402	MOV	#205,@#\$FATAL		:MOVE TO MAILBOX # ***** 205 *****
1903	007642	005212			INC	(R2)		:SET MSGTYP TO FATAL ERROR


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1904 007644 000000          HALT          ;Z NOT SET
1905                                     ; TO SCOPE REPLACE HALT W/ 240
1906                                     ; AND REPLACE NEXT INST W/ 700
1907 007646                2$:          BVS          3$
1908 007646 102405          MOV          #206,@#$FATAL ;MOVE TO MAILBOX # ***** 206 *****
1909 007650 012737 000206 000402      INC          (R2)      ;SET MSGTYP TO FATAL ERROR
1910 007656 005212          HALT          ;V NOT SET
1911 007660 000000          ; TO SCOPE REPLACE HALT W/ 240
1912                                     ; AND REPLACE NEXT INST W/ 672
1913
1914 007662                3$:          BCS          4$
1915 007662 103405          MOV          #207,@#$FATAL ;MOVE TO MAILBOX # ***** 207 *****
1916 007664 012737 000207 000402      INC          (R2)      ;SET MSGTYP TO FATAL ERROR
1917 007672 005212          HALT          ;C NOT SET
1918 007674 000000          ; TO SCOPE REPLACE HALT W/ 240
1919                                     ; AND REPLACE NEXT INST W/ 664
1920
1921 007676 106767 003404      4$:          MFPS          STATUS
1922 007702 016700 003400      MOV          STATUS,%0
1923 007706 022700 000357      CMP          #357,%0
1924 007712 001405          BEQ          STPBB
1925 007714
1926 007714 012737 000210 000402      STP1:      MOV          #210,@#$FATAL ;MOVE TO MAILBOX # ***** 210 *****
1927 007722 005212          INC          (R2)      ;SET MSGTYP TO FATAL ERROR
1928 007724 000000          HALT          ;PRIORITY WAS CHANGED,OR WRONG $TESTN
1929                                     ; TO SCOPE REPLACE HALT W/ 240
1930                                     ; AND REPLACE NEXT INST W/ 650
1931 007726 012767 000006 170050      STPBB:      MOV          #6,4
1932 007734 005067 170046          CLR          6

```



```
1976  
1977  
1978  
1979  
1980 010110 005237 000404  
1981 010114 022737 000054 000404  
1982 010122 001016  
1983 010124 012706 000500  
1984 010130 012767 010150 167656  
1985 010136 012746 000020  
1986 010142 012746 010150  
1987 010146 000002  
1988  
1989 010150 022767 010150 170316 RETCT: CMP #,BUFF-4  
1990 010156 001405 BEQ TST55  
1991 010160 012737 000214 000402 MOV #214,@#$FATAL ;MOVE TO MAILBOX # ***** 214 *****  
1992 010166 005212 INC (R2) ;SET MSGTYP TO FATAL ERROR  
1993 010170 000000 HALT ;CORRECT PC WAS NOT SAVED ON STACK,OR WRONG $TESTN  
1994 ; TO SCOPE REPLACE HALT W/ 240  
1995 ; AND REPLACE NEXT INST W/ 754  
1996  
1997  
1998  
1999  
2000  
2001 010172 005237 000404  
2002 010176 022737 000055 000404  
2003 010204 001015  
2004  
2005 010206 012706 000500  
2006 010212 005001  
2007 010214 012746 000020  
2008 010220 012746 010234  
2009 010224 012767 010252 167562  
2010 010232 000006  
2011 010234 000240  
2012 010236 001405  
2013 010240 012737 000215 000402  
2014 010246 005212  
2015 010250 000000  
2016  
2017  
2018  
2019 010252  
  
:*****  
:TEST 54 FOR PROPER PC ON STACK  
:*****  
TST54: INC @#$TESTN ;UPDATE TEST NUMBER  
CMP #54,@#$TESTN ;SEQUENCE ERROR?  
BNE TST55-12 ;BR TO ERROR HALT ON SEQ ERROR  
MOV #BUFF,SP  
MOV #RETCT,RTRAP4  
MOV #20,-(SP) ;PUSH T BIT  
MOV #.+6,-(SP) ;PUSH PC  
RTI ;SET T BIT  
;TRAP HERE  
RETCT: CMP #,BUFF-4  
BEQ TST55  
MOV #214,@#$FATAL ;MOVE TO MAILBOX # ***** 214 *****  
INC (R2) ;SET MSGTYP TO FATAL ERROR  
HALT ;CORRECT PC WAS NOT SAVED ON STACK,OR WRONG $TESTN  
; TO SCOPE REPLACE HALT W/ 240  
; AND REPLACE NEXT INST W/ 754  
  
:*****  
:TEST 55 THAT RTT POPS T- BIT  
:*****  
TST55: INC @#$TESTN ;UPDATE TEST NUMBER  
CMP #55,@#$TESTN ;SEQUENCE ERROR?  
BNE TST56-12 ;BR TO ERROR HALT ON SEQ ERROR  
MOV #BUFF,SP  
CLR R1 ;CLEAR R1  
MOV #20,-(SP)  
MOV #RTT1,-(SP)  
MOV #RTT2,14  
RTT  
RTT1: NOP  
BEQ TST56  
MOV #215,@#$FATAL ;MOVE TO MAILBOX # ***** 215 *****  
INC (R2) ;SET MSGTYP TO FATAL ERROR  
HALT ;T-BIT DID NOT TRAP,OR WRONG $TESTN  
; TO SCOPE REPLACE HALT W/ 240  
; AND REPLACE NEXT INST W/ 755  
  
RTT2:
```

```
2020
2021
2022
2023
2024 010252 005237 000404
2025 010256 022737 000056 000404
2026 010264 001031
2027 010266 012705 177777
2028 010272 012706 000500
2029 010276 012746 000020
2030 010302 012746 010320
2031 010306 012767 010340 167500
2032 010314 005001
2033 010316 000006
2034 010320 005201
2035 010322 005205
2036 010324 001762
2037 010326 012737 000216 000402
2038 010334 005212
2039 010336 000000
2040
2041
2042 010340 005301
2043 010342 001407
2044 010344 005205
2045 010346 001751
2046 010350 012737 000217 000402
2047 010356 005212
2048 010360 000000
2049
2050
2051 010362
```

```
*****
:TEST 56 THAT RTT ALLOWS ONE INST. BEFORE TRAP
*****
TST56: INC @#$TESTN ;UPDATE TEST NUMBER
        CMP #56,@#$TESTN ;SEQUENCE ERROR?
        BNE TST57-12 ;BR TO ERROR HALT ON SEQ ERROR
        MOV #177777,%5
RTT5: MOV #BUFF,SP
      MOV #20,-(SP)
      MOV #RTT3,-(SP)
      MOV #RTT4,14
      CLR R1 ;CLEAR R0
      RTT ;SET T-BIT
RTT3: INC R1
      INC %5
      BEQ RTT5 ;DO THIS TEST NO MORE THAN 2 TIMES
      MOV #216,@#$FATAL ;MOVE TO MAILBOX # ***** 216 *****
      INC (R2) ;SET MSGTYP TO FATAL ERROR
      HALT ;DID NOT TRAP
        ; TO SCOPE REPLACE HALT W/ 240
        ; AND REPLACE NEXT INST W/ 752
        ;SEE IF RTT ALLOWS 1 INST.
RTT4: DEC R1
      BEQ RTT6
      INC %5 ;DO THIS TEST NO MORE THAN TWO TIMES
      BEQ RTT5
      MOV #217,@#$FATAL ;MOVE TO MAILBOX # ***** 217 *****
      INC (R2) ;SET MSGTYP TO FATAL ERROR
      HALT ;RTT DID NOT ALLOW 1 INST.,OR WRONG $TESTN
        ; TO SCOPE REPLACE HALT W/ 240
        ; AND REPLACE NEXT INST W/ 741
RTT5:
```

```
2052
2053
2054
2055
2056 010362 005237 000404
2057 010366 022737 000057 000404
2058 010374 001023
2059 010376 012706 000500
2060 010402 012746 000020
2061 010406 012746 010424
2062 010412 012767 010440 167374
2063 010420 005001
2064 010422 000002
2065 010424 005201
2066 010426 012737 000220 000402
2067 010434 005212
2068 010436 000000
2069
2070
2071 010440 005701
2072
2073 010442 001405
2074 010444 012737 000221 000402
2075 010452 005212
2076 010454 000000
2077
2078

;*****
;TEST 57 THAT RTI DOES NOT ALLOW 1 INST.
;*****
TST57: INC @#$TESTN ;UPDATE TEST NUMBER
        CMP #57,@#$TESTN ;SEQUENCE ERROR?
        BNE TST60-12 ;BR TO ERROR HALT ON SEQ ERROR
        MOV #BUFF,SP
        MOV #20,-(SP)
        MOV #RTI1,-(SP)
        MOV #RTI2,14
        CLR R1
        RTI ;SET T-BIT
RTI1: INC R1 ;RTI SHOULD NOT ALLOW THIS
        MOV #220,@#$FATAL ;MOVE TO MAILBOX # ***** 220 *****
        INC (R2) ;SET MSGTYP TO FATAL ERROR
        HALT ;T- BIT DID NOT CAUSE TRAP
        ; TO SCOPE REPLACE HALT W/ 240
        ; AND REPLACE NEXT INST W/ 756
RTI2: TST R1
        BEQ TST60 ;RTI SHOULD NOT ALLOW 1 INST. BEFORE TRAP
        MOV #221,@#$FATAL ;MOVE TO MAILBOX # ***** 221 *****
        INC (R2) ;SET MSGTYP TO FATAL ERROR
        HALT ;RTI DID ALLOW 1 INST. BEFORE TRAP,OR WRONG $TESTN
        ; TO SCOPE REPLACE HALT W/ 240
        ; AND REPLACE NEXT INST W/ 747
```

```
2079
2080
2081
2082
2083 010456 005237 000404
2084 010462 022737 000060 000404
2085 010470 001033
2086
2087
2088 010472 012705 177777
2089 010476 012706 000500
2090 010502 012767 010554 167304
2091 010510 005027 000016
2092 010514 005027 000022
2093 010520 012767 010572 167272
2094 010526 012746 000020
2095 010532 012746 010540
2096 010536 000006
2097 010540 000004
2098 010542 012737 000222 000402
2099 010550 005212
2100 010552 000000
2101
2102
2103 010554 005205
2104 010556 001747
2105 010560
2106 010560 012737 000223 000402
2107 010566 005212
2108 010570 000000
2109
2110
2111 010572 012767 000016 167214
2112 010600 012767 000022 167212

:*****
:TEST 60 TRAP ON TRAP
:*****
TST60: INC @#$TESTN ;UPDATE TEST NUMBER
        CMP #60,@#$TESTN ;SEQUENCE ERROR?
        BNE TRACE ;BR TO ERROR HALT ON SEQ ERROR
;TEST THAT TRACE BIT TRAPS ARE INHIBITED ON TRAP INST

TRPTRP: MOV #177777,%5
        MOV #BUFF,%6
        MOV #TRACE1,14 ;TRACE TRAP
        CLR #16 ;
        CLR #22 ;
        MOV #TONT1,20 ;IOT TRAP
        MOV #20,-(SP) ;PUSH T BIT
        MOV #.+6,-(SP) ;PUSH PC
        RTT ;SET T BIT
        IOT ;TRAP, NEW STATUS HAVE TRACE RESET
        MOV #222,@#$FATAL ;MOVE TO MAILBOX # ***** 222 *****
        INC (R2) ;SET MSGTYP TO FATAL ERROR
        HALT ;NO TRAP OCCURRED
        ; TO SCOPE REPLACE HALT W/ 240
        ; AND REPLACE NEXT INST W/ 746
TRACE1: INC %5 ;IF FAILED TRY THIS TEST TWICE BUT NO MORE
        BEQ TRPTRP
TRACE: MOV #223,@#$FATAL ;MOVE TO MAILBOX # ***** 223 *****
        INC (R2) ;SET MSGTYP TO FATAL ERROR
        HALT ;IOT SHOULD HAVE CLEARED THE T BIT,OR WRONG $TESTN
        ; TO SCOPE REPLACE HALT W/ 240
        ; AND REPLACE NEXT INST W/ 737
TONT1: MOV #16,14
        MOV #22,20
```

```
2113
2114
2115 :*****
2116 :TEST 61 THAT THE TRACE BIT WILL CAUSE A TRAP
2117 :*****
2117 010606 005237 000404 TST61: INC @#$TESTN ;UPDATE TEST NUMBER
2118 010612 022737 000061 000404 CMP #61,@#$TESTN ;SEQUENCE ERROR?
2119 010620 001026 BNE TST62-12 ;BR TO ERROR HALT ON SEQ ERROR
2120 010622 012706 000500 MOV #BUFF,%6 ;SET UP STACK POINTER
2121 010626 012767 010666 167160 MOV #TRC1,14 ;TRACE TRAP RETURN
2122 010634 005067 167156 CLR 16
2123 010640 012746 000020 MOV #20,-(SP) ;PUSH T BIT
2124 010644 012746 010652 MOV #.+6,-(SP) ;PUSH PC
2125 010650 000002 RTI ;SET T BIT
2126 010652 000240 NOP
2127 010654 012737 000224 000402 MOV #224,@#$FATAL ;MOVE TO MAILBOX # ***** 224 *****
2128 010662 005212 INC (R2) ;SET MSGTYP TO FATAL ERROR
2129 010664 000000 HALT ;DO NOT TRAP
2130 ; TO SCOPE REPLACE HALT W/ 240
2131 ; AND REPLACE NEXT INST W/ 755
2132 010666 036727 167604 000020 TRC1: BIT BUFF-2,#20 ;CHECK FOR T BIT ON STACK
2133 010674 001005 BNE TST62
2134 010676 012737 000225 000402 MOV #225,@#$FATAL ;MOVE TO MAILBOX # ***** 225 *****
2135 010704 005212 INC (R2) ;SET MSGTYP TO FATAL ERROR
2136 010706 000000 HALT ;T BIT NOT SAVED ON STACKED,OR WRONG $TESTN
2137 ; TO SCOPE REPLACE HALT W/ 240
2138 ; AND REPLACE NEXT INST W/ 744
2139 :*****
2140 :TEST 62 THAT AN RTI POPS THE T BIT
2141 :*****
2142 010710 005237 000404 TST62: INC @#$TESTN ;UPDATE TEST NUMBER
2143 010714 022737 000062 000404 CMP #62,@#$TESTN ;SEQUENCE ERROR?
2144 010722 001020 BNE TST63-12 ;BR TO ERROR HALT ON SEQ ERROR
2145 010724 012706 000500 MOV #BUFF,%6 ;SET UP THE STACK
2146 010730 012746 000020 MOV #20,-(6) ;FUTURE T BIT ON STACK
2147 010734 012746 010750 MOV #TRC2,-(6) ;RTI RETURN
2148 010740 012767 010764 167046 MOV #TRC3,14 ;TRACE TRAP INTERRUPT POINTER
2149 010746 000002 RTI
2150
2151 010750 000240 TRC2: NOP ;TRACE IS SET SHOULD TRAP TO 14
2152 010752 012737 000226 000402 MOV #226,@#$FATAL ;MOVE TO MAILBOX # ***** 226 *****
2153 010760 005212 INC (R2) ;SET MSGTYP TO FATAL ERROR
2154 010762 000000 HALT ;DID NOT TRACE TRAP,OR WRONG $TESTN
2155 ; TO SCOPE REPLACE HALT W/ 240
2156 ; AND REPLACE NEXT INST W/ 757
2157
2158 010764 012767 000016 167022 TRC3: MOV #16,14
2159 010772 005067 167020 CLR 16
```

```

2160
2161
2162
2163
2164 010776 005237 000404
2165 011002 022737 000063 000404
2166 011010 001055
2167 011012 032767 000001 167400
2168 011020 001403
2169 011022 005767 167360
2170 011026 001055
2171 011030
2172 011030 105737 177564
2173 011034 100375
2174 011036 012706 000500
2175 011042 012767 000340 002236
2176 011050 106467 002232
2177 011054 012767 011132 167002
2178 011062 012767 000100 166474
2179 011070 012704 000120
2180 011074 077401
2181 011076 012767 011144 166730
2182 011104 012767 011156 166752
2183 011112 012767 000340 166716
2184 011120 005067 002162
2185 011124 106467 002156
2186 011130 104400
2187 011132
2188 011132 012737 000227 000402
2189 011140 005212
2190 011142 000000
2191
2192
2193 011144
2194 011144 012737 000230 000402
2195 011152 005212
2196 011154 000000
2197
2198
2199 011156 005067 166654

:*****
:TEST 63 THAT A PENDING INTERRUPT OCCURS BEFORE TRAP
:*****
TST63: INC @#$TESTN ;UPDATE TEST NUMBER
        CMP #63,@#$TESTN ;SEQUENCE ERROR?
        BNE TR1 ;BR TO ERROR HALT ON SEQ ERROR
        BIT #1,$ENV ;CHECK IF ON APT
        BEQ NOAPT ;IF NOT ON APT
        TST $PASS ;CHECK IF ON FIRST PASS
        BNE TST64 ;IF NOT FIRST PASS

NOAPT: TSTB @#TPS
        BPL -4
        MOV #BUFF,%6 ;HIGHEST PRIORITY LEVEL
        MTPS STATUS
        MOV #TR0,64
        MOV #100,TTCSR ;INTERRUPT FOR TTY PUNCH/PRINTER
        MOV #120,R4 ;SET UP DELAY IN CASE ON PDT-11 (IT TAKES LONG
        SOB R4 ;TIME FOR INTERRUPT REQUEST TO BE SEEN BY CPU)
        MOV #TR1,34 ;TRAP VECTOR
        MOV #TR2,64 ;TTY VECTOR
        MOV #340,36 ;IF TRAP TRAPS, MOVE 340 TO PRIORITY
        CLR STATUS ;SHOULD TRAP AT END OF CLR INST
        MTPS STATUS
        TRAP ;TTY INTERRUPT SHOULD OVERRIDE TRAP

TRO: MOV #227,@#$FATAL ;MOVE TO MAILBOX # ***** 227 *****
      INC (R2) ;SET MSGTYP TO FATAL ERROR
      HALT ;TTY SHOULDN'T HAVE INTERRUPTED
          ; TO SCOPE REPLACE HALT W/ 240
          ; AND REPLACE NEXT INST W/ 722

TR1: MOV #230,@#$FATAL ;MOVE TO MAILBOX # ***** 230 *****
      INC (R2) ;SET MSGTYP TO FATAL ERROR
      HALT ;INTERRUPT DID NOT OCCUR FIRST,OR WRONG $TESTN
          ; TO SCOPE REPLACE HALT W/ 240
          ; AND REPLACE NEXT INST W/ 715

TR2: CLR 36
    
```


2200
2201
2202
2203
2204
2205
2206
2207
2208
2209
2210
2211
2212
2213
2214
2215
2216
2217
2218
2219
2220
2221
2222
2223
2224
2225
2226
2227
2228
2229
2230
2231
2232
2233
2234
2235
2236

011162 005237 000404
011166 022737 000064 000404
011174 001045
011176 032767 000001 167214
011204 001403
011206 005767 167174
011212 001060
011214
011214 042767 000100 166342
011222 012706 000500
011226 012767 000340 002052
011234 106467 002046
011240 012767 000100 166316
011246 012767 011306 166560
011254 012767 011322 166602
011262 012767 011310 166530
011270 012767 000340 166524
011276 012704 000120
011302 077401
011304 104400
011306 000004
011310
011310 012737 000231 000402
011316 005212
011320 000000
011322 005067 166474
011326 005067 166534
011332 012767 000066 166524
011340 012767 000036 166466
011346 012767 000022 166444

```
*****  
:TEST 64 THAT PENDING INTERRUPT OCCURS BETWEEN TRAPS  
*****  
TST64: INC @#$TESTN ;UPDATE TEST NUMBER  
CMP #64,@#$TESTN ;SEQUENCE ERROR?  
BNE TR5 ;BR TO ERROR HALT ON SEQ ERROR  
BIT #1,$ENV ;CHECK IF ON APT  
BEQ NOAPT1 ; IF NOT  
TST $PASS ; CHECK IF ON FIRST PASS  
BNE TST65 ; IF NOT  
  
NOAPT1: BIC #100,TTCSR  
MOV #BUFF,%6  
MOV #340,STATUS  
MTPS STATUS  
MOV #100,TTCSR  
MOV #TR3,34 ;TRAP  
MOV #TR4,64 ;TTY OUTPUT  
MOV #TR5,20 ;IOT  
MOV #340,22 ;IOT PRIORITY  
MOV #120,R4 ;SET UP DELAY IN CASE ON PDT-11 (IT TAKES LONG  
SOB R4,.. ;TIME FOR INTERRUPT REQUEST TO BE SEEN BY CPU)  
TRAP ;THE ACT OF TRAPPING LOWER PRIORITY  
IOT ;INTERRUPT SHOULD OCCUR IN PLACE OF IOT TRAP  
  
TR3: IOT  
TR5: MOV #231,@#$FATAL ;MOVE TO MAILBOX # ***** 231 *****  
INC (R2) ;SET MSGTYP TO FATAL ERROR  
HALT ;NO INTERRUPT BETWEEN TRAPS,OR WRONG $TESTN  
; TO SCOPE REPLACE HALT W/ 240  
; AND REPLACE NEXT INST W/ 725  
  
TR4: CLR 22 ;CLR IOT PRIORITY  
CLR 66  
MOV #66,64  
MOV #36,34  
MOV #22,20
```

2237
2238
2239
2240
2241
2242
2243
2244
2245
2246
2247
2248
2249
2250
2251
2252
2253
2254
2255
2256
2257
2258
2259
2260
2261
2262
2263

011354 005237 000404
011360 022737 000065 000404
011366 001031
011370 106427 000340
011374 012767 000100 166162
011402 012767 000100 166150
011410 000005
011412 012704 000014
011416 077401
011420 032767 000100 166136
011426 001405
011430 012737 000232 000402
011436 005212
011440 000000

011442 032767 000100 166110 1\$:
011450 001405
011452 012737 000233 000402
011460 005212
011462 000000

```
*****  
:TEST 65 THAT 'RESET' GOES TO OUTSIDE WORLD  
*****  
TST65: INC @#$TESTN ;UPDATE TEST NUMBER  
CMP #65,@#$TESTN ;SEQUENCE ERROR?  
BNE TST66-12 ;BR TO ERROR HALT ON SEQ ERROR  
MTPS #340  
MOV #100,TTCSR ;SET INTERRUPT ENABLE  
MOV #100,TRCSR ;SET INTERRUPT ENABLE  
RESET ;SHOULD CLEAR INTERRUPT ENABLE  
MOV #14,R4 ;DELAY AFTER THE RESET IN CASE ON PDT-11 TO  
SOB R4 ;ALLOW 8085 TO BE READY FOR I/O PAGE REFERENCE  
BIT #100,TTCSR ;TEST FOR CLEAR  
BEQ 1$  
MOV #232,@#$FATAL ;MOVE TO MAILBOX # ***** 232 *****  
INC (R2) ;SET MSGTYP TO FATAL ERROR  
HALT ;RESET FAILED TO CLEAR TTCSR  
 ; TO SCOPE REPLACE HALT W/ 240  
 ; AND REPLACE NEXT INST W/ 752  
BIT #100,TRCSR ;TEST FOR CLEAR  
BEQ TST66  
MOV #233,@#$FATAL ;MOVE TO MAILBOX # ***** 233 *****  
INC (R2) ;SET MSGTYP TO FATAL ERROR  
HALT ;RESET FAILED TO CLEAR TRCSR,OR WRONG $TESTN  
 ; TO SCOPE REPLACE HALT W/ 240  
 ; AND REPLACE NEXT INST W/ 741
```

```

2264
2265
2266
2267
2268 011464 005237 000404
2269 011470 022737 000066 000404
2270 011476 001014
2271 011500 012706 000500
2272 011504 012767 011542 166302
2273 011512 012746 000020
2274 011516 012746 011524
2275 011522 000006
2276 011524 000005
2277 011526 000005
2278 011530
2279 011530 012737 000234 000402
2280 011536 005212
2281 011540 000000
2282
2283
2284 011542 005067 001540
2285 011546 106467 001534
2286 011552 012767 000016 166234
2287 011560 005067 166232

:*****
:TEST 66 THAT RESET HAS NO EFFECT ON THE TRACE TRAP
:*****
TST66:  INC @#$TESTN      ;UPDATE TEST NUMBER
        CMP #66,@#$TESTN ;SEQUENCE ERROR?
        BNE RSTP3        ;BR TO ERROR HALT ON SEQ ERROR
        MOV #BUFF,%6     ;SET STACK
        MOV #RESET2,14   ;SET UP TRACE VECTOR
        MOV #20,-(SP)    ;PUSH T BIT
        MOV #.+6,-(SP)  ;PUSH PC
        RTT              ;SET T BIT
        RESET            ;SHOULD HAVE NO EFFECT
        RESET            ;NO EFFECT

RSTP3:  MOV #234,@#$FATAL ;MOVE TO MAILBOX # ***** 234 *****
        INC (R2)         ;SET MSGTYP TO FATAL ERROR
        HALT             ;TRACE TRAP FAILED,OR WRONG $TESTN
                          ; TO SCOPE REPLACE HALT W/ 240
                          ; AND REPLACE NEXT INST W/ 756
                          ;CLEAR TRACK

RESET2: CLR STATUS
        MTPS STATUS
        MOV #16,14
        CLR 16           ;TRACE STATUS
  
```

```
2288
2289
2290
2291
2292 011564 005237 000404
2293 011570 022737 000067 000404
2294 011576 001103
2295 011600 032767 000001 166612
2296 011606 001403
2297 011610 005767 166572
2298 011614 001106
2299 011616
2300 011616 000005
2301 011620 012704 000012
2302 011624 077401
2303 011626 012706 000500
2304 011632 012767 011700 166224
2305 011640 106427 000000
2306 011644 012767 000357 166214
2307 011652 052767 000100 165704
2308 011660 012704 000120
2309 011664 077401
2310 011666 012737 000235 000402
2311 011674 005212
2312 011676 000000
2313
2314
2315 011700 106767 001402
2316 011704 022767 000357 001374
2317 011712 001405
2318 011714 012737 000236 000402
2319 011722 005212
2320 011724 000000
2321
2322
2323 011726 000005
2324 011730 012704 000012
2325 011734 077401
2326 011736 012706 000500
2327 011742 012767 011774 166114
2328 011750 005067 166112
2329 011754 106427 000017
2330 011760 052767 000100 165576
2331 011766 012704 000120
2332 011772 077401
2333 011774 106767 001306
2334 012000 005767 001302
2335 012004 001405
2336 012006
2337 012006 012737 000237 000402
2338 012014 005212
2339 012016 000000
2340
2341
2342 012020 005067 165540
2343 012024 012767 000066 166032
```

:TEST 67 THAT WHEN TTY INTERRUPTS IT POPS NEW STATUS

TST67: INC @#\$TESTN ;UPDATE TEST NUMBER
CMP #67,@#\$TESTN ;SEQUENCE ERROR?
BNE RSTP4 ;BR TO ERROR HALT ON SEQ ERROR
BIT #1,\$ENV ;CHECK IF ON APT
BEQ NOAPT2 ;IF NOT ON APT
TST \$PASS ;CHECK IF FIRST PASS
BNE TST70 ;IF NOT

NOAPT2: RESET
MOV #12,R4 ;DELAY AFTER THE RESET IN CASE ON PDT-11 TO
SOB R4 ;ALLOW 8085 TO BE READY FOR I/O PAGE REFERENCE
MOV #BUFF,%6 ;SET UP STACK
MTPS #0 ;INTERRUPT VECTOR
MOV #357,66 ;HIGH PRIORITY ON INTERRUPT
BIS #100,TTCSR ;SHOULD SET INTERRUPT ENABLE & INTERRUPT
MOV #120,R4 ;SET UP DELAY IN CASE ON PDT-11 (IT TAKES LONG
SOB R4 ;TIME FOR INTERRUPT REQUEST TO BE SEEN BY CPU)
MOV #235,@#\$FATAL ;MOVE TO MAILBOX # ***** 235 *****
INC (R2) ;SET MSGTYP TO FATAL ERROR
HALT ;NO INTERRUPT
; TO SCOPE REPLACE HALT W/ 240
; AND REPLACE NEXT INST W/ 737

TTY3: MFPS STATUS
CMP #357,STATUS
BEQ 1\$
MOV #236,@#\$FATAL ;MOVE TO MAILBOX # ***** 236 *****
INC (R2) ;SET MSGTYP TO FATAL ERROR
HALT ;INTERRUPT DID NOT POP CORRECT STATUS
; TO SCOPE REPLACE HALT W/ 240
; AND REPLACE NEXT INST W/ 724

1\$: RESET
MOV #12,R4 ;DELAY AFTER THE RESET IN CASE ON PDT-11 TO
SOB R4 ;ALLOW 8085 TO BE READY FOR I/O PAGE REFERENCE
MOV #BUFF,%6 ;STACK SET UP
MTPS #17 ;INTERRUPT VECTOR
CLR 66 ;CLR NEW STATUS
BIS #100,TTCSR ;SET INTERRUPT ENABLE
MOV #120,R4 ;SET UP DELAY IN CASE ON PDT-11
SOB R4 ;WAIT FOR TTY INTERRUPT REQUEST TO GET THRU TO CPU (ON T

TTY4: MFPS STATUS
TST STATUS
BEQ RST4

RSTP4: MOV #237,@#\$FATAL ;MOVE TO MAILBOX # ***** 237 *****
INC (R2) ;SET MSGTYP TO FATAL ERROR
HALT ;INTERRUPT DID NOT POP CORRECT STATUS,OR WRONG \$TESTN
; TO SCOPE REPLACE HALT W/ 240
; AND REPLACE NEXT INST W/ 667

RST4: CLR TTCSR
MOV #66,64

```

2344
2345 ;THIS ROUTINE TESTS THAT NO LEGAL ADDRESS TRAPS AND THAT AN ILLEGAL
2346 ;ADDRESS TRAPS TO LOCATION 4. THIS WILL RUN ON 30K SYSTEMS. IF SWITCH
2347 ;REGISTER BIT 1 = 0, THEN THE MEMORY FROM 28K-30K IS NOT LOOKED
2348 ;AT, SINCE IT MAY HAVE I/O DEVICES. IF SWR BIT 1 = 1, THEN THAT
2349 ;AREA IS CHECKED. (IT SHOULD EITHER ALL TRAP OR ALL NOT TRAP). LOC 160000
2350 ;IS NO LONGER GUARANTEED TO TRAP, SINCE IT MAY CONTAIN MEMORY. LOCATION
2351 ;177700 (THE UNIBUS ADDRESS FOR R0 ON OLDER SYSTEMS) IS USED FOR FORCING
2352 ;A TIMEOUT IN THE EVENT THAT THERE WAS NONE FROM 0K-28K(30K).
2353 ;THIS ROUTINE WILL ALSO WORK IF THERE IS A ROM ABOVE THE HIGHEST
2354 ;MEMORY LOCATION (WHETHER CONTIGUOUS WITH R/W MEMORY OR NOT).
2355 ;*****
2356 ;TEST 70 NON-EXISTENT ADDRESS TRAPS
2357 ;*****
2358 012032 005237 000404 TST70: INC @#$TESTN ;UPDATE TEST NUMBER
2359 012036 022737 000070 000404 CMP #70,@#$TESTN ;SEQUENCE ERROR?
2360 012044 001141 BNE AUTO1 ;BR TO ERROR HALT ON SEQ ERROR
2361
2362 ;THIS ROUTINE TESTS MEMORY UNTIL IT DOES A NXM STOP
2363 012046 042737 010000 012116 BIC #10000,@#HICORE ;SET HIGH CORE LIMIT TO 160000
2364 012054 032737 000002 000422 BIT #2,@#$SWREG ;CHECK IF BIT 1 IS SET
2365 012062 001403 BEQ 1$ ;BRANCH IF IT IS, LEAVE LIMIT=160000
2366 012064 052737 010000 012116 BIS #10000,@#HICORE ;SET UPPER CORE LIMIT TO 30K (170000)
2367 012072 005000 1$: CLR R0
2368 012074 005067 165706 CLR 6
2369 012100 012767 012222 165676 MOV #ATRAP,4 ;SET UP ADDRESS TRAP ENTRANCE
2370 012106 012706 000500 MOV #BUFF,SP
2371 012112 105720 NOR: TSTB (0)+ ;IF OUTSIDE OF CORE, TRAP TO 4
2372 012114 020027 CMP R0,(PC)+ ;IS POINTER INSIDE 28K (30K) CORE
2373 012116 160000 HICORE: .WORD 160000 ;MAY BE CHANGED TO 170000 IF 30K
2374 012120 103774 BLO NOR ;TEST THE REST OF CORE
2375 012122 013737 012116 012266 MOV @#HICORE,@#CORH ;FOR USE BY END OF PASS CHECKER IF ON TIM
2376 012130 012737 012154 000004 MOV #ROTRAP,@#4 ;SET UP NEW VECTOR POINTER
2377 012136 105737 177700 TRPADR: TSTB @#177700 ;CHECK R0 UNIBUS ADDRESS--SHOULD TRAP ON LSI
2378 012142
2379 012142 012737 000240 000402 MOV #240,@#$FATAL ;MOVE TO MAILBOX # ***** 240 *****
2380 012150 005212 INC (R2) ;SET MSGTYP TO FATAL ERROR
2381 012152 000000 HALT ;SHOULD HAVE TRAPED
2382 ; TO SCOPE REPLACE HALT W/ 240
2383 ; AND REPLACE NEXT INST W/ 734
2384
2385 ;TRAP TO HERE IF FORCING TRAP BY TESTING 177700
2386 012154 106767 001126 ROTRAP: MFPS STATUS
2387 012160 005767 001122 TST STATUS
2388 012164 001405 BEQ 1$
2389 012166 012737 000241 000402 MOV #241,@#$FATAL ;MOVE TO MAILBOX # ***** 241 *****
2390 012174 005212 INC (R2) ;SET MSGTYP TO FATAL ERROR
2391 012176 000000 HALT ;NEW PSW SHOULD HAVE BEEN ZERO
2392 ; TO SCOPE REPLACE HALT W/ 240
2393 ; AND REPLACE NEXT INST W/ 722
2394 012200 026727 166270 012142 1$: CMP BUFF-4,#TRPADR
2395 012206 001465 BEQ TRAPB
2396 012210 012737 000242 000402 MOV #242,@#$FATAL ;MOVE TO MAILBOX # ***** 242 *****
2397 012216 005212 INC (R2) ;SET MSGTYP TO FATAL ERROR
2398 012220 000000 HALT ;OLD PC WAS NOT SAVED
2399 ; TO SCOPE REPLACE HALT W/ 240

```

```

2400                                     ; AND REPLACE NEXT INST W/ 711
2401 ;RETURN HERE ON AN ADDRESS TRAP FROM MEMORY BELOW 28K (OR 30K)
2402 012222 005300 ATRAP: DEC R0
2403 012224 010067 000036      MOV R0,CORH ;MOVE THE FIRST NXM LOCATION IN CORH
2404 ;THIS ROUTINE DOES NXM TRAPS UNTIL IT FINDS AN EXISTANT MEMORY LOCATION
2405 012230 013700 012116      MOV @#HICORE,R0 ;SET UP THE HIGHEST MEM LOCATION
2406 012234 005300      DEC R0 ;MAKE 1 LESS THAN THE HIGHEST CORE BOUNDARY
2407 012236 000402      BR NOSUB ;DON'T SUBTRACT 1K FIRST TIME
2408 012240 162700 001000 CTRAP: SUB #1000,R0 ;SUBTRACT 1K OCTAL BYTE FROM ADDRESS BECAUSE
2409 ;IT TAKES FOREVER TO GET TIMEOUT ON TIM
2410 012244 012767 012304 165532 NOSUB: MOV #BTRAP,4 ;SET UP THE VECTOR
2411 012252 012706 000500      MOV #BUFF,SP
2412 012256 011005      MOV (R0),R5 ;SAVE THE WORD IF IT IS THERE--TRAP IF NOT
2413 012260 005010 DTRAP1: CLR (R0) ;IT WAS THERE--TRAP IF IT IS ROM
2414 012262 010510 DTRAP2: MOV R5,(R0) ;NOT ROM--RESTORE WORD
2415 012264 020027      CMP R0,(PC)+ ;DO LOCATIONS MATCH?
2416 012266 000000 CORH: .WORD 0
2417 012270 101434      BLOS TRAPB
2418 012272 012737 000243 000402      MOV #243,@#$FATAL ;MOVE TO MAILBOX # ***** 243 *****
2419 012300 005212      INC (R2) ;SET MSGTYP TO FATAL ERROR
2420 012302 000000      HALT ;CONTENTS OF R0 SHOULD HAVE BEEN LESS THAN OR EQUAL TO C
2421 ; TO SCOPE REPLACE HALT W/ 240
2422 ; AND REPLACE NEXT INST W/ 660
2423 ;IF THIS COMPARISON FAILS IT MEANS
2424 ;THAT SOME LEGAL ADDRESS TRAPPED OR
2425 ;THAT AN ILLEGAL ADDRESS DID NOT TRAP
2426 012304 106767 000776 BTRAP: MFPS STATUS
2427 012310 005767 000772      TST STATUS
2428 012314 001405      BEQ 1$
2429 012316 012737 000244 000402      MOV #244,@#$FATAL ;MOVE TO MAILBOX # ***** 244 *****
2430 012324 005212      INC (R2) ;SET MSGTYP TO FATAL ERROR
2431 012326 000000      HALT ;NEW PSW SHOULD HAVE BEEN ZERO
2432 ; TO SCOPE REPLACE HALT W/ 240
2433 ; AND REPLACE NEXT INST W/ 646
2434 012330 026727 166140 012260 1$: CMP BUFF-4,#DTRAP1
2435 012336 001740      BEQ CTRAP ;BRANCH IF TRAP PC IS OK
2436 012340 026727 166130 012262      CMP BUFF-4,#DTRAP2 ;CHECK IF IT TRAPPED ON THE CLR INSTR
2437 012346 001734      BEQ CTRAP
2438 012350 AUTO1:
2439 012350 012737 000245 000402      MOV #245,@#$FATAL ;MOVE TO MAILBOX # ***** 245 *****
2440 012356 005212      INC (R2) ;SET MSGTYP TO FATAL ERROR
2441 012360 000000      HALT ;OLD PC WAS NOT SAVED OR WRONG $TESTN
2442 ; TO SCOPE REPLACE HALT W/ 240
2443 ; AND REPLACE NEXT INST W/ 631
2444 012362 012767 000006 165414 TRAPB: MOV #6,4
2445 012370 005067 165412      CLR 6

```

```

2446
2447
2448
2449
2450 012374 005237 000404
2451 012400 022737 000071 000404
2452 012406 001070
2453 012410 032767 000001 166002
2454 012416 001403
2455 012420 005767 165762
2456 012424 001066
2457 012426 042767 000100 165130 NOAPT3:
2458 012434 012706 000500
2459 012440 012767 012534 165416
2460 012446 005067 165414
2461 012452 105767 165106 WATE1:
2462 012456 100375
2463 012460 012767 000015 165100
2464 012466 105767 165072 WATE2:
2465 012472 100375
2466 012474 012767 000015 165064
2467 012502 052767 000100 165054
2468 012510 005067 000572
2469 012514 106467 000566
2470 012520 000001
2471 012522 012737 000246 000402 WATE3:
2472 012530 005212
2473 012532 000000
2474
2475
2476 012534 106767 000546
2477 012540 005767 000542
2478 012544 001405
2479 012546 012737 000247 000402
2480 012554 005212
2481 012556 000000
2482
2483
2484 012560 026727 165710 012522 1$:
2485 012566 001405
2486 012570
2487 012570 012737 000250 000402 REES1:
2488 012576 005212
2489 012600 000000
2490
2491
2492 012602 042767 000100 164754 REES:
2493 012610 012767 000066 165246
    
```

```

:*****
:TEST 71 THE 'WAIT' INSTRUCTION
:*****
TST71:  INC    @#$TESTN    ;UPDATE TEST NUMBER
        CMP    #71,@#$TESTN ;SEQUENCE ERROR?
        BNE   REES1      ;BR TO ERROR HALT ON SEQ ERROR
        BIT   #1,$ENV ;CHECK IF ON APT
        BEQ   NOAPT3     ;BR, IF NOT ON APT
        TST   $PASS      ;CHECK IF FIRST PASS
        BNE   REES       ;BR, IF NOT
NOAPT3: BIC   #100,TPS    ;CLEAR INTERRUPT ENABLE
        MOV   #BUFF,SP   ;SET UP THE STACK
        MOV   #WATE,64   ;SET UP THE INTERRUPT VECTOR
        CLR   66
WATE1:  TSTB  TPS        ;WAIT FOR READY
        BPL   WATE1      ;TO BE UP
        MOV   #15,TPB    ;DO A CARRIAGE RETURN
WATE2:  TSTB  TPS        ;WAIT FOR READY TO COME UP
        BPL   WATE2
        MOV   #15,TPB    ;DO ANOTHER CARRIAGE RETURN
        BIS   #100,TPS   ;SET THE INTERRUPT ENABLE
        CLR   STATUS     ;CLEAR THE PSW
        MTPS STATUS
WATE3:  WAIT
        MOV   #246,@#$FATAL ;WAIT FOR THE INTERRUPT
        INC  (R2)         ;MOVE TO MAILBOX # ***** 246 *****
        HALT             ;SET MSGTYP TO FATAL ERROR
                        ;WAIT INSTRUCTION DID NOT LOOP
                        ; TO SCOPE REPLACE HALT W/ 240
                        ; AND REPLACE NEXT INST W/ 725
WATE:   MFPS  STATUS
        TST  STATUS
        BEQ  1$         ;IS THE PSW CORRECT?
        MOV  #247,@#$FATAL ;MOVE TO MAILBOX # ***** 247 *****
        INC (R2)         ;SET MSGTYP TO FATAL ERROR
        HALT             ;NEW PSW SHOULD HAVE BEEN ZERO
                        ; TO SCOPE REPLACE HALT W/ 240
                        ; AND REPLACE NEXT INST W/ 713
1$:     CMP   BUFF-4,#WATE3+2 ;IS THE OLD PC SAVED
        BEQ  REES
REES1:  MOV   #250,@#$FATAL ;MOVE TO MAILBOX # ***** 250 *****
        INC (R2)         ;SET MSGTYP TO FATAL ERROR
        HALT             ;OLD PC WAS NOT SAVED OR WRONG $TESTN
                        ; TO SCOPE REPLACE HALT W/ 240
                        ; AND REPLACE NEXT INST W/ 702
REES:   BIC   #100,TPS    ;CLEAR THE INTERRUPT ENABLE
        MOV   #66,64
    
```

```

2494
2495
2496
2497
2498 012616 005237 000404
2499 012622 022737 000072 000404
2500 012630 001002
2501 012632 000167 000013
2502 012636
2503 012636 012737 000251 000402
2504 012644 005212
2505 012646 000000
2506
2507
2508 012650 005307

```

```

:*****
:TEST 72 ,THAT ODD ADDRESSING WILL IGNORE BIT 0
:*****
TST72:  INC    @#$TESTN      ;UPDATE TEST NUMBER
        CMP    #72,@#$TESTN ;SEQUENCE ERROR?
        BNE   RSTP5        ;RR TO ERROR HALT ON SEQ ERROR
        JMP   ODD+1
RSTP5:  MOV    #251,@#$FATAL ;MOVE TO MAILBOX # ***** 251 *****
        INC   (R2)          ;SET MSGTYP TO FATAL ERROR
        HALT                ;SHOULD HAVE JUMPED,OR WRONG $TESTN
                                ; TO SCOPE REPLACE HALT W/ 240
                                ; AND REPLACE NEXT INST W/ 770
ODD:    DEC    PC

```



```
2509
2510
2511
2512
2513 012652 005237 000404
2514 012656 022737 000073 000404
2515 012664 001142
2516 012666 010267 000532
2517 012672 010700
2518 012674 010704
2519 012676 010705
2520 012700 012703 013222
2521 012704 012302
2522 012706 012301
2523 012710 020267 000326
2524 012714 001020
2525 012716 032767 000100 165476
2526 012724 001004
2527 012726 032767 000300 165472
2528 012734 001403
2529 012736 062703 000004
2530 012742 000760
2531 012744 032767 000004 165450
2532 012752 001401
2533 012754 000753
2534 012756 020267 000270
2535 012762 001007
2536 012764 032767 000020 165430
2537 012772 001403
2538 012774 062703 000010
2539 013000 000741
2540 013002 020267 000250
2541 013006 001010
2542 013010 032767 000040 165410
2543 013016 001332
2544 013020 032767 000004 165374
2545 013026 001326
2546 013030 020267 000232
2547 013034 001005
2548 013036 032767 000010 165356
2549 013044 001401
2550 013046 000716
2551 013050 020267 000216
2552 013054 001524
2553 013056 010267 000212
2554 013062 005267 000206
2555 013066 012767 013112 164714
2556 013074 012706 000500
2557 013100 005067 000202
2558 013104 106467 000176
2559 013110 000471
2560
2561
2562 013112 010267 000102
2563 013116 016702 000302
2564 013122 020627 000474

:*****
:TEST 73 THAT ALL RESERVED INSTRUCTIONS TRAP
:*****
TST73: INC @#$TESTN ;UPDATE TEST NUMBER
        CMP #73,@#$TESTN ;SEQUENCE ERROR?
        BNE RET4 ;BR TO ERROR HALT ON SEQ ERROR
        MOV R2,R2STOR ;SAVE REG 2
        MOV PC,%0 ;SET THESE
        MOV PC,%4 ;REGISTERS
        MOV PC,%5 ;TO EXISTENT MEMORY LOCATIONS
GIN1: MOV #TABLE,R3 ;TABLE POINTER
      MOV (R3)+,R2 ;FIRST OR CURRENT INSTRUCTION
      MOV (R3)+,R1 ;LAST INSTRUCTION OR GROUP
      CMP R2,EISFIS ;IS IT THE 'EISFIS' GROUP?
      BNE 3$ ;NO
      BIT #100,$SWREG ;SUPPRESS EIS/FIS?
      BNE 1$ ;BRANCH IF YES
      BIT #300,$CPUOP ;DO WE HAVE EISFIS OPTION?
      BEQ 2$ ;BRANCH IF NOT
      ADD #4,R3 ;IF YES DO NO DO THE
      BR GIN1 ;EIS FIS OP CODES
      BIT #4,$SWREG ;DO WE HAVE DIS INSTRUCTION SET
      BEQ 3$ ;NO
      BR GIN1 ;IF YES, DO NOT DO EIS OP CODES - DO JUST FIS
      CMP R2,STOP ;IS IT THE STOP GROUP
      BNE 4$ ;NO
      BIT #20,$SWREG ;DO WE WANT TO DO IT?
      BEQ 4$ ;YES
      ADD #10,R3 ;SKIP ENTIRE STOP GROUP
      BR GIN1 ;NO
      CMP R2,DIS ;IS THIS THE DIS GROUP?
      BNE 5$ ;NO
      BIT #40,$CPUOP ;DIS PRESENT??
      BNE GIN1 ;BRANCH IF YES--DON'T DO DIS
      BIT #4,$SWREG ;SUPPRESS DIS OPTION?
      BNE GIN1 ;BRANCH IF YES, SKIP THE DIS GROUP
      CMP R2,STOP1 ;IS IT THE STOP1 GROUP?
      BNE 6$ ;NO
      BIT #10,$SWREG ;DO WE WANT TO DO IT?
      BEQ 6$ ;YES
      BR GIN1 ;NO
      CMP R2,FINISH ;TESTED ALL
      BEQ GIN3 ;BRANCH IF YES, GO TO END OF PASS ROUTINE
GIN2: MOV R2,INST ;SET UP INST
      INC INST
      MOV #RET,10 ;SET UP RETURN FROM TRAP
      MOV #BUFF,SP ;SET UP STACK POINTER
      CLR STATUS ;CLEAR PRIORITY
      MTPS STATUS
      BR INST ;EXECUTE RESERVED INSTRUCTION

;TRAPPING SHOULD SEND YOU HERE
RET: MOV R2,R2SAVE ;SAVE REG 2
      MOV R2STOR,R2 ;RESTORE MAILBOX POINTER
      CMP SP,#BUFF-4 ;TEST DECREMENT OF SP
```

```

2565 013126 001405          BEQ      RET1
2566 013130 012737 000252 000402  MOV     #252,@#$FATAL ;MOVE TO MAILBOX # ***** 252 *****
2567 013136 005212          INC     (R2)          ;SET MSGTYP TO FATAL ERROR
2568 013140 000000          HALT                    ;WRONG DECREMENT
2569                                     ; TO SCOPE REPLACE HALT W/ 240
2570                                     ; AND REPLACE NEXT INST W/ 651
2571 013142 026727 165326 013276 RET1:  CMP     BUFF-4,#INST+2 ;LOC OF INST UNINCREMENTED
2572 013150 001405          BEQ     RET2
2573 013152 012737 000253 000402  MOV     #253,@#$FATAL ;MOVE TO MAILBOX # ***** 253 *****
2574 013160 005212          INC     (R2)          ;SET MSGTYP TO FATAL ERROR
2575 013162 000000          HALT                    ;INST INC ON TRAP
2576                                     ; TO SCOPE REPLACE HALT W/ 240
2577                                     ; AND REPLACE NEXT INST W/ 640
2578 013164 005767 165306          RET2:  TST     BUFF-2
2579 013170 001405          BEQ     RET3
2580 013172                                     RET4:
2581 013172 012737 000254 000402  MOV     #254,@#$FATAL ;MOVE TO MAILBOX # ***** 254 *****
2582 013200 005212          INC     (R2)          ;SET MSGTYP TO FATAL ERROR
2583 013202 000000          HALT                    ;CONDITION CODES SET ON TRAP,OR WRONG $TESTN
2584                                     ; TO SCOPE REPLACE HALT W/ 240
2585                                     ; AND REPLACE NEXT INST W/ 630
2586 013204 016702 000010          RET3:  MOV     R2SAVE,R2 ;RESTORE REG 2
2587 013210 026701 000060          CMP     INST,R1
2588 013214 001633          BEQ     GIN1          ;SET UP NEW GROUP
2589 013216 000721          BR     GIN2          ;FINISH OLD GROUP
2590 013220 000000          R2SAVE: .WORD 0
2591                                     ;END OF INSTRUCTION GROUP
2592 013222 000006          TABLE: 6
2593 013224 000077          77
2594                                     106477
2595 013226 106477          106477
2596 013230 106677          106677
2597                                     6777
2598 013232 006777          7777
2599 013234 007777          106777
2600 013236 106777          107777
2601 013240 107777          67777
2602 013242 067777          EISFIS: ;IF WE HAVE THE EIS FIS OPTION
2603 013244 073777          73777 ;THEN THE EISFIS GROUP
2604 013246 074777          FIS:    74777 ;WILL BE SKIPED
2605 013250 075037          75037
2606 013252 075377          STOP:   75377
2607 013254 076027          76027
2608 013256 076027          DIS:   76027
2609 013260 076057          76057
2610 013262 076057          76057
2611 013264 076777          76777
2612 013266 167777          STOP1: 167777
2613 013270 177777          177777
2614 013272 013272          FINISH: .
2615 013274 000000          INST:  HALT          ;END FLAG
2616 013276 000404          BR     TERR          ;WILL CONTINUE RESERVED INST
2617 013300 000403          BR     TERR
2618 013302 000402          BR     TERR
2619 013304 000401          BR     TERR
2620 013306 000000          STATUS: 0

```

2621	013310	016702	000110		TERR:	MOV	R2STOR,R2	:	RESTORE R2
2622	013314	012737	000255	000402		MOV	#255,@#\$FATAL	:	INDICATE ERROR
2623	013322	005212				INC	(R2)		
2624	013324	000000				HALT			
2625									
2626									
2627	013326	005237	000406		GIN3:	INC	@#\$PASS		
2628	013332	005267	000070			INC	PASSPT	:	SHOULD PRINT THIS PASS?
2629	013336	001013				BNE	ACT	:	NO
2630	013340	032767	000040	165054		BIT	#40,\$SWREG	:	TYPE END OF PASS?
2631	013346	001004				BNE	1\$:	BRANCH IF NOT
2632	013350	004567	000276			JSR	R5,TYPE		
2633	013354	013766					EOPMSG		
2634	013356	000005				RESET			
2635	013360	012767	177761	000040	1\$:	MOV	#177761,PASSPT	:	DO IT 15 DECIMAL TIMES
2636	013366	013700	000042		ACT:	MOV	@#42,R0	:	CHECK ACT
2637	013372	001405				BEQ	GOAGIN	:	KEEP GOING
2638	013374	000005				RESET			
2639	013376	004710			\$ENDAD:	JSR	PC,(R0)	:	ACT HOOKS
2640	013400	000240				NOP			
2641	013402	000240				NOP			
2642	013404	000240				NOP			
2643	013406	012767	000012	164374	GOAGIN:	MOV	#12,10		
2644	013414	005067	164372			CLR	12		
2645	013420	000167	165130			JMP	RESTRT	:	DO NEXT PASS
2646	013424	000000			R2STOR:	.WORD	0		
2647	013426	177777			PASSPT:	-1			

```

2648
2649
2650
2651
2652
2653 013430 010246
2654 013432 010446
2655 013434 004567 000212
2656 013440 014062
2657 013442 012702 013652
2658 013446 005042
2659 013450 005042
2660 013452 005042
2661 013454 005042
2662 013456 012737 013504 000010
2663 013464 075002
2664 013466 012737 000012 000010
2665 013474 052767 000300 164724
2666 013502 000413
2667
2668 013504 012737 000012 000010
2669 013512 062706 000004
2670 013516 042767 000300 164702
2671 013524 004567 000122
2672 013530 014104
2673 013532 004567 000114
2674 013536 014111
2675
2676 013540 004567 000106
2677 013544 014123
2678 013546 005002
2679 013550 005004
2680 013552 012737 013600 000010
2681 013560 076030
2682 013562 012737 000012 000010
2683 013570 052767 000040 164630
2684 013576 000413
2685
2686 013600 012737 000012 000010
2687 013606 042767 000040 164612
2688 013614 062706 000004
2689 013620 004567 000026
2690 013624 014104
2691 013626 004567 000020
2692 013632 014111
2693
2694 013634 012604
2695 013636 012602
2696 013640 000207
2697
2698
2699 013642 000004
2700 013652

```

```

:*****
:SIZE ROUTINE
:*****
SIZE:  MOV    R2,-(SP)      ;SAVE R2
      MOV    R4,-(SP)      ;SAVE R4
      JSR    R5,TYPE       ;TYPE 'EIS/FIS OPTION ''
      FISOPT
      MOV    #FSTACK,R2   ;SET UP FLOATING POINT STACK
      CLR    -(R2)
      CLR    -(R2)
      CLR    -(R2)
      CLR    -(R2)
      MOV    #1$,@#10     ;SET UP RESERVED INSTRUCTION VECTOR
      FADD   R2           ;EXECUTE FLOATING INSTR (0+0)
      MOV    #12,@#10    ;RESTORE VECTOR
      BIS    #300,$CPUOP  ;FLAG OPTION PRESENT
      BR     2$
:TRAP TO HERE IF NO FIS
1$:   MOV    #12,@#10     ;RESTORE VECTOR
      ADD    #4,SP        ;POP TRAP PC/PSW OFF STACK
      BIC    #300,$CPUOP  ;FLAG OPTION NOT PRESENT
      JSR    R5,TYPE       ;TYPE 'NOT ''
      NOT
2$:   JSR    R5,TYPE       ;TYPE 'PRESENT<CR>''
      PRESENT
      JSR    R5,TYPE       ;TYPE 'DIBOL INSTRUCTION SET ''
      DISOPT
      CLR    R2           ;SET UP A 0 DEST LENGTH
      CLR    R4           ;HIGH BYTE MUST BE CLEAR
      MOV    #3$,@#10    ;SET UP TRAP VECTOR
      MOVC   ;MOVE CHARACTER INSTRUCTION
      MOV    #12,@#10    ;RESTORE VECTOR
      BIS    #40,$CPUOP  ;FLAG OPTION PRESENT
      BR     4$
:TRAP HERE IF NO DIS
3$:   MOV    #12,@#10     ;RESTORE VECTOR
      BIC    #40,$CPUOP  ;FLAG NO OPTION
      ADD    #4,SP        ;POP TRAP PC/PSW OFF STACK
      JSR    R5,TYPE       ;TYPE 'NOT ''
      NOT
4$:   JSR    R5,TYPE       ;'PRESENT<CR>''
      PRESENT
      MOV    (SP)+,R4     ;RESTORE REGISTERS USED
      MOV    (SP)+,R2
      RTS    PC
:FLOATING POINT STACK FOR FIS
FSTACK: .BLKW 4

```

```
2701 :*****
2702 :TYPE ROUTINE
2703 :*****
2704
2705 013652 010046 TYPE: MOV R0,-(SP) ;SAVE REGISTER
2706 013654 012500 MOV (R5)+,R0 ;GET TEXT POINTER
2707 013656 132767 000040 164535 BITB #40,$ENVM ;SUPPRESS OUTPUT??
2708 013664 001011 BNE 3$ ;BRANCH IF YES--RETURN
2709 013666 105737 177564 1$: TSTB @#TPS ;WAIT FOR TTY READY
2710 013672 100375 BPL 1$
2711 013674 112037 177566 MOVB (R0)+,@#TPB ;TYPE CHARACTER
2712 013700 001372 BNE 1$ ;BRANCH IF IT WAS NOT TERMINATOR
2713 013702 105737 177564 2$: TSTB @#TPS ;WAIT FOR READY
2714 013706 100375 BPL 2$
2715 013710 012600 3$: MOV (SP)+,R0 ;RESTORE REGISTER
2716 013712 000205 RTS R5
2717
2718 :*****
2719 :POWER FAIL ROUTINE
2720 :*****
2721
2722 013714 012767 013724 164102 PWRDWN: MOV #PWRUP,24
2723 013722 000000 HALT
2724
2725 013724 012767 013714 164072 PWRUP: MOV #PWRDWN,24
2726 013732 012706 000500 MOV #BUFF,SP
2727 013736 005000 CLR R0 ;SET UP A DELAY
2728 013740 012701 177750 MOV #-30,R1
2729 013744 005200 1$: INC R0
2730 013746 001376 BNE 1$
2731 013750 005201 INC R1
2732 013752 001374 BNE 1$
2733 013754 004567 177672 JSR R5,TYPE ;TYPE POWER FAIL MESSAGE
2734 013760 014004 MSGPWF
2735 013762 000167 164514 JMP START
2736
2737 013766 005015 047105 020104 EOPMSG: .ASCIZ <15><12>.END OF PASS.
2738 013774 043117 050040 051501
2739 014002 000123
2740 014004 005015 047520 042527 MSGPWF: .ASCIZ <15><12>.POWER FAILED!.
2741 014012 020122 040506 046111
2742 014020 042105 000041
2743 014024 005015 053103 040513 TITLE: .ASCIZ <15><12>.CVKADC0 LSI-11 TRAPS TEST.<15><12>
2744 014032 041504 020060 051514
2745 014040 026511 030461 052040
2746 014046 040522 051520 052040
2747 014054 051505 006524 000012
2748 014062 005015 044505 027523 FISOPT: .ASCIZ <15><12>.EIS/FIS OPTION .
2749 014070 044506 020123 050117
2750 014076 044524 047117 000040
2751 014104 047516 020124 000
2752 014111 120 042522 042523 NOT: .ASCIZ .NOT .
PRESENT: .ASCIZ .PRESENT.<15><12>
2753 014116 052116 005015 000
2754 014123 104 041111 046117 DISOPT: .ASCIZ .DIBOL INSTRUCTION SET .
2755 014130 044440 051516 051124
2756 014136 041525 044524 047117
```

.MAIN. MACY11 30A(1052) 18-SEP-78 12:00 PAGE 69
CVKADC.MAC 18-SEP-78 11:54 T73 THAT ALL RESERVED INSTRUCTIONS TRAP

SEQ 0069

2757 014144 051440 052105 000040
2758 000001 .END

PRESEN	014111	2674	2692	2752#	
PWRDWN	013714	352	2722#	2725	
PWRUP	013724	2722	2725#		
RA	005412	1397	1406#		
RA1	003636	1033	1042#		
RB	005376	1396*	1399#	1406*	1407
RBBB	005400	1395	1400#		
RB1	003622	1032*	1035#	1042*	1043
RB1AA	003624	1031	1036#		
RC	005372	1398#	1408		
RC1	003616	1034#	1044		
REES	012602	2456	2485	2492#	
REES1	012570	2452	2486#		
RESET2	011542	2272	2284#		
RESTRT	000554	366#	2645		
RET	013112	2555	2562#		
RETA	002154	691	698#		
RETAT	010014	1941	1951#		
RETA1	003010	864	871#		
RETA2	003726	1054	1061#		
RETA3	004562	1227	1234#		
RETA4	005502	1418	1425#		
RETA5	006336	1599	1606#		
RETB	002204	706	708#		
RETB1	010070	1959	1969#		
RETB2	003040	879	881#		
RETB3	003756	1069	1071#		
RETB4	004612	1242	1244#		
RETB5	005532	1434	1436#		
RETB6	006366	1614	1616#		
RETC	002254	723	725#		
RETC1	010150	1984	1989#		
RETC2	003110	895	897#		
RETC3	004026	1086	1088#		
RETC4	004662	1258	1260#		
RETC5	005602	1450	1452#		
RETC6	006436	1631	1633#		
RETD	002340	739	744#		
RETD1	003174	912	917#		
RETD2	004112	1102	1107#		
RETD3	004746	1275	1280#		
RETD4	005666	1467	1472#		
RETD5	006522	1647	1652#		
RETE	002412	752	757#		
RETE1	003244	925	929#		
RETE2	004164	1115	1120#		
RETE3	005020	1288	1293#		
RETE4	005740	1480	1485#		
RETE5	006574	1660	1665#		
RETF	002470	772	775#		
RETF1	003322	944	947#		
RETF2	004242	1135	1138#		
RETF3	005076	1308	1311#		
RETF4	006016	1500	1503#		
RETF5	006652	1680	1683#		
RETG	002620	812	815#		

TST42	006406	1612	1617	1627#																		
TST43	006460	1629	1634	1643#																		
TST44	006616	1645	1666	1676#																		
TST45	007112	1678	1754	1764#																		
TST46	007154	1766	1779#																			
TST47	007224	1781	1786	1796#																		
TST5	002112	619	677	687#																		
TST50	007276	1798	1803	1813#																		
TST51	007434	1815	1836	1846#																		
TST52	007740	1937#																				
TST53	010014	1939	1955#																			
TST54	010110	1957	1970	1980#																		
TST55	010172	1982	1990	2001#																		
TST56	010252	2003	2012	2024#																		
TST57	010362	2026	2056#																			
TST6	002154	689	702#																			
TST60	010456	2058	2073	2083#																		
TST61	010606	2117#																				
TST62	010710	2119	2133	2142#																		
TST63	010776	2144	2164#																			
TST64	011162	2170	2204#																			
TST65	011354	2210	2241#																			
TST66	011464	2243	2258	2268#																		
TST67	011564	2292#																				
TST7	002224	704	709	719#																		
TST70	012032	2298	2358#																			
TST71	012374	2450#																				
TST72	012616	2498#																				
TST73	012652	2513#																				
TTCSR =	177564	251#	2178*	2212*	2216*	2245*	2250	2307*	2330*	2342*												
TTY3	011700	2304	2315#																			
TTY4	011774	2327	2333#																			
TYPE	013652	358	2632	2655	2671	2673	2676	2689	2691	2705#	2733											
WATE	012534	2459	2476#																			
WATE1	012452	2461#	2462																			
WATE2	012466	2464#	2465																			
WATE3	012520	2470#	2484																			
\$APTHD	000450	330	336#																			
\$CPUOP	000426	295#	2527	2542	2665*	2670*	2683*	2687*														
\$DEVCT	000410	286#																				
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\$ENV	000420	291#	2167	2207	2295	2453																
\$ENVM	000421	292#	360	2707																		
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		457	458#	467	468#	486	487#	499	500#	512	513#	525	526#	538								
		539#	552	553#	560	561#	569	570#	577	578#	584	585#	592	593#								
		600	601#	608	609#	624	625#	631	632#	638	639#	645	646#	657								
		658#	664	665#	671	672#	678	679#	693	694#	710	711#	727	728#								
		746	747#	759	760#	777	778#	784	785#	791	792#	798	799#	806								
		807#	817	818#	824	825#	831	832#	838	839#	849	850#	866	867#								
		883	884#	899	900#	919	920#	931	932#	949	950#	956	957#	963								
		964#	970	971#	978	979#	989	990#	996	997#	1003	1004#	1010	1011#								
		1020	1021#	1037	1038#	1056	1057#	1073	1074#	1090	1091#	1109	1110#	1122								
		1123#	1140	1141#	1147	1148#	1154	1155#	1161	1162#	1169	1170#	1180	1181#								
		1187	1188#	1194	1195#	1201	1202#	1212	1213#	1229	1230#	1246	1247#	1262								
		1263#	1282	1283#	1295	1296#	1313	1314#	1320	1321#	1327	1328#	1334	1335#								

CROSS REFERENCE TABLE -- USER SYMBOLS

1342	1343#	1353	1354#	1360	1361#	1367	1368#	1374	1375#	1384	1385#	1401
1402#	1420	1421#	1438	1439#	1454	1455#	1474	1475#	1487	1488#	1505	1506#
1512	1513#	1519	1520#	1526	1527#	1534	1535#	1545	1546#	1552	1553#	1559
1560#	1566	1567#	1577	1578#	1601	1602#	1618	1619#	1635	1636#	1654	1655#
1667	1668#	1685	1686#	1692	1693#	1699	1700#	1706	1707#	1714	1715#	1725
1726#	1732	1733#	1739	1740#	1746	1747#	1755	1756#	1770	1771#	1787	1788#
1804	1805#	1824	1825#	1837	1838#	1855	1856#	1862	1863#	1869	1870#	1876
1877#	1884	1885#	1895	1896#	1902	1903#	1909	1910#	1916	1917#	1926	1927#
1946	1947#	1964	1965#	1971	1972#	1991	1992#	2013	2014#	2037	2038#	2046
2047#	2066	2067#	2074	2075#	2098	2099#	2106	2107#	2127	2128#	2134	2135#
2152	2153#	2188	2189#	2194	2195#	2226	2227#	2252	2253#	2259	2260#	2279
2280#	2310	2311#	2318	2319#	2337	2338#	2379	2380#	2389	2390#	2396	2397#
2418	2419#	2429	2430#	2439	2440#	2471	2472#	2479	2480#	2487	2488#	2503
2504#	2566	2567#	2573	2574#	2581	2582#						
252#	369*											
290#												
319#	342											
262	283#	393*	403*	413*	424*	435*	446*	457*	467*	486*	499*	512*
525*	538*	552*	560*	569*	577*	584*	592*	600*	608*	624*	631*	638*
645*	657*	664*	671*	678*	693*	710*	727*	746*	759*	777*	784*	791*
798*	806*	817*	824*	831*	838*	849*	866*	883*	899*	919*	931*	949*
956*	963*	970*	978*	989*	996*	1003*	1010*	1020*	1037*	1056*	1073*	1090*
1109*	1122*	1140*	1147*	1154*	1161*	1169*	1180*	1187*	1194*	1201*	1212*	1229*
1246*	1262*	1282*	1295*	1313*	1320*	1327*	1334*	1342*	1353*	1360*	1367*	1374*
1384*	1401*	1420*	1438*	1454*	1474*	1487*	1505*	1512*	1519*	1526*	1534*	1545*
1552*	1559*	1566*	1577*	1601*	1618*	1635*	1654*	1667*	1685*	1692*	1699*	1706*
1714*	1725*	1732*	1739*	1746*	1755*	1770*	1787*	1804*	1824*	1837*	1855*	1862*
1869*	1876*	1884*	1895*	1902*	1909*	1916*	1926*	1946*	1964*	1971*	1991*	2013*
2037*	2046*	2066*	2074*	2098*	2106*	2127*	2134*	2152*	2188*	2194*	2226*	2252*
2259*	2279*	2310*	2318*	2337*	2379*	2389*	2396*	2418*	2429*	2439*	2471*	2479*
2487*	2503*	2566*	2573*	2581*	2622*							
337#												
308#												
312#												
315#												
318#												
281#	338	342										
302#												
310#												
313#												
316#												
338#												
288#												
289#												
282#	366	367*										
303#												
311#												
314#												
317#												
285#	348*	2169	2209	2297	2455	2627*						
340#												
271#	276											
234#												
293#	2364	2525	2531	2536	2544	2548	2630					
261	284#	386*	387	476*	477	547*	548	617*	618	687*	688	702*
703	719*	720	735*	736	768*	769	860*	861	875*	876	891*	892

\$ERROR= 000402
\$ETABL 000420
\$ETEND 000450
\$FATAL 000402

\$HIBTS 000450
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\$MADR2 000436
\$MADR3 000442
\$MADR4 000446
\$MAIL 000400
\$MAMS1 000430
\$MAMS2 000434
\$MAMS3 000440
\$MAMS4 000444
\$MEADR 000452
\$MSGAD 000414
\$MSGLG 000416
\$MSGTY 000400
\$MTYP1 000431
\$MTYP2 000435
\$MTYP3 000441
\$MTYP4 000445
\$PASS 000406
\$PASTM 000456
\$SVPC = 000400
\$SWR = 000000
\$SWREG 000422
\$TESTN 000404

\$TN = 000074

\$TSTM 000454
\$TSTNM= 000404
\$UNIT 000412
\$UNITM 000460
\$USWR 000424
\$X = 012666

\$XX = 177631

908*	909	940*	941	1029*	1030	1050*	1051	1065*	1066	1082*	1083	1098*
1099	1131*	1132	1223*	1224	1238*	1239	1254*	1255	1271*	1272	1304*	1305
1393*	1394	1414*	1415	1430*	1431	1446*	1447	1463*	1464	1496*	1497	1595*
1596	1610*	1611	1627*	1628	1643*	1644	1676*	1677	1764*	1765	1779*	1780
1796*	1797	1813*	1814	1846*	1847	1937*	1938	1955*	1956	1980*	1981	2001*
2002	2024*	2025	2056*	2057	2083*	2084	2117*	2118	2142*	2143	2164*	2165
2204*	2205	2241*	2242	2268*	2269	2292*	2293	2358*	2359	2450*	2451	2498*
2499	2513*	2514										
234#	383	389#	466	473	479#	537	544	550#	568	607	614	620#
677	684	690#	699	705#	709	716	722#	726	732	738#	758	765
771#	857	863#	872	878#	882	888	894#	898	905	911#	930	937
943#	1019	1026	1032#	1047	1053#	1062	1068#	1072	1079	1085#	1089	1095
1101#	1121	1128	1134#	1220	1226#	1235	1241#	1245	1251	1257#	1261	1268
1274#	1294	1301	1307#	1383	1390	1396#	1411	1417#	1427	1433#	1437	1443
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1630#	1634	1640	1646#	1666	1673	1679#	1754	1761	1767#	1776	1782#	1786
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1977	1983#	1990	1998	2004#	2012	2021	2027#	2053	2059#	2073	2080	2086#
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339#												
261#	368*											
287#												
341#												
294#												
389#	396	406	416	427	438	449	460	470	479#	489	502	515
528	541	550#	555	563	572	580	587	595	603	611	620#	627
634	641	648	660	667	674	681	690#	696	705#	713	722#	730
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966	973	981	992	999	1006	1013	1023	1032#	1040	1053#	1059	1068#
1076	1085#	1093	1101#	1112	1125	1134#	1143	1150	1157	1164	1172	1183
1190	1197	1204	1215	1226#	1232	1241#	1249	1257#	1265	1274#	1285	1298
1307#	1316	1323	1330	1337	1345	1356	1363	1370	1377	1387	1396#	1404
1417#	1423	1433#	1441	1449#	1457	1466#	1477	1490	1499#	1508	1515	1522
1529	1537	1548	1555	1562	1569	1580	1598#	1604	1613#	1621	1630#	1638
1646#	1657	1670	1679#	1688	1695	1702	1709	1717	1728	1735	1742	1749
1758	1767#	1773	1782#	1790	1799#	1807	1816#	1827	1840	1849#	1858	1865
1872	1879	1887	1898	1905	1912	1919	1929	1940#	1949	1958#	1967	1974
1983#	1994	2004#	2016	2027#	2040	2049	2059#	2069	2077	2086#	2101	2109
2120#	2130	2137	2145#	2155	2167#	2191	2197	2207#	2229	2244#	2255	2262
2271#	2282	2295#	2313	2321	2340	2361#	2382	2392	2399	2421	2432	2442
2453#	2474	2482	2490	2501#	2506	2516#	2569	2576	2584			
396#	406#	416#	427#	438#	449#	460#	470#	489#	502#	515#	528#	541#
555#	563#	572#	580#	587#	595#	603#	611#	627#	634#	641#	648#	660#
667#	674#	681#	696#	713#	730#	749#	762#	780#	787#	794#	801#	809#
820#	827#	834#	841#	852#	869#	886#	902#	922#	934#	952#	959#	966#
973#	981#	992#	999#	1006#	1013#	1023#	1040#	1059#	1076#	1093#	1112#	1125#
1143#	1150#	1157#	1164#	1172#	1183#	1190#	1197#	1204#	1215#	1232#	1249#	1265#
1285#	1298#	1316#	1323#	1330#	1337#	1345#	1356#	1363#	1370#	1377#	1387#	1404#
1423#	1441#	1457#	1477#	1490#	1508#	1515#	1522#	1529#	1537#	1548#	1555#	1562#
1569#	1580#	1604#	1621#	1638#	1657#	1670#	1688#	1695#	1702#	1709#	1717#	1728#
1735#	1742#	1749#	1758#	1773#	1790#	1807#	1827#	1840#	1858#	1865#	1872#	1879#
1887#	1898#	1905#	1912#	1919#	1929#	1949#	1967#	1974#	1994#	2016#	2040#	2049#
2069#	2077#	2101#	2109#	2130#	2137#	2155#	2191#	2197#	2229#	2255#	2262#	2282#
2313#	2321#	2340#	2382#	2392#	2399#	2421#	2432#	2442#	2474#	2482#	2490#	2506#

CROSS REFERENCE TABLE -- MACRO NAMES

ERROR	234#	392	402	412	423	434	445	456	466	485	498	511	524	537	551
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	693	709	726	745	758	776	782	789	796	805	816	822	829	836	847
	866	882	898	918	930	948	954	961	968	977	988	994	1001	1008	1019
	1036	1056	1072	1089	1108	1121	1139	1145	1152	1159	1168	1179	1185	1192	1199
	1210	1229	1245	1261	1281	1294	1312	1318	1325	1332	1341	1352	1358	1365	1372
	1383	1400	1420	1437	1453	1473	1486	1504	1510	1517	1524	1533	1544	1550	1557
	1564	1575	1601	1617	1634	1653	1666	1684	1690	1697	1704	1713	1724	1730	1737
	1744	1754	1770	1786	1803	1823	1836	1854	1860	1867	1874	1883	1894	1900	1907
	1914	1924	1946	1964	1970	1990	2012	2037	2045	2066	2073	2098	2104	2127	2133
	2152	2187	2193	2225	2251	2258	2278	2310	2317	2335	2378	2388	2395	2417	2428
	2437	2471	2478	2485	2502	2565	2572	2579							
LOOP	234#	396	406	416	427	438	449	460	470	489	502	515	528	541	555
	563	572	580	587	595	603	611	627	634	641	648	660	667	674	681
	696	713	730	749	762	780	787	794	801	809	820	827	834	841	852
	869	886	902	922	934	952	959	966	973	981	992	999	1006	1013	1023
	1040	1059	1076	1093	1112	1125	1143	1150	1157	1164	1172	1183	1190	1197	1204
	1215	1232	1249	1265	1285	1298	1316	1323	1330	1337	1345	1356	1363	1370	1377
	1387	1404	1423	1441	1457	1477	1490	1508	1515	1522	1529	1537	1548	1555	1562
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	2155	2191	2197	2229	2255	2262	2282	2313	2321	2340	2382	2392	2399	2421	2432
	2442	2474	2482	2490	2506	2569	2576	2584							
NWT	234#	383	473	544	614	684	699	716	732	765	857	872	888	905	937
	1026	1047	1062	1079	1095	1128	1220	1235	1251	1268	1301	1390	1411	1427	1443
	1460	1493	1592	1607	1624	1640	1673	1761	1776	1793	1810	1843	1934	1952	1977
	1998	2021	2053	2080	2114	2139	2161	2201	2238	2265	2289	2355	2447	2495	2510
STARS	234#	269	279	323	325	332	383	385	473	475	544	546	614	616	684
	686	699	701	716	718	732	734	765	767	857	859	872	874	888	890
	905	907	937	939	1026	1028	1047	1049	1062	1064	1079	1081	1095	1097	1128
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	1642	1673	1675	1761	1763	1776	1778	1793	1795	1810	1812	1843	1845	1934	1936
	1952	1954	1977	1979	1998	2000	2021	2023	2053	2055	2080	2082	2114	2116	2139
	2141	2161	2163	2201	2203	2238	2240	2265	2267	2289	2291	2355	2357	2447	2449
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	1916	1926	1946	1964	1971	1991	2013	2037	2046	2066	2074	2098	2106	2127	2134
	2152	2188	2194	2226	2252	2259	2279	2310	2318	2337	2379	2389	2396	2418	2429
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	866	883	899	919	931	949	956	963	970	978	989	996	1003	1010	1020
	1037	1056	1073	1090	1109	1122	1140	1147	1154	1161	1169	1180	1187	1194	1201
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	1384	1401	1420	1438	1454	1474	1487	1505	1512	1519	1526	1534	1545	1552	1559

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	1916	1926	1946	1964	1971	1991	2013	2037	2046	2066	2074	2098	2106	2127	2134
	2152	2188	2194	2226	2252	2259	2279	2310	2318	2337	2379	2389	2396	2418	2429
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	1121	1245	1261	1294	1383	1437	1453	1486	1617	1634	1666	1754	1786	1803	1836
	1970	1990	2012	2073	2133	2258									
\$\$LOOP	234#	396	406	416	427	438	449	460	470	489	502	515	528	541	555
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	1919	1929	1949	1967	1974	1994	2016	2040	2049	2069	2077	2101	2109	2130	2137
	2155	2191	2197	2229	2255	2262	2282	2313	2321	2340	2382	2392	2399	2421	2432
	2442	2474	2482	2490	2506	2569	2576	2584							
\$\$N	234#	383	473	544	614	684	699	716	732	765	857	872	888	905	937
	1026	1047	1062	1079	1095	1128	1220	1235	1251	1268	1301	1390	1411	1427	1443
	1460	1493	1592	1607	1624	1640	1673	1761	1776	1793	1810	1843	1934	1952	1977
	1998	2021	2053	2080	2114	2139	2161	2201	2238	2265	2289	2355	2447	2495	2510
.\$ACT1	264#	267													
.\$APT8	264#	277													
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. ABS. 014152 000

ERRORS DETECTED: 0

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RUN-TIME: 17 11 1 SECONDS

RUN-TIME RATIO: 126/30=4.1

CORE USED: 10K (.9 PAGES)