

## CUSTOMER PRINT SET INDEX

THIS IS PRINT SET

SEQUENCE		SEQUENCE	
16 BIT COMPUTER (11/05)	B-DD-11/05-0	*PKG. INST. INPLANT (5 1/4")	A-SP-3700133-0-0
16 BIT COMPUTER (11/05)	D-UA-11/05-0-0	*PKG. INST. CUSTOMER (10 1/2")	A-SP-3700098-0-0
16 BIT COMPUTER (11/05)	C-PL-11/05-0-0	BOX	
MODULE UTILIZATION (16K)	D-MU-11/05-0-02	*PKG. INST. INPLANT (10 1/2")	A-SP-3700156-0-0
MODULE UTILIZATION (8K)	D-MU-11/05-0-01	BOX	
MODULE UTILIZATION (10 1/2)	D-MU-11/05-0-07		
CENTRAL PROCESSOR	B-DD-KD11-B		
16 BIT 18MIL MEMORY	B-DD-MM11-L		
BUS TERMINATOR	D-CS-M930-0-1		
CONSOLE ASSY	B-DD-KY11-J		
H750 POWER SUPPLY	B-DD-H750-0		
LINE SET BC05-H	B-DD-BC05H-0		
LINE SET BC05-T	B-DD-BC05T-0		
LINE SET BC05-U	B-DD-BC05U-0		
INPUT HARNESS (A.C.)	E-IA-7008713-0-0		
HARNESS (D.C.)	D-IA-7008856-0-0		
BALL-D POWER TO DISTRIBUTION BOARD	E-IA-7009208-0-0		
HEADER CABLE ASSY	D-IA-7008820-0-0		
CIRCUIT SCHEMATIC	C-CS-5409949-0-1		
BERG TO BACKPLANE	C-CS-M9970-0-1		
CABLE ASSY (KL8E)	D-IA-7008360-0-0		
MAINTENANCE BOARD	D-BS-KM11-0-MB		
MAINT. MODULE OVERLAY (11/05-KM1)	A-SS-5509081-0-9		
MAINT. MODULE OVERLAY (11/05-KM2)	A-SS-5509081-0-10		
CIRCUIT SCHEMATIC (8K)	C-CS-5409818-0-1		
*ETCH/WIRE LIST (8K)	K-WL-7008843-1-1		
CIRCUIT SCHEMATIC (16K)	C-CS-5410035-0-1		
*ETCH/WIRE LIAT (16K)	K-WL-7008843-2-1		
CIRCUIT SCHEMATIC (10 1/2)	D-CS-5410329-0-1		
*ETCH/WIRE LIST (10 1/2)	K-WL-11/05-0-8		
11/05 ACCESSORY LIST	A-AL-11/05-0-04		
11/05 SOFTWARE LIST	A-SL-11/05-0-05		
*11/05 ACCEPTANCE PROC.	A-SP-11/05-0-6		
*DECAL (10.5" BOX)	A-DC-7411305-0-0		
*PKG. INST. CUSTOMER (5 1/4")	A-SP-3700061-0-0		
AWT REVISION STATUS	A-WT-7008843-2		
POWER SUPPLY	D-UA-7008731-0-0		

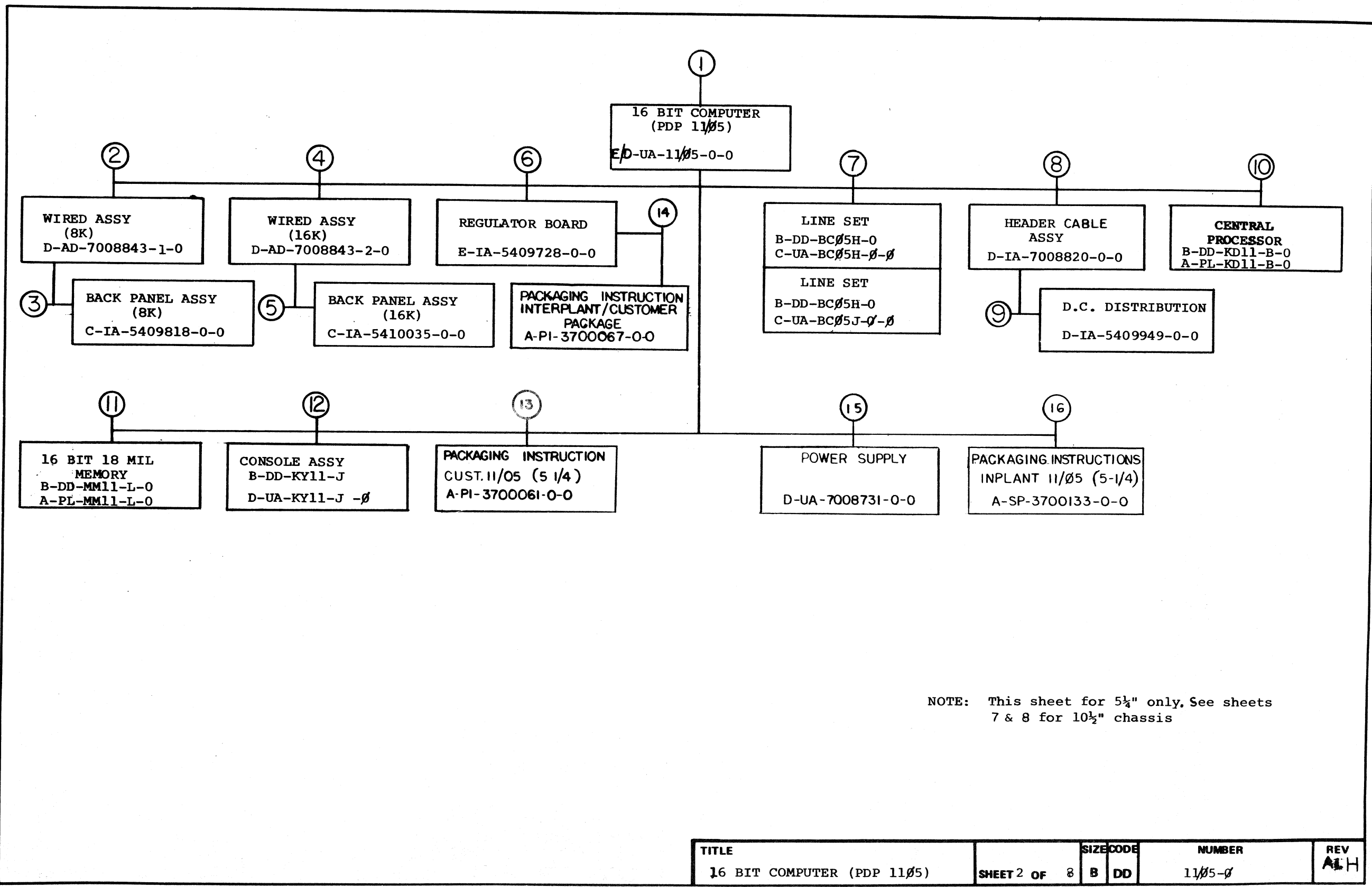
UNIT VARIATIONS		PRINT SET	
VAR	TITLE	11/05-0	
CONFIGURATION #1			
11/05-HA	KD11-B MM11-K 115V/60HZ	X	
11/05-HB	KD11-B MM11-K, 230V/50HZ	X	
11/05-JA	KD11-B MM11-L 115V/60HZ	X	
11/05-JB	KD11-B MM11-L 230V/50HZ	X	
CONFIGURATION #2			
11/05-KA	KD11-B MM11-K 115B/60HZ	X	
11/05-KB	KD11-B MM11-K 230V/50HZ	X	
11/05-LA	KD11-B MM11-L 115V/60HZ	X	
11/05-LB	KD11-B, MM11-L, 230V/50HZ	X	
CONFIGURATION #3			
11/05-MA	KD11-B MM11-K 115V/60HZ	X	
11/05-MB	KD11-B MM11-K 230V/50HZ	X	
11/05-PA	KD11-B MM11-L 115V/60HZ	X	
11/05-PB	KD11-B, MM11-L 230V/50HZ	X	
CONFIGURATION #4			
11/05-NC	KD11-B, MM11-L 115V/60HZ	X	
11/05-ND	KD11-B MM11-L 230V/50HZ	X	
11/05-FA	1105-KA, UC15 FRONT PANEL (KY11-JF) 115V		
11/05-FB	1105-KB, UC15 FRONT PANEL (KY11-JF) 230V		
11/05-FE	1105-LA, UC15 FRONT PANEL (KY11 JF) 115V		
11/05-FF	1105-LB UC15 FRONT PANEL (KY11-JF) 230V		

\* ONLY INCLUDED IN MANUFACTURING PRINT SET.

EN-01062-1A-16-R972-(325)

REVISIONS	CHG. NO.	REV	DATE
	11/05-62	AH	7-76
		REVISED & RETYPED	

USED ON OPTION/MODEL	DRN.	DATE	TITLE			
	J. CAHILL		16 BIT COMPUTER (PDP 1105)			
	CHK'D.					
	C. TESCHNER					
	PROJ ENG.		SIZE	CODE	NUMBER	REV
	B. WEEKS		B	DD	11/05-0	AH
	PROD.					
	R. PETERSON					
	FIELD SERV.					
	D. DICKHUT					
SHEET 1 OF 8						



NOTE: This sheet for 5 1/4" only. See sheets 7 & 8 for 10 1/2" chassis

TITLE	SIZE CODE	NUMBER	REV
16 BIT COMPUTER (PDP 11/05)	B DD	11/05-0	ALH
SHEET 2 OF 8			

CUSTOMER PRINT SET		ELECTRICAL					CUSTOMER PRINT SET		ELECTRICAL									
		MFG. SET	FIND NO.	DRAWING NO.	REV	NO OF SHT	DESCRIPTION	OPTION NO./FILE DATE			MFG. SET	FIND NO.	DRAWING NO.	REV	NO OF SHT	DESCRIPTION	OPTION NO./FILE DATE	
X		X	1	E-IA-7008713-0-0	#	1	AC INPUT HARNESS			C		C	7	B-DD-BC05H-0	#	3	LINE SET	
X		X		E-IA-7008856-0-0	#	1	HARNESS (D.C.)							C-UA-BC05H-0-0	#	1	LINE SET BC05H (115V)	
X		X		A-AL-1105-0-04	A	1	1105 ACCESSORY LIST							C-UA-BC05J-0-0	#	1	LINE SET BC05J (230V)	
X		X		A-SL-1105-0-05	*	1	1105 SOFTWARE LIST											
X		X		D-MU-1105-0-01	*	1	MODULE UTILIZATION (8K)											
X		X		D-MU-1105-0-02	*	1	MODULE UTILIZATION (16K)											
		X		A-SP-1105-0-6		23	1105 ACCEPTANCE PROCEDURE			X	X	8	D-IA-7008820-0-0	#	1	HEADER CABLE ASSY		
X		X		D-IA-7008360-0-0	#	1	CABLE ASSY (KL8E)											
X		X		D-BS-KM11-0-MB	#	3	MAINTENANCE BOARD											
X		X		A-SS-5509081-0-9	#	1	MAINT. MODULE OVERLAY (11/05-KM1)											
X		X		A-SS-5509081-0-10	#	1	MAINT. MODULE OVERLAY (11/05-KM2)											
X		X		D-CS-M930-0-1		1	BUS TERMINATOR					9	D-IA-5409949-0-0	#	1	D.C. DISTRIBUTION		
		X	2	D-AD-7008843-1-0	#	1	WIRED ASSY (8K)			X	X		C-CS-5409949-0-1	#	1	CIRCUIT SCHEMATIC		
		X		K-WL-7008843-1-1	#	1	ETCH/WIRE LIST (8K)						B-MH-5409949-0-6	#	1	MODULE E.C.O. HISTORY		
										C		C	10	B-DD-KD11-B	#	1	CENTRAL PROCESSOR	
			3	C-IA-5409818-0-0	#	1	BACK PANEL ASSY (8K)							A-PL-KD11-B-0-0	#	1	CENTRAL PROCESSOR (PL)	
X		X		C-CS-5409818-0-1	#	2	CIRCUIT SCHEMATIC (8K)											
										C		C	11	B-DD-MM11-L	#	3	16 BIT 18 MIL MEMORY	
X				A-WT-7008843-2	#	1	AVT. REVISION STATUS							A-PL-MM11-L-0-0	#	1	16 BIT 18 MIL MEMORY (PL)	
			4	D-AD-7008843-2-0	#	1	WIRED ASSY (16K)											
		X		K-WL-7008843-2-1	#	1	ETCH/WIRE LIST (16K)											
			5	C-IA-5410035-0-0	#	1	BACK PANEL ASSY (16K)											
X		X		C-CS-5410035-0-1	#	2	CIRCUIT SCHEMATIC (16K)											
			6	E-IA-5409728-0-0	#	1	REGULATOR BOARD											
				D-CS-5409728-0-1	#	1	CIRCUIT SCHEMATIC											
				B-MH-5409728-0-6	#	1	MODULE E.C.O. HISTORY											
										X	X	15	D-UA-7008731-0-0	#	1	POWER SUPPLY (5-1/4 INCHES)		

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CUSTOMER PRINT SET CODES

X = PRINT OF DOCUMENT INCLUDED IN PRINT SET  
C = INCLUDES ALL PRINTS INDICATED ON DOCUMENT  
S = CONFIDENTIAL AUTHORIZED SIGNATURE REQUIRED

TITLE

16 BIT COMPUTER (PDP 11/05)

SHEET 3 OF 8

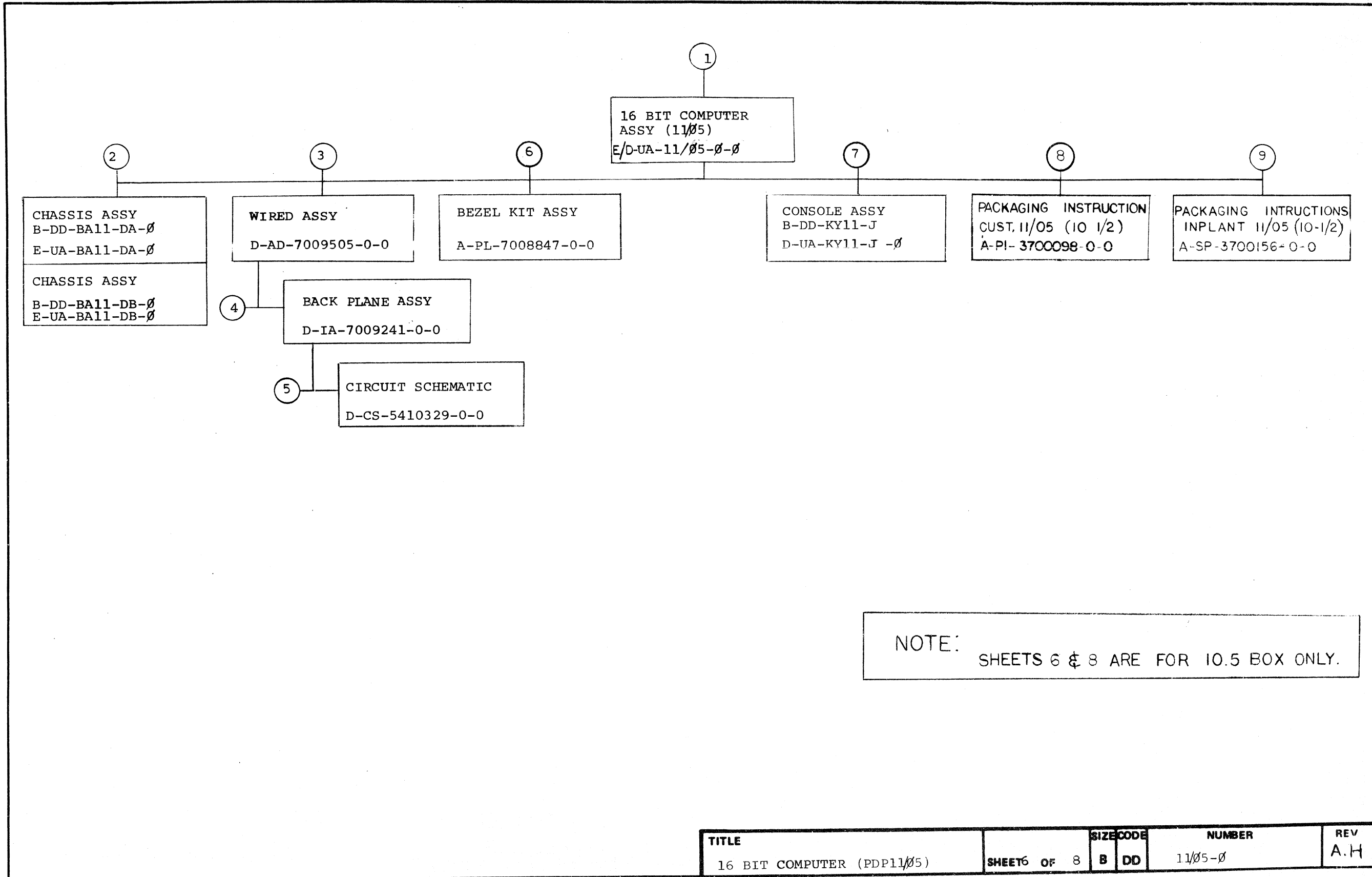
SIZE CODE  
B DD

NUMBER  
11/05-0

REV  
A



CUSTOMER PRINT SET		MECHANICAL					CUSTOMER PRINT SET		MECHANICAL										
		MFG. SET	FIND NO.	REV	NO OF SHT	DESCRIPTION	OPTION NO./FILE DATE			MFG. SET	FIND NO.	REV	NO OF SHT	DESCRIPTION	OPTION NO./FILE DATE				
			8		1	HEADER CABLE ASSY				X	14		2	REGULATOR BOARD PKG. INST.					
													1	REGULAR SLOTTED CARTON					
													1	TAPED TUBE					
													2	RAT TRAP					
													3	TAPE TUBE					
													3	DIECUT CARTON					
			9		1	D.C. DISTRIBUTION													
					1	X-Y CO-ORDINATE HOLE LOCATION													
					1	ASSY/DRILLING HOLE LAYOUT													
										X	16		3	PKG. INST. INPLANT 11/05 5 1/2					
														LAMINATED BUILDUP					
														LAMINATED BUILDUP					
														REGULAR SLOTTED CARTON					
														POLY BAG					
														CARTON SEALING TAPE					
			10		1	CENTRAL PROCESSOR													
					1	CENTRAL PROCESSOR (PL)													
			11		3	16 BIT 18 MIL MEMORY													
					1	16 BIT 18 MIL MEMORY (PL)													
C		C	12		3	DRAWING DIRECTORY (KY11-J)													
					1	CONSOLE ASSY (PDP 11/05)													
					1	CONSOLE ASSY (PDP 11/05) P.L.													
		X	13		4	11/05 PACKAGING INSTRUCTION													
						CUSTOMER 5 1/2													
						FULL TELESCOPE CAP													
						FOAM PAD													
						LAMINATED BUILDUP													
						PLASTIC STRAPPING													
CUSTOMER PRINT SET CODES							TITLE							SHEET 5 OF 8		NUMBER		REV	
X = PRINT OF DOCUMENT INCLUDED IN PRINT SET							16 BIT COMPUTER							B DD		11/05-0		AH	
C = INCLUDES ALL PRINTS INDICATED ON DOCUMENT																			
S = CONFIDENTIAL AUTHORIZED SIGNATURE REQUIRED																			



NOTE: SHEETS 6 & 8 ARE FOR 10.5 BOX ONLY.

TITLE	SHEET	OF	SIZE	CODE	NUMBER	REV
16 BIT COMPUTER (PDP11/05)	6	8	B	DD	11/05-0	A.H

CUSTOMER PRINT SET		ELECTRICAL					CUSTOMER PRINT SET		MECHANICAL							
	11/05-0	MFG. SET	FIND NO.	DRAWING NO.	REV	NO OF SHT	DESCRIPTION	OPTION NO./FILE DATE	11/05-0	MFG. SET	FIND NO.	DRAWING NO.	REV	NO OF SHT	DESCRIPTION	OPTION NO./FILE DATE
	X		1	E/D-UA-11/05-0-0		5	16 BIT COMPUTER ASSY (11/05)				1	E/D-UA-11/05-0-0		6	16 BIT COMPUTER ASSY (11/05)	
	X	X		D-IA-7009360-0-0		1	CPU POWER HARNESS					D-IA-7009360-0-0		1	CPU POWER HARNESS	
	X	X		O-MU-11/05-0-07		1	MODULE UTILIZATION (10.5)					D-MD-7410664-0-0		1	CONSOLE MTG BRKT (10.5)	
												B-IA-7409729-0-0		1	JUMPER POWER	
	X	X		C-CS-M9970-0-1	#	1	BERG TO BACK PLANE		C	C		B-DD-H750-0	#		H750 POWER SUPPLY	
			2	B-DD-BALL-D		2	DRAWING DIRECTORY (BALL-D)		C	C		B-DD-BC05T-0	#		LINE SET BC05-T	
				E-UA-BALL-D-0		2	CHASSIS ASSY		C	C		B-DD-BC05U-0	#		LINE SET BC05-U	
									X	X		E-IA-7009208-0-0	#	1	DISTRIBUTION BOARD HARNESS (BALL-D)	
			3	D-AD-7009505-0-0		1	WIRED ASSY			X		A-DC-7411305-0-0	#		DECAL (10.5" MODULE UTILIZATION)	
				C-PS-1210698-0-0		1	CARD GUIDE					A-DC-7409478-0-0		1	DECAL, PATENT	
		X		K-WL-11/05-0-8	B	1	ETCH/WIRE LIST (10 1/2)									
			4	D-IA-7009241-0-0		1	BACK PLANE ASSY					B-DD-BALL-D		2	DRAWING DIRECTORY (BALL-D)	
				5010328		1	ETCH CIRCUIT BOARD					E-UA-BALL-D-0		2	CHASSIS ASSY	
				E-PS-1211425-0-0		1	72 PIN CONN BLOCK				4	D-IA-7009241-0-0		1	BACK PLANE ASSY	
				E-PS-1211439-0-0		1	11/05 LOGIC FRAME					5010328		1	ETCH CIRCUIT BOARD	
				E-PS-1210258-0-0		1	288 PIN CONN BLOCK				5	D-CS-5410329-0-1		1	CIRCUIT SCHEMATIC (11/05 10.5)	
	X	X	5	D-CS-5410329-0-1	#	1	CIRCUIT SCHEMATIC (11/05 10.5)				6	A-PL-7008847-0-0		1	BEZEL KIT ASSY	
				K-CO-5410329-0-4		1	X-Y COORDINATE HOLE LOCATION					D-IA-7409683-0-0		1	BEZEL	
				E-AH-5410329-0-5		1	ASSY/DRILLING HOLE LAYOUT					A-PS-1211221-0-0		1	STRIP MTG	
				B-MH-5410329-0-6		1	MODULE ECO HISTORY				7	B-DD-KY11-J		3	DRAWING DIRECTORY (KY11-J)	
				5010328		1	ETCH CIRCUIT BOARD					D-UA-KY11-J-0		1	CONSOLE ASSY (PDP11/05)	
												A-PL-KY11-J-0		1	CONSOLE ASSY (PDP11/05) PL	

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CUSTOMER PRINT SET CODES	X = PRINT OF DOCUMENT INCLUDED IN PRINT SET C = INCLUDES ALL PRINTS INDICATED ON DOCUMENT S = CONFIDENTIAL AUTHORIZED SIGNATURE REQUIRED	TITLE			SIZE	CODE	NUMBER	REV
		16 BIT COMPUTER (PDP11/05)			B	DD	11/05-0	AH

SHEET 7 OF 8

CUSTOMER PRINT SET		MECHANICAL					CUSTOMER PRINT SET								
	MFG. SET	FIND NO.	DRAWING NO.	REV	NO OF SHT	DESCRIPTION	OPTION NO./FILE DATE		MFG. SET	FIND NO.	DRAWING NO.	REV	NO OF SHT	DESCRIPTION	OPTION NO./FILE DATE
	X	8	A-SP-3700098-0-0		4	PKG. INST. CUSTOMER 11/05 (10-1/2)									
			A-PS-9905647-0-0			FULL TELESCOPE CAP									
			A-PS-9905648-0-0			FOAM PAD									
			A-PS-9905641-0-0			LAMINATED BUILDUP									
			A-PS-9905734-0-0			PLASTIC STRAPPING									
	X	9	A-SP-3700156-0-0		3	PKG. INST. INPLANT 11/05 (10 1/2)									
			A-PS-9905417-0-0			REGULAR SLOTTED CARTON									
			A-PS-9905335-0-0			LAMINATED BUILDUP									
			A-PS-9905333-0-0			LAMINATED BUILDUP									
			A-PS-9905332-0-0			LAMINATED BUILDUP									
			A-PS-9905129-0-0			POLY BAG									
			A-PS-9905729-0-0			CARTON SEALING TAPE									
CUSTOMER PRINT SET CODES		X = PRINT OF DOCUMENT INCLUDED IN PRINT SET C = INCLUDES ALL PRINTS INDICATED ON DOCUMENT S = CONFIDENTIAL AUTHORIZED SIGNATURE REQUIRED					TITLE 16 BIT COMPUTER (PDP1105)		SIZE CODE B DD		NUMBER 11/05-0		REV AH		

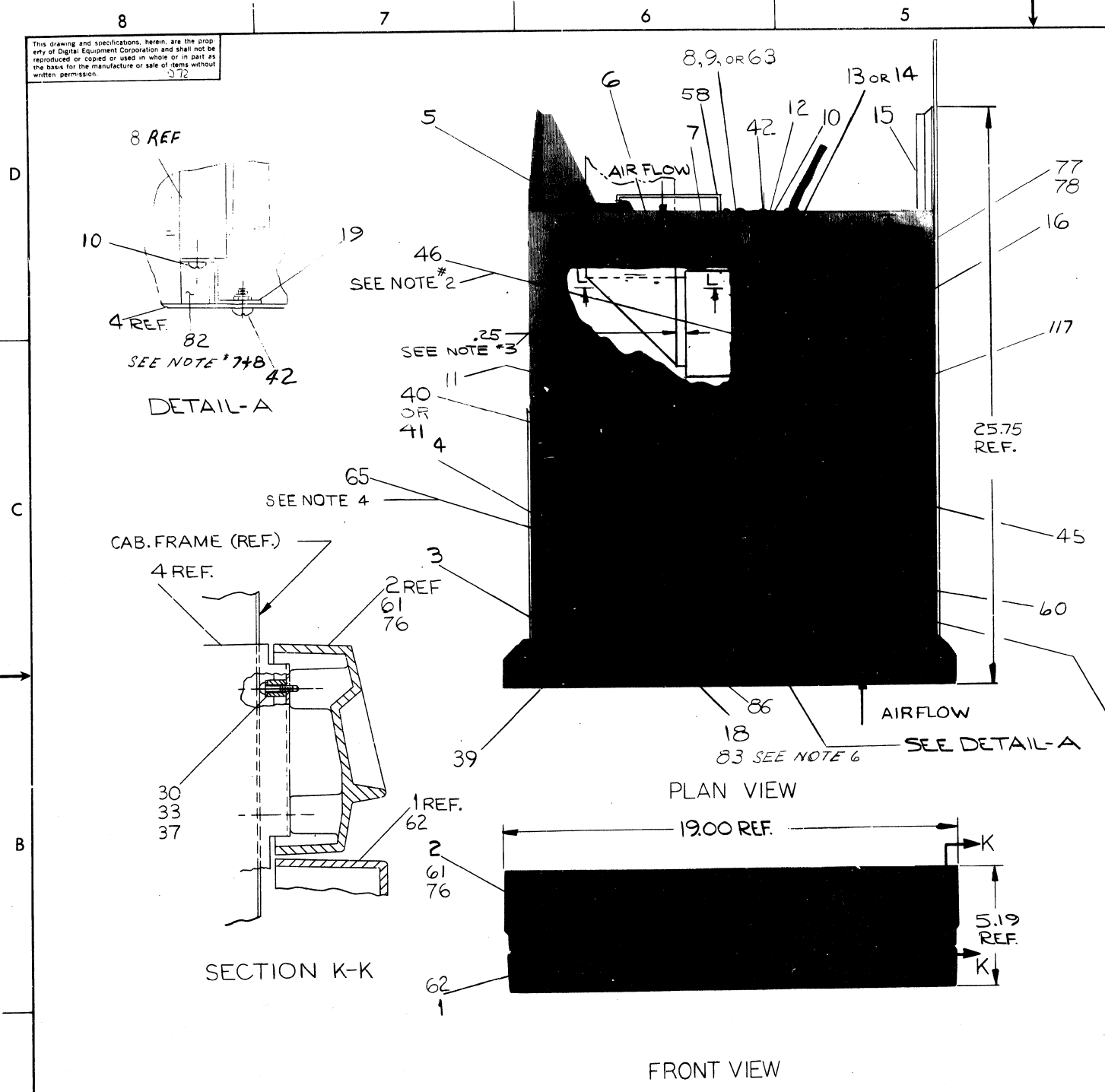
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WIRED TABLE				
PART NO.	COLOR	FROM HARNESS LEAD NO.	TO SYSTEM LOCATION	REMARKS
7008713	RED	J1	51-2	VIEW E-E
	RED	J2	51-1	
	VIO	J3	51-6	
	BLK	J4	51-5	
	RED	J12	H740 P.S. J3	THERMAL INTERLOCK
	RED	J11	H740 FAN	P.S. FAN TAB
	WHT	J10	H740 FAN	P.S. FAN TAB
		J6	P9	BACK OF CHASSIS
		J5	P8	BACK OF CHASSIS
		J9	TRANSFORMER P5	VIEW H-H
		P1	A.C. INPUT BOX J13	A.C. INPUT BOX
	RED	J7	11/05 FAN	CHASSIS FAN
7008713	WHT	J8	11/05 FAN	CHASSIS FAN
7008856		P1	H740 P.S. J2	P.S. MAIN-N-LOCK
	VIO	8	BP-1	VIEW F-F
	RED	9	BP-3	
	YEL	10	BP-2	
	BLK	11	BP-4	
	BLU	12	BP-5	
	ORN	13	LOGIC POINT COLU	
	BRN	14	LOGIC POINT COLU	+10
	BLK	15	BP-6	VIEW F-F
7008856	RED	16	BP-7	
		TRANSFORMER P2	7009721 P/S J1	VIEW H-H & POWER CARD TO TRANSFORMER
7409729-2	RED	B.P.-7	CONSOLE +5	CONSOLE TO BACK PLANE & VIEW D-D
7409729-1	BLK	B.P.-6	CONSOLE GND	CONSOLE TO BACK PLANE & VIEW D-D
7409903	BLK	B.P.-6	BACK PLANE-GND	BACK PLANE TO CHASSIS-DETAIL-A

- NOTES:
- INSTALLATION OF I/O CABLE, WHICH IS ALREADY CONNECTED TO ITEM # (2), CONSOLE ASSY, IS AS FOLLOWS. RUN I/O CABLE DOWN INNER SIDE OF ITEM # (18) CARD GUIDE ASSY. SLIDE CABLE THROUGH OPENING ON BOTTOM OF ITEM # (18) CARD GUIDE ASSY, TO ONE OF THE THREE EXIT LOCATIONS FOR THE CABLE, AFTER EXIT LOCATION IS DECIDED. FOLD CABLE 90° AND HOLD IN PLACE BY USING ITEM # 35 (CLAMP) AND ITEM # 46 (TAPE) AS SHOWN IN VIEW C-C.
  - USE TAPE (ITEM # 46) TO HOLD HEADER CABLE ASSY TO CHASSIS.
  - FOLD CABLE 90° AS SHOWN TO INSURE CORRECT LOCATION IN CABLE CLAMP.
  - ITEM # 65 (ELECTROMAGNETIC SHIELD) GOES BETWEEN CENTRAL PROCESSOR BOARDS & MEMORY BOARDS.
  - ITEMS # 79 & 80 (THREAD CUTTING SCREWS) MUST BE ASSEMBLED USING A TORQUE SCREW DRIVER. SCREWS WILL BE ATTACHED USING 8 INCH POUNDS OF TORQUE.
  - ADHERE FOAM (9009542) TO TOP OF CARD GUIDE (\*1211099).
  - ADHERE FOAM (9009543) TO END OF LOGIC FRAME.
  - DONOT ADD ITEM # 82 TO VARIATIONS MA, MB, PA, OR PB.
  - INSTALL GAFFLE (1211301) IN UNUSED SLOTS ON RIGHT HAND CARD GUIDE (1211099).

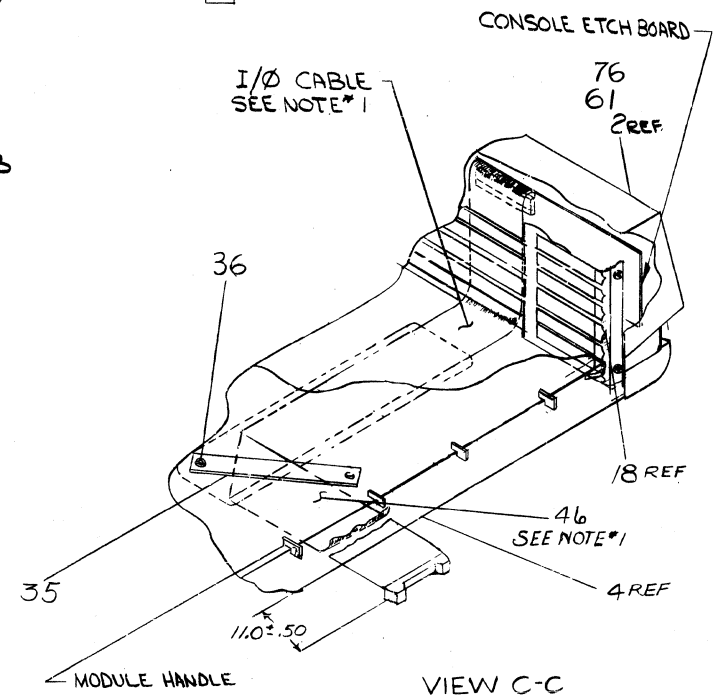
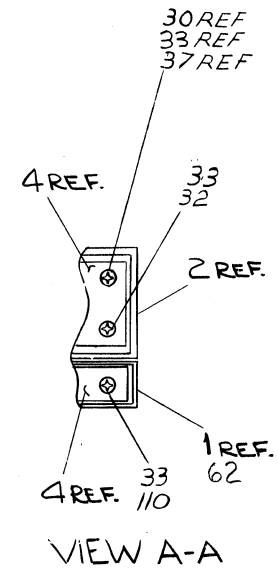
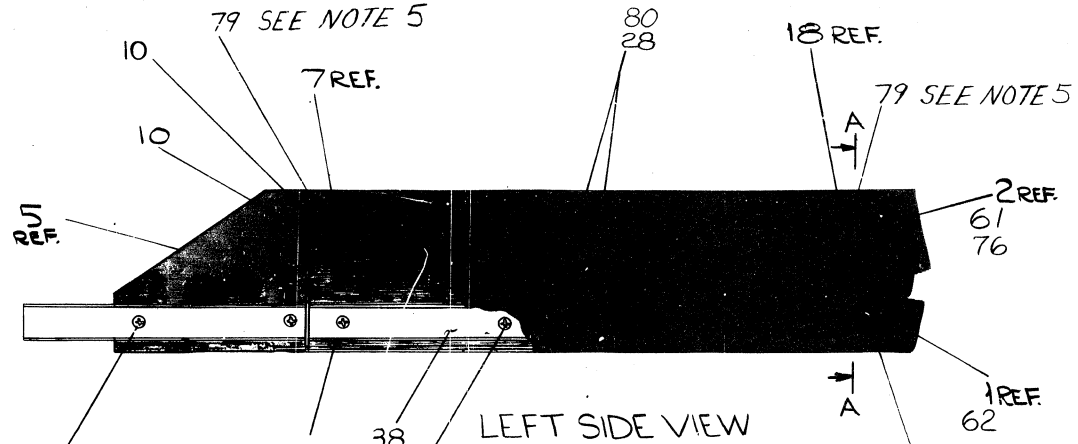
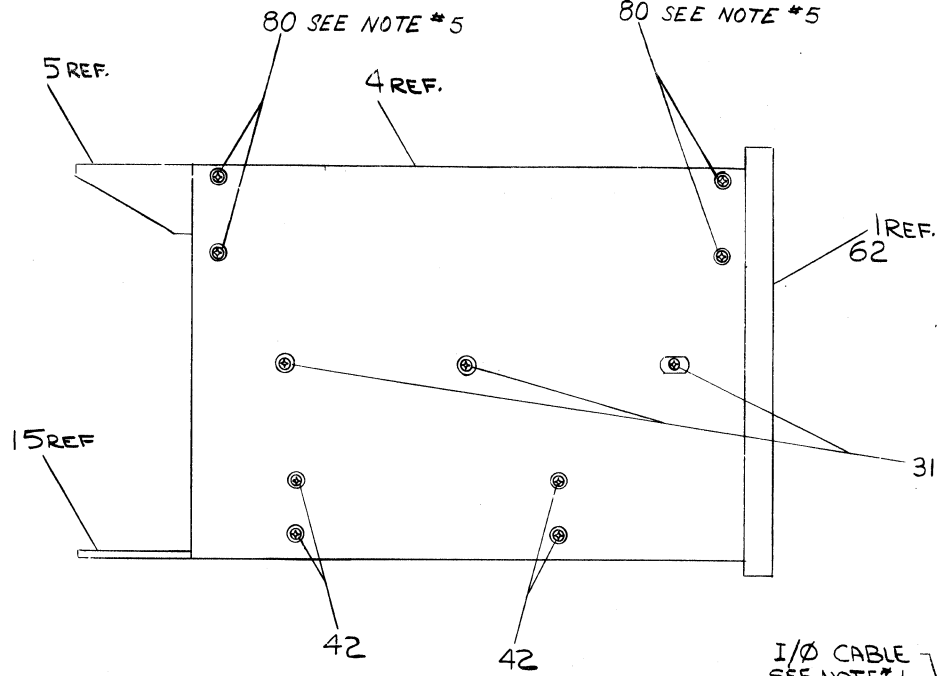
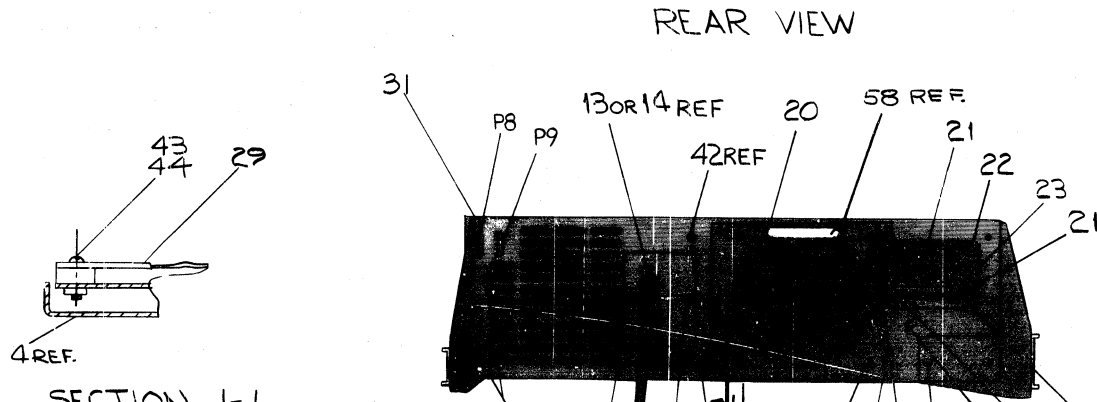
THIS CONNECTION IS SHOWN 90° OUT OF POSITION FOR CLARITY.

REV	CHANGE NO.	REV
1	1105-00017	A
2	1105-00017	A
3	1105-00030	B
4	1105-00034	C
5	1105-00035	D
6	1105-00039	E
7	1105-00041	H
8	1105-00044	I
9	1105-00046	K
10	1105-00047	L
11	1105-00051	P
12	1105-00052	R
13	1105-00053	S
14	1105-00055	T
15	1105-00056	U
16	1105-00058	V
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48	1105-00092	BD
49	1105-00093	BE
50	1105-00094	BF
51	1105-00095	BG
52	1105-00096	BH
53	1105-00097	BI
54	1105-00098	BJ
55	1105-00099	BK
56	1105-00100	BL
57	1105-00101	BM
58	1105-00102	BN
59	1105-00103	BO
60	1105-00104	BP
61	1105-00105	BQ
62	1105-00106	BR
63	1105-00107	BS
64	1105-00108	BT
65	1105-00109	BU
66	1105-00110	BV
67	1105-00111	BW
68	1105-00112	BX
69	1105-00113	BY
70	1105-00114	BZ
71	1105-00115	CA
72	1105-00116	CB
73	1105-00117	CC
74	1105-00118	CD
75	1105-00119	CE
76	1105-00120	CF
77	1105-00121	CG
78	1105-00122	CH
79	1105-00123	CI
80	1105-00124	CJ
81	1105-00125	CK
82	1105-00126	CL
83	1105-00127	CM
84	1105-00128	CN
85	1105-00129	CO
86	1105-00130	CP
87	1105-00131	CQ
88	1105-00132	CR
89	1105-00133	CS
90	1105-00134	CT
91	1105-00135	CU
92	1105-00136	CV
93	1105-00137	CW
94	1105-00138	CX
95	1105-00139	CY
96	1105-00140	CZ
97	1105-00141	DA
98	1105-00142	DB
99	1105-00143	DC
100	1105-00144	DD

FIRST USED ON OPTION/MODEL	QTY.	DESCRIPTION	PART NO.	ITEM NO.
PDP 1105				
PARTS LIST				
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES. TOLERANCES				
DECIMALS	ANGLES	TITLE		
XXX + .005	± 0° 30'	16 BIT COMPUTER ASSY (PDP 1105)		
XX + .02		SCALE NONE		
X + .1		SHEET 1 OF 5		
MATERIAL		NEXT HIGHER ASSY.	SIZE CODE	NUMBER
FINISH		B-DD-1105-0	DUA	1105-0-0
				REV. Z

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2 0-0-0011 1105 Z



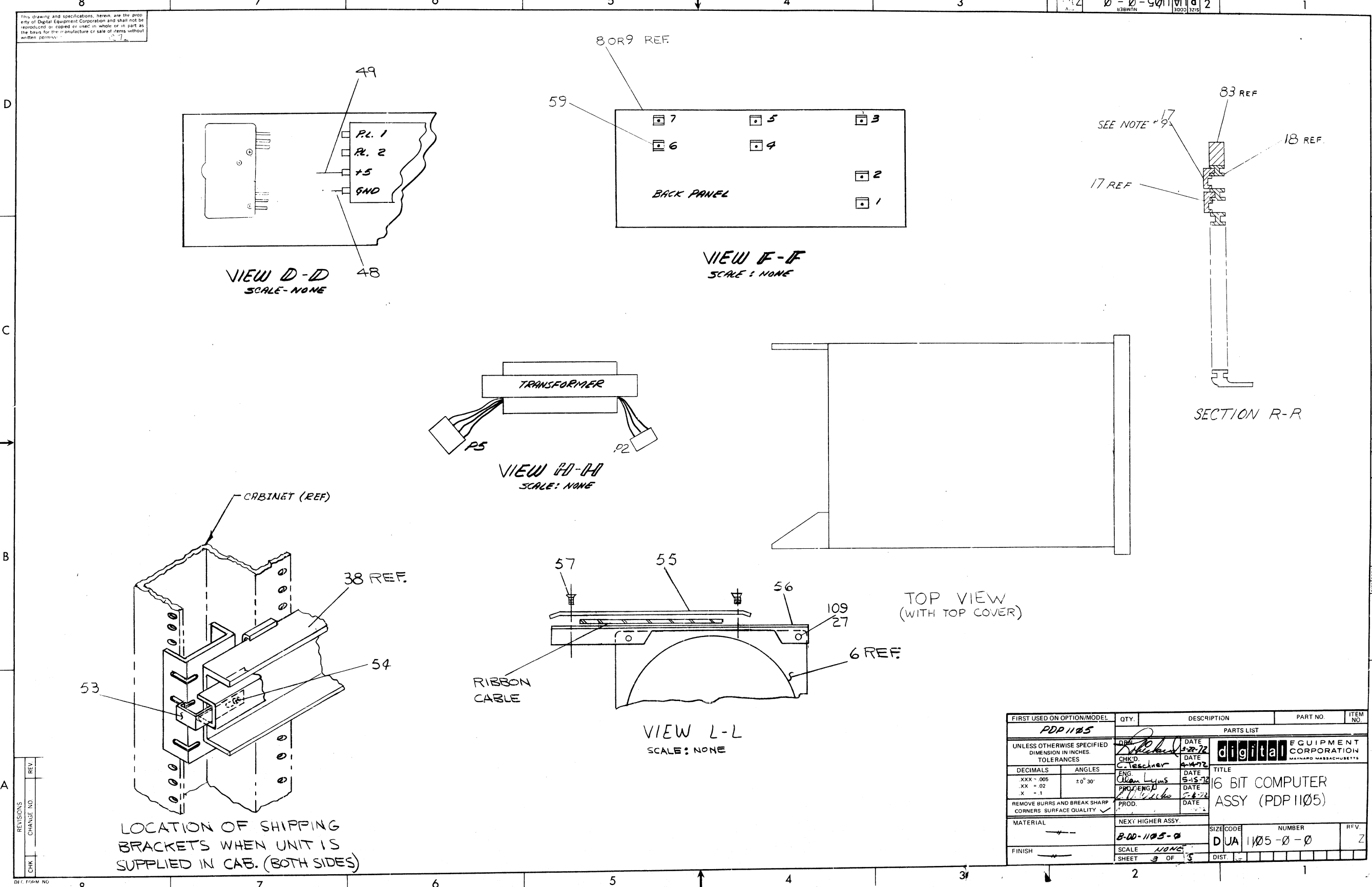
SEE VIEW C-C

REVISIONS	REV	
CHANGE NO		
CHK		

FIRST USED ON OPTION/MODEL	QTY.	DESCRIPTION	PART NO	ITEM NO
PDP 1105				
PARTS LIST				
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES TOLERANCES	BRN DATE 2-21-72	DATE 2-21-72	 digital EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS	
DECIMALS .005	CHP DATE 2-14-72	DATE 2-14-72		
ANGLES ± 0° 30'	ENG DATE 5-16-72	DATE 5-16-72	TITLE 16 BIT COMPUTER ASSY (PDP 1105)	
XXX - .005 XX - .02 X - .1	PROJ ENG DATE 5-16-72	DATE 5-16-72		
REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY	PROD DATE 12/20/72	DATE 12/20/72		
MATERIAL	NEXT HIGHER ASSY		SIZE CODE	NUMBER
FINISH	B-DD-1105-0		DUA	1105-0-0
	SCALE NONE	SHEET 2 OF 5	DIST 10	REV Z

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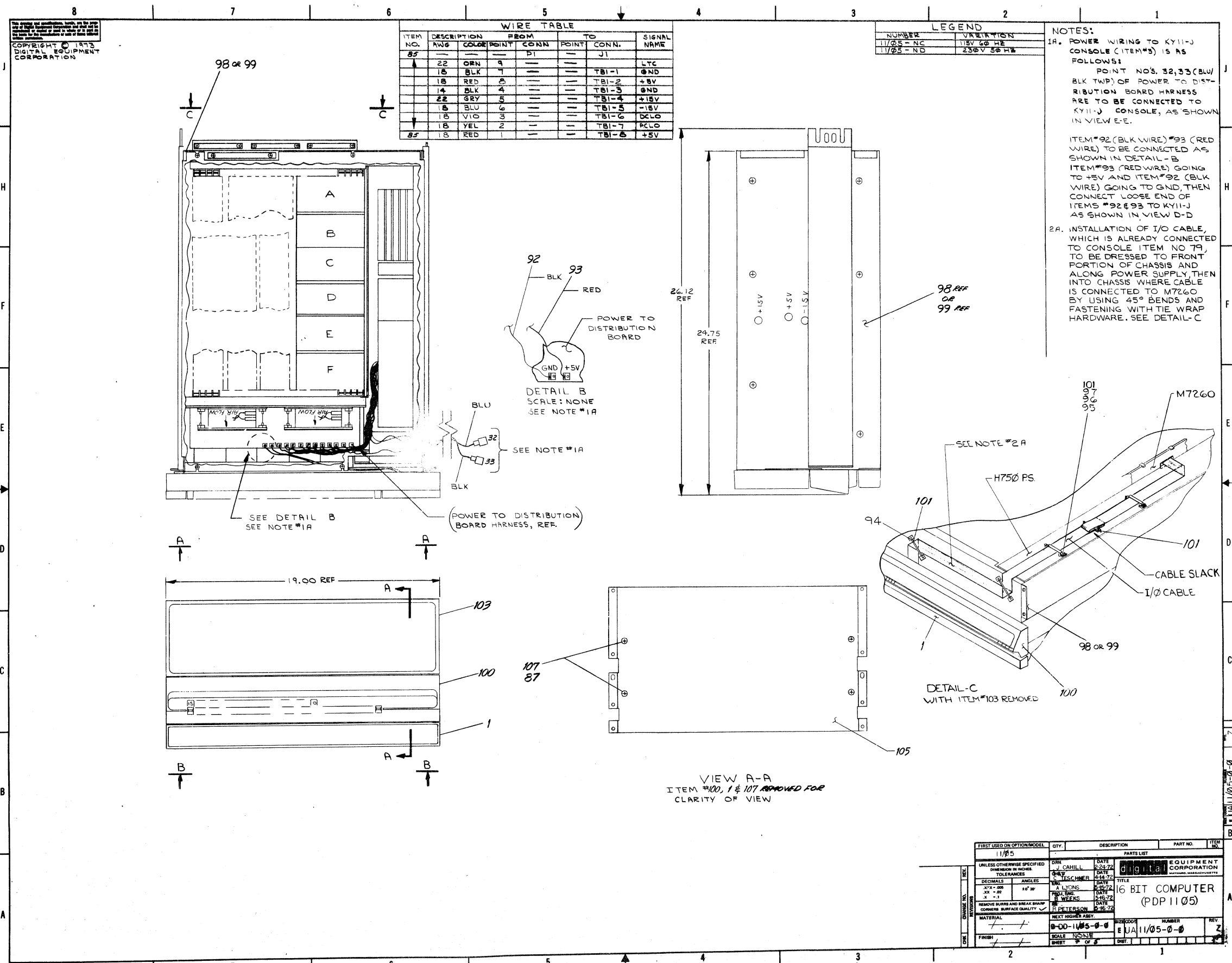
Ø - Ø - 9011 V A 2



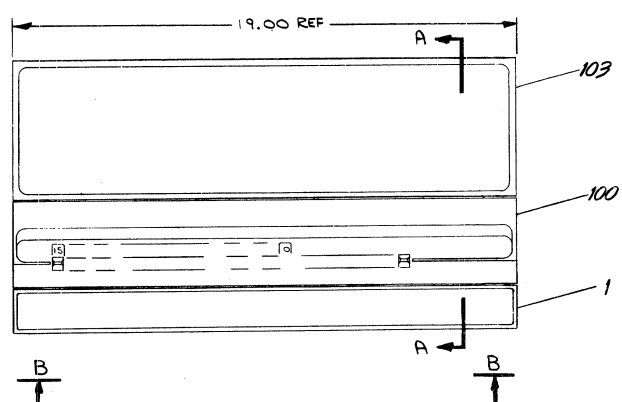
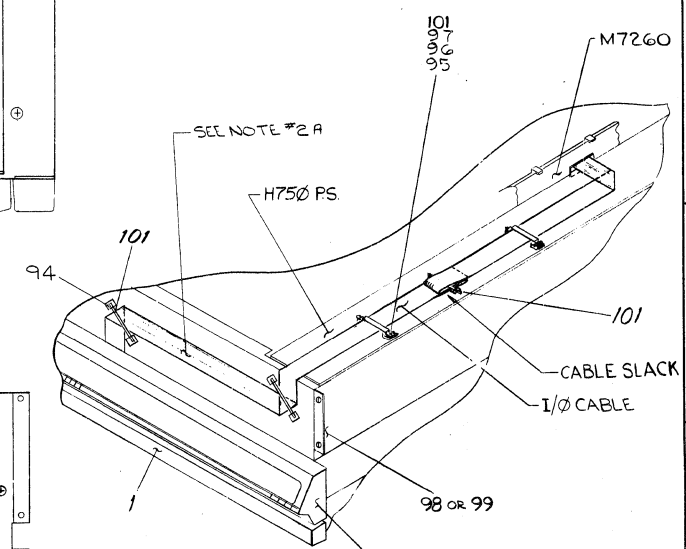
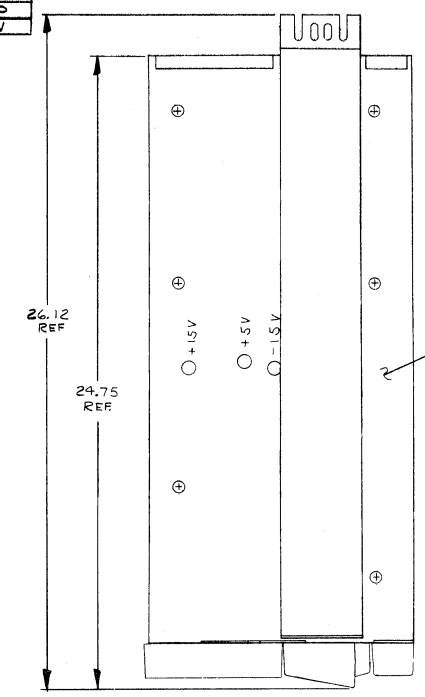
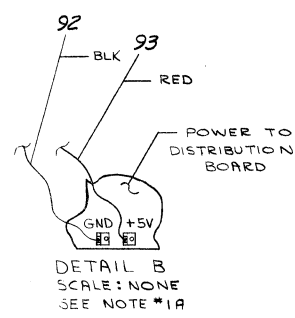
REV	NO
CHG	NO
REVISIONS	CHANGE NO

DEF. FORM NO. 1100-1

FIRST USED ON OPTION/MODEL <b>PDP 1105</b>	QTY.	DESCRIPTION	PART NO.	ITEM NO.
PARTS LIST				
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES TOLERANCES	DATE 3-27-72	DATE 4-14-72	<b>digital</b> EQUIPMENT CORPORATION MAYNARD MASSACHUSETTS <b>16 BIT COMPUTER          ASSY (PDP 1105)</b>	
DECIMALS .XXX ± .005	CHK'D C. Jeschner	DATE 5-15-72		
ANGLES ± 0° 30'	ENG. Alan Lyons	DATE 5-24-72		
REMOVE BURRS AND BREAK SHARP CORNERS. SURFACE QUALITY ✓	PROF/ENG C. W. White	DATE 5-24-72		
MATERIAL	NEXT HIGHER ASSY.	SIZE CODE	NUMBER	REV.
FINISH	<b>B-00-1105-0</b>	<b>D UA</b>	<b>1105-0-0</b>	<b>Z</b>
	SCALE <b>NONE</b>	DIST.		
	SHEET <b>3</b> OF <b>5</b>			



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VIEW A-A  
 ITEM #100, 1 & 107 REMOVED FOR CLARITY OF VIEW

REV.	DESCRIPTION	DATE	BY	CHKD.	DATE	BY	CHKD.
1	ISSUED FOR FABRICATION	2-23-72	D. J. CAMILLI		2-23-72		
2	REVISED TO ADD I/O CABLE	4-14-72	C. J. FISCHER		4-14-72		
3	REVISED TO ADD TIE WRAP	5-16-72	A. LYONS		5-16-72		
4	REVISED TO ADD I/O CABLE	5-16-72	B. WEEKS		5-16-72		
5	REVISED TO ADD I/O CABLE	5-16-72	R. PETERSON		5-16-72		

**PARTS LIST**

QTY.	DESCRIPTION	PART NO.	ITEM NO.
1	16 BIT COMPUTER (PDP 11/05)		

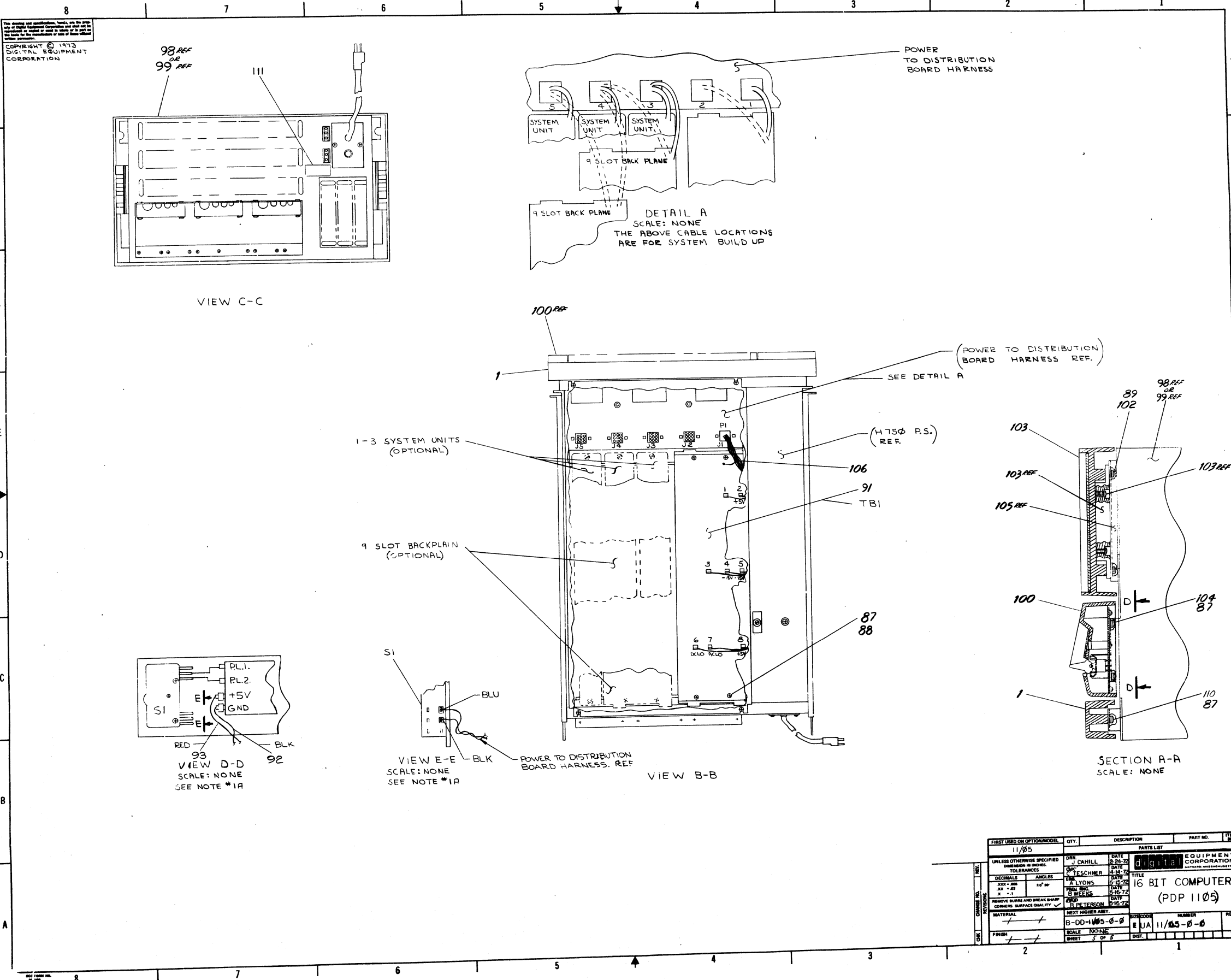
**REVISIONS**

REV.	DESCRIPTION	DATE	BY	CHKD.
1	ISSUED FOR FABRICATION	2-23-72	D. J. CAMILLI	
2	REVISED TO ADD I/O CABLE	4-14-72	C. J. FISCHER	
3	REVISED TO ADD TIE WRAP	5-16-72	A. LYONS	
4	REVISED TO ADD I/O CABLE	5-16-72	B. WEEKS	
5	REVISED TO ADD I/O CABLE	5-16-72	R. PETERSON	

**PROPERTY**

DATE	BY	DESCRIPTION

**SCALE** NS/NSE  
**SHEET** 1 OF 2



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CORPORATION

REV.	DATE	DESCRIPTION	BY	CHKD.	APP'D.
1	11/05	16 BIT COMPUTER (PDP 1105)	J. CAHILL		
2	4/16/72		C. TESCHNER		
3	2/15/72		A. LYONS		
4	5/16/72		B. WEEKS		
5	5/16/72		R. PETERSON		

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ITEM NO.	DWG. NO./PART NO.	DESCRIPTION	QUANTITY / VARIATION															
			1105-PA	1105-PB	1105-PC	1105-PD	1105-PE	1105-PF	1105-PG	1105-PH	1105-PI	1105-PJ	1105-PK	1105-PL	1105-PM	1105-PN	1105-PO	
1	D-PS-1211474-0-0	1.75 FILLER STRIP	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	
2	D-UA-KY11-JA-0	CONSOLE ASSY	X	X	X	X	1	1	1	1	1	1	1	1	1	1	1	
3	C-IA-7409460-0-0	COVER, TOP	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	
4	E-IA-7409458-0-0	CHASSIS	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	
5	D-IA-7409459-0-0	BRACKET, CHASSIS SLIDE (L.H.)	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	
6	1209403	FAN, SUPER BOXER	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	
7	E-PS-1211100-0-0	CARD GUIDE ASSY (L.H.)	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	
8	D-AD-7008843-1-0	BACK PANEL ASSY (8K)	1	1	1	1	X	X	X	X	1	1	1	1	X	X	X	
9	D-AD-7008843-2-0	BACK PANEL ASSY (16K)	X	X	X	X	1	1	1	1	X	X	X	X	X	X	X	
10	9009191	SCR PHL HD PAN NYLON *8-32 x 1/4 LG	18	18	18	18	18	18	18	18	18	18	18	18	18	18	18	
11	D-IA-7008856-0-0	HARNES (D.C.)	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	
12	B-IA-7411167-0-0	RET. CONN. BLOCK (L.H.)	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	
13	C-UA-BC05H-0-0	LINE SET BC05H (115V/60HZ)	1	X	1	X	1	X	1	X	1	X	1	X	1	X	1	
14	C-UA-BC05J-0-0	LINE SET BC05J (230V/50HZ)	X	1	X	1	X	1	X	1	X	1	X	1	X	1	X	
15	C-IA-7409449-0-0	BRACKET, CHASSIS SLIDE (R.H.)	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	
16	E-IA-7008713-0-0	AC INPUT HARNES	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	
17	D-PS-1211381-0-0	AIR BAFFLE	3	3	3	3	3	3	3	3	3	3	3	3	3	3	3	
18	E-PS-1211099-0-0	CARD GUIDE ASSY REAR	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	
19	B-IA-7411163-0-0	RET. CONN. BLOCK FRONT	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	
20	D-IA-7409463-0-0	FAN MTG PLATE	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	
21	C-MD-7409479-0-0	PLATE, PRESSURE	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	
22	9006047-1	SCR PHL HD PAN *8-32 x 2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	
23	D-PS-1210931-0-0	BLOCK, CABLE RETAINING	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	
24	C-MD-7409480-0-0	SWIVEL NUT	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	
25	D-MD-7409482-0-0	PLATE LOWER RETAINER	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	
26	9006024-1	SCR PHL HD PAN *6-32 x 1/2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	
27	9006560	KEP NUT 6-32	6	6	6	6	6	6	6	6	6	6	6	6	6	6	6	
28	C-PS-1210698-0-0	GUIDE CARD CENTER	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	
29	C-IA-7008820-0-0	HEADER CABLE ASSY	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	
30	9006074-3	SCR PHL HD TRUSS #10-32 x 5/8	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	
31	9009186	SCR PHL HD PAN NYLOC #6-32 x 5/16	6	6	6	6	6	6	6	6	6	6	6	6	6	6	6	
32	9006071-3	SCR PHL HD TRUSS #10-32 x 3/8	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	
33	9007651	WASHER EXT TOOTH #10	6	6	6	6	6	6	6	6	6	6	6	6	6	6	6	
34	D-IA-7409533-0-0	COVER SIDE	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	
35	C-MD-7409591-0-0	CLAMP	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	
36	9009173	SCR BINDER HD NYLON #8-32 x 1/4	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	
37	9008074	SPACER 1/2" AF x 5/16-#10	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	
38	1210945	CHASSIS SLIDE	X	1	1	1	1	1	1	1	1	1	1	X	X	X	X	
39	9008196	CLIP-ON RECPT.-SOUTHCO #82-47-104-15	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	
40	D-MU-1105-01-0	MODULE UTILIZATION (8K)	1	1	1	1	X	X	X	X	1	1	1	1	1	1	1	
41	D-MU-1105-02-0	MODULE UTILIZATION (16K)	X	X	X	X	1	1	1	1	X	X	X	X	X	X	X	
42	9009192	SCR PHL HD PAN NYLOC #8-32 x 3/8	6	6	6	6	6	6	6	6	6	6	6	6	6	6	6	
43	9006005-1	SCR PHL HD PAN #2-56 x 1/2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	

D  
C  
B  
A

D  
C  
B  
A

REV.	CHG. NO.	REV.
F	11/05-00040	
	REVISED & REDRAWN	
	1-24-72 S. Lodge	
	M. TITELBAUM	
	D. Randall	
	11/05-00041	H
	G. GRAHAM	
	2-8-73	J
	11/05-00044	
	S. Charney 5-23-73	
	G. GRAHAM	
	6-4-73	K
	11/05-00046	
	S. Charney 7-17-73	
	G. GRAHAM	
	7-27-73	L
	11/05-00047	
	S. Charney 8-17-73	
	D. RANDALL	
	8-30-73	M
	11/05-00048	
	S. Charney 9-6-73	
	G. GRAHAM	
	9-18-73	N
	11/05-00050	
	S. Charney 11/6/73	
	G. GRAHAM	
	11-7-73	P
	11/05-00051	
	S. Manikowsky 11-26-73	
	G. GRAHAM	
	11-27-73	

FIRST USED ON OPTION/MODEL  
11/05

UNLESS OTHERWISE SPECIFIED	
DIMENSION IN INCHES	
DECIMALS	± .005
FRACTIONS	± 1/64
ANGLES	± 0°30'
FINAL SURFACE QUALITY REMOVE BURRS AND BREAK SHARP CORNERS	
MATERIAL	
FINISH	

UNLESS OTHERWISE SPECIFIED	
DRN.	J. CAHILL
CHK'D.	C. TESCHNER
ENG.	A. LYONS
PROJ. ENG.	B. WEEKS
PROD.	R. PETERSON
DATE	2/25/72
DATE	5/2/72
DATE	5/15/72
DATE	5/16/72
DATE	5/16/72
NEXT HIGHER ASSY.	
D-UA-1105-0-0	
SCALE	1 OF 3
SHEET	1 OF 3

**digital EQUIPMENT CORPORATION**  
MAYNARD, MASSACHUSETTS

TITLE  
16 BIT COMPUTER ASSY (PDP 1105)

SIZE CODE: **CPL** NUMBER: 1105-0-0 REV. **Z**

ITEM NO.	DWG. NO./PART NO.	DESCRIPTION	QUANTITY / VARIATION																			
			1105-FA	1105-FB	1105-FC	1105-FD	1105-FA	1105-FB	1105-FC	1105-FD	1105-FA	1105-FB	1105-FC	1105-FD	1105-FA	1105-FB	1105-FC	1105-FD				
44	9006631	WASHER INT TOOTH #2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	X	X			
45	9008442	CABLE CLAMP (1/4)	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	X	X			
46	9009210-6	SCOTCH BRAND ADHESIVE TRANSFER TAPE	A/R	A/RA	RA/RA	RA/RA	RA/RA	RA/RA	RA/RA	RA/RA	RA/RA	RA/RA	RA/RA	RA/RA	RA/RA	RA/RA	X	X				
47																						
48	7409729-01	JUMPER POWER (BLACK)	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	X	X			
49	7409729-02	JUMPER POWER (RED)	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	X	X		
50	9009224	SCR SLOT PAN HD #8-32 x 1/4 LG	10	10	10	10	10	10	10	10	10	10	10	10	10	10	10	X	X			
51	A-DC-5309900-0-0	POWER CONTROL DECAL (230V)	X	1	X	1	X	1	X	1	X	1	X	1	X	1	X	1	X	X		
52	A-DC-5309899-0-0	POWER CONTROL DECAL (115V)	1	X	1	X	1	X	1	X	1	X	1	X	1	X	1	X	X	X		
53	B-MD-7409816-0-0 *	SHIPPING BRACKET	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	X	X		
54	9008143-1	SCR PHL HD PAN #8-32 X 1/4 THD CUTTING	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	X	X		
55	B-MD-7409817-0-0	PLATE, CABLE CLAMP	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	X	X		
56	C-MD-7409818-0-0	BRACKET CABLE CLAMP	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	X	X		
57	9006011-2	SCR PHL HD FLAT #4-40 X 3/8	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	X	X		
58	B-MD-7409828-0-0	BRACKET	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	X	X		
59	9007694	QUIK DISCONNECT	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	X	X		
60	7409903	JUMPER POWER (BLACK)	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	X	X		
61	D-UA-KY11-JC-0	CONSOLE ASSY	X	X	X	X	X	X	X	X	X	X	X	X	X	X	1	1	1	X	X	
62	D-MD-7409978-0-0	BOTTOM TRIM	X	X	X	X	X	X	X	X	X	X	X	X	X	X	1	1	1	1	X	X
63	D-AD-7009119-0-0	BACK PANEL ASSY	X	X	X	X	X	X	X	X	X	X	X	X	X	X	1	1	1	1	X	X
		* USED ONLY WHEN INSTALLED IN CABINET																				
64	D-IA-7409533-1-0	COVER SIDE	X	X	X	X	X	X	X	X	X	X	X	X	X	X	1	1	1	1	X	X
65	1700021-0	ELECTROMAGNETIC SHIELD 1 OZ. CU	A/R	A/RA	RA/RA	RA/RA	RA/RA	RA/RA	RA/RA	RA/RA	RA/RA	RA/RA	RA/RA	RA/RA	RA/RA	RA/RA	RA/RA	RA/RA	RA/RA	RA/RA	RA/RA	RA/RA
66	E-IA-5309816-0-0	CHASSIS	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	X	X
67	D-IA-7008726-0-0	TRANSFORMER MMC 415°-1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	X	X
68	9006024-1	SCR PHL PAN HD #6-32 X 1.90 LG	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	X	X
69	9006021-1	SCR PHL PAN HD #6-32 X 1.31 LG	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	X	X
70	9006031-1	SCR PHL PAN HD #6-32 X 1.75 LG	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	X	X
71	9006563	NUT KEPS #8-32	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	X	X
72	9006560	NUT KEPS #6-32	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	X	X
73	9007649	WASHER EXT. LOCK #6-32	6	6	6	6	6	6	6	6	6	6	6	6	6	6	6	6	6	6	X	X
74	9007035	GROMMET CATER PILLAR	A/RA	RA/RA	RA/RA	RA/RA	RA/RA	RA/RA	RA/RA	RA/RA	RA/RA	RA/RA	RA/RA	RA/RA	RA/RA	RA/RA	RA/RA	RA/RA	RA/RA	RA/RA	X	X
75	E-IA-5409728-0-0	REGULATOR BOARD	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	X	X
76	D-UA-KY11-JF-0	CONSOLE ASSY	1	1	1	1	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X
77	A-DC-5309413-0-0	SPECIAL DECAL (UL)	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	X	X
78	A-DC-5309414-0-0	SPECIAL DECAL (UL)	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	X	X
79	9008181-1	SCR PHL PAN HD TYPE F 6-32 X .50LG	6	6	6	6	6	6	6	6	6	6	6	6	6	6	6	6	6	6	X	X
80	9009525-1	SCR, PHL PAN HD TYPE F 6-32 X .37LG	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	X	X
81	9009070	SCRPHL FLAT HD SPECIAL 8-32X.81LG	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	4	X	X
82	9009543	FOAM 3/8 X 1 X 3 3/4LG	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	X	X
83	9009542	FOAM 1/2 X 5/16 X 7 9/16LG	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	X	X
84	9009544	FOAM 1/2 X 1/4 X 16 X LG	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	X	X
85	9008848	FIBER WASHER 5/32ID X 1/2 OD X 3/32	6	6	6	6	6	6	6	6	6	6	6	6	6	6	6	6	6	6	X	X
86	A-DC-7411255-0-0	LABEL (REMOVE FOAM)	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	X	X

CHK	REV. CHANGE NO.	REV.
87	11/05-00052	R
	B. Weeks 12-18-73	
	G. GRAHAM	
	J. Graham 1-2-74	S
	J. Charbon 2-22-74	
	GRAHAM	
	J. Graham 3-7-74	
	J.B. 11/05-00055	T
	B. Weeks 3-26-74	
	B. MINOR	
	W.E. 3-29-74	U
	J.B. 11/05-00056	
	B. Weeks 4-16-74	
	B. ARMSTRONG	
	J. B. Weeks 4-24-74	V
	J.B. 11/05-00058	
	J. Charbon 8-16-74	
	MINOR	
	W.E. 8-22-74	
	J.B. 11/05-00059	W
	B. Weeks 12-4-74	
	B. ARMSTRONG	
	R. 12-9-74	Y
	J.B. 11/05-00060	
	J. Charbon 3-17-75	
	B. MINOR	
	W.E. 3-17-75	
	J.C. 11/05-00062	L
	W. Weeks 8-16-76	
	B. MINOR	
	W.S. 20-Aug-76	

FIRST USED ON OPTION/MODEL  
11/05

UNLESS OTHERWISE SPECIFIED	
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES	DRN. J. CAHILL DATE 2/25/72
TOLERANCES	CHK'D. C. TESCHNER DATE 5/2/72
DECIMALS ± .005	ENG. A. LYONS DATE 5/15/72
FRACTIONS ± 1/64	PROJ. ENG. B. WEEKS DATE 5/16/72
ANGLES ± 0°30'	PROD. R. PETERSON DATE 5/16/72
FINAL SURFACE QUALITY REMOVE BURRS AND BREAK SHARP CORNERS	NEXT HIGHER ASSY. D-UA-1105-0-0
MATERIAL	SCALE
FINISH	SHEET 2 OF 3

**digital EQUIPMENT CORPORATION**  
MAYNARD, MASSACHUSETTS

TITLE  
16 BIT COMPUTER ASSY (PDP 1105)

SIZE CODE: **CPL** NUMBER: 1105-0-0 REV. Z

ITEM NO.	DWG. NO./PART NO.	DESCRIPTION	QUANTITY / VARIATION																	
			1105-FA	1105-FB	1105-FC	1105-FD	1105-FA	1105-HB	1105-JA	1105-JB	1105-KA	1105-KB	1105-LA	1105-LB	1105-MA	1105-MB	1105-PA	1105-PB	1105-NC	1105-ND
87	9006635	WASH INT TOOTH #10	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X
88	9006043-1	SCR PHL HD PAN #8-32 x 1.0 LG	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X
89	9006035-1	SCR PHL HD PAN #8-32 x .25 LG	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X
90	D-MU-1105-0-07	MODULE UTILIZATION (16K)	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X
91	D-UA-7009505-0-0	WIRED ASSY	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X
92	B-IA-7409729-03	JUMPER POWER (BLK)	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X
93	B-IA-7409729-04	JUMPER POWER (RED)	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X
94	9000264	CLAMP	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X
95	9007033	TIE WRAP	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X
96	9007649	WASH EXT TOOTH #6	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X
97	9006020-1	SCR PHL HD PAN #6-32 x .1/4 LG	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X
98	E-UA-BALL-DA-0	CHASSIS ASSY (115V)	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X
99	E-UA-BALL-DB-0	CHASSIS ASSY (230V)	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X
100	D-UA-KY11-JD-0	CONSOLE ASSY	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X
101	9008442	TIE WRAP	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X
102	9006634	WASHER INT TOOTH #8	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X
103	A-PL-7008847-0-0	BEZEL KIT ASSY	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X
104	9006071-1	SCR PHL HD PAN #10-32 x .38 LG	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X
105	D-MD-7410664-0-0	CONSOLE MTG BRK'T (10.5)	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X
106	D-IA-7009360-0-0	CPU POWER HARNESS	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X
107	9006070-1	SCR PHL HD PAN #10-32 x .31 LG	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X
108	A-DC-7411305-0-0	DECAL (10 1/2" MODULE UTILIZATION)	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X
109	9006025-1	PHL PAN HD SCREW 6-32 X .62 (SEE NOTE #14 ON DWG)	6	6	6	6	6	6	6	6	6	6	6	6	6	6	6	6	6	6
110	9009573	SCR PHL HD PAN THREAD CUTTING #10-16 X .38 LG	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2
111	A-DC-7409478-0-0	DECAL PATENT	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X
112	A-PI-3700061-0-0	PACKAGING INSTRUCTION (5-1/4) CUST.	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
113	A-PI-3700098-0-0	PACKAGING INSTRUCTION (10-1/2) CUST.	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X
114	9006633	#6 INT TOOTH L WASHER	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2
115	9009165	CLIP MTG (SEE NOTE #14 ON DWG)	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2
116	9008202	CLIP MTG (SEE NOTE #14 ON DWG)	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2
117	D-UA-7008731-0-0	POWER SUPPLY 11/05 (5-1/4 INCHES)	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
118	A-SP-3700133-0-0	PACK. INST. 11/05 INPLANT (5-1/4)	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
119	A-SP-3700156-0-0	PACK. INST. 11/05 INPLANT (10-1/2)	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X

REV.	NO.
CHK	NO.

FIRST USED ON OPTION/MODEL  
1105

UNLESS OTHERWISE SPECIFIED  
DIMENSION IN INCHES  
TOLERANCES  
DECIMALS FRACTIONS ANGLES  
± .005 ± 1/64 ± 0°30'  
FINAL SURFACE QUALITY  
REMOVE BURRS AND BREAK SHARP CORNERS

DRN. J. CAHILL DATE 2/25/72  
CHK'D. C. TESCHNER DATE 5/2/72  
ENG. A. LYONS DATE 5/15/72  
PROJ. ENG. B. WEEKS DATE 5/18/72  
PROD. R. PETERSON DATE 5/16/72

**digital EQUIPMENT CORPORATION**  
MAYNARD, MASSACHUSETTS  
TITLE  
16 BIT COMPUTER ASSY (PDP1105)

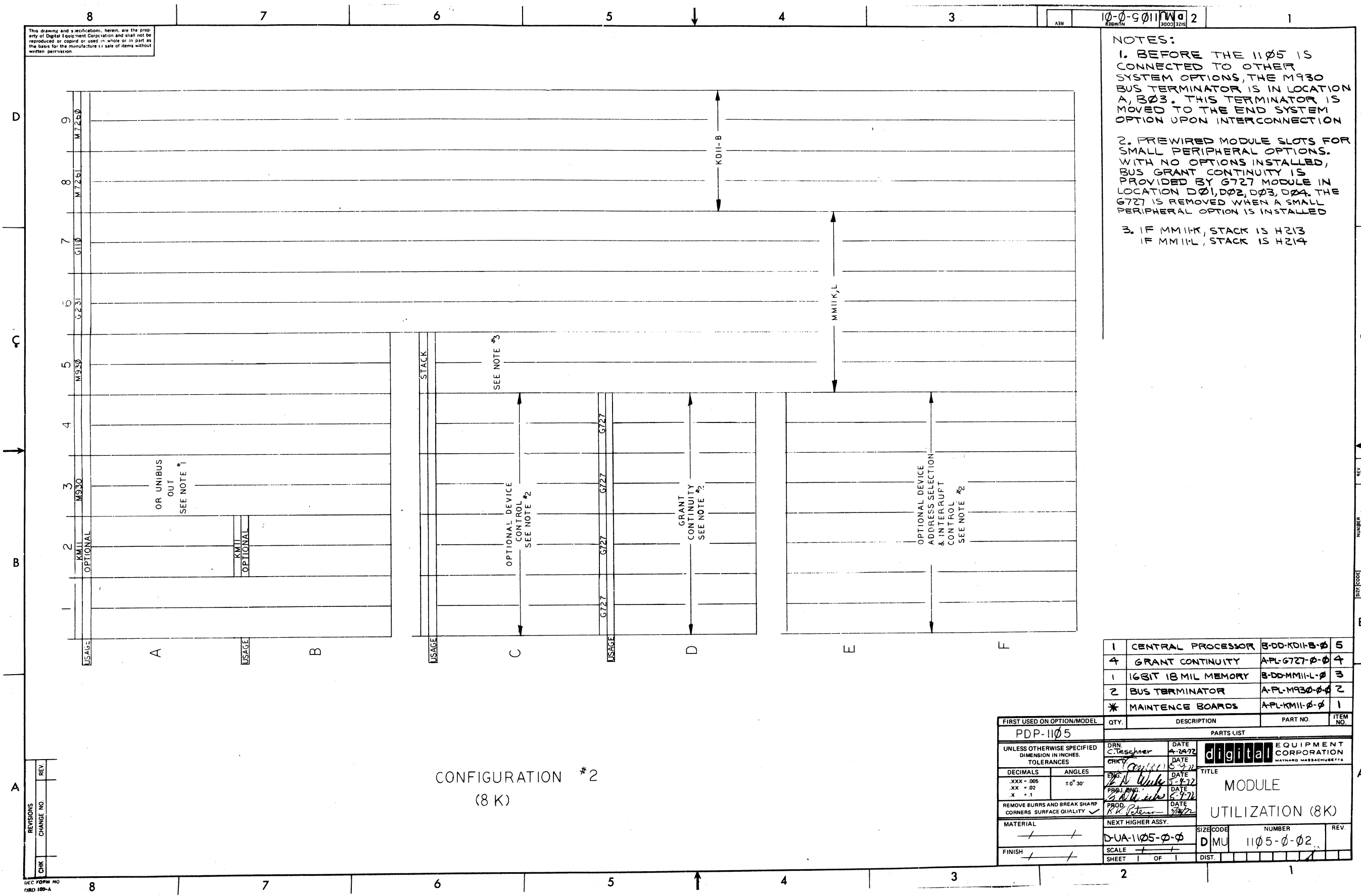
MATERIAL  
NEXT HIGHER ASSY.  
B-DD-1105-0

SCALE + +  
SHEET 3 OF 3  
SIZE CODE C PL  
NUMBER 1105-0-0  
REV. Z



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- NOTES:
- BEFORE THE 1105 IS CONNECTED TO OTHER SYSTEM OPTIONS, THE M930 BUS TERMINATOR IS IN LOCATION A, B03. THIS TERMINATOR IS MOVED TO THE END SYSTEM OPTION UPON INTERCONNECTION
  - PREWIRED MODULE SLOTS FOR SMALL PERIPHERAL OPTIONS. WITH NO OPTIONS INSTALLED, BUS GRANT CONTINUITY IS PROVIDED BY G727 MODULE IN LOCATION D01, D02, D03, D04. THE G727 IS REMOVED WHEN A SMALL PERIPHERAL OPTION IS INSTALLED
  - IF MM11K, STACK IS H213  
IF MM11L, STACK IS H214



1	CENTRAL PROCESSOR	B-DD-K011-B-0	5
4	GRANT CONTINUITY	A-PL-G727-0-0	4
1	16 BIT 18 MIL MEMORY	B-DD-MM11-L-0	3
2	BUS TERMINATOR	A-PL-M930-0-0	2
*	MAINTENANCE BOARDS	A-PL-KMI1-0-0	1

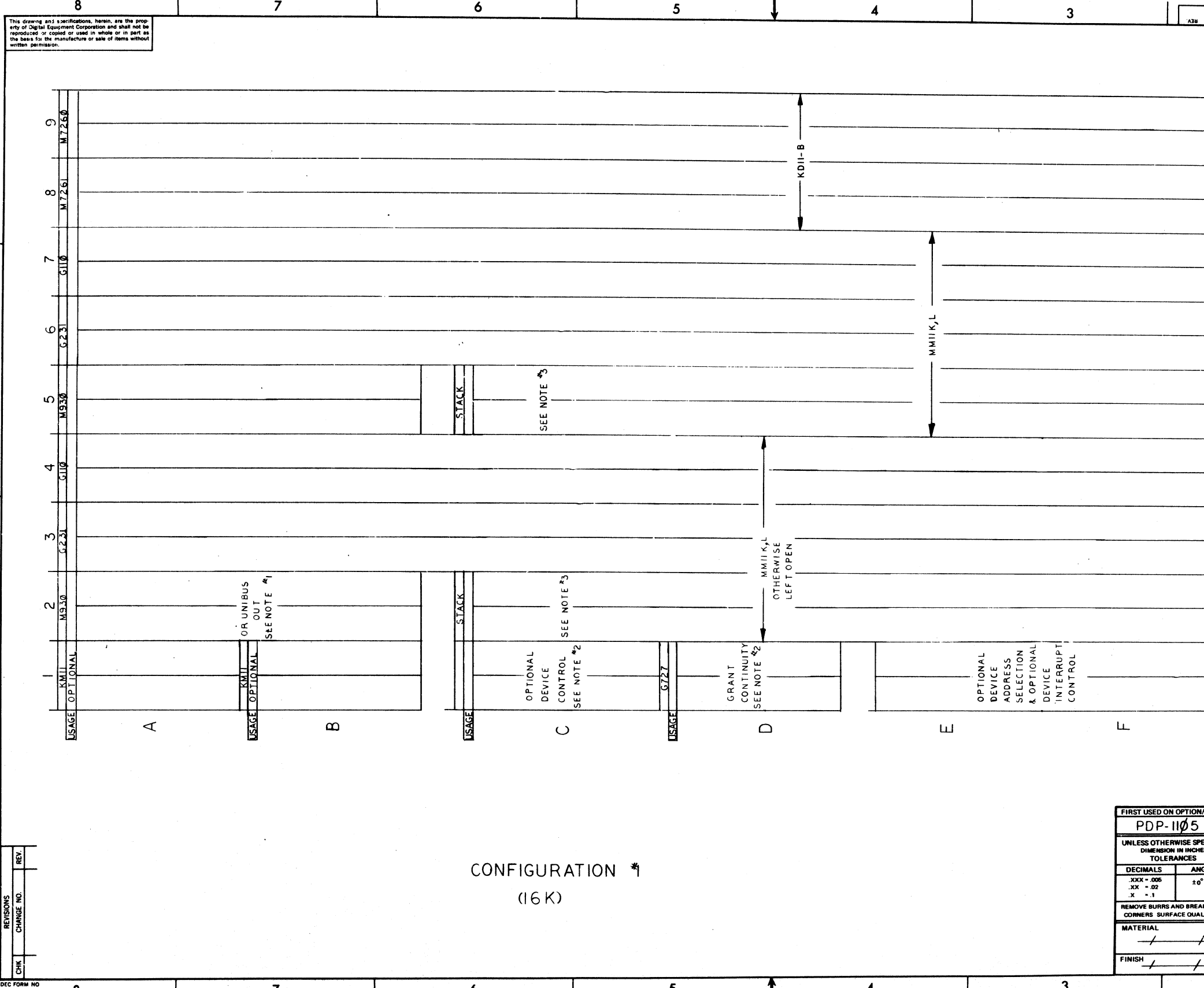
FIRST USED ON OPTION/MODEL PDP-1105		QTY.		DESCRIPTION		PART NO.		ITEM NO.	
PARTS LIST									
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES. TOLERANCES		DRN. C. Teschner	DATE 4-24-72	digital EQUIPMENT CORPORATION MAYNARD MASSACHUSETTS					
DECIMALS	ANGLES	CHKD. <i>(Signature)</i>	DATE 5-9-72	TITLE MODULE UTILIZATION (8K)					
.XXX = .005	±0° 30'	ENG. <i>(Signature)</i>	DATE 5-9-72	MATERIAL					
.XX = .02		FRG. ENG. <i>(Signature)</i>	DATE 5-9-72	NEXT HIGHER ASSY.					
.X = .1		PROD. R.R. Peters	DATE 7/8/72	FINISH					
REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY		SCALE		SHEET		OF		DIST.	
		D-UA-1105-0-0		SIZE CODE		NUMBER		REV.	
		1 OF 1		DMU		1105-0-02			

CONFIGURATION #2  
(8K)

REVISIONS	REV.
CHANGE NO.	
CHK	

REV. NUMBER  
DMU 1105-0-02

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- NOTES:**
- BEFORE THE 1105 IS CONNECTED TO OTHER SYSTEM OPTIONS, THE M930 BUS TERMINATOR IS IN LOCATION A, B02. THIS TERMINATOR IS MOVED TO THE END SYSTEM OPTION UPON INTERCONNECTION
  - PREWIRED MODULE SLOTS FOR SMALL PERIPHERAL OPTIONS. WITH NO OPTIONS INSTALLED, BUS GRANT CONTINUITY IS PROVIDED BY G727 MODULE IN LOCATION D01. THE G727 IS REMOVED WHEN A SMALL PERIPHERAL OPTION IS INSTALLED
  - IF MMIIK, STACK IS H213  
IF MMIIK,L, STACK IS H214

CONFIGURATION \*1  
(16K)

QTY.	DESCRIPTION	PART NO.	ITEM NO.
1	CENTRAL PROCESSOR	B-DD-KDII-B-0	5
1	GRANT CONTINUITY	A-PL-G727-0-0	4
1	16BIT 18 MIL MEMORY	B-DD-MMII-L-0	3
2	BUS TERMINATOR	A-PL-M930-0-0	2
*	MAINTENANCE BOARDS	A-PL-KM11-0-0	1

FIRST USED ON OPTION/MODEL		PARTS LIST	
PDP-1105		QTY.	DESCRIPTION
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES. TOLERANCES		DRN. C. Teschner	DATE 4-24-72
DECIMALS .XXX - .005	ANGLES ±0° 30'	CHK. [Signature]	DATE 5-17-72
.XX - .02		ENG. [Signature]	DATE 5-9-72
.X - .1		PROV. ENG. [Signature]	DATE 5-9-72
REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY		PROD. [Signature]	DATE 5/12/72
MATERIAL	NEXT HIGHER ASSY.	TITLE	
FINISH		MODULE UTILIZATION (16K)	
	DUA-1105-0-0	SIZE CODE	NUMBER
	SCALE	DMU	1105-0-01
	SHEET 1 OF 1	DIST.	

REV.	CHG.	NO.

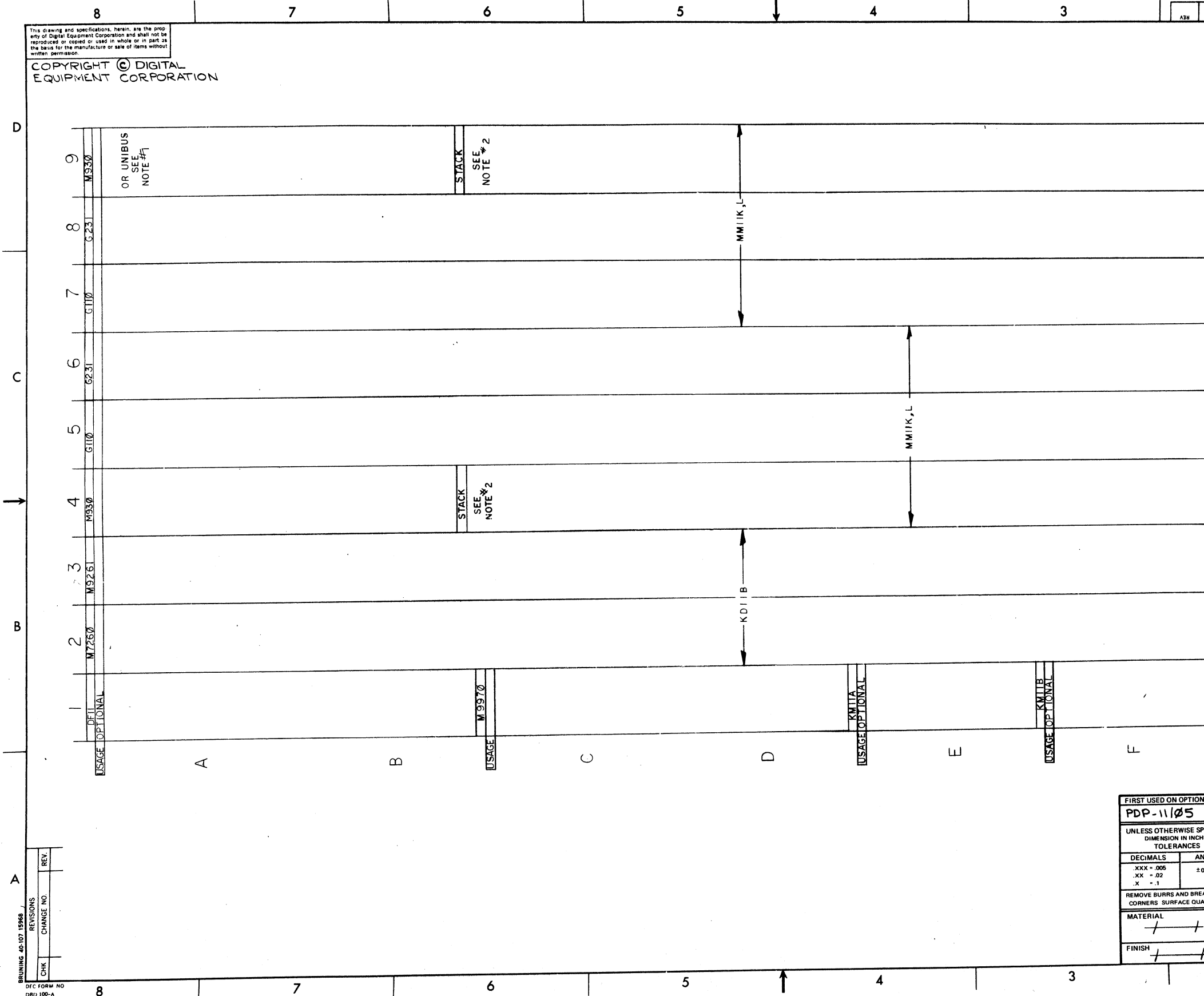
REV. 1105-0-01  
SIZE CODE DMU  
NUMBER 1105-0-01

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NOTES:

- BEFORE THE 1105 IS CONNECTED TO OTHER SYSTEM OPTIONS, THE M930 BUS TERMINATOR IS IN LOCATION A, B09. THIS TERMINATOR IS MOVED TO THE END SYSTEM OPTION UPON INTERCONNECTION.
- IF MM11-K, STACK IS HZ13; IF MM11-L, STACK IS HZ14.



QTY.	DESCRIPTION	PART NO.	ITEM NO.
1	M9970	D-CS-M9970-0-1	5
1	CENTRAL PROCESSOR	B-DD-KD11-B-0	4
1	16 BIT 18MIL MEMORY	B-DD-MM11-L-0	3
2	BUS TERMINATOR	A-PL-M930-0-2	2
*	MAINTENANCE BOARDS	A-PL-MM11-0-1	1

FIRST USED ON OPTION/MODEL		PARTS LIST	
PDP-1105		QTY.	DESCRIPTION
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES. TOLERANCES		DATE	DATE
DECIMALS	ANGLES	12-11-72	1-17-73
.XXX - .005	± 0° 30'	DATE	DATE
.XX - .02		1-17-73	1-17-73
.X - .1		DATE	DATE
		1-17-73	1-17-73
REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY		DATE	DATE
		7-2-73	7-2-73
MATERIAL	NEXT HIGHER ASSY.	TITLE	
+ +		MODULE UTILIZATION (16K)	
FINISH	SCALE	SIZE CODE	NUMBER
+ +		DMU 1105-0-07	
SHEET	OF	DIST.	

REV. NUMBER  
DMU 1105-0-07

BRUNING 40-107 15968	REVISIONS	REV.
CHK	CHANGE NO.	

DEC FORM NO. DRI 100-A



## CUSTOMER PRINT SET INDEX

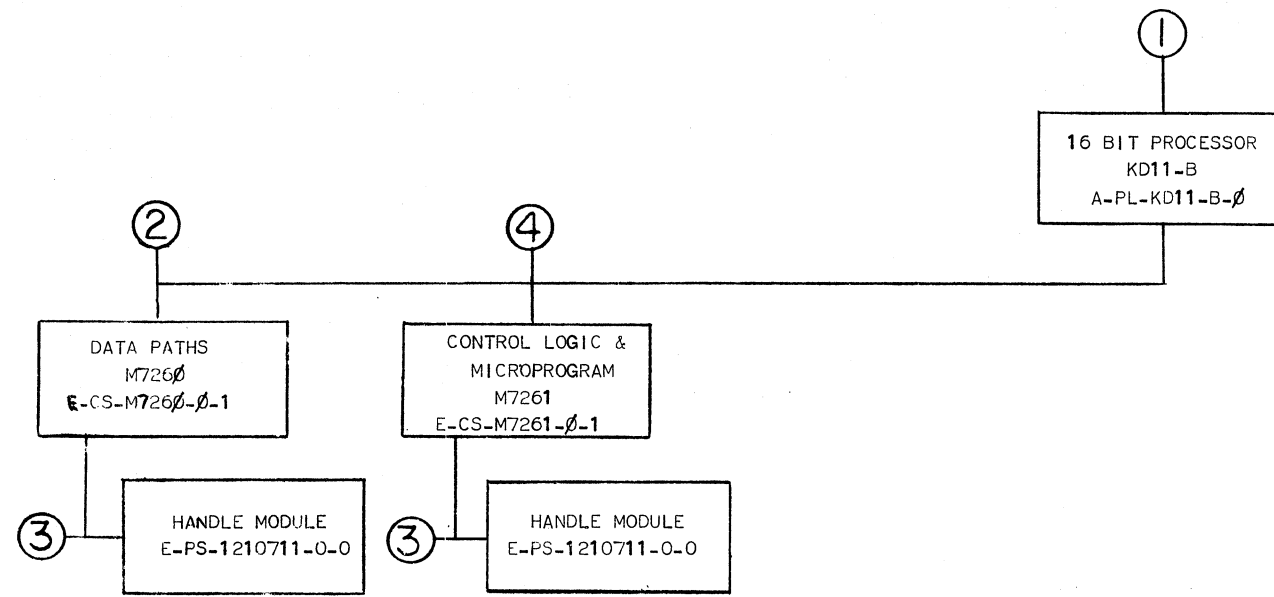
THIS IS PRINT SET

SEQUENCE	SEQUENCE
16 BIT PROCESSOR KD11-B	B-DD-KD11-B
MICROPROGRAM FLOW	K-MP-KD11-B-1
MICROPROGRAM SYMBOLIC LISTING	K-MP-KD11-B-2
MICROPROGRAM BINARY LISTING	K-MP-KD11-B-3
MICROPROGRAM CROSS REF. LISTING	K-MP-KD11-B-4
DATA PATHS	E-CS-M7261- <del>0</del> -1
DATA PATH ROM PATTERNS	K-RL-M7261- <del>0</del> -8
CONTROL LOGIC & MICROPROGRAMS	E-CS-M7261- <del>0</del> -1
CONTROL LOGIC ROM PATTERNS	K-RL-M7261- <del>0</del> -8

UNIT VARIATIONS		PRINT SET TYPE			
VARIATION	TITLE	KD11-B			
KD11-B	16 BIT PROCESSOR				

REV	REVISIONS					
	CHG. NO.	REV	DATE	CHG. NO.	REV	DATE
A	KDIIB-00002		DC	B	KDIIB-00003	
B	KDIIB-00003		MT	C	KDIIB-4	
C	KDIIB-4		M.T.	D	KDIIB-5	
D	KDIIB-5		D.R.	E	KDIIB-6	
E	KDIIB-6		D.R.			

USED ON OPTION/MODEL		DRN.	DATE	TITLE	
		J. CAHILL	4/21/72	16 BIT PROCESSOR	
		CHK'D.	DATE	KD11-B	
		C. Teschner	5-5-72	SIZE	CODE
		PROJ. ENG.	DATE	B	DD
		A. Tach	5-16-72	NUMBER	
		PROD.	DATE	KD11-B	
		D. Tecknut		REV	
		FIELD SERV.	DATE	E	
		SHEET	1 OF 3	DIST	



TITLE	SHEET	OF	SIZE	CODE	NUMBER	REV
16 BIT PROCESSOR KD11-B	SHEET	2	R	DD	KD11-B	E

CUSTOMER PRINT SET				ELECTRICAL					CUSTOMER PRINT SET				MECHANICAL								
KD11-B				MFG. SET	FIND NO.	DRAWING NO.	REV	NO OF SHT	DESCRIPTION	OPTION NO.	KD11-B				MFG. SET	FIND NO.	DRAWING NO.	REV	NO OF SHT	DESCRIPTION	OPTION NO.
					1.	A-PL-KD11-B- <del>0</del>		1	16 BIT PROCESSOR (KD11-B)							1.	A-PL-KD11-B- <del>0</del>		1	16 BIT PROCESSOR (KD11-B)	
	C					K-MP-KD11-B-1	C	22	MICROPROGRAM FLOW												
	C					K-MP-KD11-B-2	E	6	MICROPROGRAM SYMBOLIC LISTING												
	C					K-MP-KD11-B-3	E	7	MICROPROGRAM BINARY LISTING												
	C					K-MP-KD11-B-4		3	MICROPROGRAM CROSS REF. LISTING							2.	E-CS-M726 <del>0</del> - <del>0</del> -1		10	DATA PATHS (M726 <del>0</del> )	
																	B-MH-M726 <del>0</del> - <del>0</del> -6		1	MODULE ECO HISTORY	
																	K-CO-M726 <del>0</del> - <del>0</del> -4		1	X-Y CO-ORDINATE HOLE HISTORY	
																	B-AH-M726 <del>0</del> - <del>0</del> -5		1	ASSY/DRILLING HOLE	
																3.	E-PS-1210711-0-0		1	HANDLE MODULE	
X					2.	E-CS-M726 <del>0</del> - <del>0</del> -1	#	10	DATA PATHS (M726 <del>0</del> )												
						B-MH-M726 <del>0</del> - <del>0</del> -6		1	MODULE ECO HISTORY												
						K-CO-M726 <del>0</del> - <del>0</del> -4		1	X-Y CO-ORDINATE HOLE LOCATION												
						B-AH-M726 <del>0</del> - <del>0</del> -5		1	ASSY/DRILLING HOLE												
	X					K-RL-M726 <del>0</del> - <del>0</del> -8	#	15	DATA PATH ROM PATTERNS												
X					4.	E-CS-M7261- <del>0</del> -1	#	13	CONTROL LOGIC & MICROPROGRAM												
						B-MH-M7261- <del>0</del> -6		1	MODULE ECO HISTORY												
						K-CO-M7261- <del>0</del> -4		1	X-Y CO-ORDINATE HOLE LOCATION												
						B-AH-M7261- <del>0</del> -5		1	ASSY/DRILLING HOLE												
	X					K-RL-M7261- <del>0</del> -8	#	57	CONTROL LOGIC ROM PATTERNS												

TITLE: 16 BIT PROCESSOR KD11-B  
 SHEET 3 OF 3  
 SIZE CODE: B DD  
 NUMBER: KD11-B  
 REV: E





## NOTES ON NOTATION:

- 1, MICROUTINES BEGIN WITH A COMMENT  
THE FIRST CHARACTER OF WHICH IS '/\*',
- 2, ALL OTHER COMMENTS BEGIN WITH '///';
- 3, R[N] REFERS SCRATCH PAD REGISTER N,  
R[7] IS ALSO REFERRED TO AS 'PC',
- 4, R[S] REFERS TO THAT REGISTER SPECIFIED  
IN THE SOURCE PORTION OF THE CURRENT INST,  
(IR<11>); LIKEWISE, R[D] REFERS TO THAT  
REG SPECIFIED IN THE DESTINATION PORTION OF  
THE CURRENT INST, (IR<21>),
- 5, K[N] REFERS TO THAT LOCATION OF THE  
CONSTANTS CHIP CONTAINING THE CONSTANT N,
- 6, 'BUT' STANDS FOR 'BRANCH ON MICRO TEST'.

```

LOC  NXT  * INSTRUCTION FETCH
062  053  F=1  BA=PC; DATI
053  365  F=2  B=PC+2
365  364  F=3  PC=BI CKOFF
364  061  F=4  B,IR=UNIBUS DATA
061  001  F=5  B=B SEX; BUT IR DECODE
      / IF DOUBLE OP INST GOTO S0=1 THRU S7=1 DEPENDING ON SOURCE MODE
      / IF SINGLE OP INST GOTO D0=1 THRU D7=1 DEPENDING ON DEST MODE (INCLUDING JSR)
      / IF BRANCH, CHANGE PC GOTO B=1
      / IF BRANCH, PC UNCHANGED GOTO B2=2
      / IF CLEAR OR SET COND CODE(S) GOTO CCM=1
      / IF INST=RTS GOTO R1=1
      / IF INST=RTI GOTO R2=1
      / IF INST=WAIT GOTO W=1
      / IF INST=HALT GOTO H=1
      / IF INST=RESET GOTO RST=1
      / IF INST=EMT GOTO ET=1
      / IF INST=BREAKPOINT TRAP GOTO BT=1
      / IF INST=IOT GOTO IT=1
      / IF INST=TRAP GOTO T=1
      / IF RESERVED INST (NONE OF THE ABOVE) GOTO RT=1

```

```
LOC  NXT  * SOURCE MODE 0 (REGISTER), GET SOURCE DATA
          / GET TO S0=1 FROM F=5 VIA BUT IR DECODE IR<1119>=0
201  007  S0=1  B=R[S]; BUT BYTE
          / IF BYTE INST GOTO SBE=1 (MUST BE EVEN BYTE)
007  001  S0=2  R[10]+8; BUT DESTINATION
          / IF IR<5,3> =0 GOTO D0=1
          /           =1      D1=1
          /           =2      D2=1
          /           =3      D3=1
          /           =4      D4=1
          /           =5      D5=1
          /           =6      D6=1
          /           =7      D7=1
```

```
LOC  NXT  * SOURCE MODE 1 (REG, DEFERRED) GET SOURCE DATA
          / GET TO S1=1 FROM F=5 VIA BUT IR DECODE IR<1119>
203  244  S1=1  BA=RES; DATI; CKOFF; ALBYT
          / GET TO S1=2 FROM S2=3 VIA GOTO
          /           "      S3=5  "
          /           "      S6=5  "
244  007  S1=2  B=UNIBUS DATA; BUT BYTE; GOTO S0=2
          / IF ODD BYTE GOTO S0=1
          / IF EVEN BYTE GOTO SBE=1
          / IF NOT BYTE FALL THROUGH TO S0=2
```

```
LOC  NXT  * SOURCE MODE 2 (AUTO=INC,) GET SOURCE DATA
          / GET TO S2=1 FROM F=5 VIA BUT IR DECODE IR<1119>=2
205  301  S2=1  BA=RES; DATI; ALBYT
301  014  S2=2  B=R[S]+1+BYTE; BAR
          / GET TO S2=3 FROM S4=1 VIA GOTO
014  244  S2=3  R[S]+B; CKOFF; GOTO S1=2
```

```
LOC  NXT  *SOURCE MODE 3 (AUTO=INC DEFERRED) GET SOURCE DATA
          / GET TO S3=1 FROM F=5 VIA BUT IR DECODE IR<1119>=3
207  016  S3=1  BA=RES; DATI (MUST BE AN EVEN ADDRESS HERE)
016  017  S3=2  B=R[S]+2
```

017 134 / GET TO S3=3 FROM S5=1 VIA GOTO  
S3=3 R[S]=B; CKOFF  
134 274 / GET TO S3=4 FROM S7=5 VIA GOTO  
S3=4 B=UNIBUS DATA  
274 244 S3=5 BA=B; DATI; CKOFF; GOTO S1=2; ALBYT

LOC NXT \* SOURCE MODE 4 (AUTO=DEC) GET SOURCE DATA  
/ GET TO S4=1 FROM F=5 VIA BUT IR DECODE IR<1119>=4  
211 014 S4=1 B,BA=RES;=1=BYTE,BA; DATI; ENABOVER; GOTO S2=3; ALBYT

LOC NXT \* SOURCE MODE 5 (AUTO=DEC DEFERRED) GET SOURCE DATA  
/ GET TO S5=1 FROM F=5 VIA BUT IR DECODE IR<1119>=5  
213 017 S5=1 B,BA=RES;=2; DATI(MUST BE AN EVEN ADDRESS HERE); ENABOVER; GOTO S3=3

LOC NXT \* SOURCE MODE 6 (INDEXED) GET SOURCE DATA  
/ GET TO S6=1 FROM F=5 VIA BUT IR DECODE IR<1119>=6  
215 025 S6=1 BA=PC; DATI(MUST BE EVEN ADDRESS HERE)  
025 026 S6=2 B=PC+2  
026 027 S6=3 PC=B; CKOFF  
027 030 S6=4 B=UNIBUS DATA  
030 244 S6=5 BA=B+RES; DATI; CKOFF; GOTO S1=2; ALBYT

LOC NXT \* SOURCE MODE 7 (INDEXED DEFERRED) GET SOURCE DATA  
/ GET TO S7=1 FROM F=5 VIA BUT IR DECODE IR<1119>=7  
217 032 S7=1 BA=PC; DATI(MUST BE AN EVEN ADDRESS HERE)  
032 033 S7=2 B=PC+2  
033 034 S7=3 PC=B; CKOFF  
034 035 S7=4 B=UNIBUS DATA  
035 134 S7=5 BA=B+RES; DATI(MUST BE AN EVEN ADDRESS); CKOFF; GOTO S3=4

```

LOC  NXT  * SOURCE BYTE ODD
          / GETE TO SB0=1 FROM S1=2 VIA BUT BYTE (BYTE INST, AND SOURCE DATA ODD ADDR)
067  346  SB0=1 SHIFT B RIGHT; F SHIFT
346  324  SB0=2 SHIFT B RIGHT; F SHIFT
324  340  SB0=3 SHIFT B RIGHT; F SHIFT
340  361  SB0=4 SHIFT B RIGHT; F SHIFT
361  050  SB0=5 SHIFT B RIGHT; F SHIFT
050  020  SB0=6 SHIFT B RIGHT; F SHIFT
020  052  SB0=7 SHIFT B RIGHT; F SHIFT
052  047  SB0=8 SHIFT B RIGHT; GOTO SBE=1

```

```

LOC  NXT  * SOURCE EVEN BYTE
          / GET TO SBE=1 FROM SB0=8 VIA GOTO
          / GET TO SBE=1 FROM S1=2 VIA BUT BYTE, (BYTE INST AND SOURCE DATA EVEN ADDR)
          / GET TO SBE=1 FROM S0=1 VIA BUT BYTE, (BYTE INST,)
047  001  SBE=1 R[10]←B SEX; BUT DESTINATION
          / IF IF<5|3> =0 GOTO D0=1
          / " " =1 " D1=1
          / " " =2 " D2=1
          / " " =3 " D3=1
          / " " =4 " D4=1
          / " " =5 " D5=1
          / " " =6 " D6=1
          / " " =7 " D7=1

```

```

LOC  NXT  * DEST, MODE 0 (REGISTER), GET DEST DATA, OP, AND REPLACE
          / GET TO D0=1 FROM S0=2 VIA BUT DESTINATION (IR<5|3>=0)
          / GET TO D0=1 FROM SBE=1 VIA BUT DESTINATION (IR<5|3>=0)
101  154  D0=1 B←R[D]; BUT MOVE
          / IF INST=MOVE, BAR (OTHER THAN MOVE) AND BYTE GOTO DB0=1
          / IF INST=MOVE AND BYTE GOTO MB=0
          / IF INST=MOVE AND BYTE, BAR GOTO D0=3A
          / IF INST=MOVE, BAR AND BYTE, BAR FALL THROUGH TO D0=2
157  142  D0=2 R[11]←B; BUT UNARY
          / IF INST=JMP OR JSR GOTO ERT=1 (ILLEGAL INST, TRAP)
          / IF INST=SWAB GOTO SB1=1
          / IF INST=OTHER UNARY(CLR, COM, INC, DEC, NEG, ADC, SBC, TST, ROR, ROL, ASR, ASL) GOTO U1=1
          / GET TO D0=3 FROM U1=1 VIA GOTO
          / GET TO D0=3 FROM D0=1 VIA BUT BYTE (INST=MOVE AND BYTE, BAR)

```

```

162 332 D0=3 B_R[10] OP B; BUT NONMOD
      / THERE EXISTS A D03-A WHICH IS IDENTICAL TO D0-3 EXCEPT LOC=155
      / GET TO D0-3A FROM SB1-R VIA GOTO
      / IF NONMOD GOTO B2-2 (BUT SERVICE)
      / IF NOT NONMOD FALL THROUGH TO D0-4
      / GET TO D0-4 FROM R1-6 VIA GOTO
332 040 D0=4 R[D]_B; BUT SERVICE
      / PRIORITIES ARE LISTED HIGHEST TO LOWEST
      / IF T BIT TRAP GOTO BT-1
      / IF STACK OVERFLOW GOTO ERT1A
      / IF POWERFALL GOTO PF-1
      / IF BR7 GOTO BG-1
      / IF BR6 GOTO BG-1
      / IF INTERNAL LINE CLOCK GOTO LC-1
      / IF BR5 GOTO BG-1
      / IF BR4 GOTO BG-1
      / IF UART RECEIVE GOTO URTR
      / IF UART TRANSMIT GOTO URTX
      / IF CONSOLE STOP GOTO H-1
      / IF NONE OF THE ABOVE GOTO F-1

LOC  NXT * DEST. MODE 1 (REG.DEFERRED) GET DEST DATA, OP, AND REPLACE
      / GET TO D1-1 FROM S0-2 VIA BUT DESTINATION (IR<5:3>=1)
      / GET TO D1-1 FROM SBE-1 VIA BUT DESTINATION (IR<5:3>=1)
103 200 D1=1 B,BA_R[D]; DATIP; BUT JSRMP; ALBT; CKOFF
      / NOTE DATA IN PAUSE HERE
      / IF INST=JMP GOTO J1-1
      / IF INST=JSR GOTO J2-1
      / IF INST NOT JMP OR JSR FALL THROUGH TO D1-2
      / GET TO D1-2 FROM D2-3 VIA GOTO
      / GET TO D1-2 FROM D3-5 VIA GOTO
      / GET TO D1-2 FROM D6-5 VIA GOTO
200 210 D1=2 B_UNIBUS DATA; BUT 0YIE
      / IF ODD BYTE GOTO D0-1
      / IF EVEN BYTE GO TO DE-1
      / IF NOT BYTE FALL THROUGH TO D1-3
210 143 D1=3 R[11]_B; BUT UNARY
      / IF INST=SWAB GOTO SB2-1
      / IF INST=OTHER UNARY (CLR, COM, INC, DEC, NEG, ADC, SRC, TST, ROR, ROL, ASR, ASL) GOTO U2-1
      / GET TO D1-4 FROM DE-1 VIA BUT UNARY (NON UNARY)
      / GET TO D1-4 FROM U2-1 VIA GOTO
      / GET TO D1-4 FROM SB2-8 VIA GOTO
163 334 D1=4 B_R[10] OP B; BUT NONMOD
      / IF NONMOD GOTO B2-2 (BUT SERVICE)
      / IF NOT NONMOD FALL THROUGH TO D1-5
334 065 D1=5 DATQ; ALBYT; CKOFF
      / GET TO D1-6 FROM D0-18 VIA GOTO

```

```

/ IF NONMOD GOTO 02-2 (BUT SERVICE)
/ IF NOT NONMOD FALL THROUGH TO D1-5
334 065 D1-5 DATI; ALBYT; CKOFF
/ GET TO D1-6 FROM D0-18 VIA GOTO
065 305 D1-6 DRIVERS+0; GOTO 02-2 (BUT SERVICE)

```

```

LOC NXT * DEST MODE 2 (AUTO-INC) GET DEST DATA, OP AND REPLACE
/ GET TO D2-1 FROM S0-2 VIA BUT DESTINATION (IR<5:3>=2)
/ GET TO D2-1 FROM SBE-1 VIA BUT DESTINATION (IR<5:3>=2)
105 331 D2-1 BA+R[D]; DATI; ALBYT
/ NOTE DATA IN PAUSE HERE
331 341 D2-2 B+R[D]+1+BYTE, BAR
/ GET TO D2-3 FROM D4-1 VIA GOTO
341 200 D2-3 R[D]+B; BUT JSRMP; GOTO D1-2; CKOFF
/ IF INST=JMP GOTO J1-1
/ IF INST=JSR GOTO J2-1
/ IF INST NOT JMP OR JSR FALL THROUGH TO D1-2

```

```

LOC NXT * DEST MODE 3 (AUTO-INC DEFERRED) GET DEST DATA, OP AND REPLACE
/ GET TO D3-1 FROM S0-2 VIA BUT DESTINATION (IR<5:3>=3)
/ GET TO D3-1 FROM SBE-1 VIA BUT DESTINATION (IR<5:3>=3)
107 160 D3-1 BA+R[D]; DATI
160 070 D3-2 B+R[D]+2
/ GET TO D3-3 FROM D5-1 VIA GOTO
070 071 D3-3 R[D]+B; CKOFF
/ GET TO D3-4 FROM D7-5 VIA GOTO
071 072 D3-4 B+UNIBUS DATA
072 200 D3-5 BA+B; DATI; BUT JSRMP; GOTO D1-2; ALBYT; CKOFF
/ NOTE DATA IN PAUSE HERE
/ IF INST=JMP GOTO J1-1
/ IF INST=JSR GOTO J2-1
/ IF INST NOT JMP OR JSR FALL THROUGH TO D1-2

```

```

LOC NXT * DEST MODE 4 (AUTO-DEC) GET DEST DATA, OP AND REPLACE
/ GET TO S4-1 FROM S0-2 VIA BUT DESTINATION (IR<5:3>=4)
/ GET TO S4-1 FROM SBE-1 VIA BUT DESTINATION (IR<5:3>=4)
111 341 D4-1 B, BA+R[D]-1-BYTE, BAR; DATI; ENABOVER; GOTO D2-3; ALBYT

```

```

LOC  NXT  * DEST MODE 5 (AUTO=DEC DEFERRED) GET DEST DATA, OP, AND REPLACE
          / GET TO D5=1 FROM S0=2 VIA BUT DESTINATION (IR<513>=5)
          / GET TO D5=1 FROM SBE=1 VIA BUT DESTINATION (IR<513>=5)
113  070  D5=1  B,RA+REDJ=2) DATI; ENABOVER; GOTO D3=3

```

```

LOC  NXT  * DEST MODE 6 (INDEXED) GET DTA, OP, AND REPLACE
          / GET TO D6=1 FROM S0=2 VIA BUT DESTINATION (IR<513>=6)
          / GET TO D6=1 FROM SBE=1 VIA BUT DESTINATION (IR<512>=6)
115  075  D6=1  BA+PC; DATI
075  077  D6=2  B+PC+2
077  057  D6=3  PC+B; CKOFF
057  300  D6=4  B=UNIBUS DATA
300  200  D6=5  B,BA+B+REDJ; DATI; BUT JSRMP; GOTO D1=2; ALBYT; CKOFF
          / NOTE DATA IN PUASE HERE
          / IF INST=JMP GOTO J1=1
          / IF INST=JSR GOTO J2=1
          / IF INST NOT JMP OR JSR FALL THROUGH TO D1=2

```

```

LOC  NXT  * DEST MODE 7 (INDEXED DEFERRED) GET DEST DATA, OP, AND REPLACE
          / GET TO D7=1 FROM S0=2 VIA BUT DESTINATION (IR<513>=7)
          / GET TO D7=1 FROM SBE=1 VIA BUT DESTINATION (IR<513>=7)
117  310  D7=1  BA+PC; DATI
310  104  D7=2  B+PC+2
104  320  D7=3  PC+B; CKOFF
320  106  D7=4  B=UNIBUS DATA
106  071  D7=5  BA+B+REDJ; DATI; CKOFF; GOTO D3=4

```

```

LOC  NXT  * DESTINATION MODE 0, BYTE
          / GET TO DB0=1 FROM D0=1 VIA BUT BYTE (BYTE INST AND MOVE, BAR)
156  144  DB0=1 R[11],B=B SEX; BUT UNARY
          / IF UNARY OTHER THAN JSR, JMP, OR SWAB (CLR, COM, INC, DEC, NEG, ADC, SRC, TST, ROR, ROL, ASR, ASL) GOTO U3=1

```

```

304 040 DB0=3 R[D]<7:0>_B; BUT SERVICE; GOTO F-1
      / PRIORITIES ARE LISTED HIGHEST TO LOWEST
      / IF T BIT TRAP GOTO BT-1
      / IF STACK OVERFLOW GOTO ERT1A
      / IF POWER FAIL GOTO PF-1
      / IF BR7 GOTO BG-1
      / IF BR6 GOTO BG-1
      / IF INTERNAL LINE CLOCK GOTO LC-1
      / IF BR5 GOTO BG-1
      / IF BR4 GOTO BG-1
      / IF UART RECEIIVE GOTO URTR
      / IF UART TRANSMIT GOTO URTX
      / IF CONSOLE STOP GOTO H-1
      / IF NONE OF THE ABOVE GOTO F-1

```

```

LOC  NXT  * DESTINATION ODD BYTE
      / GET TO DO-1 FROM DI-2 VIA BUT BYTE (BYTE INST AND ODD ADDR)
270  123  DO-1  SHIFT B RIGHT; F SHIFT
123  124  DO-2  SHIFT B RIGHT; F SHIFT
124  125  DO-3  SHIFT B RIGHT; F SHIFT
125  126  DO-4  SHIFT B RIGHT; F SHIFT
126  127  DO-5  SHIFT B RIGHT; F SHIFT
127  130  DO-6  SHIFT B RIGHT; F SHIFT
130  131  DO-7  SHIFT B RIGHT; F SHIFT
131  132  DO-8  SHIFT B RIGHT
132  145  DO-9  R[111,B_B SEX; BUT UNARY
      / IF UNARY OTHER THAN JSR, JMP, OR SWAB (CLR,COM,INC,DEC,NEG,ADC,SHC,TST,ROR,RDL,ASR,ASL) GOTO U4-1
      / IF NOT UNARY FALL THROUGH TO DO-10
165  342  DO-10 B_R[101 OP B; BUT NONMOD
      / IF NONMOD GOTO B2=2 (BUT SERVICE)
      / IF NOT NONMOD FALL THROUGH TO DO-11
342  135  DO-11 SHIFT B LEFT; F SHIFT
135  136  DO-12 SHIFT B LEFT; F SHIFT
136  137  DO-13 SHIFT B LEFT; F SHIFT
137  140  DO-14 SHIFT B LEFT; F SHIFT
140  141  DO-15 SHIFT B LEFT; F SHIFT
141  142  DO-16 SHIFT B LEFT; F SHIFT
142  143  DO-17 SHIFT B LEFT; F SHIFT
143  065  DO-18 SHIFT B LEFT; DATO; CKOFF; GOTO D1-6; ALBYT

```

```

LOC  NXT  * DESTINATION EVEN BYTE
      / GET TO DE-1 FROM DI-2 VIA BUT BYTE (BYTE INST AND EVEN ADDR)

```



```

250 163 DE=1 R[111]_B SEX; GOTO D1=4; BUT UNARY
      / IF UNARY OTHER THAN JSR, JMP, OR SWAB (CLR, COM, INC, DEC, ADC, SBC, TST, ROR, ASR, ASL) GOTO U5=1
      / IF NOT UNARY FALL THROUGH TO D1=4

```

```

LOC  NXT  * UNARY OPERATORS GET SINGLE OPERAND IN B AND R[101]
      / GET TO U1=1 FROM D0=2 VIA BUT UNARY (INST=CLR, COM, INC, DEC, NEG, ADC, SBC, TST, ROR, ROL, ASR, ASL)
352 162 U1=1 R[101]_B; PRE AUX; GOTO D0=3
      / GET TO U2=1 FROM D1=3 VIA BUT UNARY (INST=CLR, COM, ...ASL) SEE U1=1
353 163 U2=1 R[101]_B; PRE AUX; GOTO D1=4
      / GET TO U3=1 FROM D0=1 VIA BUT UNARY (INST=CLR, COM, ...ASL) SEE U1=1
354 164 U3=1 R[101]_B; PRE AUX; GOTO D0=2
      / GET TO U4=1 FROM D0=9 VIA BUT UNARY (INST=CLR, COM, ...ASL) SEE U1=1
355 165 U4=1 R[101]_B; PRE AUX; GOTO D0=10
      / GET TO U5=1 FROM DE=1 VIA BUT UNARY (INST=CLR, COM, ...ASL) SEE U1=1
373 163 U5=1 R[101]_B; PRE AUX; GOTO D1=4

```

```

LOC  NXT  * MOVH INST
      / GET TO MB=0 FROM D0=1 VIA BUT MOVE (INST=MOVE AND BYTE)
154 240 MB=0 PRE AUX
240 152 MB=1 B_R[101] OP B
152 040 MB=2 R[D1]_B SEX; BUT SERVICE
      / PRIORITIES ARE LISTED HIGHEST TO LOWEST
      / IF I BIT TRAP GOTO BT=1
      / IF STACK OVERFLOW GOTO ERT1A
      / IF POWER FAIL GOTO PF=1
      / IF BR7 GOTO BG=1
      / IF BR6 GOTO BG=1
      / IF INTERNAL LINE CLOCK GOTO LC=1
      / IF BR5 GOTO BG=1
      / IF BG4 GOTO BG=1
      / IF UART RECEIVE GOTO URTR
      / IF UART TRANSMIT GOTO URTX
      / IF CONSOLE STOP GOTO H=1
      / IF NONE OF THE ABOVE GOTO F=1

```

```

LOC  NXT  * BRANCH, CHANGE PC
015 147 B=1 SHIFT B LEFT

```

```
/ IF BR7 GOTO BG=1
/ IF BR6 GOTO BG=1
/ IF INTERNAL LINE CLOCK GOTO LC=1
/ IF BR5 GOTO BG=1
/ IF BG4 GOTO BG=1
/ IF UART RECEIVE GOTO URTR
/ IF UART TRANSMIT GOTO URTX
/ IF CONSOLE STOP GOTO H=1
/ IF NONE OF THE ABOVE GOTO F=1
```

```
LOC NXT * BRANCH, CHANGE PC
015 147 B=1  SHIFT B LEFT
147 146 B=2  B=PC+B
146 040 B=3  PC=B| BUT SERVICE
/ PRIORITIES ARE LISTED HIGHEST TO LOWEST
/ IF T BIT TRAP GOTO BT=1
/ IF STACK OVERFLOW GOTO ERT1A
/ IF POWER FAIL GOTO PF=1
/ IF BR7 GOTO BG=1
/ IF BR6 GOTO BG=1
/ IF INTERNAL LINE CLOCK GOT LC=1
/ IF BR5 GOTO BG=1
/ IF BR4 GOTO BG=1
/ IF UART RECEIVE GOTO URTR
/ IF UART TRANSMIT GOTO URTX
/ IF CONSOLE STOP GOTO H=1
/ IF NONE OF THE ABOVE GOTO F=1
```

```
LOC NXT * CONDITION CODE MASK (FOR BOTH SET AND CLEAR)
151 350 CCM=1 R=B AND K[17]
350 112 CCM=2 BUT DEST
/ IF INST= SET, GO TO SC=1
/ IF INST= CLEAR, GOTO CC=1
```

```
LOC NXT * CLEAR CONDITION CODES
112 040 CC=1 PSW=PSW AND (B'BAR), BUT SERVICE
/ THIS EFFECTIVELY CLEARS THOSE BITS OF THE PSW WHICH ARE SET
```

LOC NXT \* SET CONDITION CODES  
 116 040 SC-1 PSW\_PSW OR B; BUT SERVICE  
 / PRIORITIES ARE LISTED HIGHEST TO LOWEST  
 / IF T BIT TRAP GOTO BT-1  
 / IF STACK OVERFLOW GOTO ERT1A  
 / IF POWER FAIL GOTO PF-1  
 / IF BR7 GOTO BG-1  
 / IF BR6 GOTO BG-1  
 / IF INTERNAL LINE CLOCK GOTO LC-1  
 / IF BR5 GOTO BG-1  
 / IF BR4 GOTO BG-1  
 / IF UART RECEIVE GOTO URTR  
 / IF UART TRANSMIT GOTO URTX  
 / IF CONSOLE STOP GOTO H-1  
 / IF NONE OF THE ABOVE GOTO F-1

LOC NXT \* SWAB, MODE 0  
 / GET TO SB1-1 FROM D0-2 VIA BUT UNARY (INST=SWAB AND MODE=0)  
 / ROTATE LEFT ACCOMPLISHED VIA ASR  
 166 172 SB1-1 ROTATE B LEFT; F SHIFT  
 172 173 SB1-2 ROTATE B LEFT; F SHIFT  
 173 174 SB1-3 ROTATE B LEFT; F SHIFT  
 174 144 SB1-4 ROTATE B LEFT; F SHIFT  
 144 176 SB1-5 ROTATE B LEFT; F SHIFT  
 176 177 SB1-6 ROTATE B LEFT; F SHIFT  
 177 006 SB1-7 ROTATE B LEFT; F SHIFT  
 006 155 SB1-8 ROTATE B LEFT; PRE AUX; GOTO D0-3A

LOC NXT \* SWAB, NOT MODE 0  
 / GET TO SB2-1 FROM D1-3 VIA BUT UNARY (INST=SWAB)  
 / ROTATE LEFT ACCOMPLISHED VIA ASR  
 167 012 SB2-1 ROTATE B LEFT; F SHIFT  
 012 220 SB2-2 ROTATE B LEFT; F SHIFT  
 220 022 SB2-3 ROTATE B LEFT; F SHIFT  
 022 023 SB2-4 ROTATE B LEFT; F SHIFT  
 023 024 SB2-5 ROTATE B LEFT; F SHIFT  
 024 031 SB2-6 ROTATE B LEFT; F SHIFT  
 031 330 SB2-7 ROTATE B LEFT; F SHIFT  
 330 163 SB2-8 ROTATE B LEFT; PRE AUX; GOTO D1-4

```

LOC  NXT  * JMP
          / GET TO J1-1 FROM D1-1 VIA BUT JSRMP (INST=JMP)
          / GET TO J1-1 FROM D2-3 VIA BUT JSRMP (INST=JMP)
          / GET TO J1-1 FROM D3-5 VIA BUT JSRMP (INST=JMP)
          / GET TO J1-1 FROM D6-5 VIA BUT JSRMP (INST=JMP)
204  260  J1-1  NOP
          / J1-1 MUST BE A NOP BECAUSE FOLLOWING A CKOFF, THE AMX WILL
          / BE FORCED TO TAKE DATA FROM THE UNIBUS.
260  040  J1-2  PC_B; BUT SERVICE
          / PRIORITIES ARE LISTED HIGHEST TO LOWEST
          / IF T BIT TRAP GOTO BT-1
          / IF STACK OVERFLOW GOTO ERT1A
          / IF POWER FAIL GOTO PF-1
          / IF BR7 GOTO BG-1
          / IF BR6 GOTO BG-1
          / IF INTERNAL LINE CLOCK GOTO LC-1
          / IF BR5 GOTO BG-1
          / IF BR4 GOTO BG-1
          / IF UART RECEIVE GOTO URTR
          / IF UART TRANSMIT GOTO URTX
          / IF CONSOLE STOP GOTO H-1
          / IF NONE OF THE ABOVE GOTO F-1

LOC  NXT  * JSR
          / GET TO J2-1 FROM D1-1 VIA BUT JSRMP (INST=JSR)
          / GET TO J2-1 FROM D2-3 VIA BUT JSRMP (INST=JSR)
          / GET TO J2-1 FROM D3-5 VIA BUT JSRMP (INST=JSR)
          / GET TO J2-1 FROM D6-5 VIA BUT JSRMP (INST=JSR)
212  261  J2-1  NOP
          / J2-1 MUST BE A NOP BECAUSE FOLLOWING A CKOFF, THE AMX WILL BE
          / FORCED TO TAKE DATA FROM THE UNIBUS.
261  262  J2-1A R[11]_R
262  214  J2-2  R,BA_R161-2; ENABOVER
214  206  J2-3  R[6]_B; CKOFF; DATA
206  216  J2-4  DRIVERS_R(S)
216  263  J2-5  B_PC
263  264  J2-6  R[S]_B
264  265  J2-7  B_R[11]
265  040  J2-8  PC_B; BUT SERVICE
          / PRIORITIES ARE LISTED HIGHEST TO LOWEST
          / IF T BIT TRAP GOTO BT-1

```

```

LOC  NXT  * JSR
        / GET TO J2=1 FROM D1=1 VIA BUT JSRMP (INST=JSR)
        / GET TO J2=1 FROM D2=3 VIA BUT JSRMP (INST=JSR)
        / GET TO J2=1 FROM D3=5 VIA BUT JSRMP (INST=JSR)
        / GET TO J2=1 FROM D6=* VIA BUT JSRMP (INST=JSR)
212  261  J2=1  NOP
        / J2=1 MUST BE A NOP BECAUSE FOLLOWING A CKOFF, THE AMX WILL BE
        / FORCED TO TAKE DATA FROM THE UNIBUS,
261  262  J2=1A R[11]=B
262  214  J2=2  B=BA+R[6]+2; ENABOVER
214  206  J2=3  R[6]=B; CKOFF; DATO
206  216  J2=4  DRIVERS=R[S]
216  263  J2=5  B=PC
263  264  J2=6  R[S]=B
264  265  J2=7  B=R[11]
265  040  J2=8  PC=B; BUT SERVICE
        / PRIORITIES ARE LISTED HIGHEST TO LOWEST
        / IF T BIT TRAP GOTO BT=1
        / IF STACK OVERFLOW GOT ERT1A
        / IF POWER FAIL GOTO PF=1
        / IF BR7 GOTO BG=1
        / IF BR6 GOTO BG=1
        / IF INTERNAL LINE CLOCK GOTO LC=1
        / IF BR5 GOTO BG=1
        / IF BR4 GOTO BG=1
        / IF UART RECEIVE GOTO URTR
        / IF UART TRANSMIT GOTO URTX
        / IF CONSOLE STOP GOTO H=1
        / IF NONE OF THE ABOVE GOTO F=1

```

```

LOC  NXT  * RTS
        / GET TO R1=1 FROM F=5 VIA BUT IR DECODE (INST=RTS)
005  221  R1=1  BA=R[6]; DATI
221  222  R1=2  B=R[6]+2
222  223  R1=3  R[6]=B
223  224  R1=4  B=R[D]
224  225  R1=5  PC=B; CKOFF
225  332  R1=6  B=UNIBUS DATA; GOTO D0=4

```

```

LOC  NXT  * RTI
        / GET TO R2=2 FROM F=5 VIA BUT IR DECODE (INST=RTI)
227  230  R2=1  BA=R[6]; DATI
230  231  R2=2  B=R[6]+2

```

```

231 232 R2=3 R[6]=B] CKOFF
232 234 R2=4 PC=UNIBUS DATA
      / THERE IS NO R2=5 (ANY MORE)
234 235 R2=6 BA=R[6]; DATI
235 236 R2=7 B=R[6]+2
236 237 R2=8 R[6]=B] CKOFF
237 305 R2=9 PS=UNIBUS DATA] GOTO B2=2 (BUT SERVICE)

```

```

LOC  NXT  * WAIT
      / GET TO W=1 FROM F=5 VIA BUT IR DECODE (INST=WAIT)
      / GET TO W=1 FROM W=1 VIA GOTO IF BUT SERVICE IS FALSE
063  040  W=1  BUT SERVICE
      / THE MICRO PROGRAM WILL LOOP ON W=1 UNTIL SOME HIGHER
      / PRIORITY CONDITION IS RECOGNIZED BY THE 'BUT SERVICE' ROM SEE F101 ON
      / THE CONE PRINT;
      / PRIORITIES ARE LISTED HIGHEST TO LOWEST
      / IF T BIT TRAP GOTO BT=1
      / IF STACK OVERFLOW GOTO ERT1A
      / IF POWER FAIL GOT PF=1
      / IF BR7 GOTO BG=1
      / IF BR6 GOTO BG=1
      / IF INTERNAL LINE CLOCK GOTO LC=1
      / IF BR5 GOTO BG=1
      / IF BR4 GOTO BG=1
      / IF UART RECEIVE URTR
      / IF UART TRANSMIT TOTO URTX
      / IF CONSOLE STOP GOTO H=1
      / IF NONE OF THE ABOVE TOTO F=1

```

```

LOC  NXT  * HALT
      / GET TO H=1 FROM F=5 VIA BUT IR DECODE (INST=HALT)
      / GET TO H=1 FROM BUT SERVICE
041  302  H=1  B=PC
      / DISPLAY PC IN LIGHTS BY PUTTING IT INTO B
      / GET TO H=2 FROM CE1=3 VIA GOTO
      / GET TO H=2 FROM CD1=5 VIA GOTO
      / GET TO H=2 FROM CL=3 VIA GOTO
302  300  H=2  BA=R[17]; BUT SWITCH
      / THE BA IS LOADED HERE SO THAT THE ADDRESS WILL BE INCREMENTED BY +1 WHEN EXAMINING (DEPOSITING INT
      / AND BY +2 WHEN EXAMINING (DEPOSITING INTO) SUCCESSIVE CORE MEMORY,
      / IF START DEPRESSED GOTO CS=1
      / IF CONTINUE DEPRESSED GOTO CCS=1

```

```

/ IF EXAMINE (1 ST) GOTO CE1=1
/ IF EXAMINE (NOT 1 ST) GOTO CE2=1
/ IF DEPOSIT (1 ST) GOTO CD1=1
/ IF DEPOSIT (NOT 1 ST) GOT CD2=1
/ IF LOAD GOTO CL=1
/ IF NO SWITCHES ARE DEPRESSED LOOP ON H=2

```

```

LOC NXT * EMT TRAP (VECTOR LOC=33)
/ GET TO ET=1 FROM F=5 VIA BUT IR DECODE (INST=EMT)
011 245 ET=1 B=K[30]
/ GET TO ET=2 FROM BT=1 VIA GOTO
/ GET TO ET=2 FROM IT=1 VIA GOTO
/ GET TO ET=2 FROM T=1 VIA GOTO
/ GET TO ET=1 FROM RT=1 VIA GOTO
/ GET TO ET=2 FROM ERT=1 VIA GOTO
/ GET TO ET=2 FROM PF=1 VIA GOTO
245 246 ET=2 R[12]=B
246 247 ET=3 B,BA=R[6]+2) ENABOVER
/ ET=4 HAS BEEN ELIMINATED
247 226 ET=5 R[6]=B) CKOFF) DATA
226 251 ET=6 DIRVERS=PS
251 252 ET=7 B,BA=R[6]+2) ENABOVER
252 253 ET=8 R[6]=B) CKOFF) DATA
253 254 ET=9 DIRVERS=PC
254 255 ET=10 RA=R[12]) DATA) CKOFF
255 256 ET=11 PC=UN[BUS DATA
256 257 ET=12 BA=R[12]+2) DATA) CKOFF
257 305 ET=13 PS=UN[BUS DATA] GOTO B2=2 (SERVICE)

```

```

LOC NXT * BREAKPOINT TRAP (VECTOR LOC=14) AND T BIT TRACE TRAP
/ GET TO BT=1 FROM ALL BUT SERVICE
/ GET TO BT=1 FROM F=5 VIA BUT IR DECODE (INST=BREAKPOINT)
045 245 BT=1 B=K[14]) GOTO ET=2

```

```

LOC NXT * IOT (VECTOR LOC=20)
/ GET TO IT=1 FROM F=5 VIA BUT IR DECODE (INST=IOT)
273 245 IT=1 B=K[20]) GOTO ET=2

```

LOC NXT • TRAP (VECTOR LOC=34)  
/ GET TO T=1 FROM F=5 VIA BUT IR DECODE (INST=TRAP)  
021 245 T=1 B=K[34]; GOTO ET=2

LOC NXT • RESERVED INST TRAP (VECTOR LOC=10)  
/ GET TO RT=1 FROM F=5 VIA BUT IR DECODE (INST=NON VALID)  
001 245 RT=1 R=K[10]; GOTO ET=2

LOC NXT • ERROR TRAP (BUS ERROR, STACK OVERFLOW, ILLEGAL INST) VECTOR LOC=4  
/ THERE EXISTS ERT=1 (LOC=10) FOR BUS ERROR  
/ THERE ALSO EXISTS ERT1A (LOC=46) FOR STACK OVERFLOW  
/ ERT1A GOES TO ET2=2, A SEQUENCE WHICH DOESN'T HAVE THE  
/ ENABOVER, WE DON'T WANT TO LOOK FOR STACK OVERFLOW WHILE  
/ DOING THE STACK OVERFLOW TRAP; THE ET2=2 SEQUENCE REJOINS THE ET SEQUENCE AT ET=8  
/ THERE ALSO EXISTS ERT1B (LOC=153) FOR ILLEGAL INST (JSP OR JMP, MODE 0)  
010 245 ERT=1 B=K[4]; GOTO ET=2

LOC NXT • CONSOLE START SWITCH  
/ GET TO CS=1 FOLLOWING RELEASE OF START SWITCH,  
100 322 CS=1 IR=ZERO  
/ CLOCKING THE IR TURNS ON THE RUN LIGHT  
322 321 CS=2 BA;B=R[17]  
321 040 CS=3 PC=0; BUT SERVICE  
/ PRIORITIES ARE LISTED HIGHEST TO LOWEST  
/ IF T BIT BRAP GOTO BT=1  
/ IF STACK OVERFLOW GOTO ERT1A  
/ IF POWER FAIL GOTO PF=1  
/ IF BR7 GOTO BG=1  
/ IF BR6 GOTO BG=1  
/ IF INTERNAL LINE CLOCK GOTO LC=1  
/ IF BR5 GOTO BG=1



/ IF BR4 GOTO BG=1  
/ IF UARY RECEIVE GOTO URTR  
/ IF UARY TRANSMIT GOTO URTX  
/ IF CONSOLE STOP GOTO H=1  
/ IF NONE OF THE ABOVE GOTO F=1

LOC NXT \* CONSOLE EXAMINE SWITCH = FIRST TIME IN SEQUENCE (DON'T INC R[17])  
/ GET TO CE1=1 FROM H=2 VIA BUT SWITCH  
/ GET TO CE1=1 FROM CE2=2 VIA GOTO  
317 307 CE1=1 BA,B=R[17]; BUT SWITCH  
/ DISPLAY ADDRESS BY PUTTING INTO THE B REGISTER WHILE EXAMINE IS DOWN  
/ LOOP ON CE1=1 UNTIL SWITCH IS RELEASED  
307 326 CE1=2 DAT; CKOFF  
326 302 CE1=3 B=UNIBUS DATA; GOTO H=2

LOC NXT \* CONSOLE EXAMINE SWITCH = OTHER THAN FIRST IN SEQUENCE (INC R[17])  
/ GET TO CE2=1 FROM H=2 VIA BUT SWITCH  
315 371 CE2=1 B=R[17]+2  
/ R[17] IS IN BA FROM H=2, THIS WILL CAUSE +2 TO BECOME +1 WHEN EXAMINING REGISTERS,  
371 317 CE2=2 R[17]=B; GOTO CE1=1

LOC NXT \* CONSOLE DEPOSIT SWITCH = FIRST TIME IN SEQUENCE (DON'T INC R[17])  
/ GET TO CD1=1 FROM H=2 VIA BUT SWITCH  
/ GET TO CD1=1 FROM CD2=2 VIA GOTO  
313 303 CD1=1 B=R[17]; BUT SWITCH  
/ LOOP ON CD1=1 UNTIL DEPOSIT SWITCH IS RELEASED  
303 374 CD1=2 BA=K[207],BAR; DAT; CKOFF  
/ COMPLEMENT OF 207 = 177570 = SWITCH REGISTER ADDRESS  
374 314 CD1=3 B=UNIBUS DATA  
314 372 CD1=4 BA=R[17]; DAT; CKOFF  
372 302 CD1=5 DRIVERS=B; GOTO H=2

LOC NXT \* CONSOLE DEPOSIT SWITCH = OTHER THAN FIRST IN SEQUENCE (INC R[17])  
/ GET TO CD2=1 FROM H=3) VIA BUT SWITCH  
312 337 CD2=1 B=R[17]+2  
/ R[17] IS IN BA; THIS WILL CAUSE +2 TO BECOME +1 WHEN DEPOSITING INTO REGISTERS  
337 313 CD2=2 R[17]+B; GOTO CD1=1

LOC NXT \* CONSOLE CONTINUE SWITCH  
/ GET TO CCS=1 FROM H=2 VIA BUT SWITCH  
316 276 CCS=1 B+PC  
276 270 CCS=2 BUT SWITCH  
272 262 CCS=3 IR=ZERO; GOTO F=1  
/ CLOCKING THE IR TURNS ON THE RUN LIGHT

LOC NXT \* CONSOLE LOAD SWITCH  
/ GET TO CL=1 FROM H=2 VIA BUT SWITCH  
311 375 CL=1 BA=K[207],BAR; DATI; CKOFF  
/ COMPLEMENT OF 207 = 177570 = SWITCH REGISTER ADDRESS  
375 367 CL=2 R=UNIBUS DATA  
367 302 CL=3 R[17]+B; GOTO H=2  
/ CL=3 GOES TO H=2 VIA GOTO, IF LOAD IS STILL DEPRESSED, THE BUT  
/ SWITCH IN H=2 WILL TAKE US BACK TO CL=1, THUS, AS LONG AS LOAD IS  
/ DEPRESSED, CHANGES IN THE SWITCHES WILL SHOW UP IN THE R REG (LIGHTS) AND IN R[17].

LOC NXT \* POWER FAIL (VECTOR LOC=24)  
/ GET TO PF=1 FROM SERVICE  
243 245 PF=1 B=K[24]; GOTO ET=2

LOC NXT \* RESTART FROM POWER FAIL (VECTOR LOC=24)  
/ GET TO RS=1 MYSTERIOUSLY AS POWER COMES UP ( NXT CHIPS, F092 AND F103 SHOWN ON THE CONF PRINT,  
/ ARE DISABLED FORCING THE MICROPROGRAM TO RS=1 IN LOC 0;  
200 241 RS=1 BA=K[24]; DATI  
241 347 RS=1A CKOFF

```

/ MUST DO CKOFF IN RS=1A BECAUSE OF CONFLICT BETWEEN
/ CKOFF AND INIT CREATED BY CKOFF ASSOCIATED WITH AUX CONTROL
347 074 RS=2 PC=UNIBUS DATA
074 351 RS=3 RA=K[24]+2; DAT1; CKOFF
351 305 RS=4 PS=UNIBUS DATA; GOTO B2=2 (SERVICE)

```

```

LOC NXT * INTERRUPT SERVICING
/ GET TO INT=1 FROM BG=2 VIA BUT INT (TRUE)
325 246 INT=1 R[12]=UNIBUS DATA; SET SLAVESYNC; GOTO ET=3

```

```

LOC NXT * BUS GRANT SERVICE
/ GET TO BG=1 FROM BUT SERVICE
040 305 BG=1 BUT INTERRUPT; GOTO B2=2 (BUT SERVICE)
/ IF INTERRUPT GOTO INT=1
/ IF NO INTERRUPT FALL THROUGH TO B2=2

```

```

LOC NXT * NOP - BRANCH CONDITION NOT TRUE (PC UNCHANGED)
/ B2=1 HAS BEEN ELIMINATED BECAUSE NEWI IS NO LONGER
/ GET TO B2=2A FROM D0=3 VIA BUT NONMOD (TRUE)
/ GET TO B2=2B FROM D1=4 VIA BUT NONMOD (TRUE)
/ GET TO B2=2C FROM D0=10 VIA BUT NONMOD (TRUE)
/ GET TO B2=2D FROM F=5 VIA BUT IR DECODE, BRANCH INST, CONDITION NOT TRUE
/ GET TO B2=2 FROM RST=1 VIA GOTO
/ GET TO B2=2 FROM D0=4 VIA GOTO
/ GET TO B2=2 FROM DB0=2 VIA BUT NONMOD (TRUE)
/ GET TO B2=2 FROM MB=2 VIA GOTO
/ GET TO B2=2 FROM CC=1 VIA GOTO
/ GET TO B2=2 FROM SC=1 VIA GOTO
/ GET TO B2=2 FROM J2=8 VIA GOTO
/ GET TO B2=2 FROM RS=10 VIA GOTO
/ GET TO B2=2 FROM ET=13 VIA GOTO
305 040 B2=2 BUT SERVICE
/ PRIORITIES ARE LISTED HIGHEST TO LOWEST
/ IF T BIT TRAP GOTO BT=1
/ IF STACK OVERFLOW GOTO ERT1A
/ IF POWER FAIL GOTO PF=1
/ IF BR7 GOTO BG=1

```

```
/ IF BR6 GOTO BG=1  
/ IF INTERNAL LINE CLOCK GOTO LC=1  
/ IF BR5 GOTO BG=1  
/ IF BR4 GOTO BG=1  
/ IF UART RECEIVE GOTO URTR  
/ IF UART TRANSMIT GOTO URTX  
/ IF CONSOLE STOP GOTO H=1  
/ IF NONE OF THE ABOVE GOTO F=1
```

```
LOC NXT * RESET  
357 305 / GET TO RST=1 FROM F=5 VIA BUT IR DECODE (INST=RESET)  
RST=1 BUT INIT; CKOFF; GOTO R2=2 (BUT SERVICE)
```

```
LOC NXT * DOUBLE BUS ERROR, GOTO HALT  
110 041 DBE=1 NOP; GOTO H=1
```

```
LOC NXT * UART XMIT (VECTOR LOC 64)  
760 245 URTX B=K[64]; GOTO ET=2
```

```
LOC NXT * UART RECEIVE (VECTOR LOC 62)  
064 245 URTR B=K[60]; GOTO ET=2
```

```
LOC NXT * LINE CLOCK (VECTOR LOC 100)  
042 245 LC=1 B=K[100]; GOTO ET=2
```

ERT1A NOT EXPLICITLY SHOWN IN FLOW  
D0-3A NOT EXPLICITLY SHOWN IN FLOW  
A14B NOT EXPLICITLY SHOWN IN FLOW  
ET2-2 NOT EXPLICITLY SHOWN IN FLOW  
ET2-3 NOT EXPLICITLY SHOWN IN FLOW  
ET2-5 NOT EXPLICITLY SHOWN IN FLOW  
ET2-6 NOT EXPLICITLY SHOWN IN FLOW  
ET2-7 NOT EXPLICITLY SHOWN IN FLOW  
ERT1B NOT EXPLICITLY SHOWN IN FLOW  
R2-2A NOT EXPLICITLY SHOWN IN FLOW  
R2-2B NOT EXPLICITLY SHOWN IN FLOW  
R2-2C NOT EXPLICITLY SHOWN IN FLOW  
R2-2D NOT EXPLICITLY SHOWN IN FLOW



NAME	LOC	ABT	ALG	ALU	AUX	BAR	BLG	BRG	BUT	CON	CKD	CRI	FSH	PSW	SAM	SPA	SPE	TNS	NXT
A145	145	YES	PSW	AA	ON	L	+1	H	MOV	NON	ON	OFF	OFF	L	ROM	R0	WRI	ZRO	
B-1	015	NO	SP	ASL	OFF	H	BRG	SL	NON	NON	OFF	OFF	OFF	H	ROM	R0	REA	NON	B-2
B-2	147	NO	SP	A+R	OFF	H	BRG	L	NON	NON	OFF	OFF	OFF	H	ROM	R7	REA	NON	B-3
B-3	146	NO	SP	RL	OFF	H	BRG	H	SRV	NON	OFF	OFF	OFF	H	ROM	R7	WRI	NON	BG-1
B2-2	305	NO	SP	AL	OFF	H	BRG	H	SRV	NON	OFF	OFF	OFF	H	ROM	R0	REA	NON	BG-1
B2-2A	333	NO	SP	AL	OFF	H	BRG	H	SRV	NON	OFF	OFF	OFF	H	ROM	R0	REA	NON	BG-1
B2-2B	335	NO	SP	AL	OFF	H	BRG	H	SRV	NON	OFF	OFF	OFF	H	ROM	R0	REA	NON	BG-1
B2-2C	343	NO	SP	AL	OFF	H	BRG	H	SRV	NON	OFF	OFF	OFF	H	ROM	R0	REA	NON	BG-1
B2-2D	013	NO	SP	AL	OFF	H	BRG	H	SRV	NON	OFF	OFF	OFF	H	ROM	R0	REA	NON	BG-1
BG-1	040	NO	SP	AL	OFF	H	BRG	H	INT	NON	OFF	OFF	OFF	H	ROM	R0	REA	NON	B2-2
BT-1	045	NO	NUL	AL	OFF	H	BRG	L	CON	14	OFF	OFF	OFF	H	ROM	R0	WRI	NON	BT-2
CC-1	112	NO	PSW	ABBAR	OFF	H	BRG	H	SRV	NON	OFF	OFF	OFF	L	ROM	R0	REA	NON	BG-1
CCM-1	151	NO	NUL	AANDB	OFF	H	BRG	L	CON	360	OFF	OFF	OFF	H	ROM	R0	WRI	NON	CCM-2
CCM-2	350	NO	SP	AL	OFF	H	BRG	H	DST	NON	OFF	OFF	OFF	H	ROM	R0	REA	NON	CC-1
CCS-1	316	NO	SP	AL	OFF	H	BRG	L	NON	NON	OFF	OFF	OFF	H	ROM	R7	REA	NON	CCS-2
CCS-2	276	NO	SP	AL	OFF	H	BRG	H	SW	NON	OFF	OFF	OFF	H	ROM	R0	REA	NON	DO-1
CCS-3	272	NO	SP	ZERO	OFF	H	BRG	H	IRC	NON	OFF	OFF	OFF	H	ROM	R0	REA	NON	F-1
CD1-1	313	NO	SP	AL	OFF	H	BRG	L	SW	NON	OFF	OFF	OFF	H	ROM	R17	REA	NON	CD1-2
CD1-2	303	NO	NUL	ABAR	OFF	L	BRG	H	CON	207	ON	OFF	OFF	H	ROM	R0	WRI	I	CD1-3
CD1-3	374	NO	NUL	AL	OFF	H	BRG	L	NON	NON	OFF	OFF	OFF	H	BAR	R0	REA	NON	CD1-4
CD1-4	314	NO	SP	AL	OFF	L	BRG	H	NON	NON	ON	OFF	OFF	H	ROM	R17	REA	0	CD1-5
CD1-5	372	NO	SP	RL	OFF	H	BRG	H	NON	NON	OFF	OFF	OFF	H	BAR	R0	REA	NON	H-2
CD2-1	312	NO	SP	A+R	OFF	H	+1	L	NON	NON	OFF	ON	OFF	H	ROM	R17	REA	NON	CD2-2
CD2-2	337	NO	SP	RL	OFF	H	BRG	H	NON	NON	OFF	OFF	OFF	H	ROM	R17	WRI	NON	CD1-1
CE1-1	317	NO	SP	AL	OFF	L	BRG	L	SW	NON	OFF	OFF	OFF	H	ROM	R17	REA	NON	CE1-2
CE1-2	307	NO	SP	AL	OFF	H	BRG	H	NON	NON	ON	OFF	OFF	H	ROM	R0	REA	I	CE1-3
CE1-3	326	NO	NUL	AL	OFF	H	BRG	L	NON	NON	OFF	OFF	OFF	H	BAR	R0	REA	NON	H-2
CE2-1	315	NO	SP	A+R	OFF	L	+1	L	NON	NON	OFF	ON	OFF	H	ROM	R17	REA	NON	CE2-2
CE2-2	371	NO	SP	RL	OFF	H	BRG	H	NON	NON	OFF	OFF	OFF	H	ROM	R17	WRI	NON	CE1-1
CL-1	311	NO	NUL	ABAR	OFF	L	BRG	H	CON	207	ON	OFF	OFF	H	ROM	R0	WRI	I	CL-2
CL-2	375	NO	NUL	AL	OFF	H	BRG	L	NON	NON	OFF	OFF	OFF	H	BAR	R0	REA	NON	CL-3
CL-3	367	NO	SP	RL	OFF	H	BRG	H	NON	NON	OFF	OFF	OFF	H	ROM	R17	WRI	NON	H-2
CS-1	100	NO	SP	ZERO	OFF	H	BRG	H	IRC	NON	OFF	OFF	OFF	H	ROM	R0	REA	NON	CS-2
CS-2	322	NO	SP	AL	OFF	L	BRG	L	NON	NON	OFF	OFF	OFF	H	ROM	R17	REA	NON	CS-3
CS-3	321	NO	SP	BL	OFF	H	BRG	H	SRV	NON	OFF	OFF	OFF	H	ROM	R7	WRI	NON	BG-1
DO-1	101	NO	SP	AL	OFF	H	BRG	L	MOV	NON	OFF	OFF	OFF	H	IRD	R0	REA	NON	MB-0
DO-2	157	NO	SP	RL	OFF	H	BRG	H	UNY	NON	OFF	OFF	OFF	H	ROM	R11	WRI	NON	DO-17
DO-3	162	NO	SP	ABAR	OFF	H	BRG	L	NMD	NON	OFF	ON	OFF	H	ROM	R10	REA	NON	DO-4
DO-3A	155	NO	SP	ABAR	OFF	H	BRG	L	NMD	NON	OFF	ON	OFF	H	ROM	R10	REA	NON	DO-4
DO-4	332	NO	SP	RL	OFF	H	BRG	H	SRV	NON	OFF	OFF	OFF	H	IRD	R0	WRI	NON	BG-1
D1-1	103	YES	SP	AL	OFF	L	BRG	L	JOT	NON	ON	OFF	OFF	H	IRD	R0	REA	IP	D1-2
D1-2	200	NO	NUL	AL	OFF	H	BRG	L	BYT	NON	OFF	OFF	OFF	H	BAR	R0	REA	NON	D1-3
D1-3	210	NO	SP	RL	OFF	H	BRG	H	UNY	NON	OFF	OFF	OFF	H	ROM	R11	WRI	NON	DO-18
D1-4	163	NO	SP	ABAR	OFF	H	BRG	L	NMD	NON	OFF	ON	OFF	H	ROM	R10	REA	NON	D1-5

Digital EQUIPMENT CORPORATION

NAME	LOC	ABT	ALG	ALU	AUX	BAR	BLG	BRG	BUT	CON	CKO	CRI	FSH	PSW	SAM	SPA	SPF	TNS	NXT
D1-5	334	YES	SP	AL	OFF	H	BRG	H	NON	NON	ON	OFF	OFF	H	ROM	R0	REA	0	D1-6
D1-6	065	NO	SP	BL	OFF	H	BRG	H	NON	NON	OFF	OFF	OFF	H	ROM	R0	REA	NON	B2-2
D2-1	105	YES	SP	AL	OFF	L	BRG	H	NON	NON	OFF	OFF	OFF	H	TRD	R0	REA	TP	D2-2
D2-2	331	NO	SP	A+B	OFF	H	+1	L	NON	NON	OFF	ON	OFF	H	TRD	R0	REA	NON	D2-3
D2-3	341	NO	SP	BL	OFF	H	BRG	H	JOJ	NON	ON	OFF	OFF	H	TRD	R0	WRI	NON	D1-2
D3-1	107	NO	SP	AL	OFF	L	BRG	H	NON	NON	OFF	OFF	OFF	H	TRD	R0	REA	I	D3-2
D3-2	160	NO	SP	A+B	OFF	H	+1	L	NON	NON	OFF	ON	OFF	H	TRD	R0	REA	NON	D3-3
D3-3	070	NO	SP	BL	OFF	H	BRG	H	NON	NON	ON	OFF	OFF	H	TRD	R0	WRI	NON	D3-4
D3-4	071	NO	NUL	AL	OFF	H	BRG	L	NON	NON	OFF	OFF	OFF	H	BAR	R0	REA	NON	D3-5
D3-5	072	YES	SP	BL	OFF	L	BRG	H	JOJ	NON	ON	OFF	OFF	H	ROM	R0	REA	TP	D1-2
D4-1	111	YES	SP	A=B-1	OFF	L	+1	L	ENO	NON	OFF	OFF	OFF	H	TRD	R0	REA	TP	D2-3
D5-1	113	NO	SP	A=B-1	OFF	L	+1	L	ENO	NON	OFF	OFF	OFF	H	TRD	R0	REA	I	D3-3
D6-1	115	NO	SP	AL	OFF	L	BRG	H	NON	NON	OFF	OFF	OFF	H	ROM	R7	REA	I	D6-2
D6-2	075	NO	SP	A+B	OFF	H	+1	L	NON	NON	OFF	ON	OFF	H	ROM	R7	REA	NON	D6-3
D6-3	077	NO	SP	BL	OFF	H	BRG	H	NON	NON	ON	OFF	OFF	H	ROM	R7	WRI	NON	D6-4
D6-4	057	NO	NUL	AL	OFF	H	BRG	L	NON	NON	OFF	OFF	OFF	H	BAR	R0	REA	NON	D6-5
D6-5	300	YES	SP	A+B	OFF	L	BRG	L	JOJ	NON	ON	OFF	OFF	H	TRD	R0	REA	TP	D1-2
D7-1	117	NO	SP	AL	OFF	L	BRG	H	NON	NON	OFF	OFF	OFF	H	ROM	R7	REA	I	D7-2
D7-2	310	NO	SP	A+B	OFF	H	+1	L	NON	NON	OFF	ON	OFF	H	ROM	R7	REA	NON	D7-3
D7-3	104	NO	SP	BL	OFF	H	BRG	H	NON	NON	ON	OFF	OFF	H	ROM	R7	WRI	NON	D7-4
D7-4	320	NO	NUL	AL	OFF	H	BRG	L	NON	NON	OFF	OFF	OFF	H	BAR	R0	REA	NON	D7-5
D7-5	106	NO	SP	A+B	OFF	L	BRG	H	NON	NON	ON	OFF	OFF	H	TRD	R0	REA	I	D3-4
DB0-1	156	NO	SP	BL	OFF	H	SEX	L	UNY	NON	OFF	OFF	OFF	H	ROM	R11	WRI	NON	SB1-5
DB0-2	164	NO	SP	ABAR	OFF	H	BRG	L	NMD	NON	OFF	ON	OFF	H	ROM	R10	REA	NON	DB0-3
DB0-3	304	NO	SPR	BL	OFF	H	BRG	H	SRV	NON	OFF	OFF	OFF	H	TRD	R0	WRI	NON	BG-1
DBE-1	110	NO	SP	AL	OFF	H	BRG	H	NON	NON	OFF	OFF	OFF	H	ROM	R0	REA	NON	H-1
DE-1	250	NO	SP	BL	OFF	H	SEX	L	UNY	NON	OFF	OFF	OFF	H	ROM	R11	WRI	NON	D1-4
DO-1	270	NO	SP	AL	OFF	H	BRG	SR	NON	NON	OFF	OFF	ON	H	ROM	R0	REA	NON	DO-2
DO-10	165	NO	SP	ABAR	OFF	H	BRG	L	NMD	NON	OFF	ON	OFF	H	ROM	R10	REA	NON	DO-11
DO-11	342	NO	SP	AL	OFF	H	BRG	SL	NON	NON	OFF	OFF	ON	H	ROM	R0	REA	NON	DO-12
DO-12	135	NO	SP	AL	OFF	H	BRG	SL	NON	NON	OFF	OFF	ON	H	ROM	R0	REA	NON	DO-13
DO-13	136	NO	SP	AL	OFF	H	BRG	SL	NON	NON	OFF	OFF	ON	H	ROM	R0	REA	NON	DO-14
DO-14	137	NO	SP	AL	OFF	H	BRG	SL	NON	NON	OFF	OFF	ON	H	ROM	R0	REA	NON	DO-15
DO-15	140	NO	SP	AL	OFF	H	BRG	SL	NON	NON	OFF	OFF	ON	H	ROM	R0	REA	NON	DO-16
DO-16	141	NO	SP	AL	OFF	H	BRG	SL	NON	NON	OFF	OFF	ON	H	ROM	R0	REA	NON	DO-17
DO-17	142	NO	SP	AL	OFF	H	BRG	SL	NON	NON	OFF	OFF	ON	H	ROM	R0	REA	NON	DO-18
DO-18	143	YES	SP	AL	OFF	H	BRG	SL	NON	NON	ON	OFF	OFF	H	ROM	R0	REA	0	D1-6
DO-2	123	NO	SP	AL	OFF	H	BRG	SR	NON	NON	OFF	OFF	ON	H	ROM	R0	REA	NON	DO-3
DO-3	124	NO	SP	AL	OFF	H	BRG	SR	NON	NON	OFF	OFF	ON	H	ROM	R0	REA	NON	DO-4
DO-4	125	NO	SP	AL	OFF	H	BRG	SR	NON	NON	OFF	OFF	ON	H	ROM	R0	REA	NON	DO-5
DO-5	126	NO	SP	AL	OFF	H	BRG	SR	NON	NON	OFF	OFF	ON	H	ROM	R0	REA	NON	DO-6
DO-6	127	NO	SP	AL	OFF	H	BRG	SR	NON	NON	OFF	OFF	ON	H	ROM	R0	REA	NON	DO-7
DO-7	130	NO	SP	AL	OFF	H	BRG	SR	NON	NON	OFF	OFF	ON	H	ROM	R0	REA	NON	DO-8
DO-8	131	NO	SP	AL	OFF	H	BRG	SR	NON	NON	OFF	OFF	OFF	H	ROM	R0	REA	NON	DO-9

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NAME	LOC	ABT	ALG	ALU	AUX	BAR	BLG	BRG	BUT	CON	CKO	CRI	FSH	PSW	SAM	SPA	SPF	TNS	NXT
DO-9	132	NO	SP	BL	OFF	H	SEX	L	UNY	NON	OFF	OFF	OFF	H	ROM	R11	WRI	NON	A145
ERT-1	010	NO	NUL	AL	OFF	H	BRG	L	CON	4	OFF	OFF	OFF	H	ROM	R0	WRI	NON	ET-2
ERT1A	046	NO	NUL	AL	OFF	H	BRG	L	CON	4	OFF	OFF	OFF	H	ROM	R0	WRI	NON	ET2-2
ERT1B	153	NO	NUL	AL	OFF	H	BRG	L	CON	4	OFF	OFF	OFF	H	ROM	R0	WRI	NON	ET-2
ET-1	011	NO	NUL	AL	OFF	H	BRG	L	CON	30	OFF	OFF	OFF	H	ROM	R0	WRI	NON	ET-2
ET-10	254	NO	SP	AL	OFF	L	BRG	H	IRC	NON	ON	OFF	OFF	H	ROM	R12	REA	T	ET-11
ET-11	255	NO	SP	AL	OFF	H	BRG	L	NON	NON	ON	OFF	OFF	H	ROM	R7	WRI	NON	ET-12
ET-12	256	NO	SP	A+B	OFF	L	+1	L	NON	NON	ON	ON	OFF	H	ROM	R12	REA	T	ET-13
ET-13	257	NO	SP	AL	OFF	H	BRG	L	NON	NON	OFF	OFF	OFF	L	ROM	R0	REA	NON	R2-2
ET-2	245	NO	SP	BL	OFF	H	BRG	H	NON	NON	OFF	OFF	OFF	H	ROM	R12	WRI	NON	ET-3
ET-3	246	NO	SP	A-B-1	OFF	L	+1	L	END	NON	OFF	OFF	OFF	H	ROM	R6	REA	NON	ET-5
ET-5	247	NO	SP	BL	OFF	H	BRG	H	NON	NON	ON	OFF	OFF	H	ROM	R6	WRI	O	ET-6
ET-6	226	NO	PSW	AL	OFF	H	BRG	H	NON	NON	OFF	OFF	OFF	H	ROM	R0	REA	NON	ET-7
ET-7	251	NO	SP	A-B-1	OFF	L	+1	L	END	NON	OFF	OFF	OFF	H	ROM	R6	REA	NON	ET-8
ET-8	252	NO	SP	BL	OFF	H	BRG	H	NON	NON	ON	OFF	OFF	H	ROM	R6	WRI	O	ET-9
ET-9	253	NO	SP	AL	OFF	H	BRG	H	NON	NON	OFF	OFF	OFF	H	ROM	R7	REA	NON	ET-10
ET2-2	003	NO	SP	BL	OFF	H	BRG	L	NON	NON	OFF	OFF	OFF	H	ROM	R12	WRI	NON	ET2-3
ET2-3	004	NO	SP	A-B-1	OFF	L	+1	L	NON	NON	OFF	OFF	OFF	H	ROM	R6	REA	NON	ET2-5
ET2-5	036	NO	SP	BL	OFF	H	BRG	H	NON	NON	ON	OFF	OFF	H	ROM	R6	WRI	O	ET2-6
ET2-6	037	NO	PSW	AL	OFF	H	BRG	H	NON	NON	OFF	OFF	OFF	H	ROM	R0	REA	NON	ET2-7
ET2-7	051	NO	SP	A-B-1	OFF	L	+1	L	NON	NON	OFF	OFF	OFF	H	ROM	R6	REA	NON	ET-8
F-1	062	NO	SP	AL	OFF	L	BRG	H	NON	NON	OFF	OFF	OFF	H	ROM	R7	REA	T	F-2
F-2	053	NO	SP	A+B	OFF	H	+1	L	NON	NON	OFF	ON	OFF	H	ROM	R7	WRI	NON	F-3
F-3	365	NO	SP	BL	OFF	H	BRG	H	NON	NON	ON	OFF	OFF	H	ROM	R7	WRI	NON	F-4
F-4	364	NO	NUL	AL	OFF	H	BRG	L	IRC	NON	OFF	OFF	OFF	H	BAR	R0	REA	NON	F-5
F-5	061	NO	SP	BL	OFF	H	SEX	L	IRD	NON	OFF	OFF	OFF	H	ROM	R0	REA	NON	RT-1
H-1	041	NO	SP	AL	OFF	H	BRG	L	NON	NON	OFF	OFF	OFF	H	ROM	R7	REA	NON	H-2
H-2	302	NO	SP	AL	OFF	L	BRG	H	SW	NON	OFF	OFF	OFF	H	ROM	R17	REA	NON	D6-5
INT-1	325	NO	SP	AL	OFF	H	BRG	H	SVS	NON	OFF	OFF	OFF	H	ROM	R12	WRI	NON	ET-3
IT-1	273	NO	NUL	AL	OFF	H	BRG	L	CON	20	OFF	OFF	OFF	H	ROM	R0	WRI	NON	ET-2
J1-1	204	NO	SP	AL	OFF	H	BRG	H	NON	NON	OFF	OFF	OFF	H	ROM	R0	REA	NON	J1-2
J1-2	260	NO	SP	BL	OFF	H	BRG	H	SRV	NON	OFF	OFF	OFF	H	ROM	R7	WRI	NON	BG-1
J2-1	212	NO	SP	AL	OFF	H	BRG	H	NON	NON	OFF	OFF	OFF	H	ROM	R0	REA	NON	J2-1A
J2-1A	261	NO	SP	BL	OFF	H	BRG	H	NON	NON	OFF	OFF	OFF	H	ROM	R11	WRI	NON	J2-2
J2-2	262	NO	SP	A-B-1	OFF	L	+1	L	END	NON	OFF	OFF	OFF	H	ROM	R6	REA	NON	J2-3
J2-3	214	NO	SP	BL	OFF	H	BRG	H	NON	NON	ON	OFF	OFF	H	ROM	R6	WRI	O	J2-4
J2-4	206	NO	SP	AL	OFF	H	BRG	H	NON	NON	OFF	OFF	OFF	H	IRS	R0	REA	NON	J2-5
J2-5	216	NO	SP	AL	OFF	H	BRG	L	NON	NON	OFF	OFF	OFF	H	ROM	R7	REA	NON	J2-6
J2-6	263	NO	SP	BL	OFF	H	BRG	H	NON	NON	OFF	OFF	OFF	H	IRS	R0	WRI	NON	J2-7
J2-7	264	NO	SP	AL	OFF	H	BRG	L	NON	NON	OFF	OFF	OFF	H	ROM	R11	REA	NON	J2-8
J2-8	265	NO	SP	BL	OFF	H	BRG	H	SRV	NON	OFF	OFF	OFF	H	ROM	R7	WRI	NON	BG-1
LC-1	042	NO	NUL	AL	OFF	H	BRG	L	CON	100	OFF	OFF	OFF	H	ROM	R0	WRI	NON	FI-2
MB-0	154	NO	SP	AL	ON	H	BRG	H	NON	NON	OFF	OFF	OFF	H	ROM	R0	REA	NON	MB-1
MB-1	240	NO	SP	ABAR	OFF	H	BRG	L	NON	NON	OFF	ON	OFF	H	ROM	R10	REA	NON	MB-2

NAME	LOC	ARI	ADG	ADD	ADJ	BAR	BRG	BRG	CON	CRU	CRU	ESH	ESK	SAM	SPA	SPE	TNS	NXT	
MB-2	152	NO	SP	BL	OFF	H	SEX	H	SRV	NON	OFF	OFF	OFF	H	IRD	R0	WRI	NON	BG-1
PF-1	043	NO	NUL	AL	OFF	H	BRG	L	CON	24	OFF	OFF	OFF	H	ROM	R0	WRI	NON	E1-2
R1-1	005	NO	SP	AL	OFF	L	BRG	H	NON	NON	OFF	OFF	OFF	H	ROM	R6	REA	I	R1-2
R1-2	221	NO	SP	A+B	OFF	H	+1	L	NON	NON	OFF	ON	OFF	H	ROM	R6	REA	NON	R1-3
R1-3	222	NO	SP	BL	OFF	H	BRG	H	NON	NON	OFF	OFF	OFF	H	ROM	R6	WRI	NON	R1-4
R1-4	223	NO	SP	AL	OFF	H	BRG	L	NON	NON	OFF	OFF	OFF	H	IRD	R0	REA	NON	R1-5
R1-5	224	NO	SP	BL	OFF	H	BRG	H	NON	NON	ON	OFF	OFF	H	ROM	R7	WRI	NON	R1-6
R1-6	225	NO	NUL	AL	OFF	H	BRG	L	NON	NON	OFF	OFF	OFF	H	BAR	R0	REA	NON	D0-4
R2-1	227	NO	SP	AL	OFF	L	BRG	H	NON	NON	OFF	OFF	OFF	H	ROM	R6	REA	I	R2-2
R2-2	230	NO	SP	A+B	OFF	H	+1	L	NON	NON	OFF	ON	OFF	H	ROM	R6	REA	NON	R2-3
R2-3	231	NO	SP	BL	OFF	H	BRG	H	NON	NON	ON	OFF	OFF	H	ROM	R6	WRI	NON	R2-4
R2-4	232	NO	SP	AL	OFF	H	BRG	H	NON	NON	OFF	OFF	OFF	H	ROM	R7	WRI	NON	R2-6
R2-6	234	NO	SP	AL	OFF	L	BRG	H	NON	NON	OFF	OFF	OFF	H	ROM	R6	REA	I	R2-7
R2-7	235	NO	SP	A+B	OFF	H	+1	L	NON	NON	OFF	ON	OFF	H	ROM	R6	REA	NON	R2-8
R2-8	236	NO	SP	BL	OFF	H	BRG	H	NON	NON	ON	OFF	OFF	H	ROM	R6	WRI	NON	R2-9
R2-9	237	NO	NUL	AL	OFF	H	BRG	H	NON	NON	OFF	OFF	OFF	L	ROM	R0	REA	NON	B2-2
RS-1	000	NO	NUL	AL	OFF	L	BRG	H	CON	24	OFF	OFF	OFF	H	ROM	R0	WRI	I	RS-1A
RS-1A	241	NO	SP	AL	OFF	H	BRG	H	NON	NON	ON	OFF	OFF	H	ROM	R0	REA	NON	RS-2
RS-2	347	NO	SP	AL	OFF	H	BRG	H	NON	NON	OFF	OFF	OFF	H	ROM	R7	WRI	NON	RS-3
RS-3	074	NO	NUL	A+B	OFF	L	+1	H	CON	24	ON	ON	OFF	H	ROM	R0	WRI	I	RS-4
RS-4	351	NO	SP	AL	OFF	H	BRG	H	NON	NON	OFF	OFF	OFF	L	ROM	R0	REA	NON	B2-2
RST-1	357	NO	SP	AL	OFF	H	BRG	H	INI	NON	ON	OFF	OFF	H	ROM	R0	REA	NON	B2-2
RI-1	001	NO	NUL	AL	OFF	H	BRG	L	CON	10	OFF	OFF	OFF	H	ROM	R0	WRI	NON	E1-2
S0-1	201	NO	SP	AL	OFF	H	BRG	L	BYT	NON	OFF	OFF	OFF	H	IRS	R0	REA	NON	S0-2
S0-2	007	NO	SP	BL	OFF	H	BRG	H	DST	NON	OFF	OFF	OFF	H	ROM	R10	WRI	NON	RT-1
S1-1	203	YES	SP	AL	OFF	L	BRG	H	NON	NON	ON	OFF	OFF	H	IRS	R0	REA	I	S1-2
S1-2	244	NO	NUL	AL	OFF	H	BRG	L	BYT	NON	OFF	OFF	OFF	H	BAR	R0	REA	NON	S0-2
S2-1	205	YES	SP	AL	OFF	L	BRG	H	NON	NON	OFF	OFF	OFF	H	IRS	R0	REA	I	S2-2
S2-2	301	NO	SP	A+B	OFF	H	+1	L	NON	NON	OFF	ON	OFF	H	IRS	R0	REA	NON	S2-3
S2-3	014	NO	SP	BL	OFF	H	BRG	H	NON	NON	ON	OFF	OFF	H	IRS	R0	WRI	NON	S1-2
S3-1	207	NO	SP	AL	OFF	L	BRG	H	NON	NON	OFF	OFF	OFF	H	IRS	R0	REA	I	S3-2
S3-2	016	NO	SP	A+B	OFF	H	+1	L	NON	NON	OFF	ON	OFF	H	IRS	R0	REA	NON	S3-3
S3-3	017	NO	SP	BL	OFF	H	BRG	H	NON	NON	ON	OFF	OFF	H	IRS	R0	WRI	NON	S3-4
S3-4	134	NO	NUL	AL	OFF	H	BRG	L	NON	NON	OFF	OFF	OFF	H	BAR	R0	REA	NON	S3-5
S3-5	274	YES	SP	BL	OFF	L	BRG	H	NON	NON	ON	OFF	OFF	H	ROM	R0	REA	I	S1-2
S4-1	211	YES	SP	A-B-1	OFF	L	+1	L	END	NON	OFF	OFF	OFF	H	IRS	R0	REA	I	S2-3
S5-1	213	NO	SP	A-B-1	OFF	L	+1	L	END	NON	OFF	OFF	OFF	H	IRS	R0	REA	I	S3-3
S6-1	215	NO	SP	AL	OFF	L	BRG	H	NON	NON	OFF	OFF	OFF	H	ROM	R7	REA	I	S6-2
S6-2	025	NO	SP	A+B	OFF	H	+1	L	NON	NON	OFF	ON	OFF	H	ROM	R7	REA	NON	S6-3
S6-3	026	NO	SP	BL	OFF	H	BRG	H	NON	NON	ON	OFF	OFF	H	ROM	R7	WRI	NON	S6-4
S6-4	027	NO	NUL	AL	OFF	H	BRG	L	NON	NON	OFF	OFF	OFF	H	BAR	R0	REA	NON	S6-5
S6-5	030	YES	SP	A+B	OFF	L	BRG	H	NON	NON	ON	OFF	OFF	H	IRS	R0	REA	I	S1-2
S7-1	217	NO	SP	AL	OFF	L	BRG	H	NON	NON	OFF	OFF	OFF	H	ROM	R7	REA	I	S7-2
S7-2	032	NO	SP	A+B	OFF	H	+1	L	NON	NON	OFF	ON	OFF	H	ROM	R7	REA	NON	S7-3

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NAME	LOC	ABT	ALG	ALU	AUX	BAR	BLG	BRG	BUT	CON	CKO	CRI	FSH	PSW	SAM	SPA	SPP	TNS	NXT
S7-3	033	NO	SP	BL	OFF	H	BRG	H	NON	NON	ON	OFF	OFF	H	ROM	R7	WRI	NON	S7-4
S7-4	034	NO	NUL	AL	OFF	H	BRG	L	NON	NON	OFF	OFF	OFF	H	BAR	RO	REA	NON	S7-5
S7-5	035	NO	SP	A+B	OFF	L	BRG	H	NON	NON	ON	OFF	OFF	H	TRS	RO	REA	I	S3-4
SB1-1	166	NO	SP	ASR	OFF	H	BRG	SL	NON	NON	OFF	OFF	ON	H	ROM	RO	REA	NON	SB1-2
SB1-2	172	NO	SP	ASR	OFF	H	BRG	SL	NON	NON	OFF	OFF	ON	H	ROM	RO	REA	NON	SB1-3
SB1-3	173	NO	SP	ASR	OFF	H	BRG	SL	NON	NON	OFF	OFF	ON	H	ROM	RO	REA	NON	SB1-4
SB1-4	174	NO	SP	ASR	OFF	H	BRG	SL	NON	NON	OFF	OFF	ON	H	ROM	RO	REA	NON	SB1-5
SB1-5	144	NO	SP	ASR	OFF	H	BRG	SL	NON	NON	OFF	OFF	ON	H	ROM	RO	REA	NON	SB1-6
SB1-6	176	NO	SP	ASR	OFF	H	BRG	SL	NON	NON	OFF	OFF	ON	H	ROM	RO	REA	NON	SB1-7
SB1-7	177	NO	SP	ASR	OFF	H	BRG	SL	NON	NON	OFF	OFF	ON	H	ROM	RO	REA	NON	SB1-8
SB1-8	006	NO	SP	ASR	ON	H	BRG	SL	NON	NON	OFF	OFF	OFF	H	ROM	RO	REA	NON	D0-3A
SB2-1	167	NO	SP	ASR	OFF	H	BRG	SL	NON	NON	OFF	OFF	ON	H	ROM	RO	REA	NON	SB2-2
SB2-2	012	NO	SP	ASR	OFF	H	BRG	SL	NON	NON	OFF	OFF	ON	H	ROM	RO	REA	NON	SB2-3
SB2-3	220	NO	SP	ASR	OFF	H	BRG	SL	NON	NON	OFF	OFF	ON	H	ROM	RO	REA	NON	SB2-4
SB2-4	022	NO	SP	ASR	OFF	H	BRG	SL	NON	NON	OFF	OFF	ON	H	ROM	RO	REA	NON	SB2-5
SB2-5	023	NO	SP	ASR	OFF	H	BRG	SL	NON	NON	OFF	OFF	ON	H	ROM	RO	REA	NON	SB2-6
SB2-6	024	NO	SP	ASR	OFF	H	BRG	SL	NON	NON	OFF	OFF	ON	H	ROM	RO	REA	NON	SB2-7
SB2-7	031	NO	SP	ASR	OFF	H	BRG	SL	NON	NON	OFF	OFF	ON	H	ROM	RO	REA	NON	SB2-8
SB2-8	330	NO	SP	ASR	ON	H	BRG	SL	NON	NON	OFF	OFF	OFF	H	ROM	RO	REA	NON	D1-4
SBE-1	047	NO	SP	BL	OFF	H	SEX	H	DST	NON	OFF	OFF	OFF	H	ROM	R10	WRI	NON	RT-1
SBO-1	067	NO	SP	AL	OFF	H	BRG	SR	NON	NON	OFF	OFF	ON	H	ROM	RO	REA	NON	SBO-2
SBO-2	346	NO	SP	AL	OFF	H	BRG	SR	NON	NON	OFF	OFF	ON	H	ROM	RO	REA	NON	SBO-3
SBO-3	324	NO	SP	AL	OFF	H	BRG	SR	NON	NON	OFF	OFF	ON	H	ROM	RO	REA	NON	SBO-4
SBO-4	340	NO	SP	AL	OFF	H	BRG	SR	NON	NON	OFF	OFF	ON	H	ROM	RO	REA	NON	SBO-5
SBO-5	361	NO	SP	AL	OFF	H	BRG	SR	NON	NON	OFF	OFF	ON	H	ROM	RO	REA	NON	SBO-6
SBO-6	050	NO	SP	AL	OFF	H	BRG	SR	NON	NON	OFF	OFF	ON	H	ROM	RO	REA	NON	SBO-7
SBO-7	020	NO	SP	AL	OFF	H	BRG	SR	NON	NON	OFF	OFF	ON	H	ROM	RO	REA	NON	SBO-8
SBO-8	052	NO	SP	AL	OFF	H	BRG	SR	NON	NON	OFF	OFF	OFF	H	ROM	RO	REA	NON	SBE-1
SC-1	116	NO	PSW	AORB	OFF	H	BRG	H	SRV	NON	OFF	OFF	OFF	L	ROM	RO	REA	NON	BG-1
T-1	021	NO	NUL	AL	OFF	H	BRG	L	CON	34	OFF	OFF	OFF	H	ROM	RO	WRI	NON	ET-2
U1-1	352	NO	SP	BL	ON	H	BRG	H	NON	NON	OFF	OFF	OFF	H	ROM	R10	WRI	NON	D0-3
U2-1	353	NO	SP	BL	ON	H	BRG	H	NON	NON	OFF	OFF	OFF	H	ROM	R10	WRI	NON	D1-4
U3-1	354	NO	SP	BL	ON	H	BRG	H	NON	NON	OFF	OFF	OFF	H	ROM	R10	WRI	NON	DB0-2
U4-1	355	NO	SP	BL	ON	H	BRG	H	NON	NON	OFF	OFF	OFF	H	ROM	R10	WRI	NON	D0-10
U5-1	373	NO	SP	BL	ON	H	BRG	H	NON	NON	OFF	OFF	OFF	H	ROM	R10	WRI	NON	D1-4
URTR	064	NO	NUL	AL	OFF	H	BRG	L	CON	60	OFF	OFF	OFF	H	ROM	RO	WRI	NON	ET-2
URTX	060	NO	NUL	AL	OFF	H	BRG	L	CON	64	OFF	OFF	OFF	H	ROM	RO	WRI	NON	ET-2
W-1	063	NO	SP	AL	OFF	H	BRG	H	SRV	NON	OFF	OFF	OFF	H	ROM	RO	REA	NON	BG-1





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N A M	L O C	N X T	A L U	C F A R S U I H X	P S S D S P P 1 W 1 3 P	S S S R M P M B O O 1 T	B B S S A T P P R P F 2	C A T K B N O T S	A B L R G G	B U T	
A145	145	0000	0000	0000	0 0 1 0	0 0 0 0	1 0 1 0	0 1 0 0	0 0 0 0	00 00	0101
B=1	015	1001	1000	1100	0 0 1 1	1 0 0 1	1 0 1 1	1 1 1 0	1 1 1 1	11 01	1111
B=2	147	1001	1001	0110	0 0 1 1	1 1 0 1	1 1 1 1	1 1 1 1	1 1 1 1	11 11	1111
B=3	146	1101	1111	0101	1 0 1 1	1 1 0 1	1 1 1 1	1 1 0 1	1 1 1 1	11 00	1100
B2=2	305	1101	1111	0000	1 0 1 1	1 0 0 1	1 0 1 1	1 1 1 0	1 1 1 1	11 00	1100
B2=2A	333	1101	1111	0000	1 0 1 1	1 0 0 1	1 0 1 1	1 1 1 0	1 1 1 1	11 00	1100
B2=2B	335	1101	1111	0000	1 0 1 1	1 0 0 1	1 0 1 1	1 1 1 0	1 1 1 1	11 00	1100
B2=2C	343	1101	1111	0000	1 0 1 1	1 0 0 1	1 0 1 1	1 1 1 0	1 1 1 1	11 00	1100
B2=2D	013	1101	1111	0000	1 0 1 1	1 0 0 1	1 0 1 1	1 1 1 0	1 1 1 1	11 00	1100
BG=1	040	0011	1010	0000	1 0 1 1	1 0 0 1	1 0 1 1	1 1 1 0	1 1 1 1	11 00	0001
BT=1	045	0101	1010	0000	1 0 1 1	1 1 0 1	1 0 1 1	1 1 0 0	1 1 1 1	10 11	1101
CC=1	112	1101	1111	0010	1 0 1 1	0 0 0 1	1 0 1 1	1 1 1 0	1 1 1 1	00 00	1100
CCM=1	151	0001	0111	0001	1 0 1 1	1 0 0 1	1 1 1 1	1 1 0 0	1 1 1 1	10 11	1101
CCM=2	350	1011	0101	0000	1 0 1 1	1 0 0 1	1 0 1 1	1 1 1 0	1 1 1 1	11 00	1001
CCS=1	316	0100	0001	0000	1 0 1 1	1 1 0 1	1 1 1 1	1 1 1 1	1 1 1 1	11 11	1111
CCS=2	276	0100	0111	0000	1 0 1 1	1 0 0 1	1 0 1 1	1 1 1 0	1 1 1 1	11 00	0110
CCS=3	272	1100	1101	0011	1 0 1 1	1 0 0 1	1 0 1 1	1 1 1 0	1 1 1 1	11 00	0000
CD1=1	313	0011	1100	0000	1 0 1 1	1 1 1 1	1 1 1 1	1 1 1 1	1 1 1 1	11 11	0110
CD1=2	303	0000	0011	1111	1 0 1 1	1 0 0 1	1 0 1 1	0 1 0 0	0 1 1 0	10 00	1101
CD1=3	374	0011	0011	0000	1 0 1 1	1 0 0 1	0 0 0 1	1 1 1 0	1 1 1 1	10 11	1111
CD1=4	314	0000	0101	0000	1 0 1 1	1 1 1 1	1 1 1 1	0 1 1 1	0 1 0 1	11 00	1111
CD1=5	372	0011	1101	0101	1 0 1 1	1 0 0 1	0 0 0 1	1 1 1 0	1 1 1 1	11 00	1111
CD2=1	312	0010	0000	0110	0 1 1 1	1 1 1 1	1 1 1 0	1 1 1 1	1 1 1 1	11 11	1111
CD2=2	337	0011	0100	0101	1 0 1 1	1 1 1 1	1 1 1 1	1 1 0 1	1 1 1 1	11 00	1111
CE1=1	317	0011	1000	0000	1 0 1 1	1 1 1 1	1 1 1 1	0 1 1 1	1 1 1 1	11 11	0110
CE1=2	307	0010	1001	0000	1 0 1 1	1 0 0 1	1 0 1 1	1 1 1 0	0 1 1 0	11 00	1111
CE1=3	326	0011	1101	0000	1 0 1 1	1 0 0 1	0 0 0 1	1 1 1 0	1 1 1 1	10 11	1111
CE2=1	315	0000	0110	0110	0 1 1 1	1 1 1 1	1 1 1 0	0 1 1 1	1 1 1 1	11 11	1111
CE2=2	371	0011	0000	0101	1 0 1 1	1 1 1 1	1 1 1 1	1 1 0 1	1 1 1 1	11 00	1111
CL=1	311	0000	0010	1111	1 0 1 1	1 0 0 1	1 0 1 1	0 1 0 0	0 1 1 0	10 00	1101
CL=2	375	0000	1000	0000	1 0 1 1	1 0 0 1	0 0 0 1	1 1 1 0	1 1 1 1	10 11	1111
CL=3	367	0011	1101	0101	1 0 1 1	1 1 1 1	1 1 1 1	1 1 0 1	1 1 1 1	11 00	1111
CS=1	100	0010	1101	0011	1 0 1 1	1 0 0 1	1 0 1 1	1 1 1 0	1 1 1 1	11 00	0000
CS=2	322	0010	1110	0000	1 0 1 1	1 1 1 1	1 1 1 1	0 1 1 1	1 1 1 1	11 11	1111
CS=3	321	1101	1111	0101	1 0 1 1	1 1 0 1	1 1 1 1	1 1 0 1	1 1 1 1	11 00	1100
D0=1	101	1001	0011	0000	1 0 1 1	1 0 0 1	0 0 1 1	1 1 1 0	1 1 1 1	11 11	0101
D0=2	157	1001	1101	0101	1 0 1 1	1 0 1 1	1 1 1 1	1 1 0 0	1 1 1 1	11 00	1010
D0=3	162	0010	0101	1111	1 1 1 1	1 0 1 1	1 0 1 1	1 1 1 0	1 1 1 1	11 11	0010
D0=3A	155	0010	0101	1111	1 1 1 1	1 0 1 1	1 0 1 1	1 1 1 0	1 1 1 1	11 11	0010
D0=4	332	1101	1111	0101	1 0 1 1	1 0 0 1	0 0 1 1	1 1 0 0	1 1 1 1	11 00	1100

N A M	L O C	N X T	A L U	C F A R S U I H X	P S S D S P P I W 1 3 P	S S S B M P M B 0 0 1 T	B B S S A T P P R P F 2	C A T K B N D T S	A R L R G G	B U T	
D1-1	103	0111	1111	0000	1 0 1 1	1 0 0 0	0 0 1 1	0 1 1 0	0 0 1 0	11 11	1011
D1-2	200	0111	0111	0000	1 0 1 1	1 0 0 1	0 0 0 1	1 1 1 0	1 1 1 1	10 11	0011
D1-3	210	1001	1100	0101	1 0 1 1	1 0 1 1	1 1 1 1	1 1 0 0	1 1 1 1	11 00	1010
D1-4	163	0010	0011	1111	1 1 1 1	1 0 1 1	1 0 1 1	1 1 1 0	1 1 1 1	11 11	0010
D1-5	334	1100	1010	0000	1 0 1 1	1 0 0 1	1 0 1 1	1 1 1 0	0 0 0 1	11 00	1111
D1-6	065	0011	1010	0101	1 0 1 1	1 0 0 1	1 0 1 1	1 1 1 0	1 1 1 1	11 00	1111
D2-1	105	0010	0110	0000	1 0 1 1	1 0 0 0	0 0 1 1	0 1 1 0	1 0 1 0	11 00	1111
D2-2	331	0001	1110	0110	0 1 1 1	1 0 0 1	0 0 1 0	1 1 1 0	1 1 1 1	11 11	1111
D2-3	341	0111	1111	0101	1 0 1 1	1 0 0 1	0 0 1 1	1 1 0 0	0 1 1 1	11 00	1011
D3-1	107	1000	1111	0000	1 0 1 1	1 0 0 1	0 0 1 1	0 1 1 0	1 1 1 0	11 00	1111
D3-2	160	1100	0111	0110	0 1 1 1	1 0 0 1	0 0 1 0	1 1 1 0	1 1 1 1	11 11	1111
D3-3	070	1100	0110	0101	1 0 1 1	1 0 0 1	0 0 1 1	1 1 0 0	0 1 1 1	11 00	1111
D3-4	071	1100	0101	0000	1 0 1 1	1 0 0 1	0 0 0 1	1 1 1 0	1 1 1 1	10 11	1111
D3-5	072	0111	1111	0101	1 0 1 1	1 0 0 0	1 0 1 1	0 1 1 0	0 0 1 0	11 00	1011
D4-1	111	0001	1110	1001	0 0 1 1	1 0 0 0	0 0 1 0	0 1 1 0	1 0 1 0	11 11	0100
D5-1	113	1100	0111	1001	0 0 1 1	1 0 0 1	0 0 1 0	0 1 1 0	1 1 1 0	11 11	0100
D6-1	115	1100	0010	0000	1 0 1 1	1 1 0 1	1 1 1 1	0 1 1 1	1 1 1 0	11 00	1111
D6-2	075	1100	0000	0110	0 1 1 1	1 1 0 1	1 1 1 0	1 1 1 1	1 1 1 1	11 11	1111
D6-3	077	1101	0000	0101	1 0 1 1	1 1 0 1	1 1 1 1	1 1 0 1	0 1 1 1	11 00	1111
D6-4	057	0011	1111	0000	1 0 1 1	1 0 0 1	0 0 0 1	1 1 1 0	1 1 1 1	10 11	1111
D6-5	300	0111	1111	0110	0 0 1 1	1 0 0 0	0 0 1 1	0 1 1 0	0 0 1 0	11 11	1011
D7-1	117	0011	0111	0000	1 0 1 1	1 1 0 1	1 1 1 1	0 1 1 1	1 1 1 0	11 00	1111
D7-2	310	1011	1011	0110	0 1 1 1	1 1 0 1	1 1 1 0	1 1 1 1	1 1 1 1	11 11	1111
D7-3	104	0010	1111	0101	1 0 1 1	1 1 0 1	1 1 1 1	1 1 0 1	0 1 1 1	11 00	1111
D7-4	320	1011	1001	0000	1 0 1 1	1 0 0 1	0 0 0 1	1 1 1 0	1 1 1 1	10 11	1111
D7-5	106	1100	0110	0110	0 0 1 1	1 0 0 1	0 0 1 1	0 1 1 0	0 1 1 0	11 00	1111
DB0-1	156	1001	1011	0101	1 0 1 1	1 0 1 1	1 1 1 1	1 0 0 0	1 1 1 1	11 11	1010
DB0-2	164	0011	1011	1111	1 1 1 1	1 0 1 1	1 0 1 1	1 1 1 0	1 1 1 1	11 11	0010
DB0-3	304	1101	1111	0101	1 0 1 1	1 0 0 1	0 0 1 1	1 1 0 0	1 1 1 1	01 00	1100
DBE-1	110	1101	1110	0000	1 0 1 1	1 0 0 1	1 0 1 1	1 1 1 0	1 1 1 1	11 00	1111
DE-1	250	1000	1100	0101	1 0 1 1	1 0 1 1	1 1 1 1	1 0 0 0	1 1 1 1	11 11	1010
DO-1	270	1010	1100	0000	1 0 0 1	1 0 0 1	1 0 1 1	1 1 1 0	1 1 1 1	11 10	1111
DO-10	165	0001	1101	1111	1 1 1 1	1 0 1 1	1 0 1 1	1 1 1 0	1 1 1 1	11 11	0010
DO-11	342	1010	0010	0000	1 0 0 1	1 0 0 1	1 0 1 1	1 1 1 0	1 1 1 1	11 01	1111
DO-12	135	1010	0001	0000	1 0 0 1	1 0 0 1	1 0 1 1	1 1 1 0	1 1 1 1	11 01	1111
DO-13	136	1010	0000	0000	1 0 0 1	1 0 0 1	1 0 1 1	1 1 1 0	1 1 1 1	11 01	1111
DO-14	137	1001	1111	0000	1 0 0 1	1 0 0 1	1 0 1 1	1 1 1 0	1 1 1 1	11 01	1111
DO-15	140	1001	1110	0000	1 0 0 1	1 0 0 1	1 0 1 1	1 1 1 0	1 1 1 1	11 01	1111
DO-16	141	1001	1101	0000	1 0 0 1	1 0 0 1	1 0 1 1	1 1 1 0	1 1 1 1	11 01	1111
DO-17	142	1001	1100	0000	1 0 0 1	1 0 0 1	1 0 1 1	1 1 1 0	1 1 1 1	11 01	1111

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N A M	L O C	N X T	A L U	C F R S U I H X	P S S D W 1 3 P	S S S B M P M B 0 0 1 T	B B S S A T P P R P F 2	C A T K B N O T S	A B L R G G	B U T	
DO-18	143	1100	1010	0000	1 0 1 1	1 0 0 1	1 0 1 1	1 1 1 0	0 0 0 1	11 0 1	1111
DO-2	123	1010	1011	0000	1 0 0 1	1 0 0 1	1 0 1 1	1 1 1 0	1 1 1 1	11 1 0	1111
DO-3	124	1010	1010	0000	1 0 0 1	1 0 0 1	1 0 1 1	1 1 1 0	1 1 1 1	11 1 0	1111
DO-4	125	1010	1001	0000	1 0 0 1	1 0 0 1	1 0 1 1	1 1 1 0	1 1 1 1	11 1 0	1111
DO-5	126	1010	1000	0000	1 0 0 1	1 0 0 1	1 0 1 1	1 1 1 0	1 1 1 1	11 1 0	1111
DO-6	127	1010	0111	0000	1 0 0 1	1 0 0 1	1 0 1 1	1 1 1 0	1 1 1 1	11 1 0	1111
DO-7	130	1010	0110	0000	1 0 0 1	1 0 0 1	1 0 1 1	1 1 1 0	1 1 1 1	11 1 0	1111
DO-8	131	1010	0101	0000	1 0 1 1	1 0 0 1	1 0 1 1	1 1 1 0	1 1 1 1	11 1 0	1111
DO-9	132	1001	1010	0101	1 0 1 1	1 0 1 1	1 1 1 1	1 0 0 0	1 1 1 1	11 1 1	1010
ERT-1	010	0101	1010	0000	1 0 1 1	1 1 0 1	1 0 1 1	1 1 0 1	1 1 1 1	10 1 1	1101
ERT1A	046	1111	1100	0000	1 0 1 1	1 1 0 1	1 0 1 1	1 1 0 1	1 1 1 1	10 1 1	1101
ERT1B	153	0101	1010	0000	1 0 1 1	1 1 0 1	1 0 1 1	1 1 0 1	1 1 1 1	10 1 1	1101
ET-1	011	0101	1010	0000	1 0 1 1	1 1 0 1	1 0 1 1	1 1 1 0	1 1 1 1	10 1 1	1101
ET-10	254	0101	0010	0000	1 0 1 1	1 1 1 1	1 0 1 1	0 1 1 0	0 1 1 0	11 0 0	0000
ET-11	255	0101	0001	0000	1 0 1 1	1 1 0 1	1 1 1 1	1 1 0 1	1 1 1 1	11 1 1	1111
ET-12	256	0101	0000	0110	0 1 1 1	1 1 1 1	1 0 1 0	0 1 1 0	0 1 1 0	11 1 1	1111
ET-13	257	0011	1010	0000	1 0 1 1	0 0 0 1	1 0 1 1	1 1 1 0	1 1 1 1	11 1 1	1111
ET-2	245	0101	1001	0101	1 0 1 1	1 1 1 1	1 0 1 1	1 1 0 0	1 1 1 1	11 0 0	1111
ET-3	246	0101	1000	1001	0 0 1 1	1 1 0 1	1 0 1 0	0 1 1 1	1 1 1 1	11 1 1	0100
ET-5	247	0110	1001	0101	1 0 1 1	1 1 0 1	1 0 1 1	1 1 0 1	0 1 0 1	11 0 0	1111
ET-6	226	0101	0110	0000	1 0 1 1	1 0 0 1	1 0 1 1	1 1 1 0	1 1 1 1	00 0 0	1111
ET-7	251	0101	0101	1001	0 0 1 1	1 1 0 1	1 0 1 0	0 1 1 1	1 1 1 1	11 1 1	0100
ET-8	252	0101	0100	0101	1 0 1 1	1 1 0 1	1 0 1 1	1 1 0 1	0 1 0 1	11 0 0	1111
ET-9	253	0101	0011	0000	1 0 1 1	1 1 0 1	1 1 1 1	1 1 1 1	1 1 1 1	11 0 0	1111
ET2-2	003	1111	1011	0101	1 0 1 1	1 1 1 1	1 0 1 1	1 1 0 0	1 1 1 1	11 1 1	1111
ET2-3	004	1110	0001	1001	0 0 1 1	1 1 0 1	1 0 1 0	0 1 1 1	1 1 1 1	11 1 1	1111
ET2-5	036	1110	0000	0101	1 0 1 1	1 1 0 1	1 0 1 1	1 1 0 1	0 1 0 1	11 0 0	1111
ET2-6	037	1101	0110	0000	1 0 1 1	1 0 0 1	1 0 1 1	1 1 1 0	1 1 1 1	00 0 0	1111
ET2-7	051	0101	0101	1001	0 0 1 1	1 1 0 1	1 0 1 0	0 1 1 1	1 1 1 1	11 1 1	1111
F-1	062	1101	0100	0000	1 0 1 1	1 1 0 1	1 1 1 1	0 1 1 1	1 1 1 0	11 0 0	1111
F-2	053	0000	1010	0110	0 1 1 1	1 1 0 1	1 1 1 0	1 1 1 1	1 1 1 1	11 1 1	1111
F-3	365	0000	1011	0101	1 0 1 1	1 1 0 1	1 1 1 1	1 1 0 1	0 1 1 1	11 0 0	1111
F-4	364	1100	1110	0000	1 0 1 1	1 0 0 1	0 0 0 1	1 1 1 0	1 1 1 1	10 1 1	0000
F-5	061	1111	1110	0101	1 0 1 1	1 0 0 1	1 0 1 1	1 0 1 0	1 1 1 1	11 1 1	0111
H-1	041	0011	1101	0000	1 0 1 1	1 1 0 1	1 1 1 1	1 1 1 1	1 1 1 1	11 1 1	1111
H-2	302	0011	1111	0000	1 0 1 1	1 1 1 1	1 1 1 1	0 1 1 1	1 1 1 1	11 0 0	0110
INT-1	325	0101	1001	0000	1 0 1 1	1 1 1 1	1 0 1 1	1 1 0 0	1 1 1 1	11 0 0	1000
IT-1	273	0101	1010	0000	1 0 1 1	1 0 0 1	1 0 1 1	1 1 1 1	1 1 1 1	10 1 1	1101
J1-1	204	0100	1111	0000	1 0 1 1	1 0 0 1	1 0 1 1	1 1 1 0	1 1 1 1	11 0 0	1111
J1-2	260	1101	1111	0101	1 0 1 1	1 1 0 1	1 1 1 1	1 1 0 1	1 1 1 1	11 0 0	1100

N A M	L O C	N X T	A L U	C F A R S U I H X	P S S D S P P I W 1 3 P	S S S B M P M B 0 0 1 T	R B S S A T P P R P P 2	C A T K B N O T S	A B L R G G	B U T	
J2-1	212	0100	1110	0000	1 0 1 1	1 0 0 1	1 0 1 1	1 1 1 0	1 1 1 1	11 00	1111
J2-1A	261	0100	1101	0101	1 0 1 1	1 0 1 1	1 1 1 1	1 1 0 0	1 1 1 1	11 00	1111
J2-2	262	0111	0011	1001	0 0 1 1	1 1 0 1	1 0 1 0	0 1 1 1	1 1 1 1	11 11	0100
J2-3	214	0111	1001	0101	1 0 1 1	1 1 0 1	1 0 1 1	1 1 0 1	0 1 0 1	11 00	1111
J2-4	206	0111	0001	0000	1 0 1 1	1 0 0 1	1 0 0 1	1 1 1 0	1 1 1 1	11 00	1111
J2-5	216	0100	1100	0000	1 0 1 1	1 1 0 1	1 1 1 1	1 1 1 1	1 1 1 1	11 11	1111
J2-6	263	0100	1011	0101	1 0 1 1	1 0 0 1	1 0 0 1	1 1 0 0	1 1 1 1	11 00	1111
J2-7	264	0100	1010	0000	1 0 1 1	1 0 1 1	1 1 1 1	1 1 1 0	1 1 1 1	11 11	1111
J2-8	265	1101	1111	0101	1 0 1 1	1 1 0 1	1 1 1 1	1 1 0 1	1 1 1 1	11 00	1100
LC-1	042	0101	1010	0000	1 0 1 1	1 0 1 1	1 1 1 1	1 1 1 0	1 1 1 1	10 11	1101
MB-0	154	0101	1111	0000	1 0 1 0	1 0 0 1	1 0 1 1	1 1 1 0	1 1 1 1	11 00	1111
MB-1	240	1001	0101	1111	1 1 1 1	1 0 1 1	1 0 1 1	1 1 1 0	1 1 1 1	11 11	1111
MB-2	152	1101	1111	0101	1 0 1 1	1 0 0 1	0 0 1 1	1 0 0 0	1 1 1 1	11 00	1100
PF-1	043	0101	1010	0000	1 0 1 1	1 0 1 1	1 0 1 1	1 1 1 0	1 1 1 1	10 11	1101
R1-1	005	0110	1110	0000	1 0 1 1	1 1 0 1	1 0 1 1	0 1 1 1	1 1 1 0	11 00	1111
R1-2	221	0110	1101	0110	0 1 1 1	1 1 0 1	1 0 1 0	1 1 1 1	1 1 1 1	11 11	1111
R1-3	222	0110	1100	0101	1 0 1 1	1 1 0 1	1 0 1 1	1 1 0 1	1 1 1 1	11 00	1111
R1-4	223	0110	1011	0000	1 0 1 1	1 0 0 1	0 0 1 1	1 1 1 0	1 1 1 1	11 11	1111
R1-5	224	0110	1010	0101	1 0 1 1	1 1 0 1	1 1 1 1	1 1 0 1	0 1 1 1	11 00	1111
R1-6	225	0010	0101	0000	1 0 1 1	1 0 0 1	0 0 0 1	1 1 1 0	1 1 1 1	10 11	1111
R2-1	227	0110	0111	0000	1 0 1 1	1 1 0 1	1 0 1 1	0 1 1 1	1 1 1 0	11 00	1111
R2-2	230	0110	0110	0110	0 1 1 1	1 1 0 1	1 0 1 0	1 1 1 1	1 1 1 1	11 11	1111
R2-3	231	0110	0101	0101	1 0 1 1	1 1 0 1	1 0 1 1	1 1 0 1	0 1 1 1	11 00	1111
R2-4	232	0110	0011	0000	1 0 1 1	1 1 0 1	1 1 1 1	1 1 0 1	1 1 1 1	11 00	1111
R2-6	234	0110	0010	0000	1 0 1 1	1 1 0 1	1 0 1 1	0 1 1 1	1 1 1 0	11 00	1111
R2-7	235	0110	0001	0110	0 1 1 1	1 1 0 1	1 0 1 0	1 1 1 1	1 1 1 1	11 11	1111
R2-8	236	0110	0000	0101	1 0 1 1	1 1 0 1	1 0 1 1	1 1 0 1	0 1 1 1	11 00	1111
R2-9	237	0011	1010	0000	1 0 1 1	0 0 0 1	1 0 1 1	1 1 1 0	1 1 1 1	10 00	1111
RS-1	000	0101	1110	0000	1 0 1 1	1 0 1 1	1 0 1 1	0 1 1 0	1 1 1 0	10 00	1101
RS-1A	241	0001	1000	0000	1 0 1 1	1 0 0 1	1 0 1 1	1 1 1 0	0 1 1 1	11 00	1111
RS-2	347	1100	0011	0000	1 0 1 1	1 1 0 1	1 1 1 1	1 1 0 1	1 1 1 1	11 00	1111
RS-3	074	0001	0110	0110	0 1 1 1	1 0 1 1	1 0 1 0	0 1 1 0	0 1 1 0	10 00	1101
RS-4	351	0011	1010	0000	1 0 1 1	0 0 0 1	1 0 1 1	1 1 1 0	1 1 1 1	11 00	1111
RST-1	357	0011	1010	0000	1 0 1 1	1 0 0 1	1 0 1 1	1 1 1 0	0 1 1 1	11 00	1110
RT-1	001	0101	1010	0000	1 0 1 1	1 1 0 1	1 0 1 1	1 1 1 1	1 1 1 1	10 11	1101
S0-1	201	1111	1000	0000	1 0 1 1	1 0 0 1	1 0 0 1	1 1 1 0	1 1 1 1	11 11	0011
S0-2	007	1111	1110	0101	1 0 1 1	1 0 1 1	1 0 1 1	1 1 0 0	1 1 1 1	11 00	1001
S1-1	203	0101	1011	0000	1 0 1 1	1 0 0 1	1 0 0 1	0 1 1 0	0 0 1 0	11 00	1111
S1-2	244	1111	1000	0000	1 0 1 1	1 0 0 1	0 0 0 1	1 1 1 0	1 1 1 1	10 11	0011
S2-1	205	0011	1110	0000	1 0 1 1	1 0 0 1	1 0 0 1	0 1 1 0	1 0 1 0	11 00	1111

DIGITAL EQUIPMENT CORPORATION





digital EQUIPMENT CORPORATION


N A M	L O C	N X T	A L U	C F A R S U T H X	P S S D S P P I W 1 3 P	S S S B M P M B 0 0 1 T	B B S S A T P P R P F 2	C A T K R N D T S	A B L R G G	B U T	
S2-2	301	1111	0011	0110	0 1 1 1	1 0 0 1	1 0 0 0	1 1 1 0	1 1 1 1	11 11	1111
S2-3	014	0101	1011	0101	1 0 1 1	1 0 0 1	1 0 0 1	1 1 0 0	0 1 1 1	11 00	1111
S3-1	207	1111	0001	0000	1 0 1 1	1 0 0 1	1 0 0 1	0 1 1 0	1 1 1 0	11 00	1111
S3-2	016	1111	0000	0110	0 1 1 1	1 0 0 1	1 0 0 0	1 1 1 0	1 1 1 1	11 11	1111
S3-3	017	1010	0011	0101	1 0 1 1	1 0 0 1	1 0 0 1	1 1 0 0	0 1 1 1	11 00	1111
S3-4	134	0100	0011	0000	1 0 1 1	1 0 0 1	0 0 0 1	1 1 1 0	1 1 1 1	10 11	1111
S3-5	274	0101	1011	0101	1 0 1 1	1 0 0 1	1 0 1 1	0 1 1 0	0 0 1 0	11 00	1111
S4-1	211	1111	0011	1001	0 0 1 1	1 0 0 1	1 0 0 0	0 1 1 0	1 0 1 0	11 11	0100
S5-1	213	1111	0000	1001	0 0 1 1	1 0 0 1	1 0 0 0	0 1 1 0	1 1 1 0	11 11	0100
S6-1	215	1110	1010	0000	1 0 1 1	1 1 0 1	1 1 1 1	0 1 1 1	1 1 1 0	11 00	1111
S6-2	025	1110	1001	0110	0 1 1 1	1 1 0 1	1 1 1 0	1 1 1 1	1 1 1 1	11 11	1111
S6-3	026	1110	1000	0101	1 0 1 1	1 1 0 1	1 1 1 1	1 1 0 1	0 1 1 1	11 00	1111
S6-4	027	1110	0111	0000	1 0 1 1	1 0 0 1	0 0 0 1	1 1 1 0	1 1 1 1	10 11	1111
S6-5	030	0101	1011	0110	0 0 1 1	1 0 0 1	1 0 0 1	0 1 1 0	0 0 1 0	11 00	1111
S7-1	217	1110	0101	0000	1 0 1 1	1 1 0 1	1 1 1 1	0 1 1 1	1 1 1 0	11 00	1111
S7-2	032	1110	0100	0110	0 1 1 1	1 1 0 1	1 1 1 0	1 1 1 1	1 1 1 1	11 11	1111
S7-3	033	1110	0011	0101	1 0 1 1	1 1 0 1	1 1 1 1	1 1 0 1	0 1 1 1	11 00	1111
S7-4	034	1110	0010	0000	1 0 1 1	1 0 0 1	0 0 0 1	1 1 1 0	1 1 1 1	10 11	1111
S7-5	035	1010	0011	0110	0 0 1 1	1 0 0 1	1 0 0 1	0 1 1 0	0 1 1 0	11 00	1111
SB1-1	166	1000	0101	1110	0 0 0 1	1 0 0 1	1 0 1 1	1 1 1 0	1 1 1 1	11 01	1111
SB1-2	172	1000	0100	1110	0 0 0 1	1 0 0 1	1 0 1 1	1 1 1 0	1 1 1 1	11 01	1111
SB1-3	173	1000	0011	1110	0 0 0 1	1 0 0 1	1 0 1 1	1 1 1 0	1 1 1 1	11 01	1111
SB1-4	174	1001	1011	1110	0 0 0 1	1 0 0 1	1 0 1 1	1 1 1 0	1 1 1 1	11 01	1111
SB1-5	144	1000	0001	1110	0 0 0 1	1 0 0 1	1 0 1 1	1 1 1 0	1 1 1 1	11 01	1111
SB1-6	176	1000	0000	1110	0 0 0 1	1 0 0 1	1 0 1 1	1 1 1 0	1 1 1 1	11 01	1111
SB1-7	177	1111	1001	1110	0 0 0 1	1 0 0 1	1 0 1 1	1 1 1 0	1 1 1 1	11 01	1111
SB1-8	006	1001	0010	1110	0 0 1 0	1 0 0 1	1 0 1 1	1 1 1 0	1 1 1 1	11 01	1111
SB2-1	167	1111	0101	1110	0 0 0 1	1 0 0 1	1 0 1 1	1 1 1 0	1 1 1 1	11 01	1111
SB2-2	012	0110	1111	1110	0 0 0 1	1 0 0 1	1 0 1 1	1 1 1 0	1 1 1 1	11 01	1111
SB2-3	220	1110	1101	1110	0 0 0 1	1 0 0 1	1 0 1 1	1 1 1 0	1 1 1 1	11 01	1111
SB2-4	022	1110	1100	1110	0 0 0 1	1 0 0 1	1 0 1 1	1 1 1 0	1 1 1 1	11 01	1111
SB2-5	023	1110	1011	1110	0 0 0 1	1 0 0 1	1 0 1 1	1 1 1 0	1 1 1 1	11 01	1111
SB2-6	024	1110	0110	1110	0 0 0 1	1 0 0 1	1 0 1 1	1 1 1 0	1 1 1 1	11 01	1111
SB2-7	031	0010	0111	1110	0 0 0 1	1 0 0 1	1 0 1 1	1 1 1 0	1 1 1 1	11 01	1111
SB2-8	330	1000	1100	1110	0 0 1 0	1 0 0 1	1 0 1 1	1 1 1 0	1 1 1 1	11 01	1111
SBE-1	047	1111	1110	0101	1 0 1 1	1 0 1 1	1 0 1 1	1 0 0 0	1 1 1 1	11 00	1001
SBO-1	067	0001	1001	0000	1 0 0 1	1 0 0 1	1 0 1 1	1 1 1 0	1 1 1 1	11 10	1111
SBO-2	346	0010	1011	0000	1 0 0 1	1 0 0 1	1 0 1 1	1 1 1 0	1 1 1 1	11 10	1111
SBO-3	324	0001	1111	0000	1 0 0 1	1 0 0 1	1 0 1 1	1 1 1 0	1 1 1 1	11 10	1111
SBO-4	340	0000	1110	0000	1 0 0 1	1 0 0 1	1 0 1 1	1 1 1 0	1 1 1 1	11 10	1111

N A M	L O C	N X T	A L U	C F A R S U I H X	P S S D S P P I W I S P	S S S B M P M B O O 1 T	B B S S A T P P R P P 2	C A T K B N O T S	A B L R G G	B U T
SBO-5	361	1101	0111	0000	1 0 0 1	1 0 0 1	1 1 1 0	1 1 1 1	1 1 1 0	1 1 1 1
SBO-6	050	1110	1111	0000	1 0 0 1	1 0 0 1	1 1 1 0	1 1 1 1	1 1 1 0	1 1 1 1
SBO-7	020	1101	0101	0000	1 0 0 1	1 0 0 1	1 1 1 0	1 1 1 1	1 1 1 0	1 1 1 1
SBO-8	052	1101	1000	0000	1 0 1 1	1 0 0 1	1 1 1 0	1 1 1 1	1 1 1 0	1 1 1 1
SC-1	116	1101	1111	0100	1 0 1 1	0 0 0 1	1 1 1 0	1 1 1 1	0 0 0 0	1 1 0 0
T-1	021	0101	1010	0000	1 0 1 1	1 0 0 1	1 1 0 1	1 1 1 1	1 0 1 1	1 1 0 1
U1-1	352	1000	1101	0101	1 0 1 0	1 0 1 1	1 1 0 0	1 1 1 1	1 1 0 0	1 1 1 1
U2-1	353	1000	1100	0101	1 0 1 0	1 0 1 1	1 1 0 0	1 1 1 1	1 1 0 0	1 1 1 1
U3-1	354	1000	1011	0101	1 0 1 0	1 0 1 1	1 1 0 0	1 1 1 1	1 1 0 0	1 1 1 1
U4-1	355	1000	1010	0101	1 0 1 0	1 0 1 1	1 1 0 0	1 1 1 1	1 1 0 0	1 1 1 1
U5-1	373	1000	1100	0101	1 0 1 0	1 0 1 1	1 1 0 0	1 1 1 1	1 1 0 0	1 1 1 1
URTR	064	0101	1010	0000	1 0 1 1	1 1 1 1	1 1 0 1	1 1 1 1	1 0 1 1	1 1 0 1
URTX	060	0101	1010	0000	1 0 1 1	1 0 0 1	1 1 1 1	1 1 1 1	1 0 1 1	1 1 0 1
W-1	063	1101	1111	0000	1 0 1 1	1 0 0 1	1 1 1 0	1 1 1 1	1 1 0 0	1 1 0 0

digital EQUIPMENT CORPORATION

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REV. NUMBER SIZE CODE KMP KDII-B-4 2 1

FIRST USED ON OPTION MODEL	QTY.	DESCRIPTION	PART NO.	ITEM NO.
KDII-B				
PARTS LIST				
DRN. <i>J. Madden</i>	DATE 4-21-72	 <b>digital</b> EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS		
CHK'D. <i>R. L. Kusik</i>	DATE 5-2-72			
ENG. <i>R. L. Kusik</i>	DATE 5-2-72			
PROJ. ENG. <i>R. L. Kusik</i>	DATE 5-2-72			
PROD. <i>R. K. Petersen</i>	DATE 5/2/72			
NEXT HIGHER ASSEMBLY		TITLE		
B-DD-KDII-B		MICROPROGRAM CROSS REFERENCE LISTING (KDII-B)		
SCALE <i>1/1</i>	SIZE CODE KMP		NUMBER KDII-B-4	REV.
SHEET 1 OF 3	DIST.			

REVISIONS	REV.
CHANGE NO.	
CHK	

4

3

2

1

B

A

4

3

2

1

000 RS-1	001 RT-1	002 *****	003 ET2-2	004 ET2-3	005 R1-1	006 SB1-8	007 S0-2
010 ERT-1	011 ET-1	012 SB2-2	013 B2-2D	014 S2-3	015 B-1	016 S3-2	017 S3-3
020 SA0-7	021 T-1	022 SB2-4	023 SB2-5	024 SB2-6	025 S6-2	026 S6-3	027 S6-4
030 S6-5	031 SB2-7	032 S7-2	033 S7-3	034 S7-4	035 S7-5	036 ET2-5	037 ET2-6
040 BG-1	041 W-1	042 LC-1	043 PF-1	044 *****	045 BT-1	046 ERT1A	047 SBE-1
050 SA0-6	051 ET2-7	052 SB0-8	053 F-2	054 *****	055 *****	056 *****	057 D6-4
060 URTX	061 F-5	062 F-1	063 W-1	064 URTR	065 D1-6	066 *****	067 SB0-1
070 D3-3	071 D3-4	072 D3-5	073 *****	074 RS-3	075 D6-2	076 *****	077 D6-3
100 CS-1	101 D0-1	102 *****	103 D1-1	104 D7-3	105 D2-1	106 D7-5	107 D3-1
110 DBE-1	111 D4-1	112 CC-1	113 D5-1	114 *****	115 D6-1	116 SC-1	117 D7-1
120 *****	121 *****	122 *****	123 D0-2	124 D0-3	125 D0-4	126 D0-5	127 D0-6
130 D0-7	131 D0-8	132 D0-9	133 *****	134 S3-4	135 D0-12	136 D0-13	137 D0-14
140 D0-15	141 D0-16	142 D0-17	143 D0-18	144 SB1-5	145 A145	146 R-3	147 R-2
150 *****	151 CCM-1	152 MB-2	153 ERT1B	154 MB-0	155 D0-3A	156 DB0-1	157 D0-2
160 D3-2	161 *****	162 D0-3	163 D1-4	164 DB0-2	165 D0-10	166 SB1-1	167 SB2-1
170 *****	171 *****	172 SB1-2	173 SB1-3	174 SB1-4	175 *****	176 SB1-6	177 SB1-7
200 D1-2	201 S0-1	202 *****	203 S1-1	204 J1-1	205 S2-1	206 J2-4	207 S3-1
210 D1-3	211 S4-1	212 J2-1	213 S5-1	214 J2-3	215 S6-1	216 J2-5	217 S7-1
220 SB2-3	221 R1-2	222 R1-3	223 R1-4	224 R1-5	225 R1-6	226 ET-6	227 R2-1
230 R2-2	231 R2-3	232 R2-4	233 *****	234 R2-6	235 R2-7	236 R2-8	237 R2-9
240 MB-1	241 RS-1A	242 *****	243 *****	244 S1-2	245 ET-2	246 ET-3	247 ET-5
250 DE-1	251 ET-7	252 ET-8	253 ET-9	254 ET-10	255 ET-11	256 ET-12	257 ET-13
260 J1-2	261 J2-1A	262 J2-2	263 J2-6	264 J2-7	265 J2-8	266 *****	267 *****
270 D0-1	271 *****	272 CCS-3	273 IT-1	274 S3-5	275 *****	276 CCS-2	277 *****
300 D6-5	301 S2-2	302 W-2	303 CD1-2	304 DB0-3	305 B2-2	306 *****	307 CE1-2
310 D7-2	311 CL-1	312 CD2-1	313 CD1-1	314 CD1-4	315 CE2-1	316 CCS-1	317 CE1-1
320 D7-4	321 CS-3	322 CS-2	323 *****	324 SB0-3	325 INT-1	326 CE1-3	327 *****
330 SB2-8	331 D2-2	332 D0-4	333 B2-2A	334 D1-5	335 B2-2B	336 *****	337 CD2-2
340 SB0-4	341 D2-3	342 D0-11	343 B2-2C	344 *****	345 *****	346 SB0-2	347 RS-2
350 CCM-2	351 RS-4	352 U1-1	353 U2-1	354 U3-1	355 U4-1	356 *****	357 RST-1
360 *****	361 SB0-5	362 *****	363 *****	364 F-4	365 F-3	366 *****	367 CL-3
370 *****	371 CE2-2	372 CD1-5	373 U5-1	374 CD1-3	375 CL-2	376 *****	377 *****


A145 145	D6-1 115	F-4 364	S5-1 213
B-1 015	D6-2 075	F-5 061	S6-1 215
B-2 147	D6-3 077	W-1 041	S6-2 025
B-3 146	D6-4 057	W-2 302	S6-3 026
R2-2 305	D6-5 300	INT-1 325	S6-4 027
B2-2A 333	D7-1 117	IT-1 273	S6-5 030
R2-2B 335	D7-2 310	J1-1 204	S7-1 217
R2-2C 343	D7-3 104	J1-2 260	S7-2 032
R2-2D 013	D7-4 320	J2-1 212	S7-3 033
RG-1 040	D7-5 106	J2-1A 261	S7-4 034
RT-1 045	DR0-1 156	J2-2 262	S7-5 035
CC-1 112	DR0-2 164	J2-3 214	SB1-1 166
CCM-1 151	DR0-3 324	J2-4 206	SB1-2 172
CCM-2 350	DRE-1 110	J2-5 216	SB1-3 173
CCS-1 316	DE-1 250	J2-6 263	SB1-4 174
CCS-2 276	D0-1 270	J2-7 264	SB1-5 144
CCS-3 272	D0-10 165	J2-8 265	SB1-6 176
CD1-1 313	D0-11 342	LC-1 042	SB1-7 177
CD1-2 303	D0-12 135	MB-0 154	SB1-8 006
CD1-3 374	D0-13 136	MB-1 240	SB2-1 167
CD1-4 314	D0-14 137	MB-2 152	SB2-2 012
CD1-5 372	D0-15 140	PF-1 043	SB2-3 220
CD2-1 312	D0-16 141	R1-1 005	SB2-4 022
CD2-2 337	D0-17 142	R1-2 221	SB2-5 023
CE1-1 317	D0-18 143	R1-3 222	SB2-6 024
CE1-2 307	D0-2 123	R1-4 223	SB2-7 031
CE1-3 326	D0-3 124	R1-5 224	SB2-8 330
CE2-1 315	D0-4 125	R1-6 225	SBE-1 047
CE2-2 371	D0-5 126	R2-1 227	SB0-1 067
CL-1 311	D0-6 127	R2-2 230	SB0-2 346
CL-2 375	D0-7 130	R2-3 231	SB0-3 324
CL-3 367	D0-8 131	R2-4 232	SB0-4 340
CS-1 100	D0-9 132	R2-6 234	SB0-5 361
CS-2 322	ERT-1 010	R2-7 235	SB0-6 050
CS-3 321	ERT1A 046	R2-8 236	SB0-7 020
DB-1 101	ERT1B 153	R2-9 237	SB0-8 032
DB-2 157	ET-1 011	RS-1 000	SC-1 116
DB-3 162	ET-10 254	RS-1A 241	T-1 021
DB-3A 155	ET-11 255	RS-2 347	U1-1 352
DB-4 332	ET-12 256	RS-3 074	U2-1 353
D1-1 103	ET-13 257	RS-4 351	U3-1 354
D1-2 200	ET-2 245	RST-1 357	U4-1 355
D1-3 210	ET-3 246	RT-1 001	U5-1 373
D1-4 163	ET-5 247	S0-1 201	URTR 064
D1-5 334	ET-6 226	S0-2 027	URTX 060
D1-6 065	ET-7 251	S1-1 203	W-1 063
D2-1 105	ET-8 252	S1-2 244	000
D2-2 331	ET-9 253	S2-1 205	000
D2-3 341	ET2-2 003	S2-2 301	000
D3-1 107	ET2-3 004	S2-3 014	000
D3-2 160	ET2-5 036	S3-1 207	000
D3-3 070	ET2-6 037	S3-2 216	000
D3-4 071	ET2-7 051	S3-3 017	000
D3-5 072	F-1 062	S3-4 134	000
D4-1 111	F-2 053	S3-5 274	000
D5-1 113	F-3 365	S4-1 211	000

PAGE REVISION CONTROL SHEET

SH NO.	PAGE REVISIONS												REMARKS
	J	K	L	M	N	P	R						
1	J	K	L	M	N	P	R						
2	J	K	L	M	N	N	N						
3	J	J	J	M	N	N	N						
4	J	J	J	M	N	N	P						
5	J	J	J	M	M	M	N						
6	J	J	J	M	M	M	N						
7	J	J	J	M	M	N	N						
8	J	J	J	M	N	N	N						
9	J	J	J	M	M	N	P						
10	J	J	J	M	M	N	P						
11	J	J	J	M	M	N	N						
12	-	-	-	M	M	N	N						

DATE	ENG.	ETCH REV.	ECO NO.
10-11-72	M.T.	B	00007
10-30-72	B.A.	B	00008
11-27-72	M.T.	B	00009
2-23-73	B.A.	C	00010 & 10A
6-19-73	M.T.	C	00012
8-20-75	R.K.	C	00014
12-NOV-76	R.G.	C	00015

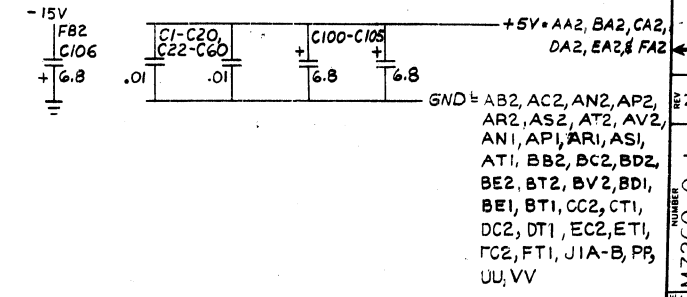
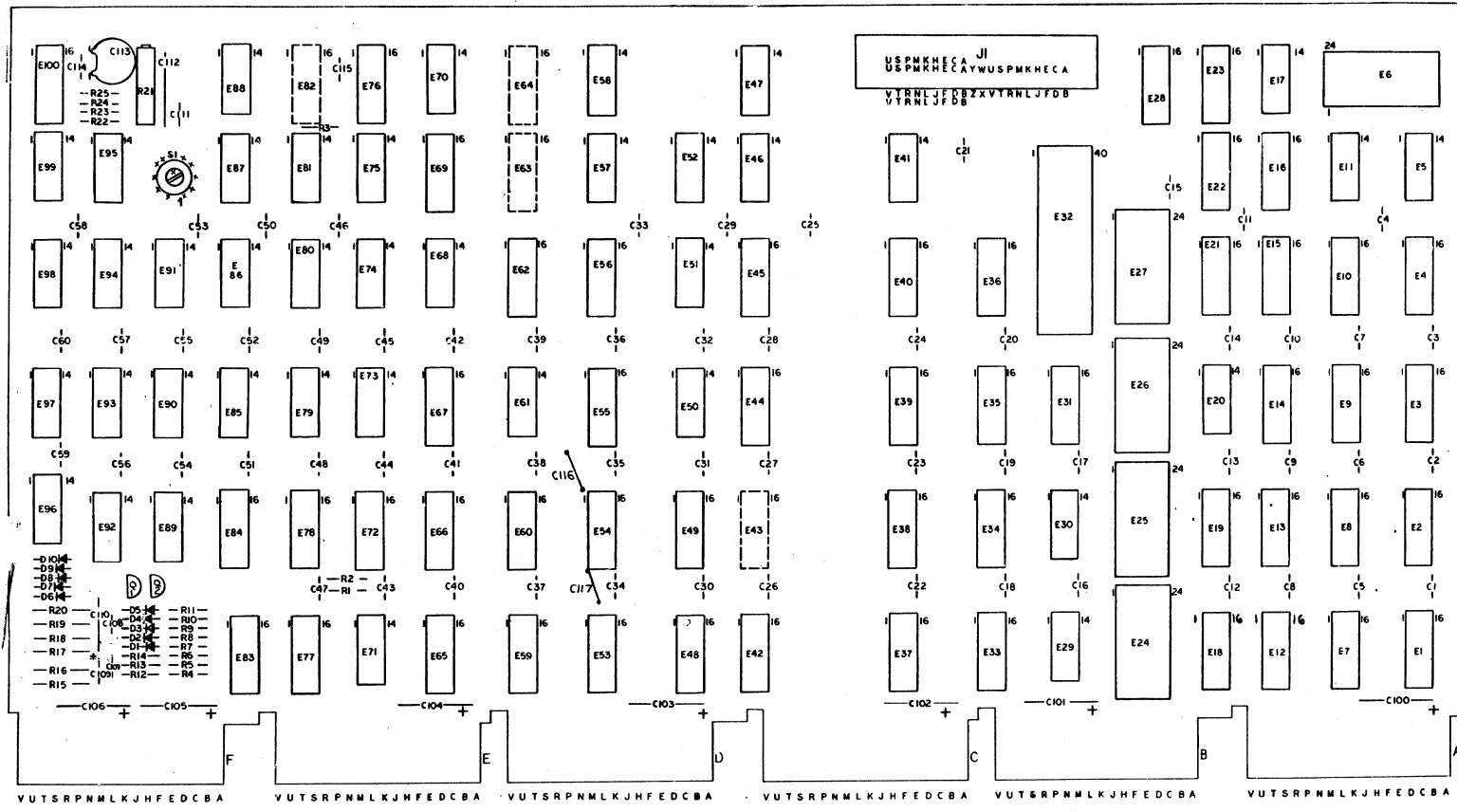
FIRST USED ON OPTION/MODEL  
KD11-B

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	CHK'D. <i>M. T. Tellan</i>	DATE 10/26/72	
	ENG. <i>M. T. Tellan</i>	DATE 10/26/72	
	PROJ. ENG. <i>M. T. Tellan</i>	DATE	
	PROD.	DATE	
	TITLE DATA PATHS		
	NEXT HIGHER ASSY. B-DD-KD11-B	SIZE B	CODE CS
	SCALE <i>++</i>	NUMBER M7260-0-1	
	SHEET 1 OF 12	DIST.	REV. R

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**NOTES:**

N 1-0-0922W 2



DEC 74174	8	16
DEC 74175	8	16
DEC 7489	8	16
DEC 1808	3	1
DEC 74182	8	16
DEC 74181	12	24
DEC 74157	8	16
DEC 74158	8	16
DEC 74153	8	16
DEC 74194	8	16
DEC 74150	12	24
DEC 8838	8	16
IC TYPE	GND	+5V
GND AND 5V ARE USUALLY PIN 7 AND 14 RESPECTIVELY. EXCEPTIONS ARE STATED ABOVE.		
IC PIN LOCATIONS		

QTY	REF DESIGNATION	DESCRIPTION	PART NO.	ITEM NO.
FIRST USED ON OPTION MODEL				
ETCH BOARD REV C				
PARTS LIST				
REV	CHANGE NO.	REVISIONS	DATE	BY
			1/22/73	
			2/23/73	
			3/23/73	
			4/23/73	
			5/23/73	
			6/23/73	
			7/23/73	
			8/23/73	
			9/23/73	
			10/23/73	
			11/23/73	
			12/23/73	
DEC NO.	EIA NO.	DEC NO.	EIA NO.	
SEMICONDUCTOR CONVERSION CHART				
NEXT HIGHER ASSY		TITLE		
B-DD-KD11-B		DATA PATHS		
SCALE	SIZE CODE	NUMBER	REV.	
	DCS	M7260-0-1	N	
SHEET	OF	DIST.		
2	12			

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NOTES:

Table with columns: REF, DESCRIPTION, PART NO., ITEM NO. Rows include: ETCH CIRCUIT BOARD, X-Y COORDINATE HOLE/LAYOUT, DATA PATH ASSY HOLE/LAYOUT, MODULE ECO HISTORY, CAP .01 MFD, 100V, 20% DISC, CAP .47 MFD, 35V, 10%, CAP 1000.0 MMF, 100V 5%, CAP 4700 MMF, 100V, 5%, CAP 39 MFD, 10V 10%, CAP 6.8 MFD, 35V, 10%, CAP 120.0 MMF, 100V, 5%, DIODE D664, IC DEC 8838, IC DEC 7430, IC DEC 74150, IC DEC 8266, IC DEC 74194, IC DEC 74153, IC DEC 7402, IC DEC 74S158, IC DEC 7400, IC DEC 74157, IC DEC 74181, IC DEC 7437, IC DEC 74182, IC DEC 1808 UART, IC DEC 7489, IC DEC 7438, IC RES NETWORK, IC DEC 7408, IC DEC 74175, IC DEC 74174, IC ROM (CONSTANTS), IC DEC 7474, IC ROM (DDP AUX), IC ROM (SOP AUX #1), IC DEC 7404, IC ROM (DDP 1R), IC DEC 7410, IC ROM (CC OR OP), IC ROM (SOP AUX #2), IC DEC 7486, IC ROM (C + V - BIT), IC ROM (BRANCH), IC DEC 9602, IC ROM (ENB UNARY), IC ROM (NOT DDP), IC ROM (ODD BYTE EQ 0), IC DEC 74197, IC DEC 7413, IC DEC 7473.

Table with columns: QTY, REF DESIGNATION, DESCRIPTION, PART NO., ITEM NO. Rows include: CONNECTOR, BERG, RES 1.5K 1/4W 5%, RES 470 1/4W 5%, RES 12K 1/2W 5%, RES 1.5K 1/2W 5%, RES 10 1/4W 5%, RES 30K 1/4W 5%, RES 150 1/4W 5%, RES 68 1/2W 10%, RES 2K 1/4W 5%, RES 20K 3/4W 10% POT, RES 1K 1/4W 5%, RES 750 1/4W 5%, RES 82 1/4W 5%, RES 560 1/2W 5%, RES 10K 1/4W 5%, TRANS DEC 6534D PNP, EYELETS, MODULE HANDLE, SWITCH, ROT 1 POLE 10 POSITION DRY, HEX NUT NYLON, CAP 120MMF 100V 5%.

Summary table with columns: QTY, REF DESIGNATION, DESCRIPTION, PART NO., ITEM NO. for both sections above.

Table with columns: QTY, REF DESIGNATION, DESCRIPTION, PART NO., ITEM NO. for the parts list section.

Table with columns: IC TYPE, GND, +5V. Rows include: DEC 9602, ROM 23AxxA1, ROM 23AxxA2, DEC 7473, DEC 9602, IC TYPE, GND, +5V. Includes note: GND AND 5V ARE USUALLY PIN 7 AND 14 RESPECTIVELY. EXCEPTIONS ARE STATED ABOVE.

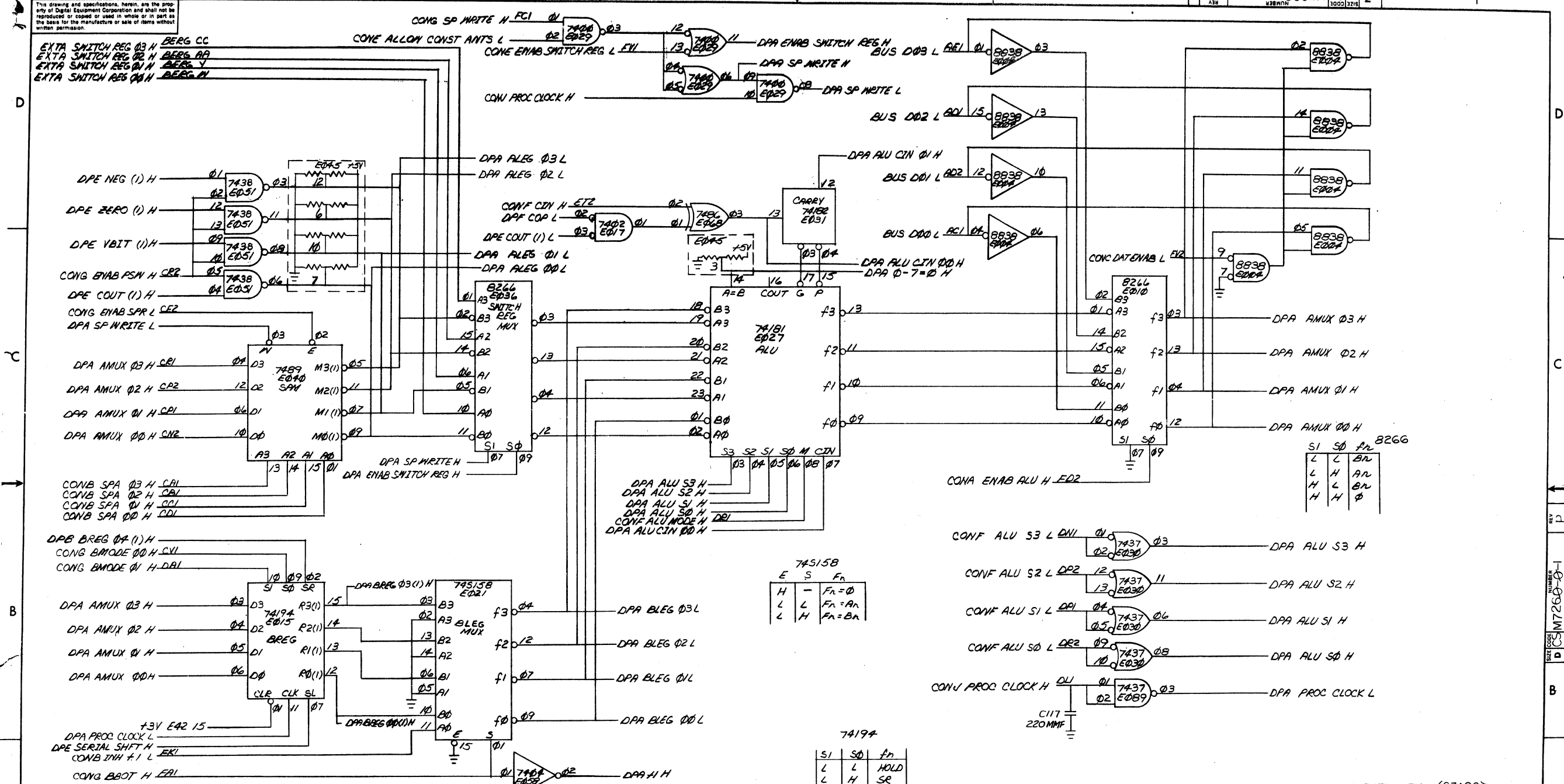
IC PIN LOCATIONS

Administrative and technical information block including: PARTS LIST, digital EQUIPMENT CORPORATION, DATA PATHS, SEMICONDUCTOR CONVERSION CHART, DEC NO., EIA NO., SCALE, SHEET 3 OF 12, DATE, and various revision dates.

BRUNING 40522 16699

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1-0-0221W CS M7260-0-1



74158

E	S	F <sub>n</sub>
H	-	F <sub>n</sub> = 0
L	L	F <sub>n</sub> = A <sub>n</sub>
L	H	F <sub>n</sub> = B <sub>n</sub>

74194

S1	S0	F <sub>n</sub>
L	L	HOLD
L	H	SR
H	L	SL
H	H	LOAD

S1	S0	F <sub>n</sub>
L	L	B <sub>n</sub>
L	H	A <sub>n</sub>
H	L	B <sub>n</sub>
H	H	0

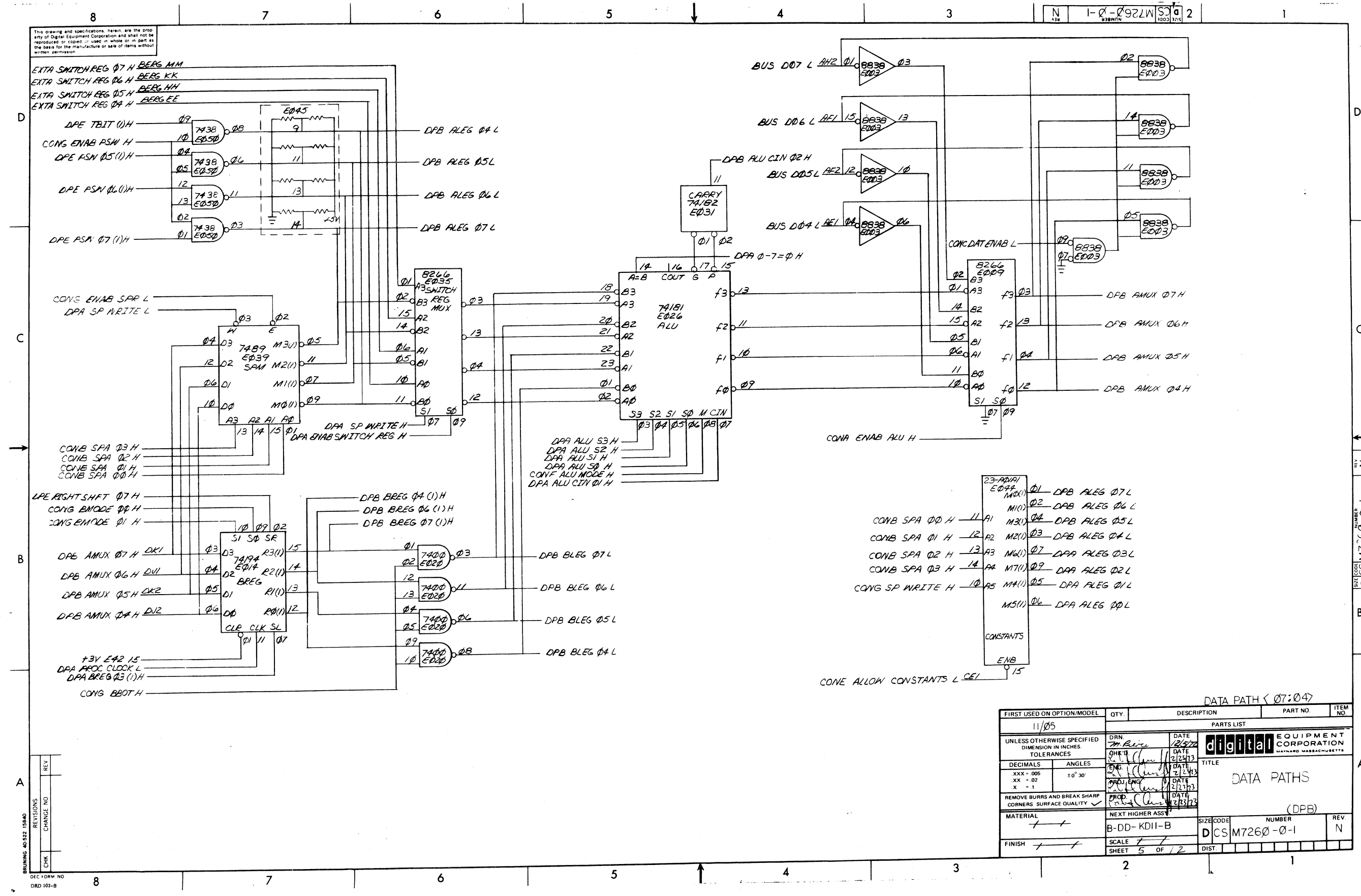
REVISIONS

REV	CHANGE NO

FIRST USED ON OPTION/MODEL		QTY.	DESCRIPTION	PART NO.	ITEM NO.									
11/05														
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES TOLERANCES														
DECIMALS	ANGLES	PARTS LIST												
.XXX = .005	±0° 30'	<table border="1"> <tr> <td>DRN</td> <td>DATE</td> <td rowspan="4"> </td> </tr> <tr> <td>CHKD</td> <td>DATE</td> </tr> <tr> <td>ENG</td> <td>DATE</td> </tr> <tr> <td>PROD</td> <td>DATE</td> </tr> </table>				DRN	DATE		CHKD	DATE	ENG	DATE	PROD	DATE
DRN	DATE													
CHKD	DATE													
ENG	DATE													
PROD	DATE													
REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY		TITLE												
MATERIAL		DATA PATH												
FINISH		NEXT HIGHER ASSY												
		B-DD-KD11-B		SIZE CODE	NUMBER									
				D	CS M7260-0-1									
		SHEET 4 OF 12		DIST.										

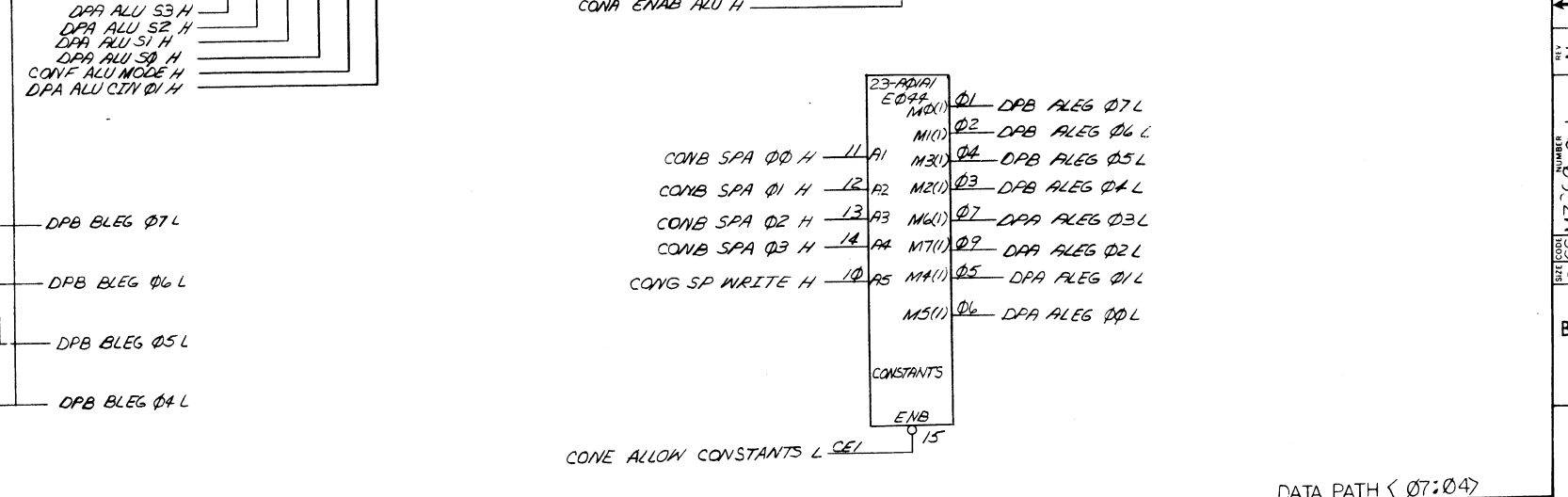
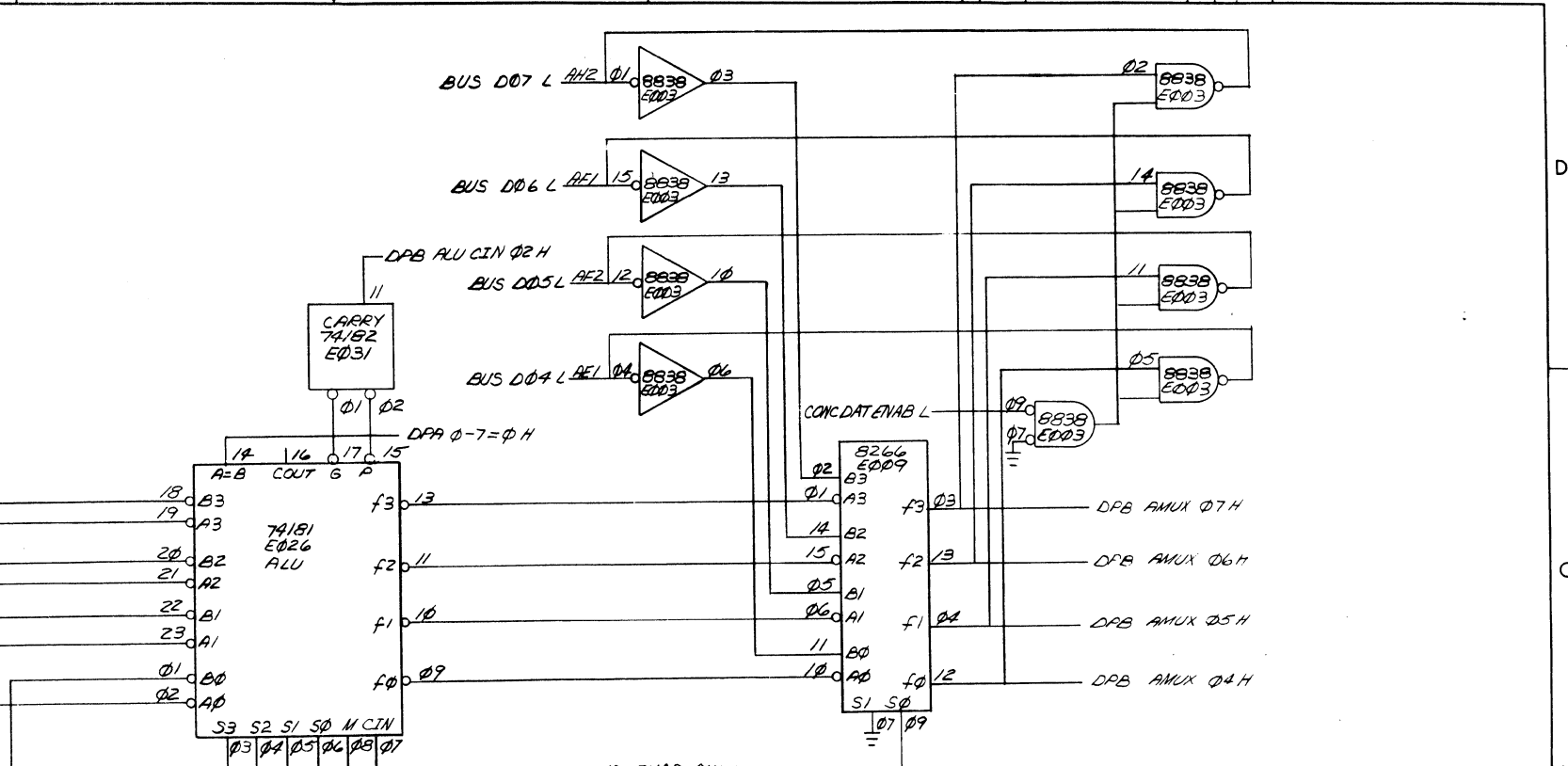
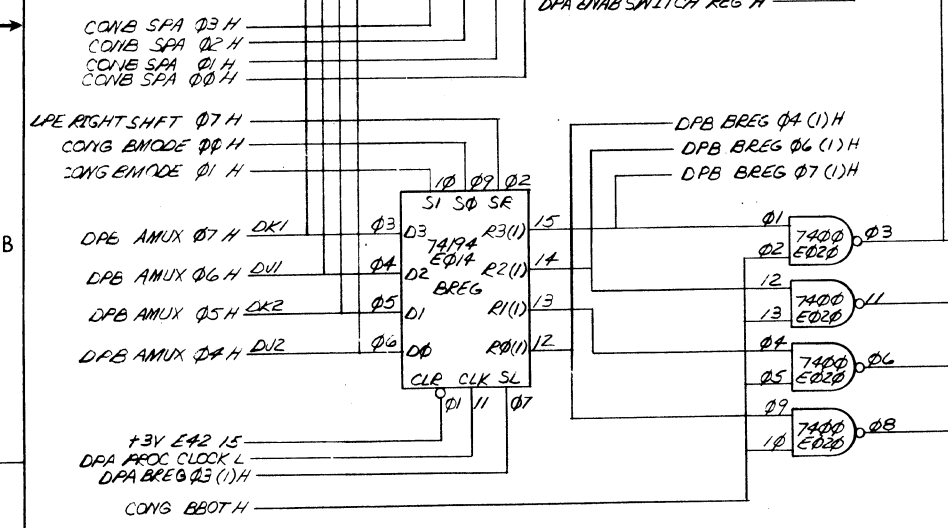
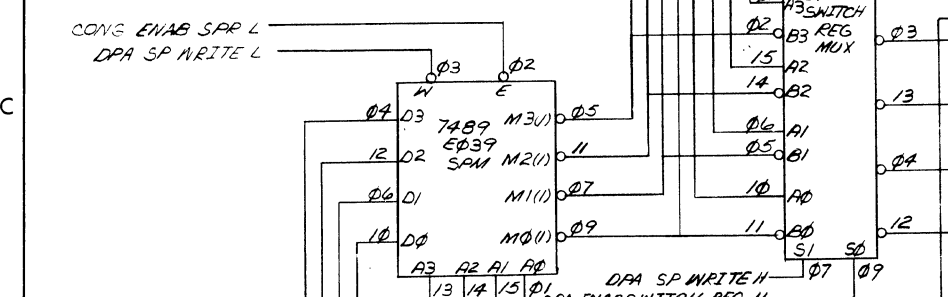
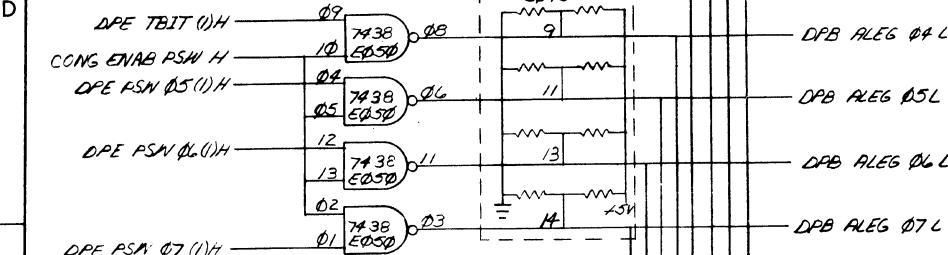
DEC FORM NO DND 102-B





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EXTRA SWITCH REG 07 H BERG MM  
 EXTRA SWITCH REG 06 H BERG KK  
 EXTRA SWITCH REG 05 H BERG HH  
 EXTRA SWITCH REG 04 H BERG EE

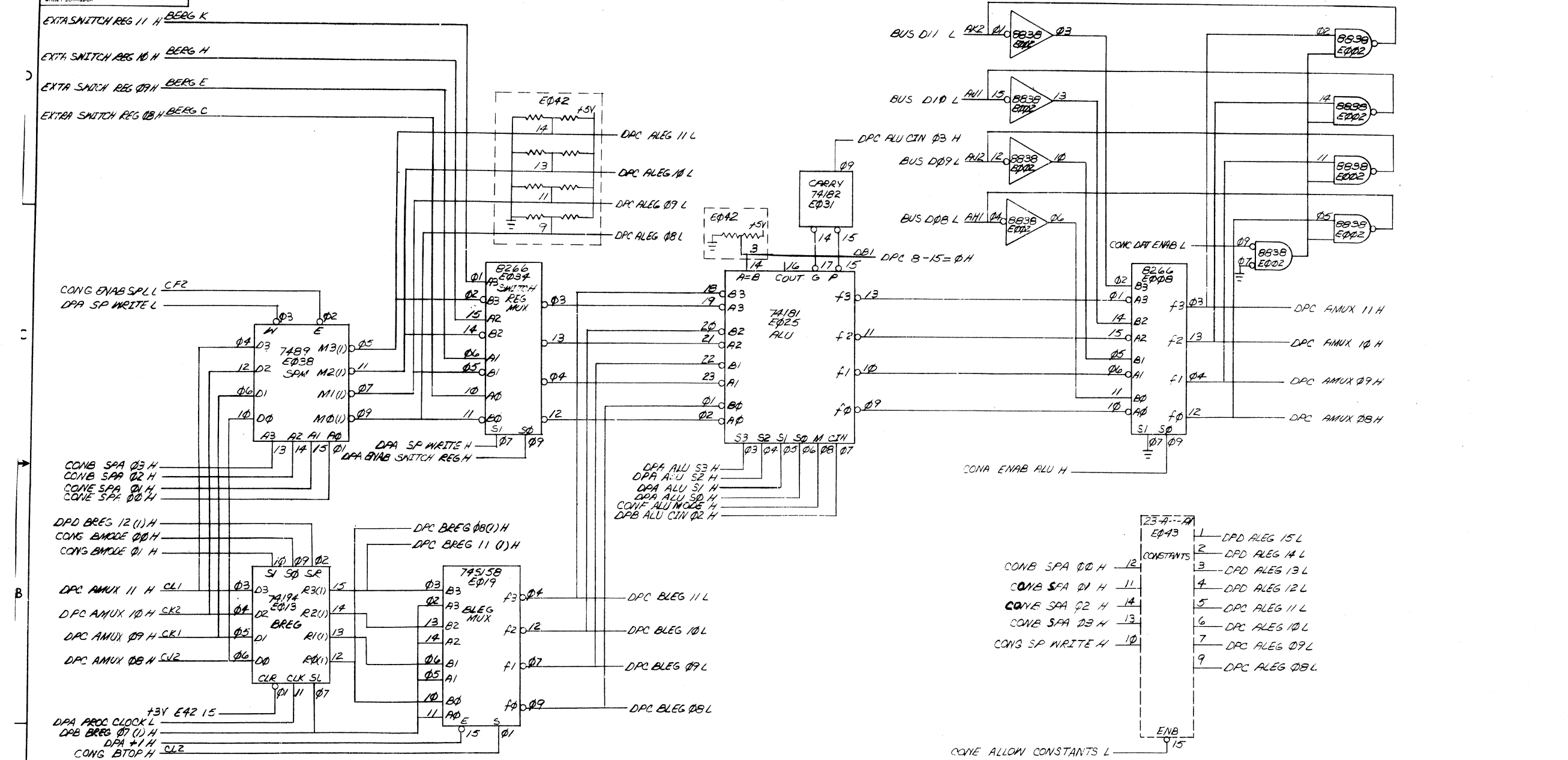


- CONSTANTS
- 01 - DPB ALEG 07 L
  - 02 - DPB ALEG 06 L
  - 03 - DPB ALEG 05 L
  - 04 - DPB ALEG 04 L
  - 05 - DPA ALEG 03 L
  - 06 - DPA ALEG 02 L
  - 07 - DPA ALEG 01 L
  - 08 - DPA ALEG 00 L

FIRST USED ON OPTION/MODEL	QTY.	DESCRIPTION	PART NO.	ITEM NO.
11/05				
PARTS LIST				
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES TOLERANCES				
DECIMALS	ANGLES	TITLE		
.XXX - .005	±0° 30'	DATA PATHS (DPB)		
REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY				
MATERIAL	NEXT HIGHER ASSY	SIZE CODE	NUMBER	REV
FINISH		B-DD-KDII-B	DCS M7260-0-1	N
SCALE		SHEET 5 OF 12		
DIST.				

REV	CHANGE NO

BRUNING 40-522 15840  
 DEC 1968 NO  
 ORD 101-B

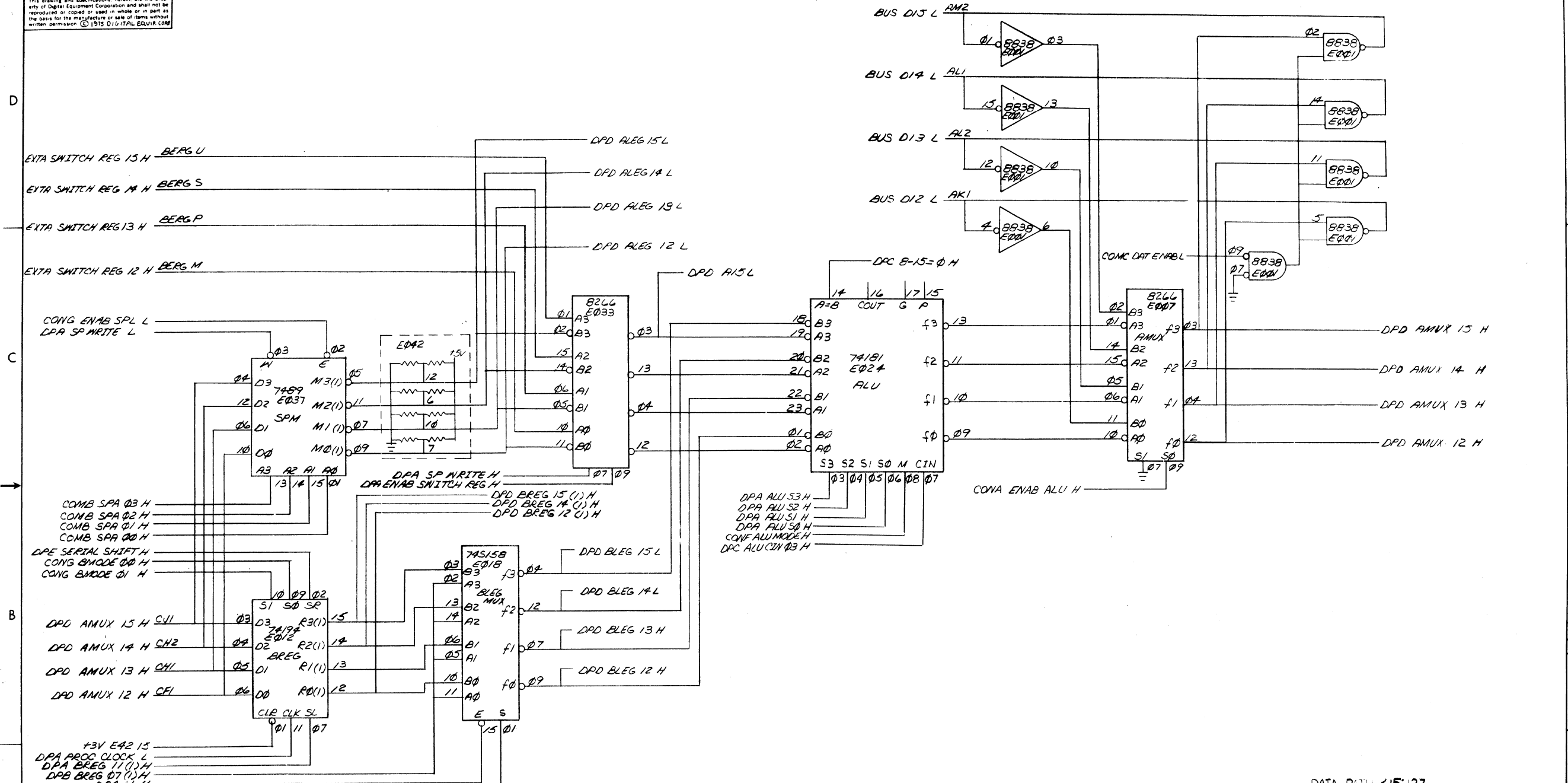


- 23-A--A  
 EQ43
- 1 - DPC ALEG 15 L
  - 2 - DPC ALEG 14 L
  - 3 - DPC ALEG 13 L
  - 4 - DPC ALEG 12 L
  - 5 - DPC ALEG 11 L
  - 6 - DPC ALEG 10 L
  - 7 - DPC ALEG 09 L
  - 9 - DPC ALEG 08 L
- ENB 15

BRUING 40-522 15640	REV
CHK	CHANGE NO
DEC FORM NO	DRD 102-B

FIRST USED ON OPTION MODEL		QTY	DESCRIPTION	PART NO.	ITEM NO.
11/05					
PARTS LIST					
UNLESS OTHERWISE SPECIFIED		DRN	DATE	digital EQUIPMENT CORPORATION	
DIMENSION IN INCHES		CHK'D	DATE	MAYNARD MASSACHUSETTS	
TOLERANCES		PRG	DATE	TITLE	
DECIMALS	ANGLES	PRJ ENG	DATE	DATA PATHS	
XXX - .005	±0° 30'	PROD. ENG.	DATE	(DPC)	
.XX - .02		DATE		SIZE CODE NUMBER REV.	
X - .1		DATE		DCS M7260-0-1 M	
REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY		PROD. DATE		SCALE	
		DATE		SHEET 6 OF 7	
MATERIAL		NEXT HIGHER ASSY		DIST.	
FINISH					

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REV	REVISIONS

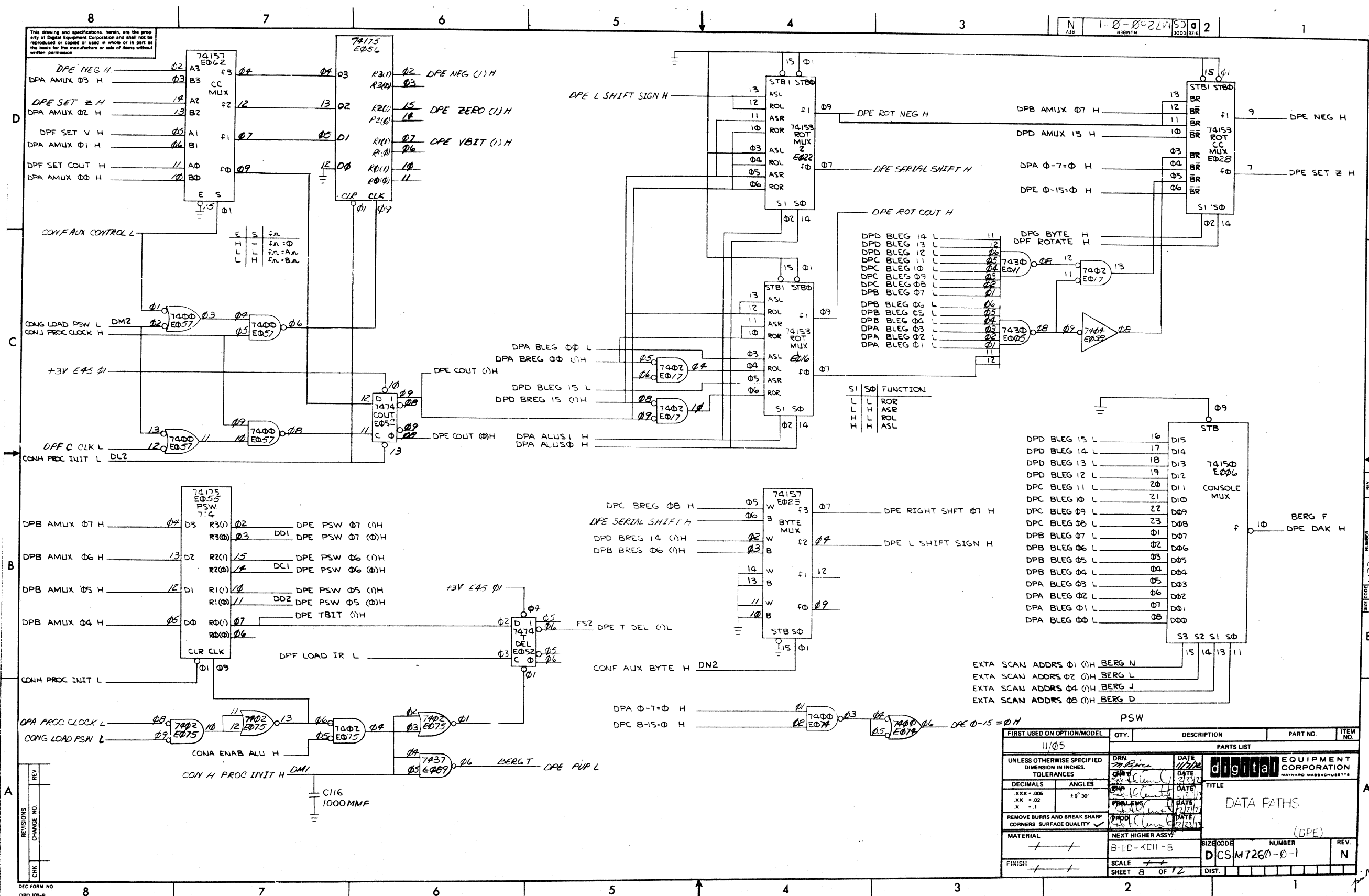
DEC FORM NO. DRD 102-B

DATA PATH <15:127

FIRST USED ON OPTION/MODEL	QTY.	DESCRIPTION	PART NO.	ITEM NO.
11/05				
PARTS LIST				
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES TOLERANCES		DRN <i>M. Payne</i>	DATE 11/27/72	 DIGITAL EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS
DECIMALS	ANGLES	CHK'D <i>[Signature]</i>	DATE 12/23/72	
XXX - 005	±0° 30'	ENG. <i>[Signature]</i>	DATE 12/23/72	TITLE  DATA PATHS
XX - 02		PRJ. ENG. <i>[Signature]</i>	DATE 12/23/72	
X - 1		PROD. <i>[Signature]</i>	DATE 12/23/72	
REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY				
MATERIAL	NEXT HIGHER ASSY			
FINISH	B-DD-KD11-B	SIZE CODE	NUMBER	REV
	SCALE 7 OF	DCS	M7260-0-1	N
	SHEET 7 OF	DIST		

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N 1-0-8022450 2



SI	S0	FUNCTION
L	L	ROR
L	H	ASR
H	L	ROL
H	H	ASL

DPD BLEG	DPB BLEG	DPA BLEG	DPC BLEG	DPA BLEG	DPA BLEG	DPA BLEG	DPA BLEG	DPA BLEG	DPA BLEG
15 L	16	D15							
14 L	17	D14							
13 L	18	D13							
12 L	19	D12							
11 L	20	D11							
10 L	21	D10							
09 L	22	D09							
08 L	23	D08							
07 L	01	D07							
06 L	02	D06							
05 L	03	D05							
04 L	04	D04							
03 L	05	D03							
02 L	06	D02							
01 L	07	D01							
00 L	08	D00							

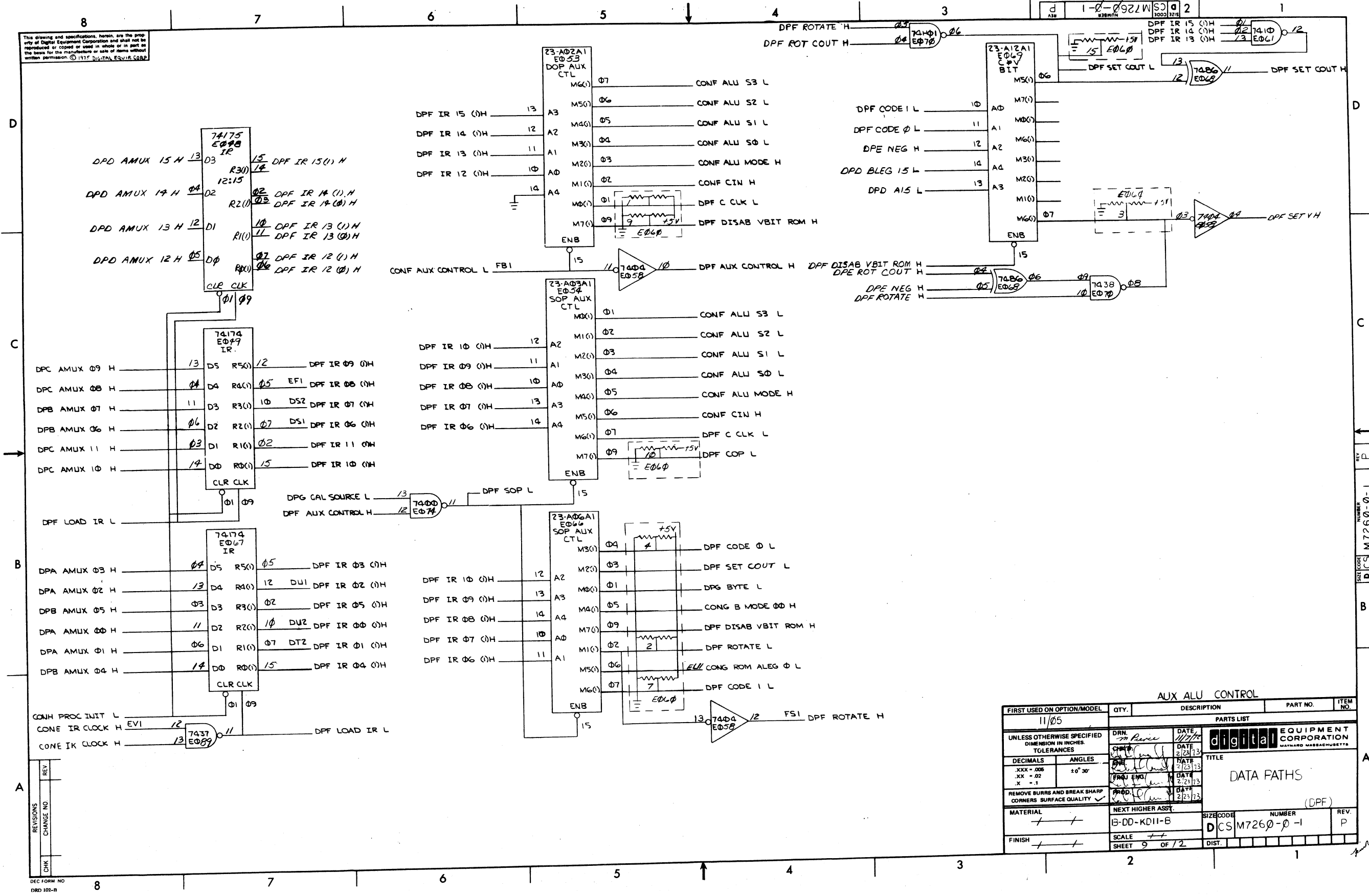
EXTRA SCAN ADDRS 01 (1)H BERG N  
 EXTRA SCAN ADDRS 02 (1)H BERG L  
 EXTRA SCAN ADDRS 04 (1)H BERG J  
 EXTRA SCAN ADDRS 08 (1)H BERG D

FIRST USED ON OPTION/MODEL	QTY.	DESCRIPTION	PART NO.	ITEM NO.
11/05				
PARTS LIST				
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES. TOLERANCES				
DECIMALS	ANGLES	DATE		
.XX - .005	±0°30'	11/72		
.XX - .02		DATE		
X - .1		11/72		
REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY				
MATERIAL		NEXT HIGHER ASSY:	SIZE CODE	NUMBER
+		B-CD-KCII-B		
FINISH		SCALE	D E S I G N	
+		SHEET 8 OF 12	D I S T.	

REV	CHANGE NO.	DATE	BY	CHK

DEC FORM NO DRD 102-B

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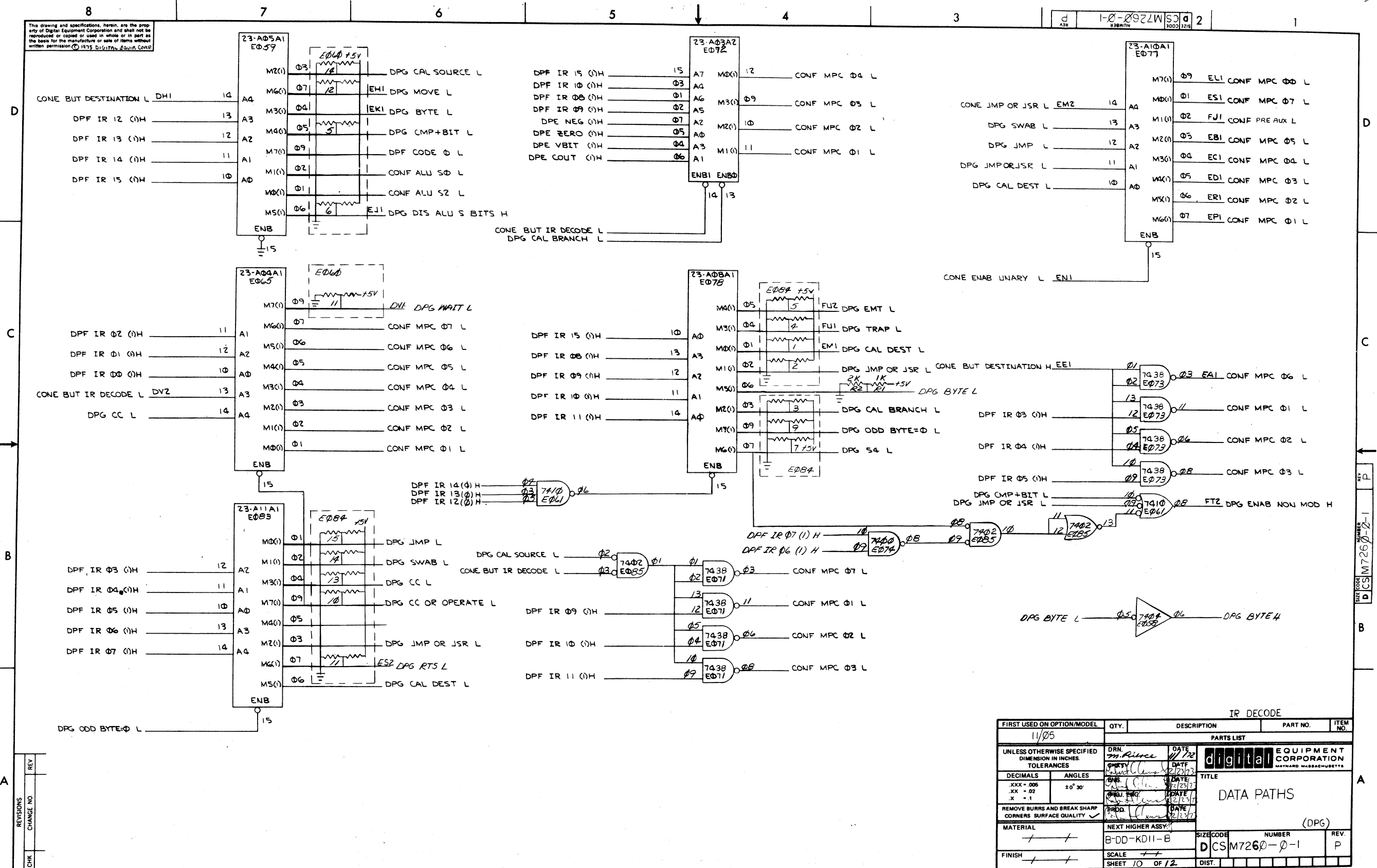
REV	CHG	NO

FIRST USED ON OPTION/MODEL		QTY.	DESCRIPTION	PART NO.	ITEM NO.
11/05					
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES. TOLERANCES					
DECIMALS	ANGLES				
.XXX - .005	±0° 30'				
.XX - .02					
.X - .1					
REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY					
MATERIAL		NEXT HIGHER ASSY.	SIZE CODE	NUMBER	REV.
		B-DD-KD11-B	DCS	M7260-0-1	P
FINISH		SCALE	SHEET	OF	TOTAL SHEETS
			9	OF	12

DATE CODE: DCS  
 NUMBER: M7260-0-1  
 REV: 1

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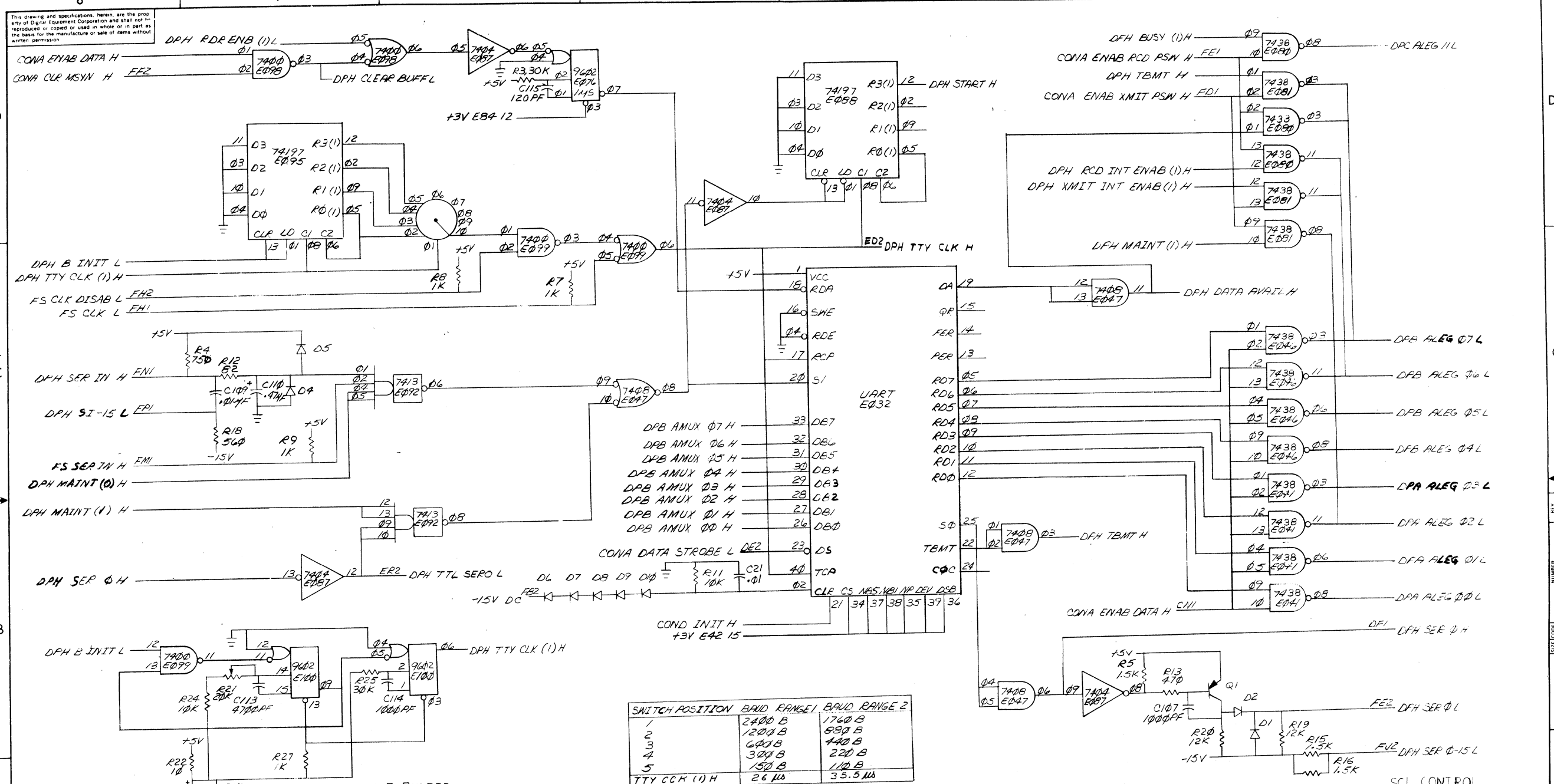
CSM7260-0-1



REV	
CHK	

IR DECODE				
FIRST USED ON OPTION/MODEL	QTY.	DESCRIPTION	PART NO.	ITEM NO.
11/05				
PARTS LIST				
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES.	DRN. <i>M. Rince</i>	DATE <i>11/72</i>	digital EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS	
TOLERANCES	QTY. <i>1</i>	DATE <i>12/73</i>	TITLE	
DECIMALS		DATE <i>12/73</i>	DATA PATHS	
ANGLES		DATE <i>12/73</i>	(DPG)	
.XX - .02		DATE <i>12/73</i>		
.X - .1		DATE <i>12/73</i>		
REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY	DRN. <i>M. Rince</i>	DATE <i>12/73</i>		
MATERIAL	NEXT HIGHER ASSY.	DATE <i>12/73</i>		
FINISH	B-DD-K011-B	DATE <i>12/73</i>		
SCALE		DATE <i>12/73</i>		
SHEET 10 OF 12		DATE <i>12/73</i>		
			SIZE CODE	NUMBER
			D	CSM7260-0-1
			DIST.	REV. P

REV. P  
NUMBER  
CSM7260-0-1



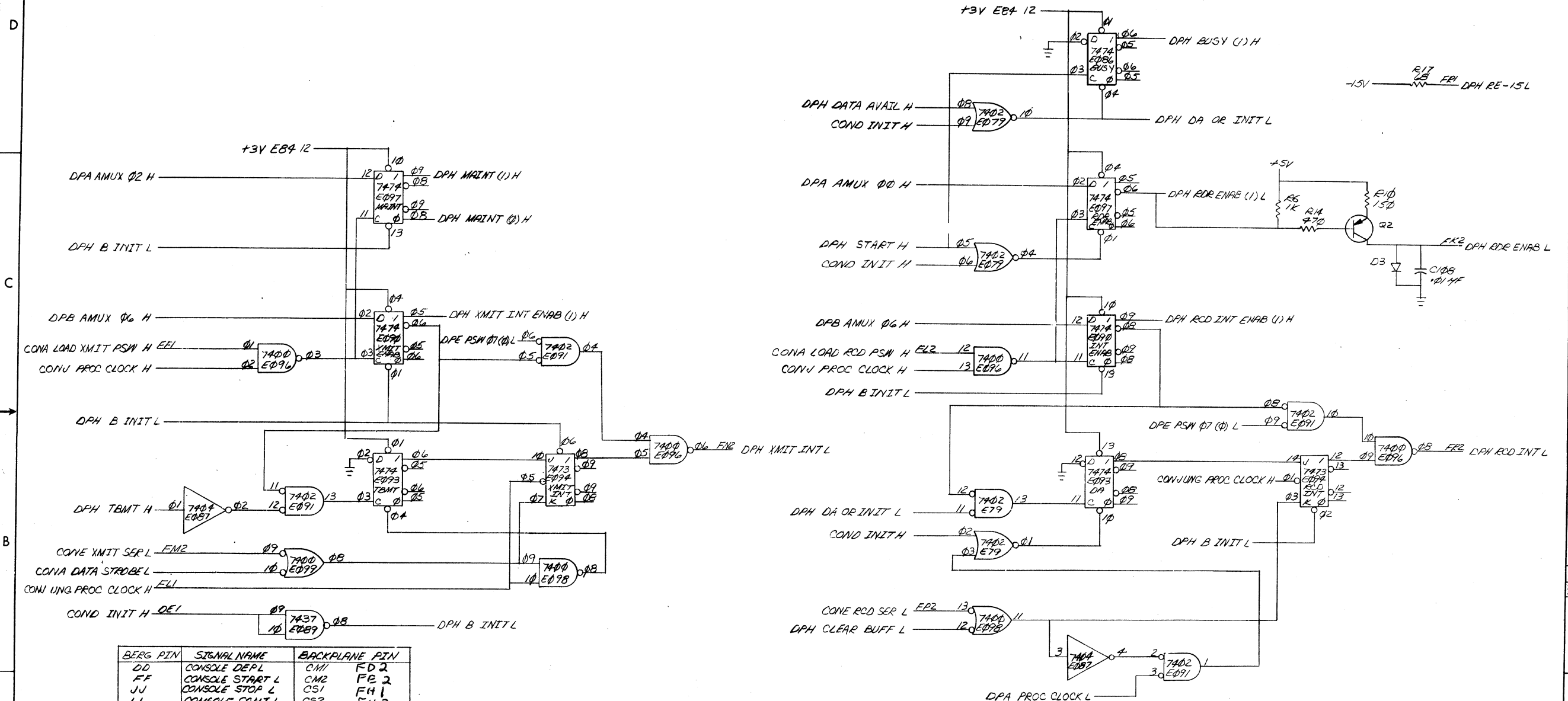
FIRST USED ON OPTION/MODEL	QTY	DESCRIPTION	PART NO.	ITEM NO.
11/05		PARTS LIST		
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES TOLERANCES	DRN 24 R10	DATE 1/31/72	digital CORPORATION MAYNARD, MASSACHUSETTS	
DECIMALS	CHK	DATE 2/22/73	TITLE DATA PATH (DPH)	
ANGLES	PROJ. ENG.	DATE 4/23/73	MATERIAL	
XXX = .005	PHOD	DATE 1/23/74	NEXT HIGHER ASSY	
XX = .02			B-DD-KD11-8	
.X = .1			SCALE	
REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY			SHEET 11 OF 12	
MATERIAL			FINISH	
			SIZE CODE NUMBER REV.	
			D CS M7260-0-1 N	

BRUNING 40-22 15840  
DEC FORM NO  
ORD 102-B

D  
C  
B  
A  
 NUMBER  
M7260-0-1  
 SIZE CODE  
D CS

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CS MT250-0-1



BERG PIN	SIGNAL NAME	BACKPLANE PIN
DD	CONSOLE DEPL	CM1 FD 2
FF	CONSOLE START L	CM2 FE 2
JJ	CONSOLE STOP L	CS1 FH 1
LL	CONSOLE CONT L	CS2 FH 2
NN	CONSOLE EXAM L	CT2 FH 3
RR	CONSOLE LOAD L	CUR FH 2
TT	CONSOLE RUN LAMP L	CUI (ET2)

PAGE CONE

↑  
console  
cont

FIRST USED ON OPTION/MODEL	QTY.	DESCRIPTION	PART NO.	ITEM NO.
110E		PARTS LIST		
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES TOLERANCES		DRN <i>M. Rine</i>	DATE 1/20/75	digital EQUIPMENT CORPORATION MAYNARD MASSACHUSETTS
DECIMALS	ANGLES	CHK'D <i>[Signature]</i>	DATE 2/27/75	
.XXX = .005	± 0° 30'	ENG <i>[Signature]</i>	DATE 2/23/75	TITLE <b>DATA PATH</b> (DPH1)
.XX = .02		PROJ. ENG <i>[Signature]</i>	DATE 2/23/75	
.X = .1		PROD. <i>[Signature]</i>	DATE 2/23/75	
REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY		NEXT HIGHER ASSY		
MATERIAL		B-DC-KEN-B	SIZE CODE	NUMBER
FINISH			DCS	MT250-0-1
SCALE			DIST	
SHEET	12 OF 12			

BRUNING 40-222 15840  
REVISIONS  
CHANGE NO.  
CHK

DEC FORM NO  
DRD 102-B



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THIS FACE SHEET CONTAINS THE FOLLOWING CHIP PART NUMBERS

- PART NUMBER
- 23-A01A1
  - 23-A02A1
  - 23-A03A1
  - 23-A04A1
  - 23-A05A1
  - 23-A06A1
  - 23-A08A1
  - 23-A10A1
  - 23-A11A1
  - 23-A12A1
  - 23-A03A2

FIRST USED ON OPTION MODEL	QTY.	DESCRIPTION	PART NO.	ITEM NO.
KDII-B				
PARTS LIST				
DRN. C. Teschner	DATE 5-8-72	<b>digital</b> EQUIPMENT CORPORATION <small>MAYNARD, MASSACHUSETTS</small> TITLE DATA PATH ROM PATTERNS		
CHK'D. M.T.	DATE 5-15-72			
ENG.	DATE			
PROJ. ENG.	DATE			
PROD.	DATE			
NEXT HIGHER ASSEMBLY B-DD-KDII-B				
SCALE ++	SIZE CODE K RL	NUMBER M7260-0-8	REV.	
SHEET 1	OF 15	DIST.		

REVISIONS	REV.
	CHANGE NO.
	CHK

```

/( #Y8 (PIN #9) DPA ALEG 02 L
*/( #Y7 (PIN #7) DPA ALEG 03 L
**/( #Y6 (PIN #6) DPA ALEG 00 L
***/( #Y5 (PIN #5) DPA ALEG 01 L
****/( #Y4 (PIN #4) DPB ALEG 05 L
*****/( #Y3 (PIN #3) DPB ALEG 04 L
*****/( #Y2 (PIN #2) DPB ALEG 06 L
*****/( #Y1 (PIN #1) DPB ALEG 07 L
*****
OCTAL DECIMAL
ADDRESS ADDRESS EDCBA DATA
000 0 00000 11111111 377
001 1 00001 01001110 116 K#207 SWR ADDRESS I,E, 177570=000207, BAR
002 2 00010 01110011 163 K#64 RECVR, VECTOR
003 3 00011 00001111 017 K#360 CONDITION CODE MASK (CCM=1)
004 4 00100 10111011 273 K#30 EMT VECTOR
005 5 00101 00111111 077 K#14 T BIT VECTOR
006 6 00110 11111111 377
007 7 00111 11111111 377
010 8 01000 11111011 373 K#20 IOT VECTOR
011 9 01001 00111011 073 K#34 TRAP VECTOR
012 10 01010 11111111 377
013 11 01011 11111111 377
014 12 01100 10111111 277 K#10 RESERVED (ILLEGAL) INSTRUCTION VECTOR
015 13 01101 01111111 177 K#4 BUS ERROR OR STACK OVERFLOW ERROR
016 14 01110 11111111 377
017 15 01111 11111111 377
020 16 10000 01111011 173 K#24 PWR FAIL VECTOR
021 17 10001 11111111 377
022 18 10010 11111101 375 K#100 LCLK INT VECTOR
023 19 10011 11111111 377
024 20 10100 11111111 377
025 21 10101 11111111 377
026 22 10110 11111111 377
027 23 10111 11111111 377
030 24 11000 11111111 377
031 25 11001 11111111 377
032 26 11010 11111111 377
033 27 11011 11111111 377
034 28 11100 11111111 377
035 29 11101 11110011 363 K#60 TRANSMIT VECTOR
036 30 11110 11111111 377
037 31 11111 11111111 377
*****
****/( A(PIN #10) IS CONG SP WRITE H
****/( B(PIN #11) IS CONG ROM SPA 00 H
****/( C(PIN #12) IS CONG ROM SPA 01 H
****/( D(PIN #13) IS CONG ROM SPA 02 H
****/( E(PIN #14) IS CONG ROM SPA 03 H

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/( #Y8 (PIN #9) DPF DISAB V BIT ROM H
*/( #Y7 (PIN #7) CONF ALU S3 L
**/( #Y6 (PIN #6) CONF ALU S2 L
***/( #Y5 (PIN #5) CONF ALU S1 L
****/( #Y4 (PIN #4) CONF ALU S0 L
*****/( #Y3 (PIN #3) CONF ALU MODE H
*****/( #Y2 (PIN #2) CONF CIN H
*****/( #Y1 (PIN #1) DPF C CLK L
*****
OCTAL DECIMAL
ADDRESS ADDRESS EDCBA DATA
000 0 00000 11111111 377
001 1 00001 10000101 205 MOV F#A
002 2 00010 01001010 112 CMP F#A MINUS B MINUS 1
003 3 00011 10001101 215 BIT F#AB
004 4 00100 10111101 275 BIC F#A, BARB
005 5 00101 10100101 245 BIS F#A+B
006 6 00110 00110000 060 ADD F#A PLUS B
007 7 00111 11111111 377 RI (RESERVED INSTRUCTION)
010 8 01000 11111111 377 RI
011 9 01001 10000101 205 MOV(B)
012 10 01010 01001010 112 CMP(B)
013 11 01011 10001101 215 BIT(B)
014 12 01100 10111101 275 BIC(B)
015 13 01101 10100101 245 BIS(B)
016 14 01110 00110010 062 SUB F#A PLUS B
017 15 01111 11111111 377 RI
020 16 10000 00000000 000 NOT ACCESSED
021 17 10001 00000000 000 MOV = NOT ACCESSED
022 18 10010 00000000 000 CMP = NOT ACCESSED
023 19 10011 00000000 000 BIT = NOT ACCESSED
024 20 10100 00000000 000 BIC = NOT ACCESSED
025 21 10101 00000000 000 BIS = NOT ACCESSED
026 22 10110 00000000 000 ADD = NOT ACCESSED
027 23 10111 00000000 000 RI = NOT ACCESSED
030 24 11000 00000000 000 RI = NOT ACCESSED
031 25 11001 00000000 000 MOV(B) = NOT ACCESSED
032 26 11010 00000000 000 CMP(B) = NOT ACCESSED
033 27 11011 00000000 000 BIT(B) = NOT ACCESSED
034 28 11100 00000000 000 BIC(B) = NOT ACCESSED
035 29 11101 00000000 000 BIS(B) = NOT ACCESSED
036 30 11110 00000000 000 SUB = NOT ACCESSED
037 31 11111 00000000 000 RI = NOT ACCESSED
*****
****/( A(PIN #10) IS DPF IR 12 (1)H
****/( B(PIN #11) IS DPF IR 13 (1)H
****/( C(PIN #12) IS DPF IR 14 (1)H
****/( D(PIN #13) IS DPF IR 15 (1)H
****/( E(PIN #14) IS DPA RUN GND L

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/( #Y8 (PIN #9) DPF COP L
*/( #Y7 (PIN #7) DPF C CLK L
**/( #Y6 (PIN #6) CONF CIN H
***/( #Y5 (PIN #5) CONF ALU MODE H
****/( #Y4 (PIN #4) CONF ALU S0 L
*****/( #Y3 (PIN #3) CONF ALU S1 L
*****/( #Y2 (PIN #2) CONF ALU S2 L
*****/( #Y1 (PIN #1) CONF ALU S3 L
*****
OCTAL ADDRESS DECIMAL ADDRESS EDCBA DATA
000 0 00000 11111111 377
001 1 00001 11111111 377
002 2 00010 10011100 234 CLR ALUF=ZERO
003 3 00011 10101001 251 NEG CIN ALUF=A MINUS B MINUS 1
004 4 00100 10111111 277 ROR
005 5 00101 11111111 377
006 6 00110 11111111 377
007 7 00111 11111111 377
010 8 01000 11111111 377
011 9 01001 11111111 377
012 10 01010 11100000 340 INC CIN ALUF=A ARITH
013 11 01011 00101111 057 SBC CIN ALUF=A MINUS 1
014 12 01100 10110111 267 ASR
015 13 01101 11111111 377
016 14 01110 11111111 377
017 15 01111 11111111 377
020 16 10000 11111111 377
021 17 10001 11111111 377
022 18 10010 10010101 225 COM ALUF=NOT B
023 19 10011 00000000 000 ADC CIN ALUF=A ARITH
024 20 10100 10111011 273 ROL
025 21 10101 11111111 377
026 22 10110 11111111 377
027 23 10111 11111111 377
030 24 11000 10011010 232 SWAB NOT B CLOCK LOW
031 25 11001 11111111 377
032 26 11010 11101111 317 DEC CIN ALUF=A MINUS 1
033 27 11011 10010000 220 TST ALUF=A
034 28 11100 10110011 263 ASL
035 29 11101 11111111 377
036 30 11110 11111111 377
037 31 11111 11111111 377
*****
****/( A(PIN #10) IS DPF IR 08 (1)H
****/( B(PIN #11) IS DPR IR 09 (1)H
**/( C(PIN #12) IS DPF IR 10 (1)H
*/( D(PIN #13) IS DPF IR 07 (1)H
/( E(PIN #14) IS DPF IR 06 (1)H

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```

/( #Y8 (PIN #9) DPG WAIT L
*/( #Y7 (PIN #7) CONF MPC 07 L
**/( #Y6 (PIN #6) CONF MPC 06 L
***/( #Y5 (PIN #5) CONF MPC 05 L
****/( #Y4 (PIN #4) CONF MPC 04 L
*****/( #Y3 (PIN #3) CONF MPC 03 L
*****/( #Y2 (PIN #2) CONF MPC 02 L
*****/( #Y1 (PIN #1) CONF MPC 01 L
*****
OCTAL ADDRESS DECIMAL ADDRESS EDCBA DATA
000 0 00000 11001011 313 CC OPR
001 1 00001 11001011 313 CC OPR
002 2 00010 11001011 313 CC OPR
003 3 00011 11001011 313 CC OPR
004 4 00100 11001011 313 CC OPR
005 5 00101 11001011 313 CC OPR
006 6 00110 11001011 313 CC OPR
007 7 00111 11001011 313 CC OPR
010 8 01000 11111111 377 RI (RESERVED INSTRUCTION)
011 9 01001 11111111 377 NOT ACCESSED FOR NOT IR DECODE
012 10 01010 11111111 377 RI
013 11 01011 11111111 377 RI
014 12 01100 11111111 377 RI
015 13 01101 11111111 377 RI
016 14 01110 11111111 377 RI
017 15 01111 11111111 377 RI
020 16 10000 11101111 357 HALT,BUT IR DEC
021 17 10001 01100110 146 WAIT,BUT IR DEC
022 18 10010 10100010 242 IOT
023 19 10011 10001000 210 RESET
024 20 10100 10110100 264 RTI
025 21 10101 11101101 355 BREAKPOINT TRAP DECODE
026 22 10110 11111111 377 RI
027 23 10111 11111111 377 RI
030 24 11000 11111111 377 RI
031 25 11001 01111111 177 WAIT,BUT IR DEC,BAR
032 26 11010 11111111 377 RI
033 27 11011 11111111 377 RI
034 28 11100 11111111 377 RI
035 29 11101 11111111 377 RI
036 30 11110 11111111 377 RI
037 31 11111 11111111 377 RI
*****
****/( A(PIN #10) IS DPF IR 08 (1)H
****/( B(PIN #11) IS DPF IR 02 (1)H
**/( C(PIN #12) IS DPF IR 01 (1)H
*/( D(PIN #13) IS GONE BUT IR DECODE L
/( E(PIN #14) IS DPG CC L

```

```

/( =Y8 (PIN #9) DPF CODE 0 L
*/( =Y7 (PIN #7) DPG MOVE L
**/( =Y6 (PIN #6) DPG DIS ALU S BITS H
***/( =Y5 (PIN #5) DPG CMP OR BIT L
****/( =Y4 (PIN #4) DPG BYTE L
*****/( =Y3 (PIN #3) DPG CAL SOURCE L
*****/( =Y2 (PIN #2) CONF ALU S0 L
*****/( =Y1 (PIN #1) CONF ALU S2 L
*****
OCTAL      DECIMAL
ADDRESS    ADDRESS
0000      0      00000 11011111 337 BR/CC (BRANCH OR CC OPERATOR)
0001      1      00001 11011111 337 BR/CC
0002      2      00010 11011011 333 BIC
0003      3      00011 11010011 323 BIC(B)
0004      4      00100 11001011 313 CMP
0005      5      00101 11000011 303 CMP(B)
0006      6      00110 01011011 133 ADD
0007      7      00111 01111000 170 SUB DIS ALU BITS H
0010      8      01000 10011011 233 MOV
0011      9      01001 10010011 223 MOV(B)
0012     10      01010 11011011 333 BIS
0013     11      01011 11010011 323 BIS(B)
0014     12      01100 11001011 313 BIT
0015     13      01101 11000011 303 BIT(B)
0016     14      01110 11011111 337 RI TRAP
0017     15      01111 11011111 337 RI TRAP
0020     16      10000 11011111 337 BR/CC
0021     17      10001 11011111 337 BR/CC
0022     18      10010 11011011 333 BIC
0023     19      10011 11010011 323 BIC(B)
0024     20      10100 11001011 313 CMP
0025     21      10101 11000011 303 CMP(B)
0026     22      10110 01011011 133 ADD
0027     23      10111 01011011 133 ADD
0030     24      11000 10011011 233 MOV
0031     25      11001 10010011 223 MOV(B)
0032     26      11010 11011011 333 BIS
0033     27      11011 11010011 323 BIS(B)
0034     28      11100 11001011 313 BIT
0035     29      11101 11000011 303 BIT(B)
0036     30      11110 11011111 337 RI TRAP
0037     31      11111 11011111 337 RI TRAP
*****
****/( A(PIN #10) IS DPF IR 15 (1)H
****/( B(PIN #11) IS DPF IR 14 (1)H
**/( C(PIN #12) IS DPF IR 13 (1)H
*/( D(PIN #13) IS DPF IR 12 (1)H
/( E(PIN #14) IS CONF BUT DESTINATION L

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/( =Y8 (PIN #9) DPF DISAB V BIT ROM H
*/( =Y7 (PIN #7) DPF CODE 1 L
**/( =Y6 (PIN #6) CONG ROM ALEG 0 L
***/( =Y5 (PIN #5) CONG B MODE 00 H
****/( =Y4 (PIN #4) DPF CODE 0 L
*****/( =Y3 (PIN #3) DPF SET CARRY L
*****/( =Y2 (PIN #2) DPF ROTATE L
*****/( =Y1 (PIN #1) DPG BYTE L
*****
OCTAL      DECIMAL
ADDRESS    ADDRESS
0000      0      00000 11111111 377
0001      1      00001 11111111 377
0002      2      00010 11111111 377
0003      3      00011 10110110 266 SWAB DISAB V BIT ROM
0004      4      00100 11110101 365 ROR
0005      5      00101 11110101 365 ASR
0006      6      00110 11100101 345 ROL
0007      7      00111 11100101 345 ASL
0010      8      01000 10110111 267 CLR
0011      9      01001 00110111 067 INC
0012     10      01010 11110011 363 COM
0013     11      01011 00111111 077 DEC
0014     12      01100 00000001 001 BCC
0015     13      01101 11111111 377
0016     14      01110 11111111 377
0017     15      01111 11111111 377
0020     16      10000 00000001 001 BMI
0021     17      10001 11111111 377
0022     18      10010 11111111 377
0023     19      10011 11111111 377
0024     20      10100 00000001 001 BVS
0025     21      10101 11111111 377
0026     22      10110 11111111 377
0027     23      10111 11111111 377
0030     24      11000 01011111 137 NEG
0031     25      11001 00111111 077 SBC
0032     26      11010 00110111 067 ADC
0033     27      11011 11110111 367 TST
0034     28      11100 11111111 377
0035     29      11101 11111111 377
0036     30      11110 11111111 377
0037     31      11111 11111111 377
*****
****/( A(PIN #10) IS DPF IR 07 (1)H
****/( B(PIN #11) IS DPF IR 06 (1)H
**/( C(PIN #12) IS DPF IR 10 (1)H
*/( D(PIN #13) IS DPF IR 09 (1)H
/( E(PIN #14) IS DPF IR 08 (1)H

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/( #Y8 (PIN #9) DPG ODD BYTE = 0L
*/( #Y7 (PIN #7) DPG 54 L
**/( #Y6 (PIN #6) DPG BYTE L
***/( #Y5 (PIN #5) DPG EMT L
****/( #Y4 (PIN #4) DPG TRAP L
*****/( #Y3 (PIN #3) DPG CAL BRANCH L
*****/( #Y2 (PIN #2) DPG JSR L
*****/( #Y1 (PIN #1) DPG CAL DEST L
*****
OCTAL
ADDRESS DATA
000 0 00000 01111111 177
001 1 00001 11111011 373 BPL
002 2 00010 11111011 373 BGE
003 3 00011 11111011 373 BVC
004 4 00100 11111011 373 BNE
005 5 00101 11111011 373 BHI
006 6 00110 11111011 373 BGT
007 7 00111 11111011 373 BCC
010 8 01000 11111011 373 BR
011 9 01001 11111011 373 BMI
012 10 01010 11111011 373 BLT
013 11 01011 11111011 373 BVS
014 12 01100 11111011 373 BEQ
015 13 01101 11111011 373 BLOS
016 14 01110 11111011 373 BLE
017 15 01111 11111011 373 BCS
020 16 10000 11111100 374 JSR
021 17 10001 11101111 357 EMT
022 18 10010 11111110 376 SOP (CC) ROR/ROL/ASR/ASL
023 19 10011 11011110 336 OPR (DST) ROR(B)/ROL(B)/ASR(B)/ASL(B)
024 20 10100 11111110 376 SOP (CC) CLR/COM/INC/DEC
025 21 10101 11011110 336 OPR (DST) CLR(B)/COM(B)/INC(B)/DEC(B)
026 22 10110 11111111 377 RI RESERVED INST
027 23 10111 11111111 377 RI RESERVED INST
030 24 11000 11111100 374 JSR
031 25 11001 11110111 367 TRAP
032 26 11010 11111111 377 RI RESERVED INST
033 27 11011 11111111 377 RI RESERVED INST
034 28 11100 10111110 276 SOP NEG/ADC/SBC/TST
035 29 11101 10011110 236 OPR (DST) NEG(B)/ADC(B)/SBC(B)/TST(B)
036 30 11110 11111111 377 RI RESERVED INST
037 31 11111 11111111 377 RI RESERVED INST
*****
****/( A(PIN #10) IS DPG IR 15 (1)H
**/( B(PIN #11) IS DPG IR 10 (1)H
*/( C(PIN #12) IS DPG IR 09 (1)H
/( D(PIN #13) IS DPG IR 08 (1)H
/( E(PIN #14) IS DPG IR 11 (1)H

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```

/( #Y8 (PIN #9) CONF MPC 00 L
*/( #Y7 (PIN #7) CONF MPC 01 L
**/( #Y6 (PIN #6) CONF MPC 02 L
***/( #Y5 (PIN #5) CONF MPC 03 L
****/( #Y4 (PIN #4) CONF MPC 04 L
*****/( #Y3 (PIN #3) CONF MPC 05 L
*****/( #Y2 (PIN #2) CONF MPC 06 L
*****/( #Y1 (PIN #1) CONF MPC 07 L
*****
OCTAL
ADDRESS DATA
000 0 00000 11111111 377
001 1 00001 11111111 377
002 2 00010 11111111 377
003 3 00011 11111111 377
004 4 00100 11111111 377
005 5 00101 11111111 377
006 6 00110 11111111 377
007 7 00111 11111111 377
010 8 01000 11011111 337 JMP BADR TO J=1 @204
011 9 01001 11111111 377
012 10 01010 11011111 337 JMP BADR TO J=1 @204
013 11 01011 11111111 377
014 12 01100 10101111 257 JSR BADR TO J2=1 @ 212
015 13 01101 11111111 377
016 14 01110 11111111 377 NOT JMP OR JSR FALL THRU TO D1=2 @ 200
017 15 01111 11111111 377
020 16 10000 11111111 377
021 17 10001 11111111 377
022 18 10010 11111111 377
023 19 10011 11111111 377
024 20 10100 11111111 377
025 21 10101 11111111 377
026 22 10110 11010111 327 SWAB BADR 024 INOR NEXT
027 23 10111 11111111 377
030 24 11000 01101111 157 JMP BADR 011 INOR NEXT
031 25 11001 11111111 377
032 26 11010 01101111 157 JMP BADR 011 INOR NEXT
033 27 11011 11111111 377
034 28 11100 01101111 157 JSR BADR 011 INOR NEXT
035 29 11101 11111111 377
036 30 11110 11101110 356 SOP BADR 210 INOR NXT
037 31 11111 11110101 365 UNARY AND NOT JMP,JSR,SWAB
*****
****/( A(PIN #10) IS DPG CAL DEST L
**/( B(PIN #11) IS DPG JMP L OR JSR L
*/( C(PIN #12) IS DPG JMP L
/( D(PIN #13) IS DPG SWAB L
/( E(PIN #14) IS CONG JMP OR JSR L

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```

/( #Y8 (PIN #9) DPG CC OR OPERATE L
*/( #Y7 (PIN #7) DPG RTS L
**/( #Y6 (PIN #6) DPG CAL DEST L
***/( #Y5 (PIN #5)
****/( #Y4 (PIN #4) DPG CC L
*****/( #Y3 (PIN #3) DPG JMP L OR JSR L
*****/( #Y2 (PIN #2) DPG SWAB L
*****/( #Y1 (PIN #1) DPG JMP L
*****
OCTAL DECIMAL
ADDRESS ADDRESS EDCBA DATA
000 0 00000 01111111 177 OPR(HALT/WAIT/RTI/BK/LOT/TR)
001 1 00001 11111111 377 RI
002 2 00010 11111111 377 RI
003 3 00011 11111111 377 RI
004 4 00100 11111111 377 RI
005 5 00101 11111111 377 RI
006 6 00110 11111111 377 RI
007 7 00111 11111111 377 RI
010 8 01000 11011010 332 JMP MODE 0
011 9 01001 11011010 332 JMP MODE 4
012 10 01010 11011010 332 JMP MODE 2
013 11 01011 11011010 332 JMP MODE 6
014 12 01100 11011010 332 JMP MODE 1
015 13 01101 11011010 332 JMP MODE 5
016 14 01110 11011010 332 JMP MODE 3
017 15 01111 11011010 332 JMP MODE 7
020 16 10000 10111111 277 RTS NOT DPG CC OR OPR L
021 17 10001 01110111 167 CC OPR 240
022 18 10010 11111111 377 RI
023 19 10011 01110111 167 CC OPR 260
024 20 10100 11111111 377 RI
025 21 10101 01110111 167 CC OPR 240
026 22 10110 11111111 377 RI
027 23 10111 01110111 167 CC OPR 260
030 24 11000 11011101 335 SWAB MODE 0
031 25 11001 11011101 335 SWAB MODE 4
032 26 11010 11011101 335 SWAB MODE 2
033 27 11011 11011101 335 SWAB MODE 6
034 28 11100 11011101 335 SWAB MODE 1
035 29 11101 11011101 335 SWAB MODE 5
036 30 11110 11011101 335 SWAB MODE 3
037 31 11111 11011101 335 SWAB MODE 7
*****
****/( A(PIN #10) IS DPF IR 05 (1)H
****/( B(PIN #11) IS DPF IR 04 (1)H
**/( C(PIN #12) IS DPF IR 03 (1)H
*/( D(PIN #13) IS DPF IR 06 (1)H
/( E(PIN #14) IS DPF IR 07 (1)H

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```

/( #Y8 (PIN #9)
*/( #Y7 (PIN #7) DPF SET V L
**/( #Y6 (PIN #6) DPF SET COUT L
***/( #Y5 (PIN #5)
****/( #Y4 (PIN #4)
*****/( #Y3 (PIN #3)
*****/( #Y2 (PIN #2)
*****/( #Y1 (PIN #1)
*****
OCTAL DECIMAL
ADDRESS ADDRESS EDCBA DATA
000 0 00000 11011111 337 INC OR ADC
001 1 00001 10011111 237 ADD OR SUB
002 2 00010 10111111 277 DEC OR SBC
003 3 00011 11111111 377 CMP OR NEG
004 4 00100 11111111 377 INC OR ADC
005 5 00101 11011111 337 ADD OR SUB
006 6 00110 11111111 377 DEC OR SBC
007 7 00111 11011111 337 CMP OR NEG
010 8 01000 11111111 377 INC OR ADC
011 9 01001 11011111 337 ADD OR SUB
012 10 01010 11111111 377 DEC OR SBC
013 11 01011 11011111 337 CMP OR NEG
014 12 01100 10111111 277 INC OR ADC
015 13 01101 11111111 377 ADD OR SUB
016 14 01110 11011111 337 DEC OR SBC
017 15 01111 10011111 237 CMP OR NEG
020 16 10000 11011111 337 INC OR ADC
021 17 10001 11011111 337 ADD OR SUB
022 18 10010 10111111 277 DEC OR SBC
023 19 10011 10111111 277 CMP OR NEG
024 20 10100 11111111 377 INC OR ADC
025 21 10101 11111111 377 ADD OR SUB
026 22 10110 11111111 377 DEC OR SBC
027 23 10111 11111111 377 CMP OR NEG
030 24 11000 11111111 377 INC OR ADC
031 25 11001 11111111 377 ADD OR SUB
032 26 11010 11111111 377 DEC OR SBC
033 27 11011 11111111 377 CMP OR NEG
034 28 11100 10111111 277 INC OR ADC
035 29 11101 10111111 277 ADD OR SUB
036 30 11110 11011111 337 DEC OR SBC
037 31 11111 11011111 337 CMP OR NEG
*****
****/( A(PIN #10) IS DPF CODE 1 DEL (1)L
****/( B(PIN #11) IS DPF CODE 0 DEL (1)L
**/( C(PIN #12) IS DPE NEG DEL (1)H
*/( D(PIN #13) IS DPD ALEG 15 DEL (1)L
/( E(PIN #14) IS DPD BLEG 15 DEL (1)L

```

/( =Y4 (PIN # 9) CONF MPC 03 L  
\*/( =Y3 (PIN #10) CONF MPC 02 L  
\*\*/( =Y2 (PIN #11) CONF MPC 01 L  
\*\*\*/( =Y1 (PIN #12) CONF MPC 04 L

OCTAL ADDRESS	DECIMAL ADDRESS	HGFEDCBA	OCTAL DATA
000	0	00000000	1111 017
001	1	00000001	1111 017
002	2	00000010	1111 017
003	3	00000011	1111 017
004	4	00000100	1111 017
005	5	00000101	1111 017
006	6	00000110	1111 017
007	7	00000111	1111 017
010	8	00001000	1111 017
011	9	00001001	1111 017
012	10	00001010	1111 017
013	11	00001011	1111 017
014	12	00001100	1111 017
015	13	00001101	1111 017
016	14	00001110	1111 017
017	15	00001111	1111 017
020	16	00010000	0011 003
021	17	00010001	0011 003
022	18	00010010	0011 003
023	19	00010011	0011 003
024	20	00010100	0101 005
025	21	00010101	0101 005
026	22	00010110	0101 005
027	23	00010111	0101 005
030	24	00011000	0101 005
031	25	00011001	0101 005
032	26	00011010	0101 005
033	27	00011011	0101 005
034	28	00011100	0011 003
035	29	00011101	0011 003
036	30	00011110	0011 003
037	31	00011111	0011 003

NOT ACCESSED

\*\*\*\*\*

HGE

\*\*\*\*\*

040	32	00100000	0011 003
041	33	00100001	0101 005
042	34	00100010	0011 003
043	35	00100011	0101 005
044	36	00100100	0011 003
045	37	00100101	0101 005
046	38	00100110	0011 003
047	39	00100111	0101 005
050	40	00101000	0011 003
051	41	00101001	0101 005
052	42	00101010	0011 003
053	43	00101011	0101 005
054	44	00101100	0011 003
055	45	00101101	0101 005
056	46	00101110	0011 003
057	47	00101111	0101 005
060	48	00110000	0011 003
061	49	00110001	0101 005
062	50	00110010	0011 003
063	51	00110011	0101 005
064	52	00110100	0101 005
065	53	00110101	0101 005
066	54	00110110	0101 005
067	55	00110111	0101 005
070	56	00111000	0101 005
071	57	00111001	0101 005
072	58	00111010	0101 005
073	59	00111011	0101 005
074	60	00111100	0011 003
075	61	00111101	0101 005
076	62	00111110	0011 003
077	63	00111111	0101 005

BNE

BGT

\*\*\*\*\*  
\*\*\*\*\*/( A(PIN #05) IS DPE CC ZERO (1)H  
\*\*\*\*\*/( B(PIN #06) IS DPE CC GOUT (1)H  
\*\*\*\*\*/( C(PIN #07) IS DPE CC NEG (1)H  
\*\*\*\*\*/( D(PIN #04) IS DPE CC VBIT (1)H  
\*\*\*\*\*/( E(PIN #03) IS DPF IR 10 (1)H  
\*\*\*\*\*/( F(PIN #02) IS DPF IR 09 (1)H  
\*\*\*\*\*/( G(PIN #01) IS DPF IR 08 (1)H  
\*\*\*\*\*/( H(PIN #15) IS DPF IR 15 (1)H

```

      /{( #Y4 (PIN # 9) CONF MPC 03 L
      +/{ #Y3 (PIN #10) CONF MPC 02 L
      **/{ #Y2 (PIN #11) CONF MPC 01 L
      ****/{ #Y1 (PIN #12) CONF MPC 04 L
      ****
      OCTAL
      ADDRESS DATA
      100 64 01000000 0011 003
      101 65 01000001 0011 003
      102 66 01000010 0011 003
      103 67 01000011 0011 003
      104 68 01000100 0011 003
      105 69 01000101 0011 003
      106 70 01000110 0011 003
      107 71 01000111 0011 003
      110 72 01001000 0011 003
      111 73 01001001 0011 003
      112 74 01001010 0011 003
      113 75 01001011 0011 003
      114 76 01001100 0011 003
      115 77 01001101 0011 003
      116 78 01001110 0011 003
      117 79 01001111 0011 003
      120 80 01010000 0101 005
      121 81 01010001 0101 005
      122 82 01010010 0101 005
      123 83 01010011 0101 005
      124 84 01010100 0011 003
      125 85 01010101 0011 003
      126 86 01010110 0011 003
      127 87 01010111 0011 003
      130 88 01011000 0011 003
      131 89 01011001 0011 003
      132 90 01011010 0011 003
      133 91 01011011 0011 003
      134 92 01011100 0101 005
      135 93 01011101 0101 005
      136 94 01011110 0101 005
      137 95 01011111 0101 005
  
```

BR (ALWAYS)

\*\*\*\*\*

BLT

\*\*\*\*\*

```

      140 96 01100000 0101 005
      141 97 01100001 0011 003
      142 98 01100010 0101 005
      143 99 01100011 0011 003
      144 100 01100100 0101 005
      145 101 01100101 0011 003
      146 102 01100110 0101 005
      147 103 01100111 0011 003
      150 104 01101000 0101 005
      151 105 01101001 0011 003
      152 106 01101010 0101 005
      153 107 01101011 0011 003
      154 108 01101100 0101 005
      155 109 01101101 0011 003
      156 110 01101110 0101 005
      157 111 01101111 0011 003
      160 112 01110000 0101 005
      161 113 01110001 0011 003
      162 114 01110010 0101 005
      163 115 01110011 0011 003
      164 116 01110100 0011 003
      165 117 01110101 0011 003
      166 118 01110110 0011 003
      167 119 01110111 0011 003
      170 120 01111000 0011 003
      171 121 01111001 0011 003
      172 122 01111010 0011 003
      173 123 01111011 0011 003
      174 124 01111100 0101 005
      175 125 01111101 0011 003
      176 126 01111110 0101 005
      177 127 01111111 0011 003
  
```

BEQ

\*\*\*\*\*

BLE

\*\*\*\*\*

```

      *****/( A(PIN #05) IS DPE CC ZERO (1)H
      *****/( B(PIN #06) IS DPE CC COUT (1)H
      *****/( C(PIN #07) IS DPE CC NEG (1)H
      *****/( D(PIN #04) IS DPE CC VBIT (1)H
      *****/( E(PIN #03) IS DPF IR 10 (1)H
      *****/( F(PIN #02) IS DPF IR 09 (1)H
      *****/( G(PIN #01) IS DPF IR 08 (1)H
      *****/( H(PIN #15) IS DPF IR 15 (1)H
  
```



```

      /* =Y4 (PIN # 9) CONF MPC 03 L
      /* =Y3 (PIN #10) CONF MPC 02 L
      /* =Y2 (PIN #11) CONF MPC 01 L
      /* =Y1 (PIN #12) CONF MPC 04 L
      ****
      OCTAL  OCTAL
      ADDRESS ADDRESS HGFEDCBA DATA
      200 120 10000000 0011 003
      201 121 10000001 0011 003
      202 130 10000010 0011 003
      203 131 10000011 0011 003
      204 132 10000100 0101 005
      205 133 10000101 0101 005
      206 134 10000110 0101 005
      207 135 10000111 0101 005
      210 136 10001000 0011 003
      211 137 10001001 0011 003
      212 138 10001010 0011 003
      213 139 10001011 0011 003
      214 140 10001100 0101 005
      215 141 10001101 0101 005
      216 142 10001110 0101 005
      217 143 10001111 0101 005
      220 144 10010000 0011 003
      221 145 10010001 0011 003
      222 146 10010010 0011 003
      223 147 10010011 0011 003
      224 148 10010100 0011 003
      225 149 10010101 0011 003
      226 150 10010110 0011 003
      227 151 10010111 0011 003
      230 152 10011000 0101 005
      231 153 10011001 0101 005
      232 154 10011010 0101 005
      233 155 10011011 0101 005
      234 156 10011100 0101 005
      235 157 10011101 0101 005
      236 158 10011110 0101 005
      237 159 10011111 0101 005
  
```

BPL  
-----

BPC  
-----

```

      240 160 10100000 0011 003
      241 161 10100001 0101 005
      242 162 10100010 0101 005
      243 163 10100011 0101 005
      244 164 10100100 0011 003
      245 165 10100101 0101 005
      246 166 10100110 0101 005
      247 167 10100111 0101 005
      250 168 10101000 0011 003
      251 169 10101001 0101 005
      252 170 10101010 0101 005
      253 171 10101011 0101 005
      254 172 10101100 0011 003
      255 173 10101101 0101 005
      256 174 10101110 0101 005
      257 175 10101111 0101 005
      260 176 10110000 0011 003
      261 177 10110001 0011 003
      262 178 10110010 0101 005
      263 179 10110011 0101 005
      264 180 10110100 0011 003
      265 181 10110101 0011 003
      266 182 10110110 0101 005
      267 183 10110111 0101 005
      270 184 10111000 0011 003
      271 185 10111001 0011 003
      272 186 10111010 0101 005
      273 187 10111011 0101 005
      274 188 10111100 0011 003
      275 189 10111101 0011 003
      276 190 10111110 0101 005
      277 191 10111111 0101 005
  
```

BHI  
-----

BCC  
-----

```

      *****
      /* A(PIN #05) IS DPE CC ZERO (1)H
      /* B(PIN #06) IS DPE CC COUT (1)H
      /* C(PIN #07) IS DPE CC NEG (1)H
      /* D(PIN #04) IS DPE CC VBIT (1)H
      /* E(PIN #03) IS DPF IR 10 (1)H
      /* F(PIN #02) IS DPF IR 09 (1)H
      /* G(PIN #01) IS DPF IR 08 (1)H
      /* H(PIN #15) IS DPF IR 15 (1)H
  
```

/( \*Y4 (PIN # 9) CONF MPC 03 L  
\*/( \*Y3 (PIN #10) CONF MPC 02 L  
\*\*/( \*Y2 (PIN #11) CONF MPC 01 L  
\*\*\*/( \*Y1 (PIN #12) CONF MPC 04 L

OCTAL ADDRESS	DECIMAL ADDRESS	HGFEDCBA	OCTAL DATA
300	192	11000000	0101 005
301	193	11000001	0101 005
302	194	11000010	0101 005
303	195	11000011	0101 005
304	196	11000100	0011 003
305	197	11000101	0011 003
306	198	11000110	0011 003
307	199	11000111	0011 003
310	200	11001000	0101 005
311	201	11001001	0101 005
312	202	11001010	0101 005
313	203	11001011	0101 005
314	204	11001100	0011 003
315	205	11001101	0011 003
316	206	11001110	0011 003
317	207	11001111	0011 003
320	208	11010000	0101 005
321	209	11010001	0101 005
322	210	11010010	0101 005
323	211	11010011	0101 005
324	212	11010100	0101 005
325	213	11010101	0101 005
326	214	11010110	0101 005
327	215	11010111	0101 005
330	216	11011000	0011 003
331	217	11011001	0011 003
332	218	11011010	0011 003
333	219	11011011	0011 003
334	220	11011100	0011 003
335	221	11011101	0011 003
336	222	11011110	0011 003
337	223	11011111	0011 003

BM1  
\*\*\*\*\*

BVS  
\*\*\*\*\*

340	224	11100000	0101 005
341	225	11100001	0011 003
342	226	11100010	0011 003
343	227	11100011	0011 003
344	228	11100100	0101 005
345	229	11100101	0011 003
346	230	11100110	0011 003
347	231	11100111	0011 003
350	232	11101000	0101 005
351	233	11101001	0011 003
352	234	11101010	0011 003
353	235	11101011	0011 003
354	236	11101100	0101 005
355	237	11101101	0011 003
356	238	11101110	0011 003
357	239	11101111	0011 003
360	240	11110000	0101 005
361	241	11110001	0101 005
362	242	11110010	0011 003
363	243	11110011	0011 003
364	244	11110100	0101 005
365	245	11110101	0101 005
366	246	11110110	0011 003
367	247	11110111	0011 003
370	248	11111000	0101 005
371	249	11111001	0101 005
372	250	11111010	0011 003
373	251	11111011	0011 003
374	252	11111100	0101 005
375	253	11111101	0101 005
376	254	11111110	0011 003
377	255	11111111	0011 003

BLOS  
\*\*\*\*\*

BCS  
\*\*\*\*\*

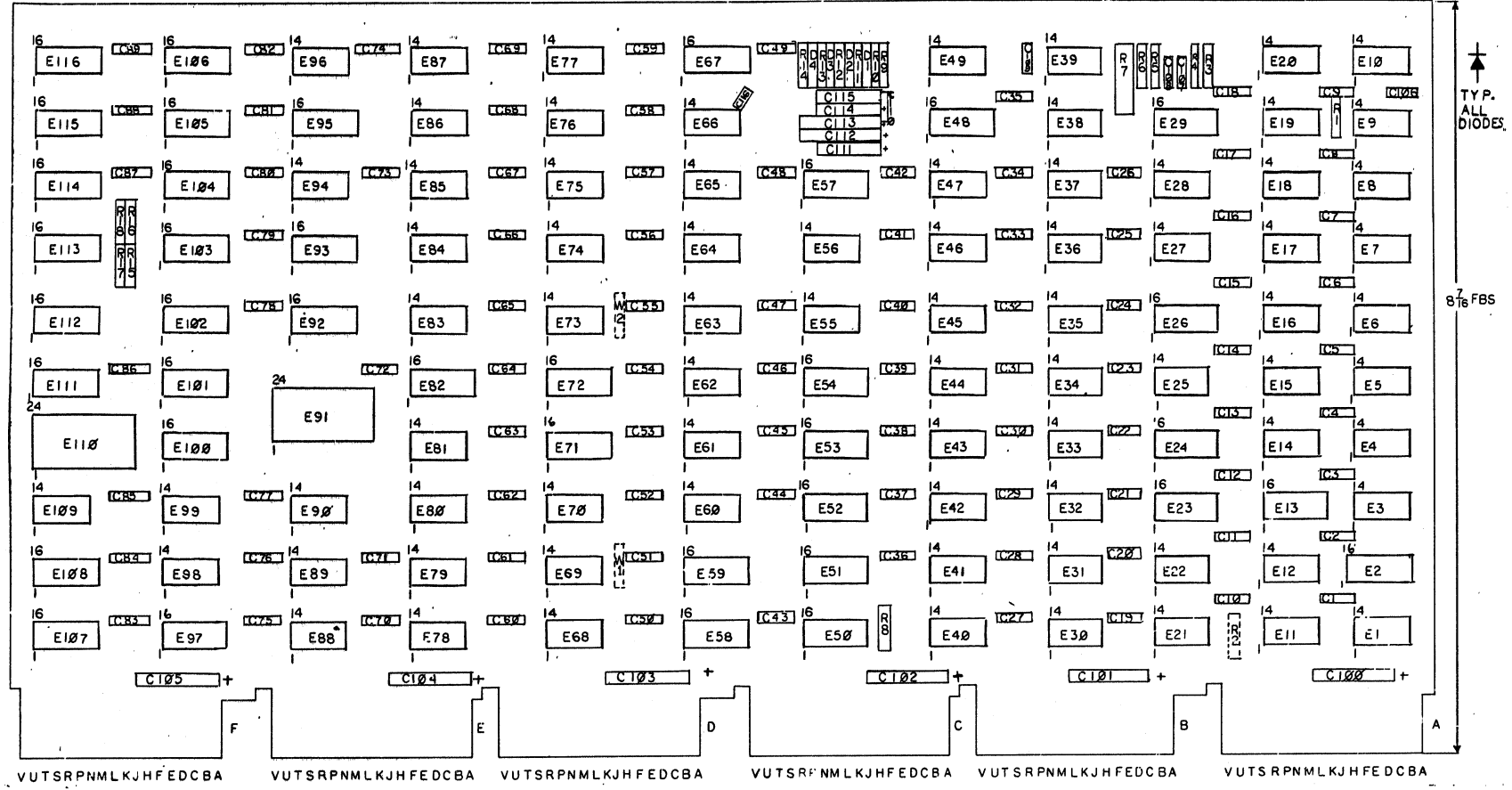
\*\*\*\*\*  
\*\*\*\*\*/( A(PIN #05) IS DPE CC ZERO (1)H  
\*\*\*\*\*/( B(PIN #06) IS DPE CC GOUT (1)H  
\*\*\*\*\*/( C(PIN #07) IS DPE CC NEG (1)H  
\*\*\*\*\*/( D(PIN #04) IS DPE CC VBIT (1)H  
\*\*\*\*\*/( E(PIN #03) IS DPF IR 10 (1)H  
\*\*\*\*\*/( F(PIN #02) IS DPF IR 09 (1)H  
\*\*\*\*\*/( G(PIN #01) IS DPF IR 08 (1)H  
\*\*\*\*\*/( H(PIN #15) IS DPF IR 15 (1)H



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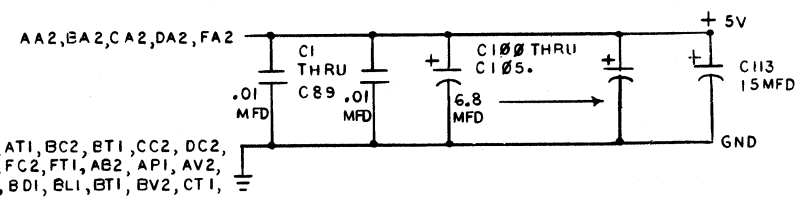
NOTE: UNLESS OTHERWISE NOTED, RESISTANCE IS IN OHMS. CAPACITANCE IS IN PICOFARADS. \* DEC 8640'S WERE PHASED IN AS 380 REPLACEMENTS. ANY 380 FAILURES SHOULD BE REPLACED BY 8640'S.

1-0-1922W



TYP. ALL DIODES.

876 FBS



AA2, BA2, CA2, DA2, FA2  
AC2, AT1, BC2, BT1, CC2, DC2, ET1, FC2, FT1, AB2, AP1, AV2, BB2, BD1, BL1, BT1, BV2, CT1,

QTY.	REF. DESIGNATION	DESCRIPTION	PART NO.	ITEM NO.
6	C100 THRU C105	CAPACITOR, 6.8 MFD, 35V, 10%, S.TA	1005306	69
89	C1 THRU C89	CAPACITOR, .01 MFD, 100V, 20%, DISC	1001610	68
2	C114, 115	CAPACITOR, .68 MFD, 35V, 10%, S.TA	1009964	67
1	C113	CAPACITOR, 15MFD, 20V, 10%, S.TA	1004812	66
1	C112	CAPACITOR, 10MFD, 20V, 10%, S.TA	1004813	65
1	C111	CAPACITOR, 2.2MFD, 20V, 10%, S.TA	1002627	64
1	C110	CAPACITOR, 220 PF, 100V, 5%, DM	1000021	63
1	C109	CAPACITOR, 120 PF, 100V, 5%, DM	1000018	62
2	C108, C116	CAPACITOR, 1000 PF, 100V, 5%, DM	1000042	61
1	C107	CAPACITOR, 2200 PF, 250V, 20%, DISC	1000055	60
1	C106	CAPACITOR, 470 PF, 100V, DM	1000024	59
4	D1, 2, 3, 4	DIODE D664	1100114	58
2	R6, R8	RESISTOR, 390, 1/4W, 5%	1300309	57
1	R14	RESISTOR, 5.6K, 1/4W, 5%	1301874	56
1	R11	RESISTOR, 10K, 1/4W, 5%	1300479	55
1	R7	RESISTOR, 1K, 3/4W, 10%, POT.	1309143-07	54
6	R4, 5, 9, 10, 12, 13	RESISTOR, 30K, 1/4W, 5%	1302394	53
3	R3, 16, 18	RESISTOR, 1K, 1/4W, 5%	1300365	52
6	E54, 82, 92, 93, 95, 100	RESISTOR NETWORK	1311003-02	51
1	R1	RESISTOR, 150, 1/4W, 5%	1300250	50
2	R15, 17	RESISTOR, 2K, 1/4W, 5%	1302388	48
1	E24	IC A01A2	23A01A2	45
1	E116	IC A19A2	23A19A2	44
1	E115	IC A13A2	23A13A2	43
1	E114	IC A11A2	23A11A2	42
1	E113	IC A18A2	23A18A2	41
1	E112	IC A10A2	23A10A2	40
1	E108	IC A14A1	23A14A1	39
1	E107	IC A09A2	23A09A2	38
1	E106	IC A17A2	23A17A2	37
1	E105	IC A16A2	23A16A2	36
1	E104	IC A05A2	23A05A2	35
1	E103	IC A20A2	23A20A2	34
1	E102	IC A04A2	23A04A2	33
1	E72	IC A09A1	23A09A1	32
1	E71	IC A07A1	23A07A1	31
1	E53	IC A02A2	23A02A2	30
2	E91, 110	IC DEC 74154	1909701	29
1	E89	IC DEC 7405	1909930	28
1	E61	IC DEC 7410	1905576	27
1	E60	IC DEC 7430	1905578	26
2	E58, 59	IC DEC 74153	1909937	25
3	E50, 51, 97	IC DEC 74174	1910652	24
3	E49, 78, 109	IC DEC 74H01-1	1909849	23
2	E46, 62	IC DEC 7427	1910878	22
2	E45, 69	IC DEC 8815	1909713	21
1	E39	IC DEC 7413	1909989	20
2	E34, 75	IC DEC 7420	1905577	19
4	E29, 48, 57, 67	IC DEC 9602	1910951	18
4	E25, 63, 83, 90	IC DEC 7404	1909686	17
7	E20, 28, 36, 73, 76, 87, 98	IC DEC 7474	1905547	16
1	E17	IC DEC 7437	1910091	15
3	E12, 22, 32	IC DEC 6640	1311469	14
4	E10, 64, 81, 84	IC DEC 74H40	1905586	13
10	E9, 27, 37, 47, 55, 64, 80, 85, 96, 99	IC DEC 7400	1905575	12
13	E6, 14, 18, 19, 33, 35, 38, 68, 70, 74, 77, 88, 94	IC DEC 7402	1909004	11
9	E4, 7, 8, 15, 16, 44, 56, 65, 86	IC DEC 7473	1905587	10
3	E3, 5, 79	IC DEC 7408	1910155	9
7	E2, 13, 23, 26, 52, 101, 111	IC DEC 74175	1910651	8
9	E1, 11, 21, 30, 31, 40 THRU 43	IC DEC 8881	1909705	7
12		EYELET	9006732	6
1		HANDLE, MODULE	E-PS-1210711-02	5
1		ETCHED CIRCUIT BOARD	5009745	4
REF		MODULE ECO HISTORY	B-MH-M7261-0-6	3
REF		ASSY/DRILL HOLE LAYOUT	D-AH-M7261-0-5	2
REF		X-Y COORDINATE, HOLE LOCATION	K-CO-M7261-0-4	1

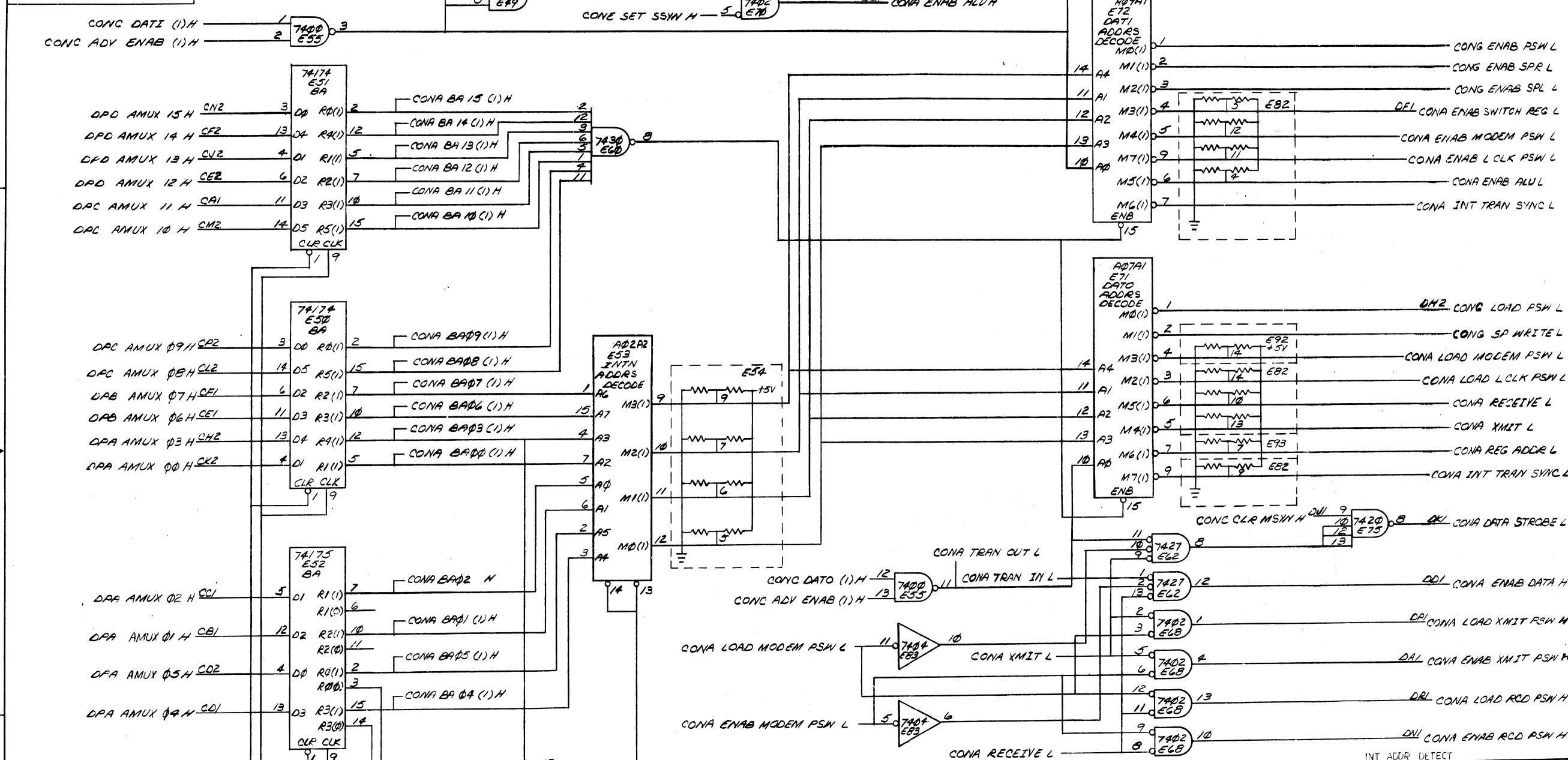
PARTS LIST

REVISIONS	DATE	BY	DESCRIPTION

DATE	9-5-73	TRANSISTOR & DIODE CONVERSION CHART
DATE	10-30-72	
DATE	10-30-72	

TITLE	CONTROL LOGIC & MICROPROGRAM
SIZE	D
CODE	CS M7261-0-1
NUMBER	U
REV	

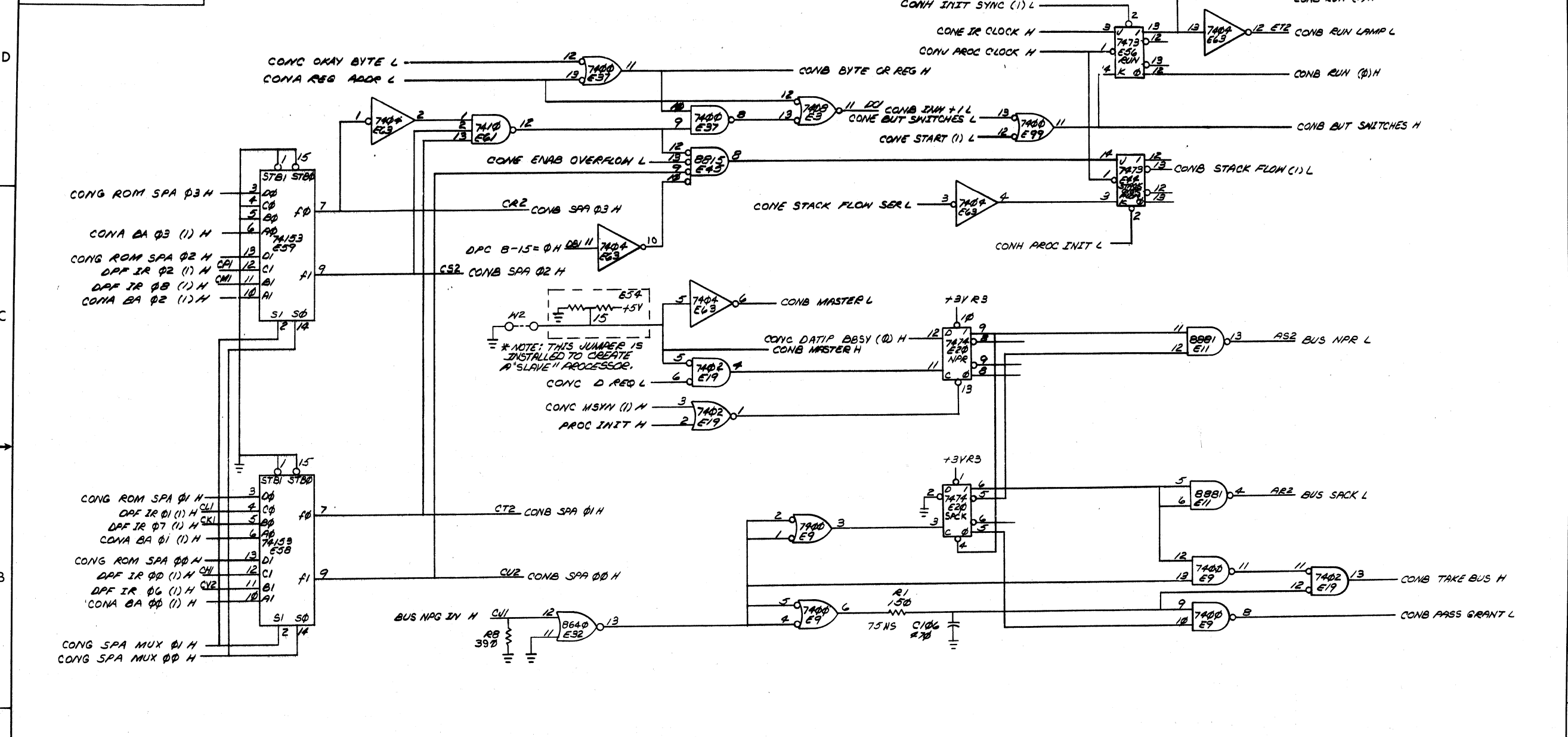
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NOTE: JUMPER W1 IS INSTALLED TO DISABLE THE INTERNAL SERIAL INTERFACE. WITH W1 INSTALLED THE CPU DOES NOT RESPOND INTERNALLY TO ADDRESSES 177560 THRU 177566 AND ANOTHER INTERFACE RESPONDING TO THESE STANDARD CONSOLE DEVICE ADDRESSES CAN BE CONFIGURED WITH THE K11-B.

FIRST USED ON OPTION/MODEL	QTY.	DESCRIPTION	PART NO.	ITEM NO.
11/05		INT ADDR DETECT		
PARTS LIST				
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES		DRN. <i>Don Price</i>	DATE <i>4/11/73</i>	<b>digital</b> EQUIPMENT CORPORATION
TOLERANCES		CHK'D. <i>[Signature]</i>	DATE <i>10/30/73</i>	
DECIMALS .xxx = .005	ANGLES 0 30'	TITLE <b>CONTROL LOGIC &amp; MICROPROGRAM (CONA)</b>		
xx = .02	x = .1			
REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY 1		PREP. DATE <i>10/26/73</i>	DATE <i>10/26/73</i>	
MATERIAL		PROD. DATE <i>4/11/73</i>		
FINISH		NEXT HIGHER ASSY.	SIZE CODE	NUMBER
		B-DD-KD11-B	D CS M7261-0-1	R
		SCALE	SHEET 3 OF 14	
		DIST		

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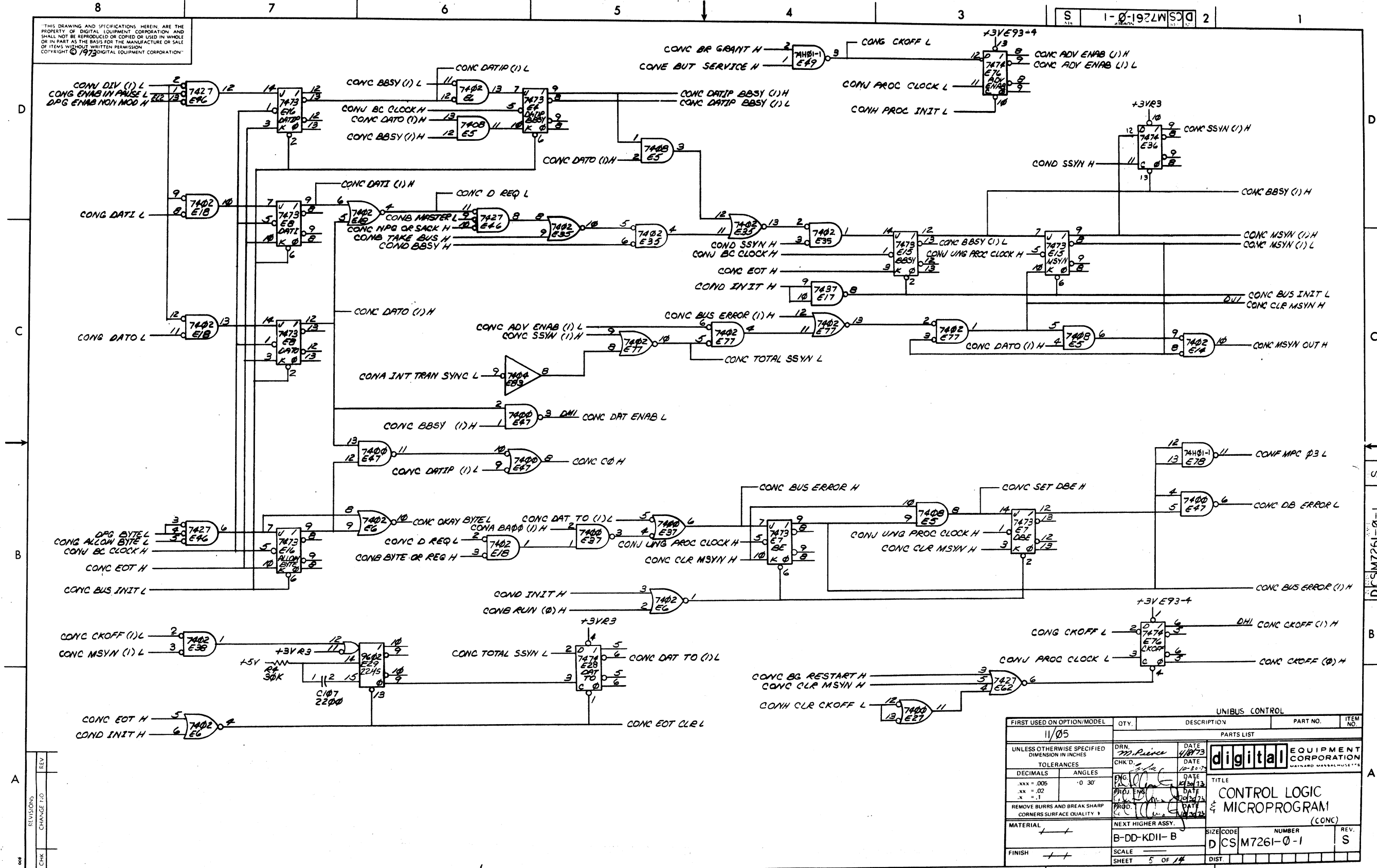


REV	
CHG	

FIRST USED ON OPTION/MODEL		QTY.	DESCRIPTION	PART NO.	ITEM NO.
1105					
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES					
TOLERANCES					
DECIMALS	ANGLES				
.xxx = .005	'0 30'				
.xx = .02					
.x = .1					
REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY Y					
MATERIAL		NEXT HIGHER ASSY.		SIZE/CODE	NUMBER
		B-DD-KDII-B		D CS	M7261-0-1
FINISH		SCALE		SHEET	4 OF 4
				DIST.	

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1-0-1922MCS D 2



UNIBUS CONTROL			
FIRST USED ON OPTION/MODEL	QTY.	DESCRIPTION	PART NO.
11/05			
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES			
TOLERANCES			
DECIMALS	ANGLES		
.xxx = .005	.0 30'		
.xx = .02			
.x = .1			
REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY 1			
MATERIAL		NEXT HIGHER ASSY.	SIZE CODE
FINISH		B-DD-KD11-B	NUMBER
			DCSM7261-0-1
		SHEET	5 OF 14
		DIST	

REV.	CHANGE	DATE

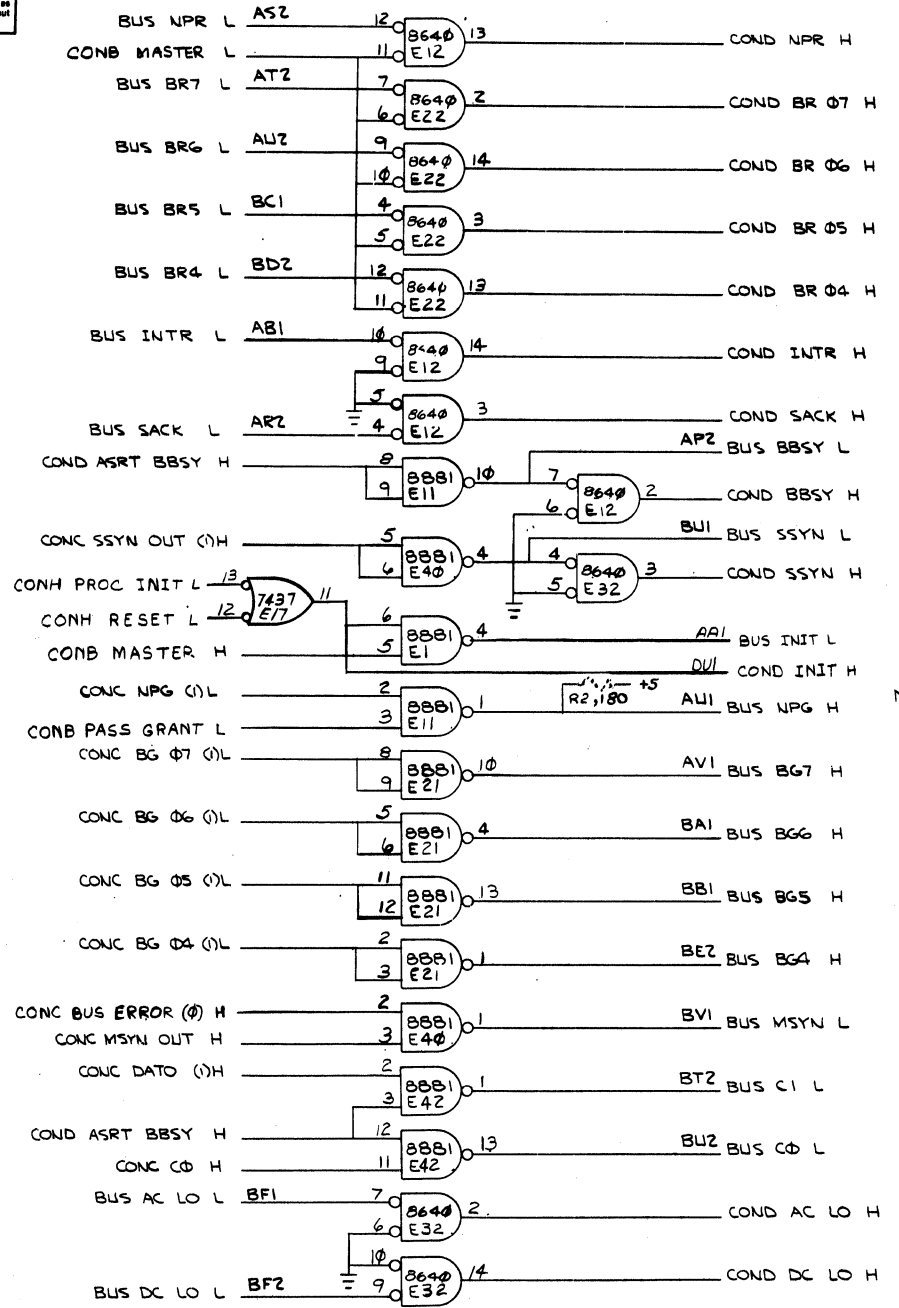
DEC FORM NO. DED 102-C



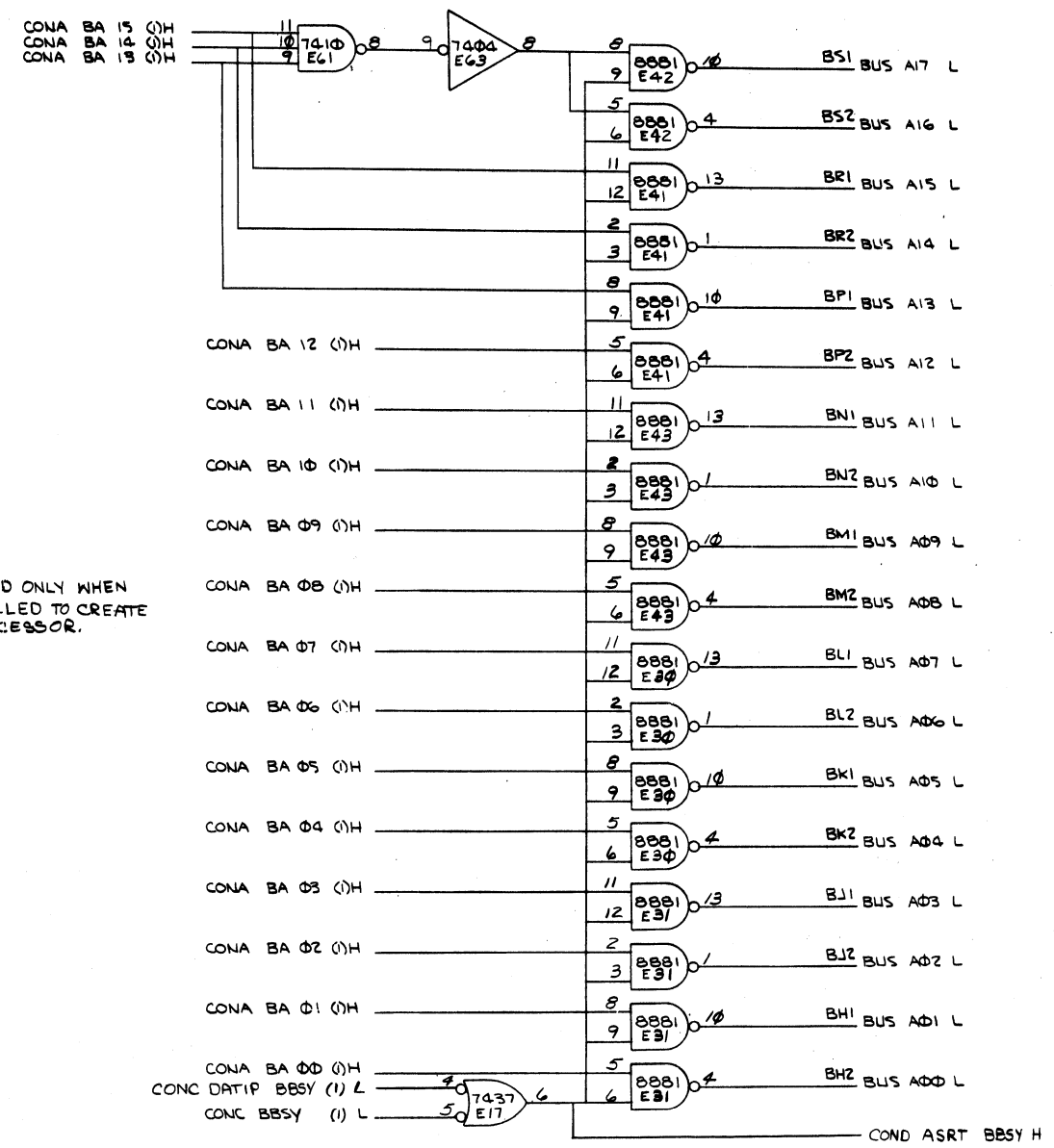


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CS M7261-0-1



NOTE: R2 INSTALLED ONLY WHEN W2 IS INSTALLED TO CREATE A SLAVE PROCESSOR.



DRIVERS & RECEIVERS

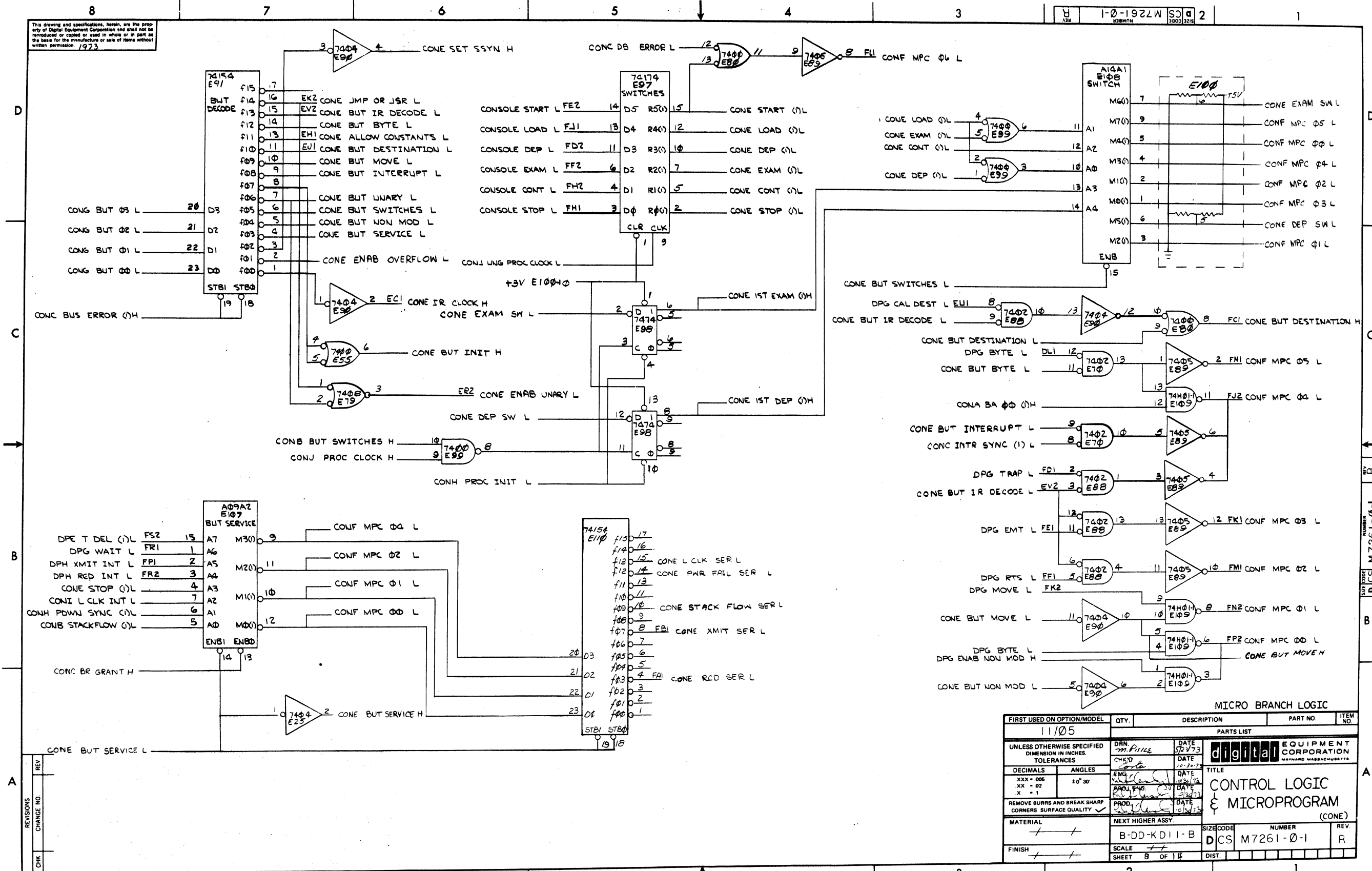
FIRST USED ON OPTION/MODEL	QTY.	DESCRIPTION	PART NO.	ITEM NO.
11/05				
PARTS LIST				
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES. TOLERANCES	DRN: <i>M. Pierce</i>	DATE: 4/24/73	digital EQUIPMENT CORPORATION NATYARD, MASSACHUSETTS	
DECIMALS .XXX - .006 .XX - .02 .X - .1	ANGLES ±0° 30'	CHK'D: <i>[Signature]</i>	DATE: 12/29/73	TITLE: CONTROL LOGIC & MICROPROGRAM (COND)
REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY		APP'D: <i>[Signature]</i>	DATE: 12/29/73	
MATERIAL	NEXT HIGHER ASSY:	SCALE	SIZE CODE	NUMBER
	B-DD-KD11-B	7 OF 14	DCS	M7261-0-1
FINISH			DIST	REV S

REVISIONS  
CHANGE NO.  
REV.

DEC FORM NO  
DND 102-B

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1-0-1922W S3 2

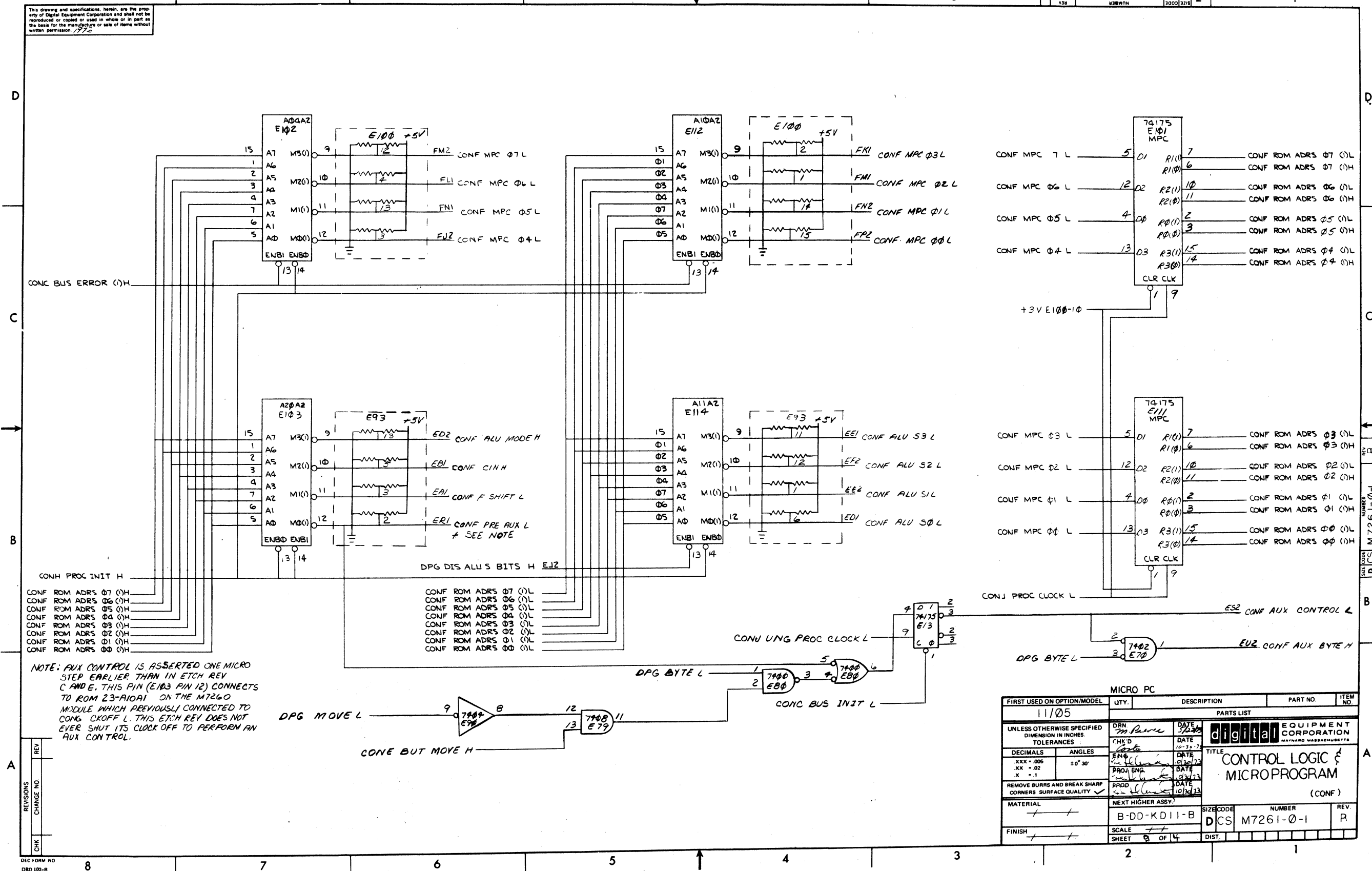


FIRST USED ON OPTION/MODEL	QTY.	DESCRIPTION	PART NO.	ITEM NO.
11/05				
PARTS LIST				
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES. TOLERANCES	DRN PM Price	DATE 5/2/73	<b>digital</b> EQUIPMENT CORPORATION MAYNARD MASSACHUSETTS	
DECIMALS	CHK'D	DATE 10-30-73		
ANGLES	ENG	DATE 3/26/73		
XXX + .005 XX + .02 X + .1	PROJ. ENG.	DATE 2/24/73		
REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY	PROD.	DATE 1/11/73	TITLE <b>CONTROL LOGIC &amp; MICROPROGRAM</b> (CONE)	
MATERIAL	NEXT HIGHER ASSY.			
FINISH	SCALE			
	B-DD-KD11-B	SIZE CODE	NUMBER	REV
	8 OF 14	DCS	M7261-0-1	R
		DIST.		

REV. R  
 NUMBER M7261-0-1  
 DATE DCS  
 SHEET B OF 14

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CS M7261-0-1 2



- CONF ROM ADDR 07 (L)
- CONF ROM ADDR 06 (L)
- CONF ROM ADDR 05 (L)
- CONF ROM ADDR 04 (L)
- CONF ROM ADDR 03 (L)
- CONF ROM ADDR 02 (L)
- CONF ROM ADDR 01 (L)
- CONF ROM ADDR 00 (L)

- CONF ROM ADDR 07 (L)
- CONF ROM ADDR 06 (L)
- CONF ROM ADDR 05 (L)
- CONF ROM ADDR 04 (L)
- CONF ROM ADDR 03 (L)
- CONF ROM ADDR 02 (L)
- CONF ROM ADDR 01 (L)
- CONF ROM ADDR 00 (L)

- CONF MPC 7 L
- CONF MPC 06 L
- CONF MPC 05 L
- CONF MPC 04 L

- CONF MPC 03 L
- CONF MPC 02 L
- CONF MPC 01 L
- CONF MPC 00 L

NOTE: AUX CONTROL IS ASSERTED ONE MICRO STEP EARLIER THAN IN ETCH REV C AND E. THIS PIN (E103 PIN 12) CONNECTS TO ROM 23-A10A1 ON THE M7260 MODULE WHICH PREVIOUSLY CONNECTED TO CONG CROFF L. THIS ETCH REV DOES NOT EVER SHUT ITS CLOCK OFF TO PERFORM AN AUX CONTROL.

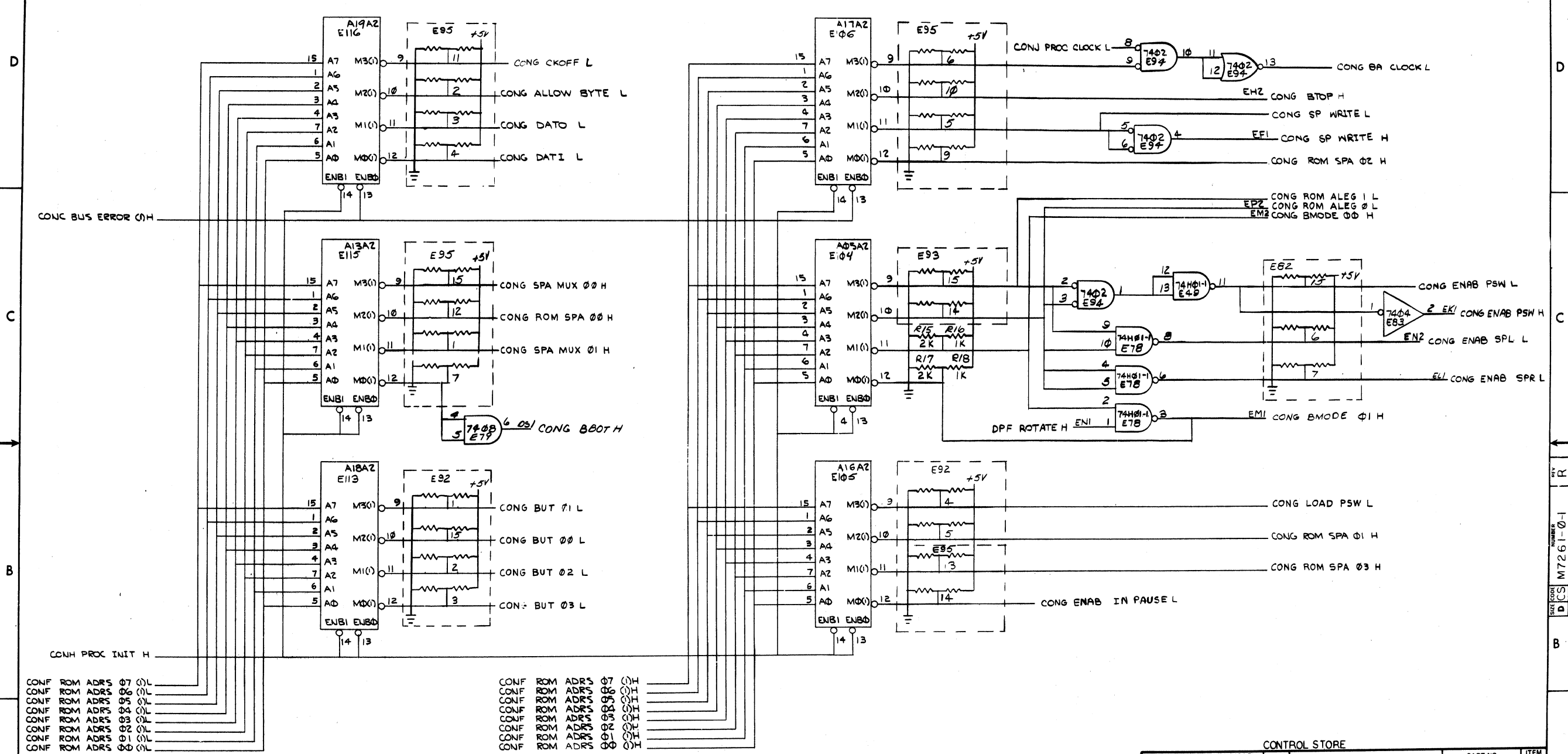
FIRST USED ON OPTION/MODEL		UTY.	DESCRIPTION	PART NO.	ITEM NO.
11/05					
PARTS LIST					
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES.		DRN	DATE	digital EQUIPMENT CORPORATION MAYNARD MASSACHUSETTS	
TOLERANCES		CHK'D	DATE		
DECIMALS	ANGLES	ENG	DATE	TITLE CONTROL LOGIC & MICROPROGRAM (CONF)	
.XXX - .005	± 0° 30'	PROJ. ENG.	DATE		
.XX - .02		PROD.	DATE	SIZE CODE NUMBER REV. DCS M7261-0-1 R	
.X - .1			DATE		
REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY					
MATERIAL		NEXT HIGHER ASSY.			
FINISH		SCALE			
		SHEET			

REV	NO
CHANGE	NO
CHK	

DEC 1978 NO DRD 102-B

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1-0-1927M CS 2



- |                      |                      |
|----------------------|----------------------|
| CONF ROM ADRS 07 01L | CONF ROM ADRS 07 01H |
| CONF ROM ADRS 06 01L | CONF ROM ADRS 06 01H |
| CONF ROM ADRS 05 01L | CONF ROM ADRS 05 01H |
| CONF ROM ADRS 04 01L | CONF ROM ADRS 04 01H |
| CONF ROM ADRS 03 01L | CONF ROM ADRS 03 01H |
| CONF ROM ADRS 02 01L | CONF ROM ADRS 02 01H |
| CONF ROM ADRS 01 01L | CONF ROM ADRS 01 01H |
| CONF ROM ADRS 00 01L | CONF ROM ADRS 00 01H |

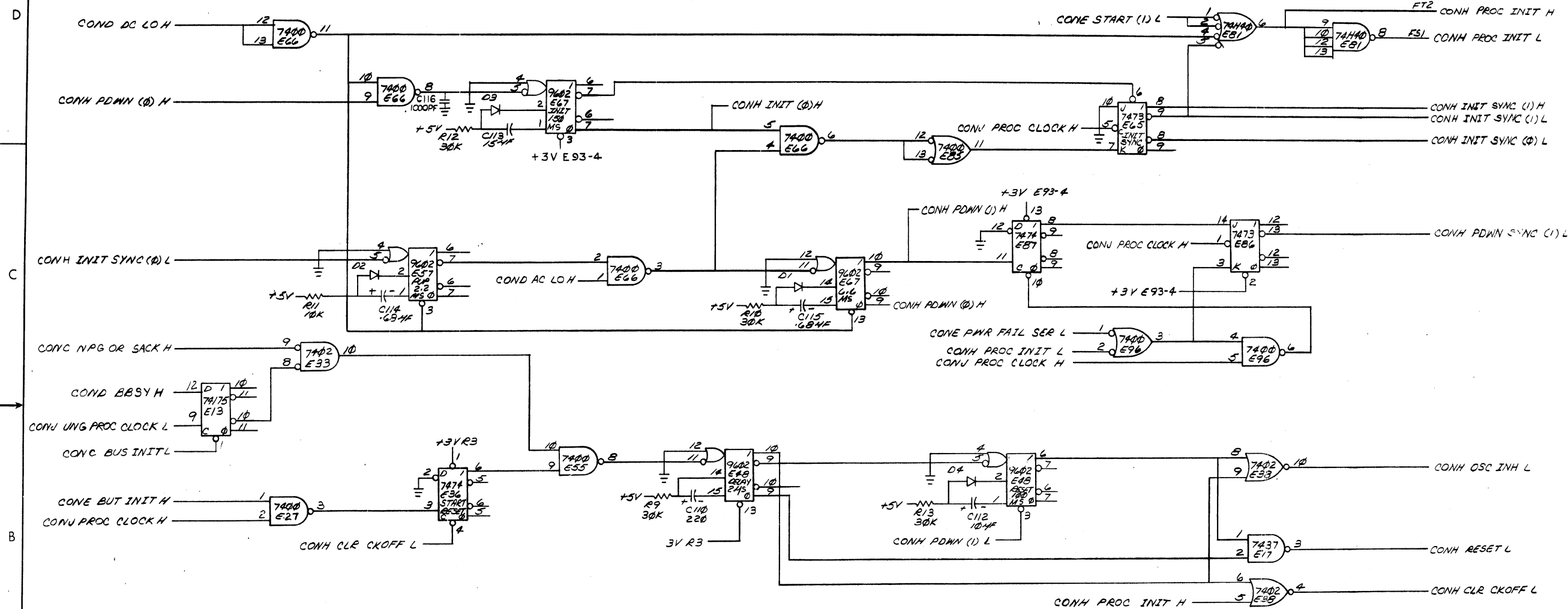
REV	
CHG	

FIRST USED ON OPTION/MODEL		QTY.	DESCRIPTION	PART NO.	ITEM NO.
11/05					
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES. TOLERANCES		PARTS LIST			
DECIMALS	ANGLES	DRN. <i>W. Pine</i>	DATE <i>5/2/73</i>	 <b>digital EQUIPMENT CORPORATION</b> <small>MAYNARD MASSACHUSETTS</small> <b>CONTROL LOGIC &amp; MICROPROGRAM</b> TITLE (CONG)	
.XXX - .005	±0° 30'	CHK'D <i>W. Pine</i>	DATE <i>10-23-73</i>		
.XX - .02		ENG. <i>W. Pine</i>	DATE <i>10-23-73</i>		
.X - .1		PROD. <i>W. Pine</i>	DATE <i>10-23-73</i>		
REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY		NEXT HIGHER ASSY			
MATERIAL		B-DD-KD11-B		SIZE CODE	NUMBER
FINISH		SCALE <i>1:1</i>		D/CS	M7261-0-1
		SHEET 10 OF 14		DIST.	

DEC FORM NO DRD 102-B

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D E S I G N I D E N T I F I C A T I O N  
 1-0-1927M CS 2



POWER FAIL			
FIRST USED ON OPTION/MODEL	QTY.	DESCRIPTION	PART NO.
11/05			
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES			
TOLERANCES			
DECIMALS	ANGLES	DATE	
.xxx = .005	'0 30'	10/30/73	
.xx = .02		10/30/73	
.x = .1		10/30/73	
REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY Y			
MATERIAL			
NEXT HIGHER ASSY.			
FINISH		SCALE	SHEET
		11 OF 14	

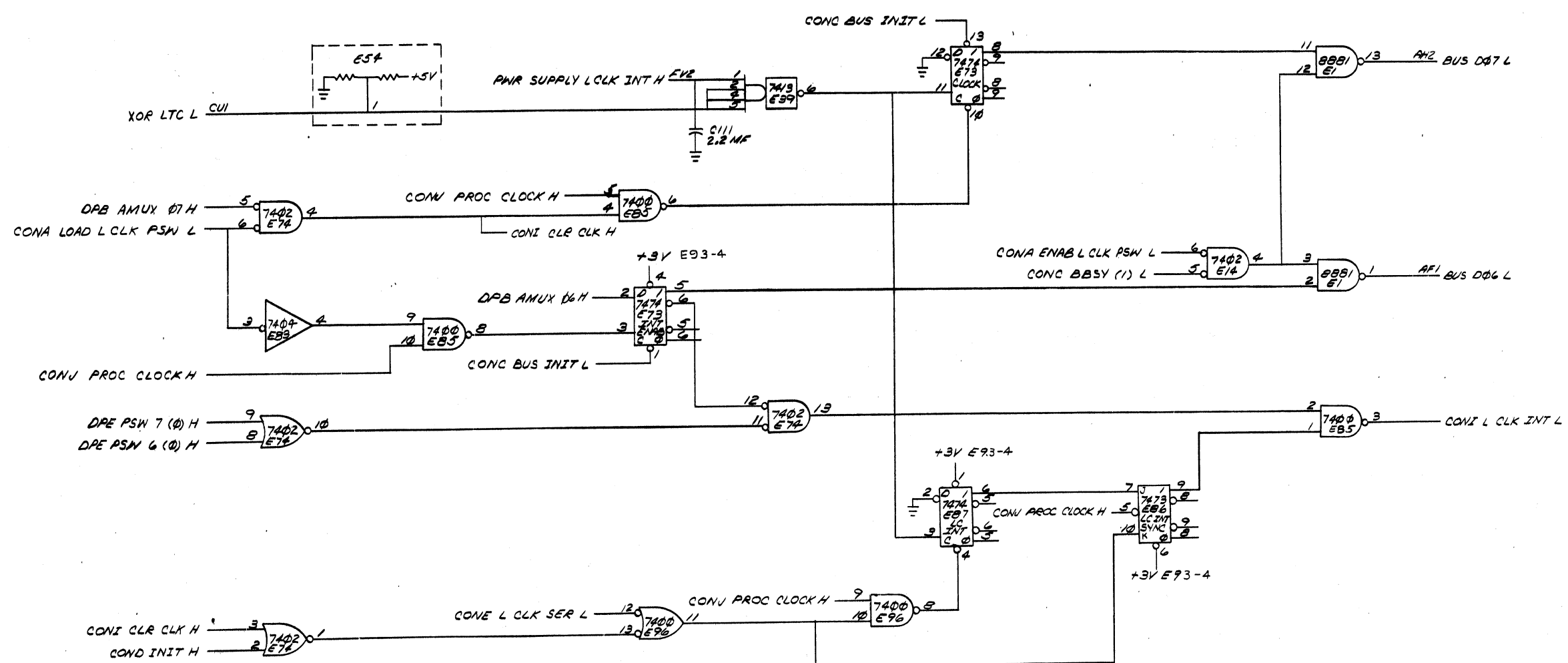
PARTS LIST		TITLE	
DRN.	DATE	CONTROL LOGIC & MICROPROGRAM	
M. Prime	10/30/73	(CONH)	
CHK'D.	DATE	DIGITAL EQUIPMENT CORPORATION	
ENG.	DATE	CORPORATION	
PROJ. ENG.	DATE	MATERIALS MANAGER/DESIGNER	
PROD.	DATE		
MATERIAL		SIZE CODE	NUMBER
		B-DD-KDII-B	DCSM7261-0-1
FINISH		SCALE	REV.
			T

REVISED:  
 CHANGE NO.  
 DATE

DEC FORM NO. DDD 102-C

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S 1-0-192LWCS D 2



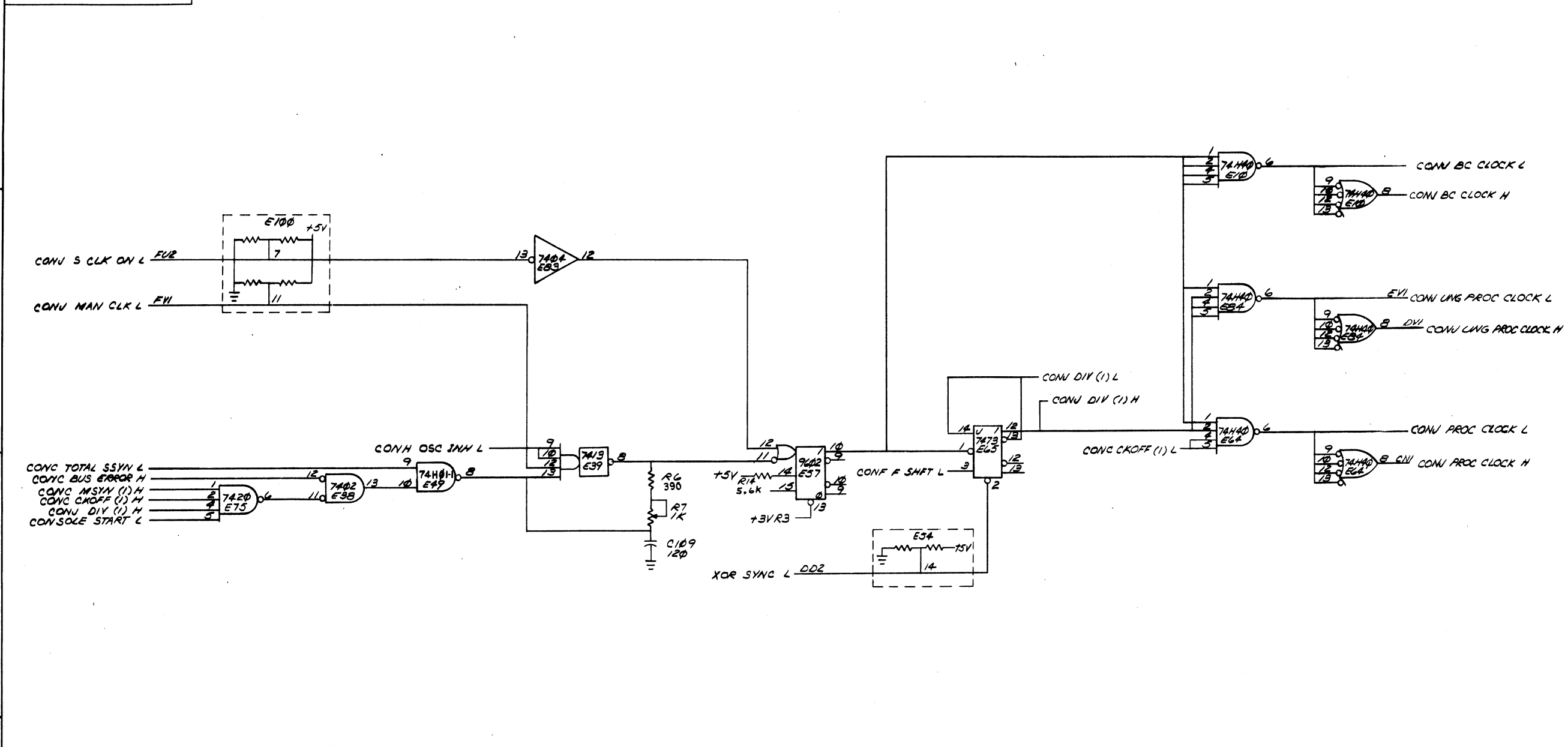
FIRST USED ON OPTION/MODEL		QTY.	DESCRIPTION	PART NO.	ITEM NO.
11/05					
PARTS LIST					
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES		DRN. 7/11/73	DATE 12/20/73		
TOLERANCES		CHK'D. [Signature]	DATE 12/20/73		
DECIMALS	ANGLES	ENGR. [Signature]	DATE 10/30/73	<b>TITLE</b> CONTROL LOGIC & MICROPROGRAM (CONT)	
.xxx = .005	'0 30'	PROJ. ENG. [Signature]	DATE 10/30/73		
.xx = .02		PROD. [Signature]	DATE 10/30/73		
REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY Y					
MATERIAL	NEXT HIGHER ASSY.	B-DD-KDII-B		SIZE CODE	NUMBER
FINISH	SCALE	D CS M7261-0-1		REV.	S
	SHEET	12 OF 14		DIST.	

REV.	CHG.	DESCRIPTION

DEC FORM NO. 080 102-C

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DCSM7261-0-1 2



REV	
CHG	
CHK	

FIRST USED ON OPTION/MODEL	QTY.	DESCRIPTION	PART NO.	ITEM NO.
11/05				
PARTS LIST				
UNLESS OTHERWISE SPECIFIED		DRN	DATE	
DIMENSION IN INCHES		CHK'D	DATE	
TOLERANCES		ANG	DATE	
DECIMALS ANGLES		PRJ. ENGR	DATE	
.xxx = .005	'0 30'	PROD. ENGR	DATE	<b>CONTROL LOGIC &amp; MICROPROGRAM</b> (CONJ)
.xx = .02		PROD. ENGR	DATE	
REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY V				
MATERIAL		NEXT HIGHER ASSY.	SIZE CODE	NUMBER
		B-DD-KDII-B	DCSM7261-0-1	RVF. S
FINISH		SCALE	SHEET	DIST.
		1/4	3 OF 14	

DCSM7261-0-1

A

B

D

C

D

C

B

A

8

7

6

5

4

3

1

8

7

6

5

4

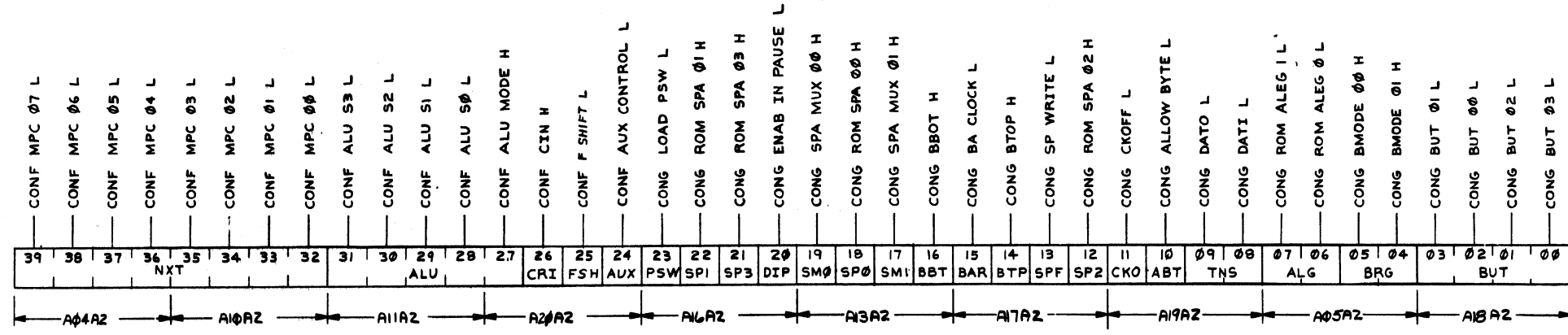
3

2

1

DEC FORM NO. 102-C

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	31	30	29	28	27
AL	L	L	L	L	H
AA	L	L	L	L	L
AB	L	L	L	L	L
AB	L	L	L	L	L
0	L	L	L	L	L
A OR B	L	L	L	L	L
BL	L	L	L	L	L
A PLUS B	L	L	L	L	L
A XOR B	L	L	L	L	L
A-B-I	L	L	L	L	L
B	L	L	L	L	L
-I	L	L	L	L	L
A-I	L	L	L	L	L
A	L	L	L	L	L
ASL	L	L	L	L	L
ROL	L	L	L	L	L
ASR	L	L	L	L	L
ROR	L	L	L	L	L

26 CRI	23 PSW	14 BTP	10 ABT	05 04 BRG	03 02 01 00 BUT
OFF ON	H L	HOLD LOAD	H L	H L	H L
25 FSH	20 DIP	16 BBT	13 SPF	09 08 TNS	NON JMP/USR
OFF ON	H L	BRG SEX +1	H L	H L	IR DECODE
24 AUX	19 17 SM0 SMI	15 BAR	11 CKO	07 06 ALG	BYTE CONST
OFF ON	H L	HOLD LOAD	H L	H L	DEST MOV
	ROM IRS IRD BA				INTR INIT
	H L				UNARY
	H L				SWITCHES
	H L				NON MOD
	H L				SERVICE
	H L				SSYNC
	H L				ENOVFLO
	H L				IR CLK

CONTROL STORE WORD FORMAT

FIRST USED ON OPTION/MODEL	QTY.	DESCRIPTION	PART NO.	ITEM NO.
11/05				
PARTS LIST				
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES TOLERANCES	DRN. <i>M. Rive</i>	DATE 5/23/73	digital EQUIPMENT CORPORATION MAYFIELD MASSACHUSETTS	
DECIMALS .XXX - .006	CHKD. <i>[Signature]</i>	DATE 11-30-73	TITLE	
ANGLES ±0° 30'	ENG. <i>[Signature]</i>	DATE 10/20/73	CONTROL LOGIC & MICROPROGRAM	
REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY	PROJ. ENG. <i>[Signature]</i>	DATE 11/2/73	SIZE CODE	
	PRDD. <i>[Signature]</i>	DATE 11/2/73	NUMBER	
MATERIAL	NEXT HIGHER ASSY		DIGITAL	M7261-0-1
FINISH	B-DD-KD11-B	SCALE		REV R
		SHEET 1A OF 14	DIST	

REVISIONS  
 CHANGE NO. REV.  
 CHK. /

DIGITAL EQUIPMENT CORPORATION  
 M7261-0-1





/ ( =Y8 (PIN #9) CONA INT TRAN SYNC L  
 \* / ( =Y7 (PIN #7) CONA REG ADDR L  
 \*\* / ( =Y6 (PIN #6) CONA RECEIVE L  
 \*\*\* / ( =Y5 (PIN #5) CONA TRANSMIT L  
 \*\*\*\* / ( =Y4 (PIN #4) CONA LOAD MODEM PSW L  
 \*\*\*\*\* / ( =Y3 (PIN #3) CONA LOAD L CLK PSW L  
 \*\*\*\*\*/ ( =Y2 (PIN #2) CONG SP WRITE L  
 \*\*\*\*\*/ ( =Y1 (PIN #1) CONG LOAD PSW L

OCTAL ADDRESS    DECIMAL ADDRESS

OCTAL ADDRESS	DECIMAL ADDRESS	E0CBA	DATA
000	0	00000	11111111
001	1	00001	11111111
002	2	00010	11111111
003	3	00011	11111111
004	4	00100	01111110
005	5	00101	11111111
006	6	00110	01111011
007	7	00111	11111111
010	8	01000	00111011
011	9	01001	10111111
012	10	01010	01111111
013	11	01011	11111111
014	12	01100	11111111
015	13	01101	11111111
016	14	01110	01111111
017	15	01111	11111111
020	16	10000	01010111
021	17	10001	11011111
022	18	10010	01100111
023	19	10011	11101111
024	20	10100	01011111
025	21	10101	11011111
026	22	10110	01101111
027	23	10111	11101111
030	24	11000	11111111
031	25	11001	11111111
032	26	11010	11111111
033	27	11011	11111111
034	28	11100	11111111
035	29	11101	11111111
036	30	11110	11111111
037	31	11111	11111111

PSW ,TRAN OUT BA=177776  
 PSW ,TRAN OUT, BAR  
 LCLK ,TRANOUT  
 LCLK ,TRANOUT, BAR  
 GR<R0IR17> ,TRANOUT RA=1777XX  
 GR<R0IR17> ,TRANOUT, RAR  
 ODD BYTE (LCLK/TK/TP)

SWR ,TRANOUT BA=177570  
 SWR ,TRANOUT, BAR  
 TKS ,TRANOUT BA=177560  
 TKS ,TRANOUT, BAR  
 TPS ,TRANOUT BA=177564  
 TPS ,TRANOUT, BAR  
 TKR ,TRANOUT BA=177562  
 TKR ,TRANOUT, BAR  
 TPB ,TRANOUT BA=177566  
 TPB ,TRANOUT, BAR

\*\*\*\*\*( A(PIN #10) IS CONA TRAN OUT L  
 \*\*\*/( B(PIN #11) IS Y3 OF F025  
 \*\*/( C(PIN #12) IS Y2 OF F025  
 \*/( D(PIN #13) IS Y1 OF F025  
 /( E(PIN #14) IS Y4 OF F025

```

/ ( =Y8 (PIN #9) CONA ENAB L CLK PSW L
*/ ( =Y7 (PIN #7) CONA INT TRAN SYNC L
**/ ( =Y6 (PIN #6) CONA ENAB ALU L
*** / ( =Y5 (PIN #5) CONA ENAB MODEM PSW L
**** / ( =Y4 (PIN #4) CONA ENAB SWITCH REG L
***** / ( =Y3 (PIN #3) CONG ENAB SPL L
***** / ( =Y2 (PIN #2) CONG ENAB SPR L
***** / ( =Y1 (PIN #1) CONG ENAB PSW L
*****
*****
OCTAL      DECIMAL
ADDRESS    ADDRESS    EDCBA    DATA
000        0          00000    11111111 377
001        1          00001    11111111 377
002        2          00010    11111111 377
003        3          00011    11111111 377
004        4          00100    10011110 236
005        5          00101    11111111 377
006        6          00110    00111111 277
007        7          00111    11111111 377
010        8          01000    10011001 231
011        9          01001    11111111 377
012       10          01010    10111111 277
013       11          01011    11111111 377
014       12          01100    11111111 377
015       13          01101    11111111 377
016       14          01110    10010111 227
017       15          01111    11111111 377
020       16          10000    10001111 217
021       17          10001    11111111 377
022       18          10010    10001111 217
023       19          10011    11111111 377
024       20          10100    10011111 237
025       21          10101    11111111 377
026       22          10110    10011111 237
027       23          10111    11111111 377
030       24          11000    11111111 377
031       25          11001    11111111 377
032       26          11010    11111111 377
033       27          11011    11111111 377
034       28          11100    11111111 377
035       29          11101    11111111 377
036       30          11110    11111111 377
037       31          11111    11111111 377
*****
*****/ ( A (PIN #10) IS CONA TRAN IN L
*****/ ( B (PIN #11) IS Y3 OF F025
*****/ ( C (PIN #12) IS Y2 OF F025
*****/ ( D (PIN #13) IS Y1 OF F025
*****/ ( E (PIN #14) IS Y4 OF F025

```

```

PSW ,TRANIN,PA=177776
PSW ,TRANIN,PAR
LCLK ,TRANIN,BA=177546
LCLK ,TRANIN,BAR
GEN REG ,TRANIN,BA=1777YX
GEN REG ,TRANIN,PAR
ODD BYTE ADDRESS (LCLK/TK/TP)

```

```

SWR ,TRANIN,BA=177570
SWR ,TRANIN,PAR
TKS ,TRANIN,BA=177560
TKS ,TRANIN,BAR
TPS ,TRANIN,BA=177564
TPS ,TRANIN,BAR
TKB ,TRANIN,BA=177562
TKB ,TRANIN,BAR
TPB ,TRANIN,BA=177566
TPB ,TRANIN,BAR

```

A13A1

27-JUL-72

\*3=A13A1

ROM LISTING M7261-8 REV. A

/( =Y8 (PIN #9)  
 \*(( =Y7 (PIN #7) CONE LINE CLOCK SER L  
 \*\*/( =Y6 (PIN #6) CONE STACK FLOW L  
 \*\*\*/( =Y5 (PIN #5) CONE PWR FAIL SER L  
 \*\*\*\*/( =Y4 (PIN #4) CONE RCD SER L  
 \*\*\*\*\*/( =Y3 (PIN #3) CONE XMIT SER L  
 \*\*\*\*\*/( =Y2 (PIN #2)  
 \*\*\*\*\*/( =Y1 (PIN #1)

OCTAL ADDRESS	DECIMAL ADDRESS	EDCBA	DATA	OCTAL DATA	
000	0	00000	11111111	377	
001	1	00001	11111111	377	
002	2	00010	11111111	377	
003	3	00011	11111111	377	
004	4	00100	11111111	377	
005	5	00101	11111111	377	
006	6	00110	11111111	377	
007	7	00111	11111111	377	
010	8	01000	11111111	377	
011	9	01001	11111111	377	
012	10	01010	11110111	367	UART RCD INT MPC=64
013	11	01011	11111111	377	
014	12	01100	11111111	377	
015	13	01101	11111111	377	
016	14	01110	11110111	373	UART XMIT INT MPC=60
017	15	01111	11111111	377	
020	16	10000	11111111	377	
021	17	10001	11111111	377	
022	18	10010	11011111	337	ERT=1A STACK FLOW MPC=46
023	19	10011	11111111	377	
024	20	10100	11101111	357	PWR FAIL MPC=43
025	21	10101	11111111	377	
026	22	10110	10111111	277	LINE CLK INT MPC=42
027	23	10111	11111111	377	
030	24	11000	11111111	377	
031	25	11001	11111111	377	
032	26	11010	11111111	377	
033	27	11011	11111111	377	
034	28	11100	11111111	377	
035	29	11101	11111111	377	
036	30	11110	11111111	377	
037	31	11111	11111111	377	

\*\*\*\*\*  
 \*\*\*\*\*/( A(PIN #10) IS CONH RUN GND L  
 \*\*\*/( B(PIN #11) IS CONF MPC 00 L  
 \*\*/( C(PIN #12) IS CONF MPC 02 L  
 \*/( D(PIN #13) IS CONF MPC 01 L  
 /( E(PIN #14) IS CONF MPC 04 L

```

/(( =Y8 (PIN #9) CONF MPC 05 L
*(( =Y7 (PIN #7) CONE EXAM SW L
**(( =Y6 (PIN #6) CONE DEP SW L
***(( =Y5 (PIN #5) CONF MPC 00 L
****(( =Y4 (PIN #4) CONF MPC 04 L
*****(( =Y3 (PIN #3) CONF MPC 01 L
*****(( =Y2 (PIN #2) CONF MPC 02 L
*****(( =Y1 (PIN #1) CONF MPC 03 L
*****
OCTAL ADDRESS DATA
000 0 00000 11110000 370 CONTINUE
001 1 00001 11111011 373 TWO SW =CONT,DEP
002 2 00010 11111011 373 TWO SW EXAM,CONT
003 3 00011 11111011 373 TWO SW LOAD,CONT
004 4 00100 11111011 373 NO SW
005 5 00101 11001010 312 DEP SW1 GOTO 313
006 6 00110 10101000 250 EXAM GOTO TO 317
007 7 00111 11101110 356 LOAD GOTO TO 311
010 8 01000 11111000 370 CONT=CLR EXAM
011 9 01001 11111011 373 TWO SW =CONT,DEP
012 10 01010 11101011 353 TWO SW EXAM,CONT
013 11 01011 11101011 353 TWO SW LOAD,CONT
014 12 01100 10111011 273 NO SW 1ST EXAM
015 13 01101 11001010 312 DEP SW1
016 14 01110 10101100 254 EXAM AND 1ST EXAM 323
017 15 01111 11101110 356 LOAD GOTO 311
020 16 10000 11111000 370 CONT=CLR DEP
021 17 10001 11111011 373 TWO SW =CONT,DEP
022 18 10010 11111011 373 TWO SW EXAM,CONT
023 19 10011 11011011 333 TWO SW LOAD,CONT
024 20 10100 11011011 333 NO SW 1ST DEP
025 21 10101 11011010 332 DEP SW2 GOTO 312
026 22 10110 10101000 250 EXAM,1ST DEP 317
027 23 10111 11101110 356 LOAD GOTO 311
030 24 11000 00000100 004
031 25 11001 00000000 000
032 26 11010 00000000 000
033 27 11011 00000000 000
034 28 11100 00000000 000
035 29 11101 00000000 000
036 30 11110 00000000 000
037 31 11111 00000000 000

```

```

*****
****(( A(PIN #10) IS CONE LOAD (1)L ,AND, DEP (1)L ;BAR
***(( B(PIN #11) IS CONE LOAD (1)L ,AND, EXAM (1)L ;BAR
**(( C(PIN #12) IS CONE CONT (1)L
*(( D(PIN #13) IS CONE 1ST EXAM (1)L
/(( E(PIN #14) IS CONE 1ST DEP (1)L

```

6

```

// Y4 (PIN # 9) CONC SET BG 07 L
// Y3 (PIN #10) CONC SET BG 06 L
// Y2 (PIN #11) CONC SET BG 05 L
// Y1 (PIN #12) CONC SET BG 04 L

```

OCTAL ADDRESS	DECIMAL ADDRESS	HGFEDCBA	OCTAL DATA
000	0	00000000	1111 017
001	1	00000001	1111 017
002	2	00000010	1111 017
003	3	00000011	1111 017
004	4	00000100	1111 017
005	5	00000101	1111 017
006	6	00000110	1111 017
007	7	00000111	1111 017
010	8	00001000	1111 017
011	9	00001001	1111 017
012	10	00001010	1111 017
013	11	00001011	1111 017
014	12	00001100	1111 017
015	13	00001101	1111 017
016	14	00001110	1111 017
017	15	00001111	1111 017
020	16	00010000	1111 017
021	17	00010001	1111 017
022	18	00010010	1111 017
023	19	00010011	1111 017
024	20	00010100	1111 017
025	21	00010101	1111 017
026	22	00010110	1111 017
027	23	00010111	1111 017
030	24	00011000	1111 017
031	25	00011001	1111 017
032	26	00011010	1111 017
033	27	00011011	1111 017
034	28	00011100	1111 017
035	29	00011101	1111 017
036	30	00011110	1111 017
037	31	00011111	1111 017

6  
Cont

040	32	00100000	0111 007
041	33	00100001	1011 013
042	34	00100010	0111 007
043	35	00100011	1111 017
044	36	00100100	0111 007
045	37	00100101	1011 013
046	38	00100110	0111 007
047	39	00100111	1101 015
050	40	00101000	0111 007
051	41	00101001	1011 013
052	42	00101010	0111 007
053	43	00101011	1111 017
054	44	00101100	0111 007
055	45	00101101	1011 013
056	46	00101110	0111 007
057	47	00101111	1110 016
060	48	00110000	0111 007
061	49	00110001	1011 013
062	50	00110010	0111 007
063	51	00110011	1111 017
064	52	00110100	0111 007
065	53	00110101	1011 013
066	54	00110110	0111 007
067	55	00110111	1101 015
070	56	00111000	0111 007
071	57	00111001	1011 013
072	58	00111010	0111 007
073	59	00111011	1111 017
074	60	00111100	0111 007
075	61	00111101	1011 013
076	62	00111110	0111 007
077	63	00111111	1111 017

```

*****
*****/( A(PIN #05) IS CONC BR 07 (1)L
*****/( B(PIN #06) IS CONC BR 06 (1)L
*****/( C(PIN #07) IS CONI LCLK INT L
****/( D(PIN #04) IS CONC BR 05 (1)L
**/( E(PIN #03) IS CONC BR 04 (1)L
**/( F(PIN #02) IS DPE PSW 07 (0)H
**/( G(PIN #01) IS DPE PSW 06 (0)H
/( H(PIN #15) IS DPE PSW 05 (0)H

```

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```

      / ( #Y4 (PIN # 9) CONC SET BG 07 L
      * / ( #Y3 (PIN #10) CONC SET BG 06 L
      ** / ( #Y2 (PIN #11) CONC SET BG 05 L
      *** / ( #Y1 (PIN #12) CONC SET BG 04 L
      ****
      OCTAL
      ADDRESS ADDRESS HGFEDCBA DATA
100 64 01000000 0111 007
101 65 01000001 1011 013
102 66 01000010 0111 007
103 67 01000011 1111 017
104 68 01000100 0111 007
105 69 01000101 1011 013
106 70 01000110 0111 007
107 71 01000111 1111 017
110 72 01001000 0111 007
111 73 01001001 1011 013
112 74 01001010 0111 007
113 75 01001011 1111 017
114 76 01001100 0111 007
115 77 01001101 1011 013
116 78 01001110 0111 007
117 79 01001111 1111 017
120 80 01010000 0111 007
121 81 01010001 1011 013
122 82 01010010 0111 007
123 83 01010011 1111 017
124 84 01010100 0111 007
125 85 01010101 1011 013
126 86 01010110 0111 007
127 87 01010111 1111 017
130 88 01011000 0111 007
131 89 01011001 1011 013
132 90 01011010 0111 007
133 91 01011011 1111 017
134 92 01011100 0111 007
135 93 01011101 1011 013
136 94 01011110 0111 007
137 95 01011111 1111 017
    
```

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7 of 25  
7 cont

```

140 96 01100000 0111 007
141 97 01100001 1011 013
142 98 01100010 0111 007
143 99 01100011 1111 017 LCLK
144 100 01100100 0111 007
145 101 01100101 1011 013
146 102 01100110 0111 007
147 103 01100111 1101 015
150 104 01101000 0111 007
151 105 01101001 1111 017
152 106 01101010 0111 007
153 107 01101011 1111 017 LCLK
154 108 01101100 0111 007
155 109 01101101 1011 013
156 110 01101110 0111 007
157 111 01101111 1110 016
160 112 01110000 0111 007
161 113 01110001 1011 013
162 114 01110010 0111 007
163 115 01110011 1111 017 LCLK
164 116 01110100 0111 007
165 117 01110101 1011 013
166 118 01110110 0111 007
167 119 01110111 1101 015
170 120 01111000 0111 007
171 121 01111001 1011 013
172 122 01111010 0111 007
173 123 01111011 1111 017
174 124 01111100 0111 007
175 125 01111101 1011 013
176 126 01111110 0111 007
177 127 01111111 1111 017
    
```

```

*****
***** / ( A (PIN #05) IS CONC BR 07 (1)L
***** / ( B (PIN #06) IS CONC BR 06 (1)L
***** / ( C (PIN #07) IS CONT LCLK INT L
***** / ( D (PIN #04) IS CONC BR 05 (1)L
*** / ( E (PIN #03) IS CONC BR 04 (1)L
** / ( F (PIN #02) IS DPE PSW 07 (0)M
* / ( G (PIN #01) IS DPE PSW 06 (0)M
/ ( H (PIN #15) IS DPE PSW 05 (0)M
    
```

8

```

      / ( =Y4 (PIN # 9) CONC SET BG 07 L
      * / ( =Y3 (PIN #10) CONC SET BG 06 L
      ** / ( =Y2 (PIN #11) CONC SET BG 05 L
      *** / ( =Y1 (PIN #12) CONC SET BG 04 L
      ****
OCTAL DECIMAL
ADDRESS ADDRESS HGFEDCBA **** OCTAL
200 128 10000000 0111 007
201 129 10000001 1111 017
202 130 10000010 0111 007
203 131 10000011 1111 017
204 132 10000100 0111 007
205 133 10000101 1111 017
206 134 10000110 0111 007
207 135 10000111 1111 017
210 136 10001000 0111 007
211 137 10001001 1111 017
212 138 10001010 0111 007
213 139 10001011 1111 017
214 140 10001100 0111 007
215 141 10001101 1111 017
216 142 10001110 0111 007
217 143 10001111 1111 017
220 144 10010000 0111 007
221 145 10010001 1111 017
222 146 10010010 0111 007
223 147 10010011 1111 017
224 148 10010100 0111 007
225 149 10010101 1111 017
226 150 10010110 0111 007
227 151 10010111 1111 017
230 152 10011000 0111 007
231 153 10011001 1111 017
232 154 10011010 0111 007
233 155 10011011 1111 017
234 156 10011100 0111 007
235 157 10011101 1111 017
236 158 10011110 0111 007
237 159 10011111 1111 017
    
```

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```

240 160 10100000 0111 007
241 161 10100001 1011 013
242 162 10100010 0111 007
243 163 10100011 1111 017 LCLK
244 164 10100100 0111 007
245 165 10100101 1011 013
246 166 10100110 0111 007
247 167 10100111 1101 015
250 168 10101000 0111 007
251 169 10101001 1011 013
252 170 10101010 0111 007
253 171 10101011 1111 017 LCLK
254 172 10101100 0111 007
255 173 10101101 1011 013
256 174 10101110 0111 007
257 175 10101111 1110 016
260 176 10110000 0111 007
261 177 10110001 1011 013
262 178 10110010 0111 007
263 179 10110011 1111 017 LCLK
264 180 10110100 0111 007
265 181 10110101 1011 013
266 182 10110110 0111 007
267 183 10110111 1101 015
270 184 10111000 0111 007
271 185 10111001 1011 013
272 186 10111010 0111 007
273 187 10111011 1111 017
274 188 10111100 0111 007
275 189 10111101 1011 013
276 190 10111110 0111 007
277 191 10111111 1111 017
    
```

8 COD

```

***** / ( A (PIN #05) IS CONC BR 07 (1)L
***** / ( B (PIN #06) IS CONC BR 06 (1)L
***** / ( C (PIN #07) IS CONC LCLK INT L
***** / ( D (PIN #04) IS CONC BR 05 (1)L
** / ( E (PIN #03) IS CONC BR 04 (1)L
* / ( F (PIN #02) IS DPE PSW 07 (0)H
/ ( G (PIN #01) IS DPE PSW 06 (0)H
/ ( H (PIN #15) IS DPE PSW 05 (0)H
    
```



9

```

      / ( =Y4 (PIN # 9) CONC SET BG 07 L
      */( =Y3 (PIN #10) CONC SET BG 06 L
      **/( =Y2 (PIN #11) CONC SET BG 05 L
      ***/( =Y1 (PIN #12) CONC SET BG 04 L
      ****
      OCTAL
      ADDRESS ADDRESS HGFE0CRA **** DATA
      300 192 11000000 0111 007
      301 193 11000001 1011 013
      302 194 11000010 0111 007
      303 195 11000011 1111 017 LCLK
      304 196 11000100 0111 007
      305 197 11000101 1011 013
      306 198 11000110 0111 007
      307 199 11000111 1101 015
      310 200 11001000 0111 007
      311 201 11001001 1011 013
      312 202 11001010 0111 007
      313 203 11001011 1111 017 LCLK
      314 204 11001100 0111 007
      315 205 11001101 1011 013
      316 206 11001110 0111 007
      317 207 11001111 1111 017
      320 208 11010000 0111 007
      321 209 11010001 1011 013
      322 210 11010010 0111 007
      323 211 11010011 1111 017 LCLK
      324 212 11010100 0111 007
      325 213 11010101 1011 013
      326 214 11010110 0111 007
      327 215 11010111 1101 015
      330 216 11011000 0111 007
      331 217 11011001 1011 013
      332 218 11011010 0111 007
      333 219 11011011 1111 017
      334 220 11011100 0111 007
      335 221 11011101 1011 013
      336 222 11011110 0111 007
      337 223 11011111 1111 017
    
```

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```

      340 224 11100000 0111 007
      341 225 11100001 1011 013
      342 226 11100010 0111 007
      343 227 11100011 1111 017 LCLK
      344 228 11100100 0111 007
      345 229 11100101 1011 013
      346 230 11100110 0111 007
      347 231 11100111 1101 015
      350 232 11101000 0111 007
      351 233 11101001 1011 013
      352 234 11101010 0111 007
      353 235 11101011 1111 017 LCLK
      354 236 11101100 0111 007
      355 237 11101101 1011 013
      356 238 11101110 0111 007
      357 239 11101111 1110 016
      360 240 11110000 0111 007
      361 241 11110001 1011 013
      362 242 11110010 0111 007
      363 243 11110011 1111 017 LCLK
      364 244 11110100 0111 007
      365 245 11110101 1011 013
      366 246 11110110 0111 007
      367 247 11110111 1101 015
      370 248 11111000 0111 007
      371 249 11111001 1011 013
      372 250 11111010 0111 007
      373 251 11111011 1111 017
      374 252 11111100 0111 007
      375 253 11111101 1011 013
      376 254 11111110 0111 007
      377 255 11111111 1111 017
    
```

```

      *****
      *****/( A(PIN #05) IS CONC BR 07 (1)L
      *****/( B(PIN #06) IS CONC BR 06 (1)L
      *****/( C(PIN #07) IS CONC LCLK INT L
      *****/( D(PIN #04) IS CONC BR 05 (1)L
      *****/( E(PIN #03) IS CONC BR 04 (1)L
      *****/( F(PIN #02) IS DPE PSW 07 (0)H
      *****/( G(PIN #01) IS DPE PSW 06 (0)H
      *****/( H(PIN #15) IS DPE PSW 05 (0)H
    
```

9

```

      / ( =Y4 (PIN # 9) Y4
      * / ( =Y3 (PIN #10) Y3
      ** / ( =Y2 (PIN #11) Y2
      *** / ( =Y1 (PIN #12) Y1
      ****
      OCTAL
      DATA
      ****
  
```

OCTAL ADDRESS	DECIMAL ADDRESS	HGFEDCBA	OCTAL	DATA
000	0	00000000	0000	000
001	1	00000001	0000	000
002	2	00000010	0000	000
003	3	00000011	0000	000
004	4	00000100	0000	000
005	5	00000101	0000	000
006	6	00000110	0000	000
007	7	00000111	0000	000
010	8	00001000	0000	000
011	9	00001001	0000	000
012	10	00001010	0000	000
013	11	00001011	0000	000
014	12	00001100	0000	000
015	13	00001101	0000	000
016	14	00001110	0000	000
017	15	00001111	0000	000
020	16	00010000	0000	000
021	17	00010001	0000	000
022	18	00010010	0000	000
023	19	00010011	0000	000
024	20	00010100	0000	000
025	21	00010101	0000	000
026	22	00010110	0000	000
027	23	00010111	0000	000
030	24	00011000	0000	000
031	25	00011001	0000	000
032	26	00011010	0000	000
033	27	00011011	0000	000
034	28	00011100	0000	000
035	29	00011101	0000	000
036	30	00011110	0000	000
037	31	00011111	0000	000

M7261-8 REV A

040	32	00100000	0000	000
041	33	00100001	0000	000
042	34	00100010	0000	000
043	35	00100011	0000	000
044	36	00100100	0000	000
045	37	00100101	0000	000
046	38	00100110	0000	000
047	39	00100111	0000	000
050	40	00101000	0000	000
051	41	00101001	0000	000
052	42	00101010	0000	000
053	43	00101011	0000	000
054	44	00101100	0000	000
055	45	00101101	0000	000
056	46	00101110	0000	000
057	47	00101111	0000	000
060	48	00110000	0000	000
061	49	00110001	0000	000
062	50	00110010	0000	000
063	51	00110011	0000	000
064	52	00110100	0000	000
065	53	00110101	0000	000
066	54	00110110	0000	000
067	55	00110111	0000	000
070	56	00111000	0000	000
071	57	00111001	0000	000
072	58	00111010	0000	000
073	59	00111011	0000	000
074	60	00111100	0000	000
075	61	00111101	0000	000
076	62	00111110	0000	000
077	63	00111111	0000	000

\*\*\*\*\*  
 \*\*\*\*\*/ ( A (PIN #05) IS CONA BA 02 (1)H  
 \*\*\*\*\*/ ( B (PIN #06) IS CONA BA 01 (1)H  
 \*\*\*\*\*/ ( C (PIN #07) IS CONA BA 00 (1)H  
 \*\*\*\*\*/ ( D (PIN #04) IS CONA BA 03 (1)H  
 \*\*\*\*\*/ ( E (PIN #03) IS CONA BA 04 (1)H  
 \*\*\*\*\*/ ( F (PIN #02) IS CONA BA 05 (1)H  
 \*\*\*\*\*/ ( G (PIN #01) IS CONA BA 07 (1)H  
 \*\*\*\*\*/ ( H (PIN #15) IS CONA BA 06 (1)H

```

      /C =Y4 (PIN # 9) Y4
      */( =Y3 (PIN #10) Y3
      **/( =Y2 (PIN #11) Y2
      ***/( =Y1 (PIN #12) Y1
      ****
      OCTAL
      DATA
OCTAL DECIMAL
ADDRESS ADDRESS HGFEDCBA
100 64 01000000 0000 000
101 65 01000001 0000 000
102 66 01000010 0000 000
103 67 01000011 0000 000
104 68 01000100 0000 000
105 69 01000101 0000 000
106 70 01000110 0000 000
107 71 01000111 0000 000
110 72 01001000 0000 000
111 73 01001001 0000 000
112 74 01001010 0000 000
113 75 01001011 0000 000
114 76 01001100 0000 000
115 77 01001101 0000 000
116 78 01001110 0000 000
117 79 01001111 0000 000
120 80 01010000 0000 000
121 81 01010001 0000 000
122 82 01010010 0000 000
123 83 01010011 0000 000
124 84 01010100 0000 000
125 85 01010101 0000 000
126 86 01010110 0000 000
127 87 01010111 0000 000
130 88 01011000 0000 000
131 89 01011001 0000 000
132 90 01011010 0000 000
133 91 01011011 0000 000
134 92 01011100 0000 000
135 93 01011101 0000 000
136 94 01011110 0000 000
137 95 01011111 0000 000

```

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```

140 96 01100000 0000 000
141 97 01100001 0000 000
142 98 01100010 0000 000
143 99 01100011 0000 000
144 100 01100100 0000 000
145 101 01100101 0000 000
146 102 01100110 0000 000
147 103 01100111 0000 000
150 104 01101000 0000 000
151 105 01101001 0000 000
152 106 01101010 0000 000
153 107 01101011 0000 000
154 108 01101100 0000 000
155 109 01101101 0000 000
156 110 01101110 0000 000
157 111 01101111 0000 000
160 112 01110000 0000 000
161 113 01110001 0000 000
162 114 01110010 0000 000
163 115 01110011 0000 000
164 116 01110100 0000 000
165 117 01110101 0000 000
166 118 01110110 0000 000
167 119 01110111 0000 000
170 120 01111000 0000 000
171 121 01111001 0000 000
172 122 01111010 0000 000
173 123 01111011 0000 000
174 124 01111100 0000 000
175 125 01111101 0000 000
176 126 01111110 0000 000
177 127 01111111 0000 000

```

```

*****
*****/( A(PIN #05) IS CONA BA 02 (1)H
*****/( B(PIN #06) IS CONA BA 01 (1)H
*****/( C(PIN #07) IS CONA BA 00 (1)H
*****/( D(PIN #04) IS CONA BA 03 (1)H
*****/( E(PIN #03) IS CONA BA 04 (1)H
*****/( F(PIN #02) IS CONA BA 05 (1)H
*****/( G(PIN #01) IS CONA BA 07 (1)H
*****/( H(PIN #15) IS CONA BA 06 (1)H

```

```

      / ( #Y4 (PIN # 9) Y4
      * / ( #Y3 (PIN #10) Y3
      ** / ( #Y2 (PIN #11) Y2
      *** / ( #Y1 (PIN #12) Y1
OCTAL DECIMAL
ADDRESS ADDRESS HGFEDCBA
200 128 10000000 0000 000
201 129 10000001 0000 000
202 130 10000010 0000 000
203 131 10000011 0000 000
204 132 10000100 0000 000
205 133 10000101 0000 000
206 134 10000110 0000 000
207 135 10000111 0000 000
210 136 10001000 0000 000
211 137 10001001 0000 000
212 138 10001010 0000 000
213 139 10001011 0000 000
214 140 10001100 0000 000
215 141 10001101 0000 000
216 142 10001110 0000 000
217 143 10001111 0000 000
220 144 10010000 0000 000
221 145 10010001 0000 000
222 146 10010010 0000 000
223 147 10010011 0000 000
224 148 10010100 0000 000
225 149 10010101 0000 000
226 150 10010110 0000 000
227 151 10010111 0000 000
230 152 10011000 0000 000
231 153 10011001 0000 000
232 154 10011010 0000 000
233 155 10011011 0000 000
234 156 10011100 0000 000
235 157 10011101 0000 000
236 158 10011110 0000 000
237 159 10011111 0000 000

```

12

M7261-8 REV A

```

240 160 10100000 0000 000
241 161 10100001 0000 000
242 162 10100010 0000 000
243 163 10100011 0110 006
244 164 10100100 0000 000
245 165 10100101 0000 000
246 166 10100110 0000 000
247 167 10100111 0101 005
250 168 10101000 0000 000
251 169 10101001 0000 000
252 170 10101010 0000 000
253 171 10101011 0000 000
254 172 10101100 0000 000
255 173 10101101 0000 000
256 174 10101110 0000 000
257 175 10101111 0000 000
260 176 10110000 1000 010
261 177 10110001 1100 014
262 178 10110010 1010 012
263 179 10110011 1110 016
264 180 10110100 0101 005
265 181 10110101 0101 005
266 182 10110110 0101 005
267 183 10110111 0101 005
270 184 10111000 0111 007
271 185 10111001 0000 000
272 186 10111010 0000 000
273 187 10111011 0000 000
274 188 10111100 0111 007
275 189 10111101 0000 000
276 190 10111110 0000 000
277 191 10111111 0000 000

```

KW11=L LINE CLK CSR

KW11=L (ODD BYTE)

TKS TTY KEYBOARD CSR  
TPS PRINTER CSR  
TKB TTY KEYBOARD DBR  
TPB TTY PRINTER DBR  
TKS (ODD BYTE)  
TPS (ODD BYTE)  
TKB (ODD BYTE)  
TPB (ODD BYTE)  
SWITCH REGISTER

CONSOLE SW REG; (ODD BYTE)

```

*****
*****/ ( A (PIN #05) IS CONA BA 02 (1)H
*****/ ( B (PIN #06) IS CONA BA 01 (1)H
*****/ ( C (PIN #07) IS CONA BA 00 (1)H
*****/ ( D (PIN #04) IS CONA BA 03 (1)H
*****/ ( E (PIN #03) IS CONA BA 04 (1)H
*****/ ( F (PIN #02) IS CONA BA 05 (1)H
*****/ ( G (PIN #01) IS CONA BA 07 (1)H
*****/ ( H (PIN #15) IS CONA BA 06 (1)H

```

12

```

/( =Y4 (PIN # 9) Y4
*/( =Y3 (PIN #10) Y3
**/( =Y2 (PIN #11) Y2
***/( =Y1 (PIN #12) Y1
****
OCTAL
ADDRESS ADDRESS HGFEDCBA ***** DATA
300 192 11000000 0001 001 REG R0
301 193 11000001 0001 001 REG R4
302 194 11000010 0001 001 REG R2
303 195 11000011 0001 001 REG R6
304 196 11000100 0001 001 REG R1
305 197 11000101 0001 001 REG R5
306 198 11000110 0001 001 REG R3
307 199 11000111 0001 001 REG R7
310 200 11001000 0001 001 REG R10
311 201 11001001 0001 001 REG R14
312 202 11001010 0001 001 REG R12
313 203 11001011 0001 001 REG R16
314 204 11001100 0001 001 REG R11
315 205 11001101 0001 001 REG R15
316 206 11001110 0001 001 REG R13
317 207 11001111 0001 001 REG R17
320 208 11010000 0000 000
321 209 11010001 0000 000
322 210 11010010 0000 000
323 211 11010011 0000 000
324 212 11010100 0000 000
325 213 11010101 0000 000
326 214 11010110 0000 000
327 215 11010111 0000 000
330 216 11011000 0000 000
331 217 11011001 0000 000
332 218 11011010 0000 000
333 219 11011011 0000 000
334 220 11011100 0000 000
335 221 11011101 0000 000
336 222 11011110 0000 000
337 223 11011111 0000 000

```

13

M7261-8 REV A

```

340 224 11100000 0000 000
341 225 11100001 0000 000
342 226 11100010 0000 000
343 227 11100011 0000 000
344 228 11100100 0000 000
345 229 11100101 0000 000
346 230 11100110 0000 000
347 231 11100111 0000 000
350 232 11101000 0000 000
351 233 11101001 0000 000
352 234 11101010 0000 000
353 235 11101011 0000 000
354 236 11101100 0000 000
355 237 11101101 0000 000
356 238 11101110 0000 000
357 239 11101111 0000 000
360 240 11110000 0000 000
361 241 11110001 0000 000
362 242 11110010 0000 000
363 243 11110011 0000 000
364 244 11110100 0000 000
365 245 11110101 0000 000
366 246 11110110 0000 000
367 247 11110111 0000 000
370 248 11111000 0000 000
371 249 11111001 0000 000
372 250 11111010 0000 000
373 251 11111011 0010 002 PSW
374 252 11111100 0000 000
375 253 11111101 0000 000
376 254 11111110 0000 000
377 255 11111111 0010 002 PSW (ODD BYTE)

```

13 CONT

```

*****
*****/( A(PIN #05) IS CONA BA 02 (1)H
*****/( B(PIN #06) IS CONA BA 01 (1)H
*****/( C(PIN #07) IS CONA BA 00 (1)H
****/( D(PIN #04) IS CONA BA #3 (1)H
***/( E(PIN #03) IS CONA BA 04 (1)H
*/( F(PIN #02) IS CONA BA 05 (1)H
/( G(PIN #01) IS CONA BA 07 (1)H
/( H(PIN #15) IS CONA BA 06 (1)H

```

```

      / ( #Y4 (PIN # 9) CONF MPC 04 L
      * / ( #Y3 (PIN #10) CONF MPC 01 L
      ** / ( #Y2 (PIN #11) CONF MPC 02 L
      *** / ( #Y1 (PIN #12) CONF MPC 00 L
      ****
      OCTAL
      ADDRESS ADDRESS HGFE0CBA DATA
000 0 00000000 1100 014 T
001 1 00000001 1100 014 T
002 2 00000010 1100 014 T
003 3 00000011 1100 014 T
004 4 00000100 1100 014 T
005 5 00000101 1100 014 T
006 6 00000110 1100 014 T
007 7 00000111 1100 014 T
010 8 00001000 1100 014 T
011 9 00001001 1100 014 T
012 10 00001010 1100 014 T
013 11 00001011 1100 014 T
014 12 00001100 1100 014 T
015 13 00001101 1100 014 T
016 14 00001110 1100 014 T
017 15 00001111 1100 014 T
020 16 00010000 1100 014 T
021 17 00010001 1100 014 T
022 18 00010010 1100 014 T
023 19 00010011 1100 014 T
024 20 00010100 1100 014 T
025 21 00010101 1100 014 T
026 22 00010110 1100 014 T
027 23 00010111 1100 014 T
030 24 00011000 1100 014 T
031 25 00011001 1100 014 T
032 26 00011010 1100 014 T
033 27 00011011 1100 014 T
034 28 00011100 1100 014 T
035 29 00011101 1100 014 T
036 30 00011110 1100 014 T
037 31 00011111 1100 014 T

```

14

M7261-8 REV A

```

040 32 00100000 1100 014 T
041 33 00100001 1100 014 T
042 34 00100010 1100 014 T
043 35 00100011 1100 014 T
044 36 00100100 1100 014 T
045 37 00100101 1100 014 T
046 38 00100110 1100 014 T
047 39 00100111 1100 014 T
050 40 00101000 1100 014 T
051 41 00101001 1100 014 T
052 42 00101010 1100 014 T
053 43 00101011 1100 014 T
054 44 00101100 1100 014 T
055 45 00101101 1100 014 T
056 46 00101110 1100 014 T
057 47 00101111 1100 014 T
060 48 00110000 1100 014 T
061 49 00110001 1100 014 T
062 50 00110010 1100 014 T
063 51 00110011 1100 014 T
064 52 00110100 1100 014 T
065 53 00110101 1100 014 T
066 54 00110110 1100 014 T
067 55 00110111 1100 014 T
070 56 00111000 1100 014 T
071 57 00111001 1100 014 T
072 58 00111010 1100 014 T
073 59 00111011 1100 014 T
074 60 00111100 1100 014 T
075 61 00111101 1100 014 T
076 62 00111110 1100 014 T
077 63 00111111 1100 014 T

```

14

```

*****
*****/ ( A (PIN #05) IS COND STACKFLOW (1)L
*****/ ( B (PIN #06) IS CONH POWN SYNC (1)L
*****/ ( C (PIN #07) IS CONI LCLK INT (1)L
****/ ( D (PIN #04) IS CONE STOP (1)L
***/ ( E (PIN #03) IS DPH RCD INT (1)L
**/ ( F (PIN #02) IS DPH XMIT INT (1)L
*/ ( G (PIN #01) IS DPG WAIT L
/ ( H (PIN #15) IS DPE Y DEL (1)L

```

```

/( #Y4 (PIN # 9) CONF MPC 04 L
*/( #Y5 (PIN #10) CONF MPC 01 L
**/( #Y2 (PIN #11) CONF MPC 02 L
***/( #Y1 (PIN #12) CONF MPC 00 L
****
OCTAL
DATA

```

OCTAL ADDRESS	DECIMAL ADDRESS	HGFEDCBA	OCTAL DATA		
100	64	01000000	1100	014	T
101	65	01000001	1100	014	T
102	66	01000010	1100	014	T
103	67	01000011	1100	014	T
104	68	01000100	1100	014	T
105	69	01000101	1100	014	T
106	70	01000110	1100	014	T
107	71	01000111	1100	014	T
110	72	01001000	1100	014	T
111	73	01001001	1100	014	T
112	74	01001010	1100	014	T
113	75	01001011	1100	014	T
114	76	01001100	1100	014	T
115	77	01001101	1100	014	T
116	78	01001110	1100	014	T
117	79	01001111	1100	014	T
120	80	01010000	1100	014	T
121	81	01010001	1100	014	T
122	82	01010010	1100	014	T
123	83	01010011	1100	014	T
124	84	01010100	1100	014	T
125	85	01010101	1100	014	T
126	86	01010110	1100	014	T
127	87	01010111	1100	014	T
130	88	01011000	1100	014	T
131	89	01011001	1100	014	T
132	90	01011010	1100	014	T
133	91	01011011	1100	014	T
134	92	01011100	1100	014	T
135	93	01011101	1100	014	T
136	94	01011110	1100	014	T
137	95	01011111	1100	014	T

15

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140	96	01100000	1100	014	T
141	97	01100001	1100	014	T
142	98	01100010	1100	014	T
143	99	01100011	1100	014	T
144	100	01100100	1100	014	T
145	101	01100101	1100	014	T
146	102	01100110	1100	014	T
147	103	01100111	1100	014	T
150	104	01101000	1100	014	T
151	105	01101001	1100	014	T
152	106	01101010	1100	014	T
153	107	01101011	1100	014	T
154	108	01101100	1100	014	T
155	109	01101101	1100	014	T
156	110	01101110	1100	014	T
157	111	01101111	1100	014	T
160	112	01110000	1100	014	T
161	113	01110001	1100	014	T
162	114	01110010	1100	014	T
163	115	01110011	1100	014	T
164	116	01110100	1100	014	T
165	117	01110101	1100	014	T
166	118	01110110	1100	014	T
167	119	01110111	1100	014	T
170	120	01111000	1100	014	T
171	121	01111001	1100	014	T
172	122	01111010	1100	014	T
173	123	01111011	1100	014	T
174	124	01111100	1100	014	T
175	125	01111101	1100	014	T
176	126	01111110	1100	014	T
177	127	01111111	1100	014	T

15

```

*****
****/( A(PIN #05) IS CONB STACKFLOW (1)L
****/( B(PIN #06) IS CONB PDWN SYNC (1)L
****/( C(PIN #07) IS CONI LCLK INT (1)L
****/( D(PIN #04) IS CONE STOP (1)L
****/( E(PIN #03) IS DPH RCD INT (1)L
**/( F(PIN #02) IS DPH XHIT INT (1)L
/( G(PIN #01) IS DPG WAIT L
/( H(PIN #15) IS DPE T DEL (1)L

```

```

/ ( #Y4 (PIN # 9) CONF MPC 04 L
*/ ( #Y5 (PIN #10) CONF MPC 01 L
**/ ( #Y2 (PIN #11) CONF MPC 02 L
***/ ( #Y1 (PIN #12) CONF MPC 00 L
****
OCTAL
DATA
STKFL
PWRP
LCLK
STKFL
PWRP
STKFL
RCD
STKFL
PWRP
STKFL
LCLK
STKFL
PWRP
STKFL
RCD
STKFL
PWRP
STKFL
LCLK
STKFL
PWRP
STKFL
XMIT
STKFL
PWRP
STKFL
LCLK
STKFL
PWRP
STKFL
XMIT

```

OCTAL ADDRESS	DECIMAL ADDRESS	HGFEDCBA	OCTAL	DATA	STKFL
200	128	10000000	1001	011	STKFL
201	129	10000001	1010	012	PWRP
202	130	10000010	1001	011	STKFL
203	131	10000011	1011	013	LCLK
204	132	10000100	1001	011	STKFL
205	133	10000101	1010	012	PWRP
206	134	10000110	1001	011	STKFL
207	135	10000111	0101	005	RCD
210	136	10001000	1001	011	STKFL
211	137	10001001	1010	012	PWRP
212	138	10001010	1001	011	STKFL
213	139	10001011	1011	013	LCLK
214	140	10001100	1001	011	STKFL
215	141	10001101	1010	012	PWRP
216	142	10001110	1001	011	STKFL
217	143	10001111	0101	005	RCD
220	144	10010000	1001	011	STKFL
221	145	10010001	1010	012	PWRP
222	146	10010010	1001	011	STKFL
223	147	10010011	1011	013	LCLK
224	148	10010100	1001	011	STKFL
225	149	10010101	1010	012	PWRP
226	150	10010110	1001	011	STKFL
227	151	10010111	0111	007	XMIT
230	152	10011000	1001	011	STKFL
231	153	10011001	1010	012	PWRP
232	154	10011010	1001	011	STKFL
233	155	10011011	1011	013	LCLK
234	156	10011100	1001	011	STKFL
235	157	10011101	1010	012	PWRP
236	158	10011110	1001	011	STKFL
237	159	10011111	0111	007	XMIT

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240	160	10100000	1001	011	STKFL
241	161	10100001	1010	012	PWRP
242	162	10100010	1001	011	STKFL
243	163	10100011	1011	013	LCLK
244	164	10100100	1001	011	STKFL
245	165	10100101	1010	012	PWRP
246	166	10100110	1001	011	STKFL
247	167	10100111	0101	005	RCD
250	168	10101000	1001	011	STKFL
251	169	10101001	1010	012	PWRP
252	170	10101010	1001	011	STKFL
253	171	10101011	1011	013	LCLK
254	172	10101100	1001	011	STKFL
255	173	10101101	1010	012	PWRP
256	174	10101110	1001	011	STKFL
257	175	10101111	0101	005	RCD
260	176	10110000	1001	011	STKFL
261	177	10110001	1010	012	PWRP
262	178	10110010	1001	011	STKFL
263	179	10110011	1011	013	LCLK
264	180	10110100	1001	011	STKFL
265	181	10110101	1010	012	PWRP
266	182	10110110	1001	011	STKFL
267	183	10110111	1110	016	STOP
270	184	10111000	1001	011	STKFL
271	185	10111001	1010	012	PWRP
272	186	10111010	1001	011	STKFL
273	187	10111011	1011	013	LCLK
274	188	10111100	1001	011	STKFL
275	189	10111101	1010	012	PWRP
276	190	10111110	1001	011	STKFL
277	191	10111111	0010	002	WAIT

16

```

*****
*****/ ( A (PIN #05) IS CONB STACKFLOW (1)L
*****/ ( B (PIN #06) IS CONH PDWN SYNC (1)L
*****/ ( C (PIN #07) IS CONI LCLK INT (1)L
*****/ ( D (PIN #04) IS CONE STOP (1)L
*****/ ( E (PIN #03) IS DPH RCD INT (1)L
*****/ ( F (PIN #02) IS DPH XMIT INT (1)L
*****/ ( G (PIN #01) IS DPG WAIT L
*****/ ( H (PIN #15) IS DPE Y DEL (1)L

```



/( #Y4 (PIN # 9) CONF MPC #4 L  
\*/( #Y3 (PIN #10) CONF MPC #1 L  
\*\*/( #Y2 (PIN #11) CONF MPC #2 L  
\*\*\*/( #Y1 (PIN #12) CONF MPC #0 L

OCTAL ADDRESS	DECIMAL ADDRESS	HGFEDCBA	OCTAL DATA	STKFL	
300	192	11000000	1001	011	STKFL
301	193	11000001	1010	012	PWRF
302	194	11000010	1001	011	STKFL
303	195	11000011	1011	013	LCLK
304	196	11000100	1001	011	STKFL
305	197	11000101	1010	012	PWRF
306	198	11000110	1001	011	STKFL
307	199	11000111	0101	005	RCD
310	200	11001000	1001	011	STKFL
311	201	11001001	1010	012	PWRF
312	202	11001010	1001	011	STKFL
313	203	11001011	1011	013	LCLK
314	204	11001100	1001	011	STKFL
315	205	11001101	1010	012	PWRF
316	206	11001110	1001	011	STKFL
317	207	11001111	0101	005	RCD
320	208	11010000	1001	011	STKFL
321	209	11010001	1010	012	PWRF
322	210	11010010	1001	011	STKFL
323	211	11010011	1011	013	LCLK
324	212	11010100	1001	011	STKFL
325	213	11010101	1010	012	PWRF
326	214	11010110	1001	011	STKFL
327	215	11010111	0111	007	XMIT
330	216	11011000	1001	011	STKFL
331	217	11011001	1010	012	PWRF
332	218	11011010	1001	011	STKFL
333	219	11011011	1011	013	LCLK
334	220	11011100	1001	011	STKFL
335	221	11011101	1010	012	PWRF
336	222	11011110	1001	011	STKFL
337	223	11011111	0111	007	XMIT

~~17~~  
17

340	224	11100000	1001	011	STKFL
341	225	11100001	1010	012	PWRF
342	226	11100010	1001	011	STKFL
343	227	11100011	1011	013	LCLK
344	228	11100100	1001	011	STKFL
345	229	11100101	1010	012	PWRF
346	230	11100110	1001	011	STKFL
347	231	11100111	0101	005	RCD
350	232	11101000	1001	011	STKFL
351	233	11101001	1010	012	PWRF
352	234	11101010	1001	011	STKFL
353	235	11101011	1011	013	LCLK
354	236	11101100	1001	011	STKFL
355	237	11101101	1010	012	PWRF
356	238	11101110	1001	011	STKFL
357	239	11101111	0101	005	RCD
360	240	11110000	1001	011	STKFL
361	241	11110001	1010	012	PWRF
362	242	11110010	1001	011	STKFL
363	243	11110011	1011	013	LCLK
364	244	11110100	1001	011	STKFL
365	245	11110101	1010	012	PWRF
366	246	11110110	1001	011	STKFL
367	247	11110111	1110	016	STOP
370	248	11111000	1001	011	STKFL
371	249	11111001			

~~17~~  
17



# DRAWING DIRECTORY

## CUSTOMER PRINT SET INDEX

THIS IS PRINT SET 

--	--	--	--

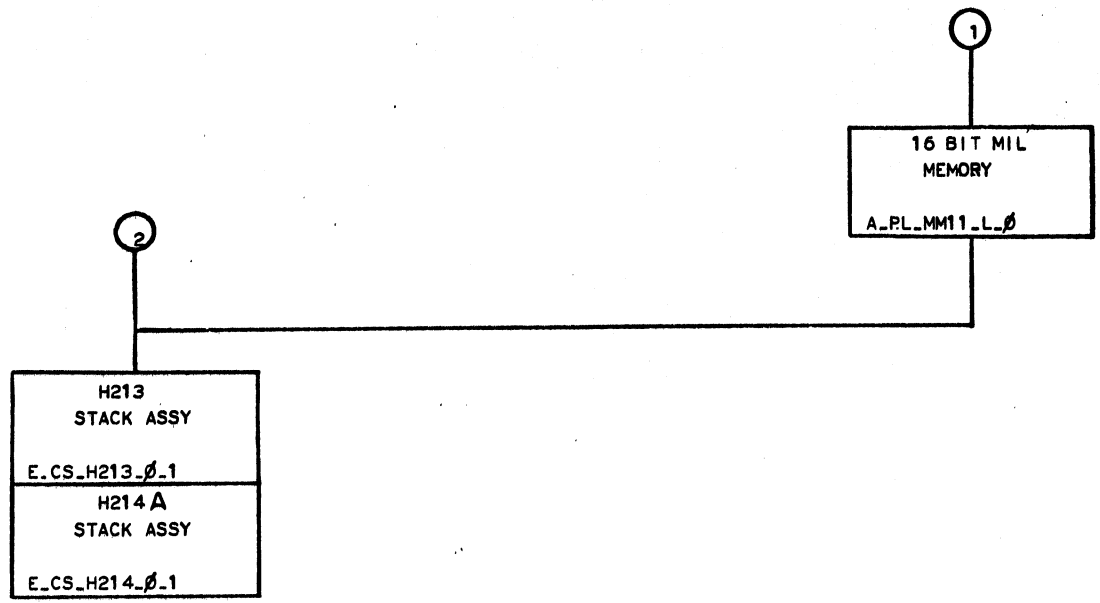
MODULE UTILIZATION	SEQUENCE
MODULE UTILIZATION	D_MJ_MM11_L-1
BLOCK DIAGRAM	D_BD_MM11_L-2
TIMING DIAGRAM	D_TD_MM11_L-3
* MEMORY DRIVERS	E_SC_G231_0-1
* CONTROL & DATA LOOPS	E_CS_G110_0-1
STACK SCHEMATIC	E_CS_H213_0-1
STACK SCHEMATIC	E_CS_H214_0-1
BLOCK DIAGRAM	D_BD_MM11_S-2
SK MEMORY	A_PL_MM11_L-0

SEQUENCE	MFG. PRINT SET
	MFG. TEST PRO. FOR MM11/K,L,M,S&SP
	ASP-MM11-L-5

UNIT VARIATIONS		PRINT SET TYPE			
VARIATION	TITLE	MM11_K	MM11_L		
MM11_K	4K 16 BIT 18 MIL MEMORY	X			
MM11_L	8K 16 BIT 18 MIL MEMORY	X			

\* SPECIAL REVISION PRINTS ARE AVAILABLE ON C ETCH REV. MODULES. CARE SHOULD BE TAKEN TO INSURE THAT PROPER PRINTS ARE ORDERED.

REVISIONS	DATE	CHG. NO.	REV	USED ON OPTION/MODEL	DRN.	DATE	TITLE		REV	
	9-72	MM11L-0001	A			<i>X. Kalayhan</i>	1-25-72	16 BIT 18 MIL MEMORY		J
11-72	MM11L0002	B		MM11_K	<i>X. Kalayhan</i>	1-25-72				
1-73	MISC-00107	C		MM11_L	<i>1: Duvvuri</i>	1-25-72				
2-73	MISC-00111	D			<i>PROD. L</i>	1-26-72	SIZE	CODE	NUMBER	
2-73	MISC-00112	E			<i>KR. S. Varma</i>	1-26-72	B	DD	MM11_L	
2-73	MISC-00113	F			<i>FIELD SERV.</i>	1-26-72				
1-74	MM11L-3	H			<i>W.P. Sweeney</i>	1-26-72				
6-75	MM11L-5	J								
					SHEET 1	OF 3	DIST			



TITLE	SHEET	OF	SIZE	CODE	NUMBER	REV
16 BIT 10 MIL MEMORY	2	3	B	DD	#M11_L	J

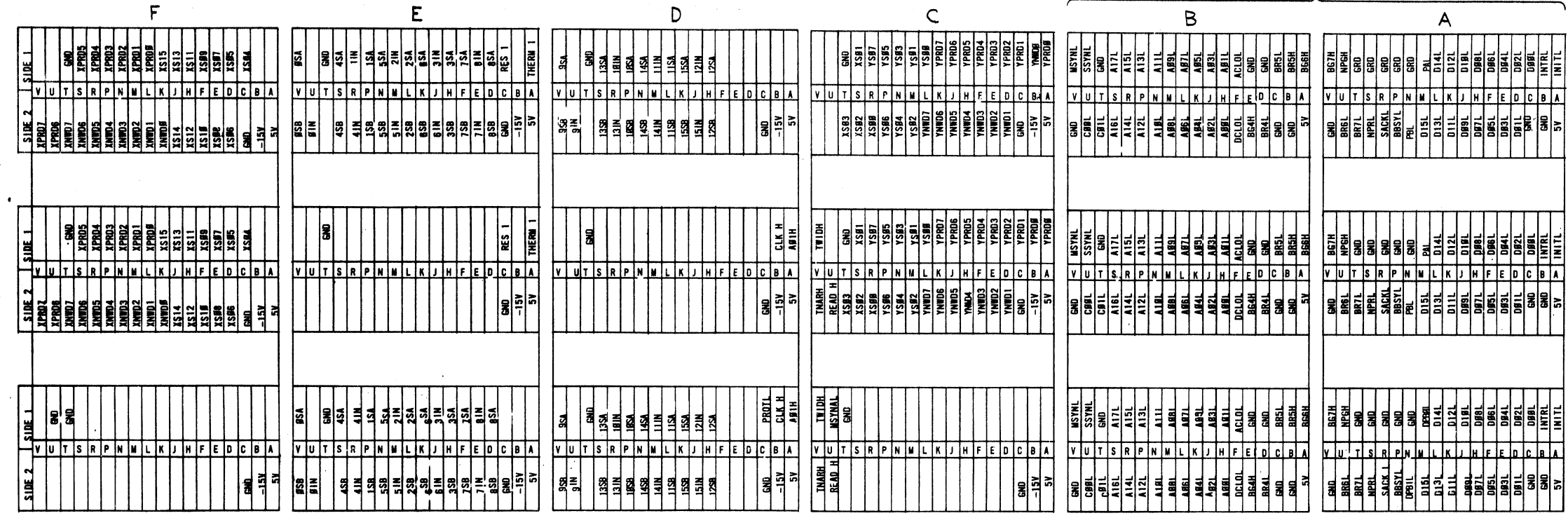
CUSTOMER PRINT SET					ELECTRICAL					CUSTOMER PRINT SET					MECHANICAL						
M11-L					FIND NO.	DRAWING NO.	REV	NO OF SHT	DESCRIPTION	OPTION NO.	M11-L					FIND NO.	DRAWING NO.	REV	NO OF SHT	DESCRIPTION	OPTION NO.
X					1	D_MU_MM11_L.1	A	1	MODULE UTILIZATION							1	A_PL_MM11_L.0	A	1	MEMORY	
X						D_BD_MM11_L.2	A	1	BLOCK DIAGRAM												
X						D_TD_MM11_L.3	#	1	TIMING DIAGRAM								A_PS_3010654_0_0			PURCHASE SPEC	
X						E_CS_G231_0.1	#	5	MEMORY DRIVER												
X						E_CS_G110_0.1	#	5	CONTROL & DATA LOOPS												
X						D_BD_MM11-S-2	#	2	BLOCK DIAGRAM												
c								1								2	B_DD_H214_0.1	#	2	STACK SCHEMATIC	
X						A_PL_MM11-L-0		1	BK MEMORY								B_DD_H213_0	#	2	STACK SCHEMATIC	
						A-SP-MM11-1-7	A	4	PRELIM. ENG. SPEC. FOR MM11-K,L												
X					2	E_CS_H213_0.1	#	2	STACK SCHEMATIC												
X						E_CS_H214_0.1	#	2	STACK SCHEMATIC								A-PL_G645_0-0		1	STACK BOARD	
						A-SP-G109-0-8		1	G109,G110 CONT & DATA LOOP MFG. SPEC.												
						A-SP-G231-0-8		1	MEMORY DRIVER MFG. SPEC.												
						A-SP-MM11-L-4		1	MM11-K,L,S & SP MFG. TEST SPEC.												
				X		A-SP-MM11-L-5		29	MFG. TEST PRO. MM11/K,L,M,S & SP												

TITLE: 16 BIT 10 MIL MEMORY  
 SHEET 3 OF 3  
 SIZE CODE: B DD  
 NUMBER: MM11-L  
 REV: J

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PIN SIDE VIEW OF BACKPLANE

THESE 2 SLOTS ARE UNIBUS WIRED ON ALL 3 CONNECTORS



H213, H214 OR H215 STACK  
(F, E, D, C) (QUAD B 1/2)  
UNIBUS CONN OR TERM (A, B)

DRIVE  
(F, E, D, C, B, A) (HEX B 1/2)  
G2 B1

SENSE-CONTROL  
(F, E, D, B, C, A) (HEX B 1/2)  
G1 B

REVISIONS

CHK	CHANGE NO.	REV
1	MM11-0005	A
2	6-03-75	
P. DURANT		
10-15-75		

FIRST USED ON OPTION/MODEL	QTY.	DESCRIPTION	PART NO.	ITEM NO.
MM11-L				
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES. TOLERANCES				
DECIMALS	ANGLES	DATE	PARTS LIST	
.xxx - .006	± 0° 30'	1/24/72	digital EQUIPMENT CORPORATION MAYNARD MASSACHUSETTS	
.xx - .02		1-24-72	TITLE	
.x - .1		1-24-72	MODULE UTILIZATION	
REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY	PROD. DATE	1-24-72	SIZE CODE	
			B-DD-MM11-L	
MATERIAL	NEXT HIGHER ASSY.	SCALE	NUMBER	REV
///	B-DD-MM11-L	1 OF 1	DMU MM11-L-1	A
FINISH		SHEET	DIST.	
///		1 OF 1		

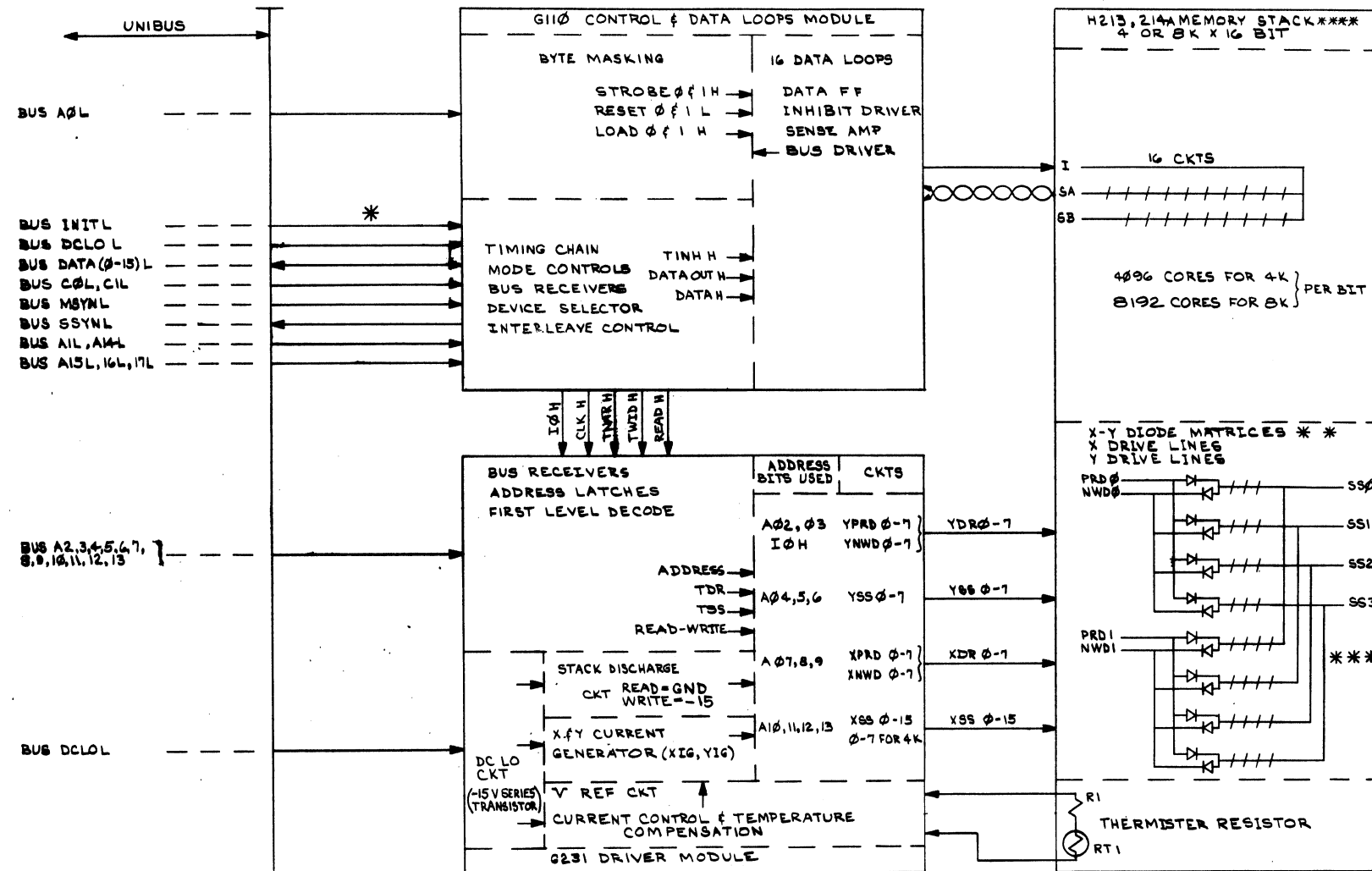
DEC FORM NO. DRD 102-B

REV A  
NUMBER  
DMU MM11-L-1

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SIZE CODE  
BDDMMII-L-2  
NUMBER

NOTES:  
 \*1. ALL ARROWS SHOW SIGNAL FLOW DIRECTION.  
 \*\*2. MATRIX SHOWN IS FOR ILLUSTRATION ONLY.  
 \*\*\*3. ACTUAL MATRIX HAS  
 { Y AXIS 8PRD, 8NRD, 866  
 X AXIS 4K 8PRD, 8NRD, 866  
 X AXIS 8K 8PRD, 8NRD, 1666  
 \*\*\*\*4. H214A IS A DESIGNATOR FOR EITHER AN H214 OR H215 MEMORY STACK, SEE E-CS-H214-0-1



FIRST USED ON OPTION/MODEL	QTY.	DESCRIPTION	PART NO.	ITEM NO.
MMII-L				
PARTS LIST				
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES. TOLERANCES		DRN <i>J. Carbery</i> DATE 11/19/71	digital EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS	
DECIMALS	ANGLES	CHK'D <i>J. Carbery</i> DATE 1-25-72	TITLE BLOCK DIAGRAM	
.XXX - .005	±0° 30'	ENG. <i>P. Durant</i> DATE 1-25-71		
.XX - .02		PROJ. ENG. <i>P. Durant</i> DATE 1-25-71		
.X - .1		PROD. <i>R.K. Peterson</i> DATE 1-24-72		
REMOVE BURRS AND BREAK SHARP CORNERS. SURFACE QUALITY				
MATERIAL	NEXT HIGHER ASSY.	SIZE CODE	NUMBER	REV.
	B-DD MMII-L	BDD	MMII-L-2	A
FINISH	SCALE	SHEET	OF	DIST.

REV.	CHANGE NO.	CHK.	DATE
A	0005		
	0006		

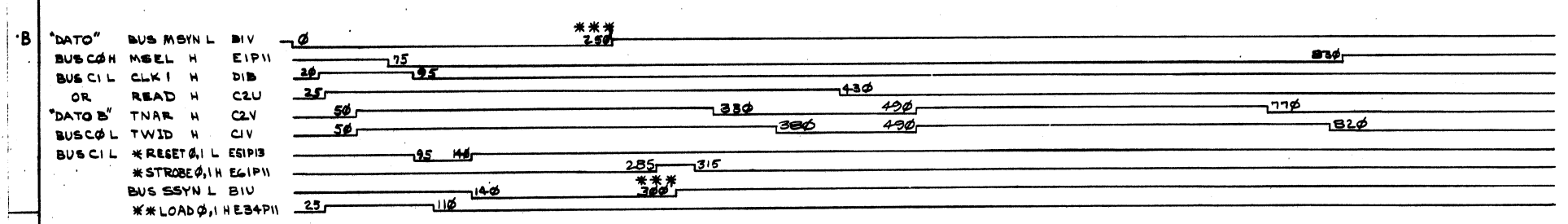
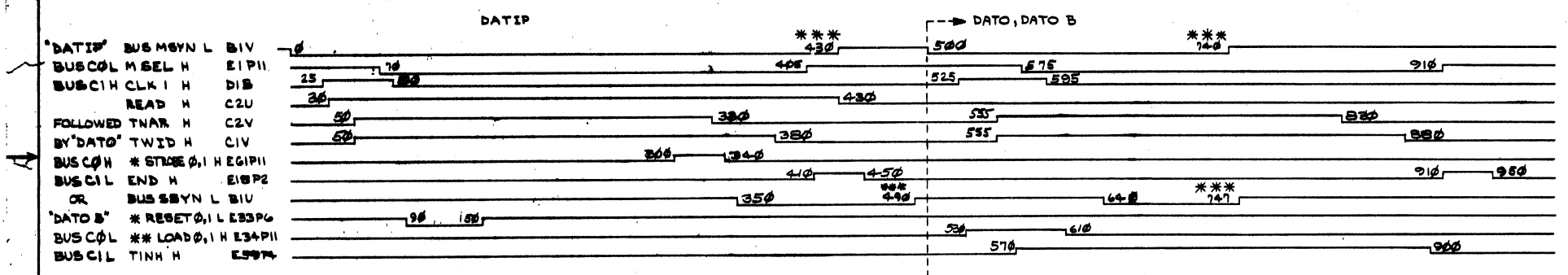
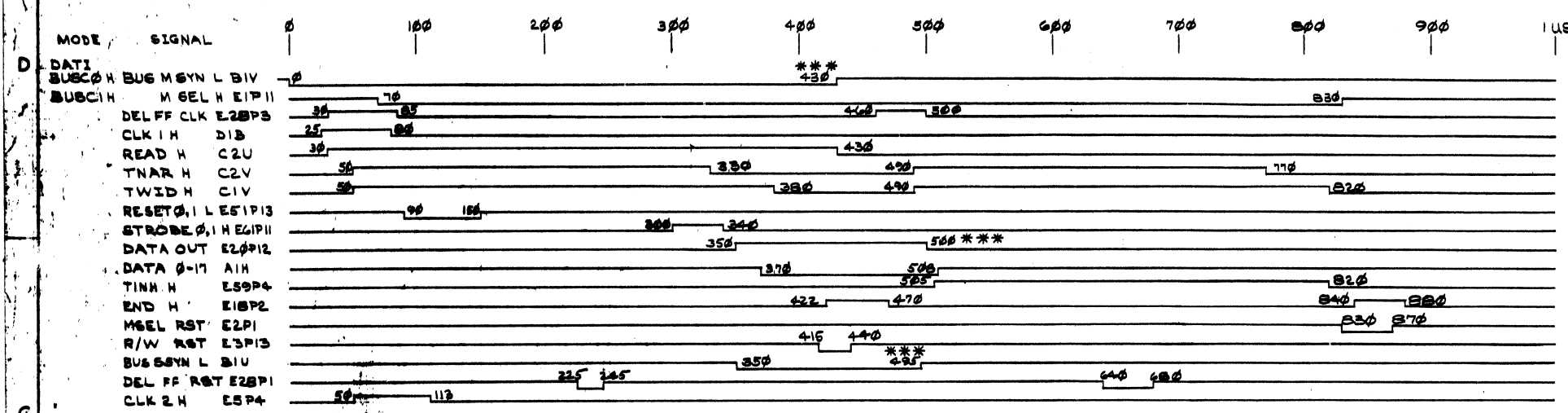
REVISIONS  
 DURANT 10-25-75

DEC FORM NO. DRD 102-B

8 7 6 5 4 3

2 1

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- NOTES:
1. ANY SIGNALS NOT SHOWN ON DATIP, DATO OR DATO B ARE AS SHOWN ON DATI TIMING.
  - \* 2. RESET L AND STROBE H DO NOT OCCUR IN DATO MODE. THEY ONLY OCCUR FOR THE BYTE NOT BEING ADDRESSED IN DATO B MODE.
  - \* \* 3. LOAD H OCCURS FOR BOTH BITS IN DATO MODE AND ONLY FOR THE ADDRESSED BYTE IN THE DATO B MODE.
  - \* \* \* 4. ACTUAL TIME DEPENDS ON BUS AND PROCESSOR DELAYS.
  5. ALL SIGNALS ON G109 OR G110 MODULE

REV	
CHG	
REV	
CHG	

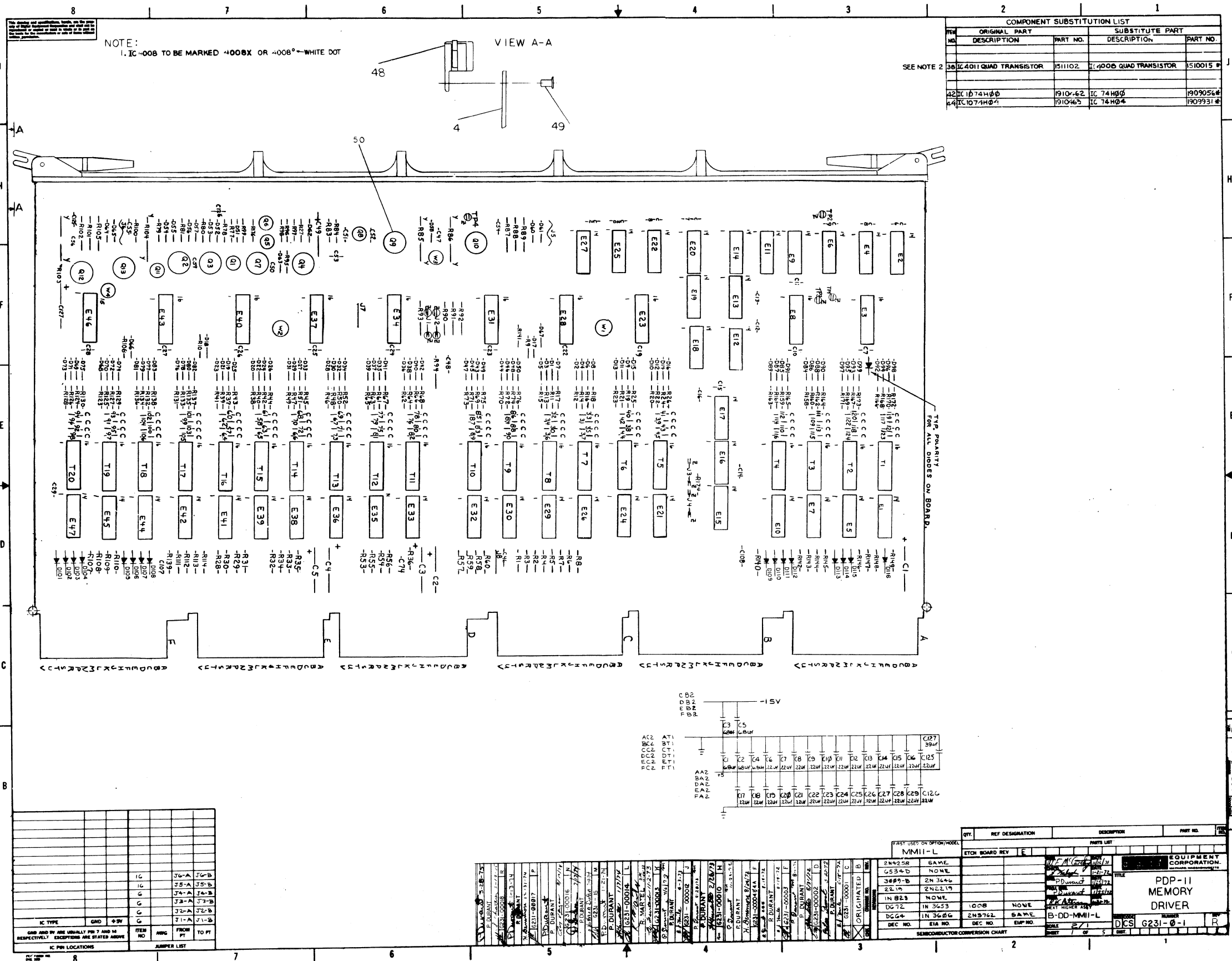
FIRST USED ON OPTION/MODEL	QTY.	DESCRIPTION	PART NO.	ITEM NO.
MM11-L				
PARTS LIST				
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES. TOLERANCES:		DRN J. Carney	DATE 12/3/71	digital EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS
DECIMALS .005		CHKD J. Carney	DATE 1-27-72	
ANGLES ±0° 30'		ENG P. Duvant	DATE 1-25-72	TITLE TIMING DIAGRAM
.XX = .02		PROJ. ENG. P. Duvant	DATE 1-25-72	
X = .1		PROD. R. Peterson	DATE 1-27-72	NUMBER (MM11-L, MM11-K)
REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY				
MATERIAL	NEXT HIGHER ASSY.	SIZE CODE		
	B-DD-MM11-L	D TD MM11-L-3		
FINISH	SCALE	NUMBER		
	SHEET 1 OF 1	REV.		

8 7 6 5 4 3 2 1

DEC FORM NO. DRD 109-B

MM11-L-3





NOTE:  
1. IC -008 TO BE MARKED -008X OR -008° - WHITE DOT

VIEW A-A

SEE NOTE 2

COMPONENT SUBSTITUTION LIST			
ITEM NO.	ORIGINAL PART DESCRIPTION	PART NO.	SUBSTITUTE PART DESCRIPTION
38	IC 4011 QUAD TRANSISTOR	151102	IC 4000 QUAD TRANSISTOR
42	IC 7414	1910-62	IC 7414
44	IC 7414	1910-65	IC 7414

FOR ALL PARTS ON BOARD

IC PIN LOCATIONS	JUMPER LIST
IC TYPE	FROM PT TO PT
IC PIN LOCATIONS	JUMPER LIST

ITEM NO.	AVG	FROM PT	TO PT
1		J6-A	J6-B
2		J5-A	J5-B
3		J4-A	J4-B
4		J3-A	J3-B
5		J2-A	J2-B
6		J1-A	J1-B

ITEM NO.	AVG	FROM PT	TO PT
1		J6-A	J6-B
2		J5-A	J5-B
3		J4-A	J4-B
4		J3-A	J3-B
5		J2-A	J2-B
6		J1-A	J1-B

QTY.	REF DESIGNATION	DESCRIPTION	PART NO.
1	E1	IC 7414	1910-62
1	E2	IC 7414	1910-62
1	E3	IC 7414	1910-62
1	E4	IC 7414	1910-62
1	E5	IC 7414	1910-62
1	E6	IC 7414	1910-62
1	E7	IC 7414	1910-62
1	E8	IC 7414	1910-62
1	E9	IC 7414	1910-62
1	E10	IC 7414	1910-62
1	E11	IC 7414	1910-62
1	E12	IC 7414	1910-62
1	E13	IC 7414	1910-62
1	E14	IC 7414	1910-62
1	E15	IC 7414	1910-62

DCS G231-0-1 R

PDP-11  
MEMORY  
DRIVER

DCS G231-0-1 R

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DCS G231-0-1 R

QTY	REF DESIGNATION	DESCRIPTION	PART NO.	ITEM NO.	QTY	REF DESIGNATION	DESCRIPTION	PART NO.	ITEM NO.
4	C127	SCREW, NYLON, #6 - 32	9008212	55	REF	X-Y COORDINATE HOLE LOCATION	K-CO-G231-04	1	
1	C56	CAP., 39 UF, 10V	1000076	56	REF	ASSY DRILLING HOLE LAYOUT	E-4H-G231-05	2	
1	C52	CAP., 680 PF, 100V, 5%	1000026	57	REF	MODULE ECD HISTORY	P-4H-G231-06	3	
1	J7, J8	CAP., 470 PF, 100V, 5%	1000024	58	1	ETCHED CIRCUIT BOARD	5089708	4	
2	R176	JUMPER MACH. INSERTED	9009185	59	2	C51, C55	CAP., 39PF, 100V, 5% DM	1009010	5
1	J5, J6	RES., 4.7K, 1/4W, 5%	1300447	60	A/R	WIRE JUMPER AWG. #22 SOLID	9107560-01	6	
2		#22 AWG., STRANDED TEFLON INSULATED	1700035-00	61	80	C30-C45, C57, C58, C60-C73, C75-C106, C109-C124	CAP., 1000PF, 100, 5% DM	1000042	7
					36	C6-C29, C46, C47, C48, C50, C53, C54, C74, C107, C108, C125, C126, C59	CAP., 22UF, 50V, -20%, 80%	1010274	8
					6	C1-C5, C49	CAP., 6.8 MFD, 35V, 20%	1000067	9
					88	D1-D16, D19-D54, D56, D57, D62, D63, D68-D99	DIODE, D664	1100114	10
					25	D17, D18, D55, D60, D61, D64-D67, D101-D116	DIODE, D672	1105275	11
					1	D58	DIODE, 1N823, ZENOR	1105508	12
					41	R1-R8, R28-R35, R53-R60, R96, R107-R114, R142-R149	RES., 150, 1/4W, 5%	1300250	14
					3	R70, R80, R98	RES., 330, 1/4W, 5%	1300285	15
					1	Q6	TRANS, 6534C	1503409-2	16
					1	R93	RES., 470, 1/4W, 5%	1300315	17
					3	R84, R99, R100	RES., 1.5K, 1/4W, 5%	1300391	18
					1	R79	RES., 470, 1/4W, 10%	1300317	19
					4	R87, R88, R101, R102	RES., 1.5K, 1/4W, 5%	1300394	20
					1	R95	RES., 2.2K, 1/4W, 5%	1300417	21
					1	R174	RES., 10K, 1/4W, 5%	1300479	22
					1	R97	RES., 15K, 1/4W, 5%	1300496	23
					3	R36, R139, R140	RES., 5.1, 1/4W, 5%	1309422	24
					1	R83	RES., 750, 1/4W, 5%	1301401	25
					4	R9, R10, R106, R141	RES., 680, 1/4W, 5%	1301424	26
					2	R89, R105	RES., 2K, 1/4W, 5%	1302387	27
					2	R90, R94	RES., 147, 1/8W, 1%	1302874	28
					1	R91	RES., 909, 1/8W, 1%	1302685	29
					1	R92	RES., 1.78K, 1/8W, 1%	1302612	30
					4	R85, R86, R 104, R103	RES., 16.9, 6W, 1%	1310032	31
					1	Q4	TRANSISTOR, 2219	1501881	32
					3	Q2, Q3, Q7	TRANSISTOR, 1008	1502155	33
					1	Q5	TRANSISTOR, 3009 B	1503100	34
					1	Q1	TRANSISTOR, 6534 D	1503409	35
					2	Q8, Q11	TRANSISTOR, 2N4258	1505321	36
					4	Q9, Q10, Q12, Q13	TRANSISTOR, 2N3762	1509649	37
					20	E1, E5, E7, E10, E21, E24, E26, E29, E30, E32, E33, E35, E36, E38, E39, E41, E42, E44, E45, E47	IC, 4011 QUAD TRANSISTOR	1511102 *	38
					4	E9, E15, E16, E17	IC, DEC, 8640	1911469	39
					20	T1-T20	TRANSFORMER	1609996	40
					2	E22, E25	IC, 74H10	1909057	41
					3	E2, E4, E27	IC, 1074H00	1918462 *	42
					10	E3, E8, E23, E28, E31, E34, E37, E40, E43, E46	IC, 8251-1	1909854	43
					1	L6	IC, 1074H04	1910463 *	44
					7	E11-E14, E18, E19, E20	IC, 74H74	1909667	45
					4		HEAT SINK	1210001	46
					12		SPLIT LUGS	9006735	47
					1		HANDLE	1210711-02	48
					12		EYELET, #GS4-7, E B, STIMPSON	9006732	49
					4		TRANSIPAD	9007200	50
									51
									52
					83	R11-R26, R37-R52, R61-R76, R123-R138, R158-R173, R77, R81, R27	RES., 100, 1/4W, 5%	1300229	53
					4	W1-W4	STANDOFF THREADED, INSULATED 1/4 X 3/8 LG.	9008213	54

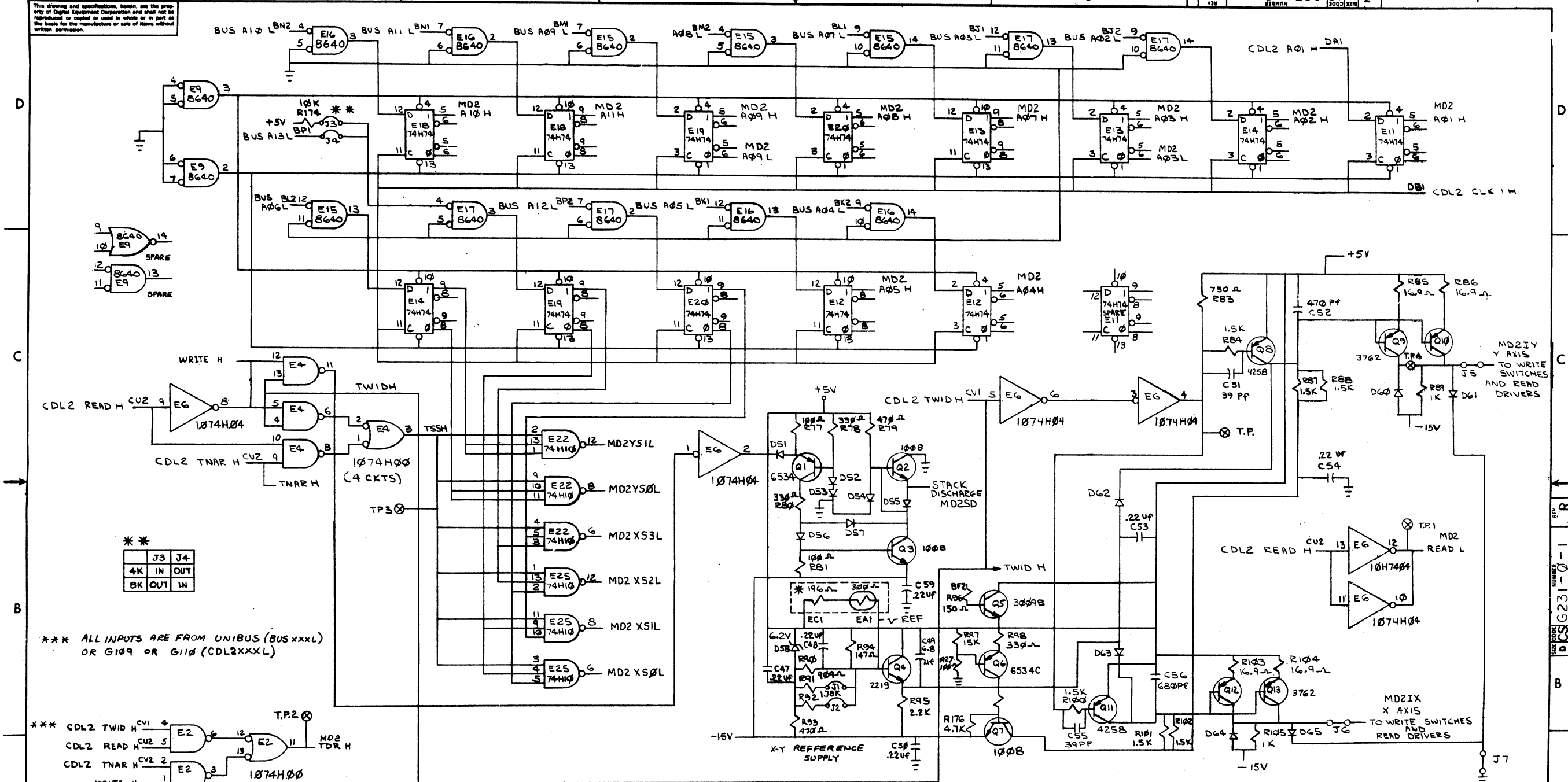
REVISIONS		
CHK	CHANGE NO.	REV.

						TITLE		PDP-11		SIZE CODE		NUMBER		REV.	
						MEMORY DRIVER		DCS		G231-0-1		R			
						SCALE		SHEET 2 OF 5		DIST.					

REV. NUMBER DCS G231-0-1 R

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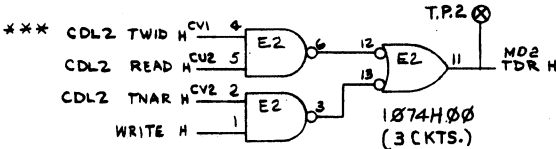
1-0-1225 SQ 2



\*\*\*

J3	J4
4K	IN
BK	OUT
	IN

\*\*\* ALL INPUTS ARE FROM UNIBUS (BUS XXXL) OR G109 OR G110 (CDL2XXXL)



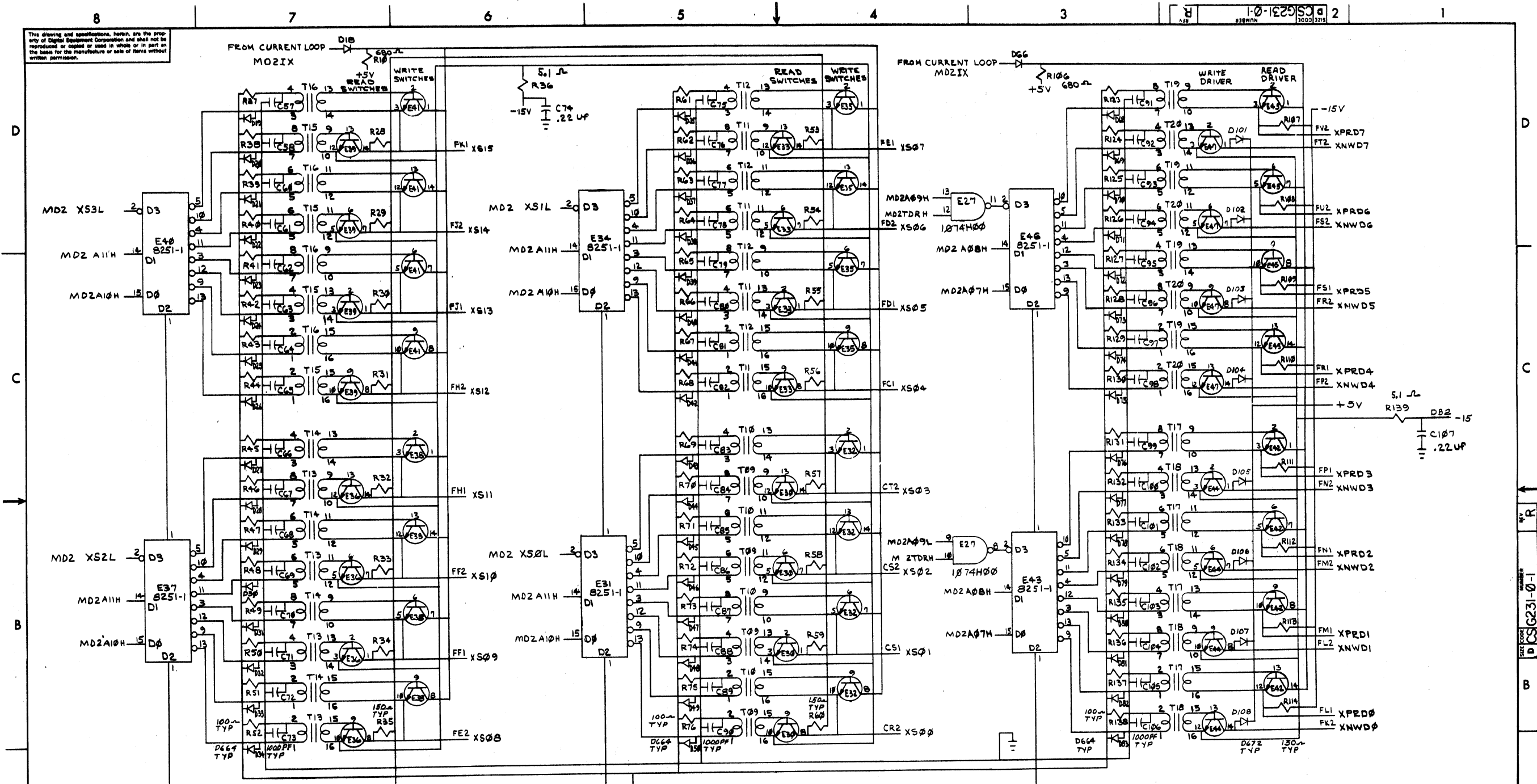
\* THIS CIRCUIT IS ON STACK BOARD

FIRST USED ON OPTION/MODEL	QTY.	DESCRIPTION	PART NO.	ITEM NO.
PARTS LIST				
UNLESS OTHERWISE SPECIFIED, DIMENSION IN INCHES				
TOLERANCES				
DECIMALS	ANGLES	DATE 9-29-71		
XXX - .006	±0° 30'	DATE 1-21-72		
.XX - .02		DATE 1-25-72		
.X - .1		DATE 1-25-72		
REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY				
MATERIAL				
NEXT HIGHER ASSY.				
FINISH				
SCALE		SIZE CODE		REV.
SHEET 3 OF 5		DCS G231-0-1		R
DIST.				

REVISIONS

NO.	DATE	DESCRIPTION
1		
2		
3		
4		
5		
6		
7		
8		

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ALL INPUTS FROM G231 SHEET 2 (MD2)  
ALL OUTPUTS GO TO MEMORY STACK

FIRST USED ON OPTION/MODEL	QTY.	DESCRIPTION	PART NO.	ITEM NO.
PARTS LIST				
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES TOLERANCES	DRN 2	DATE 10-1-71	 <b>DIGITAL EQUIPMENT CORPORATION</b> <small>MAYNARD MASSACHUSETTS</small>	
DECIMALS	ENG. P. D. V. P. A. S.	DATE 1-21-72		
ANGLES	PROJ. ENG. P. D. V. P. A. S.	DATE 1-25-72		
XXX - .006 XX - .02 X - .1	PROD. R. K. P. A. S.	DATE 1-26-72		
REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY			TITLE <b>PDP-11 MEMORY DRIVER</b>	
MATERIAL	NEXT HIGHER ASSY.		SIZE CODE DCS	NUMBER G231-0-1
FINISH	SCALE			REV. R
	SHEET	4 OF 5	DIST.	

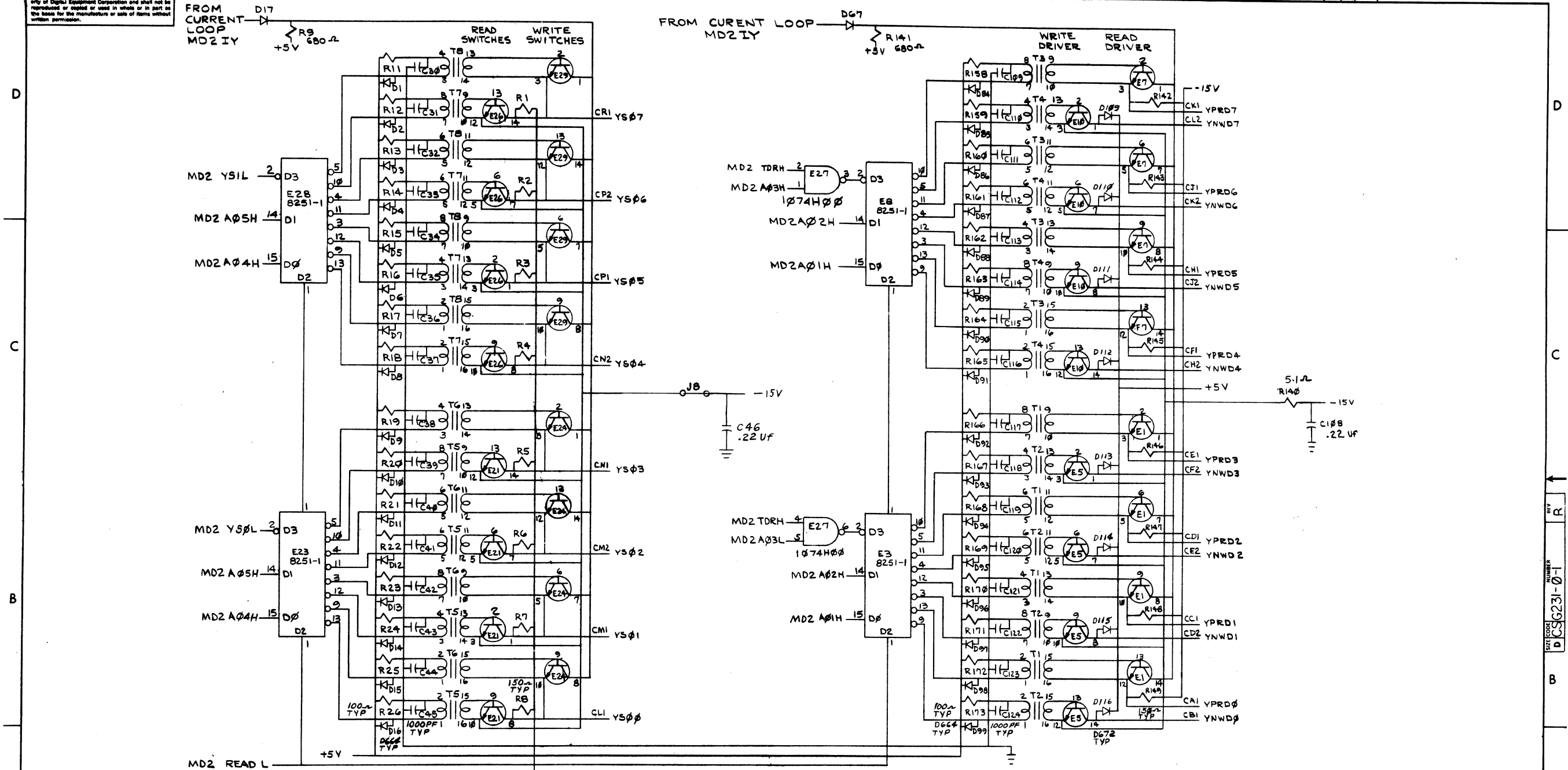
REV.	
CHANGE NO.	
CHK	

DEC FORM NO. DRD 102-B

DCS G231-0-1

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0-102950 2

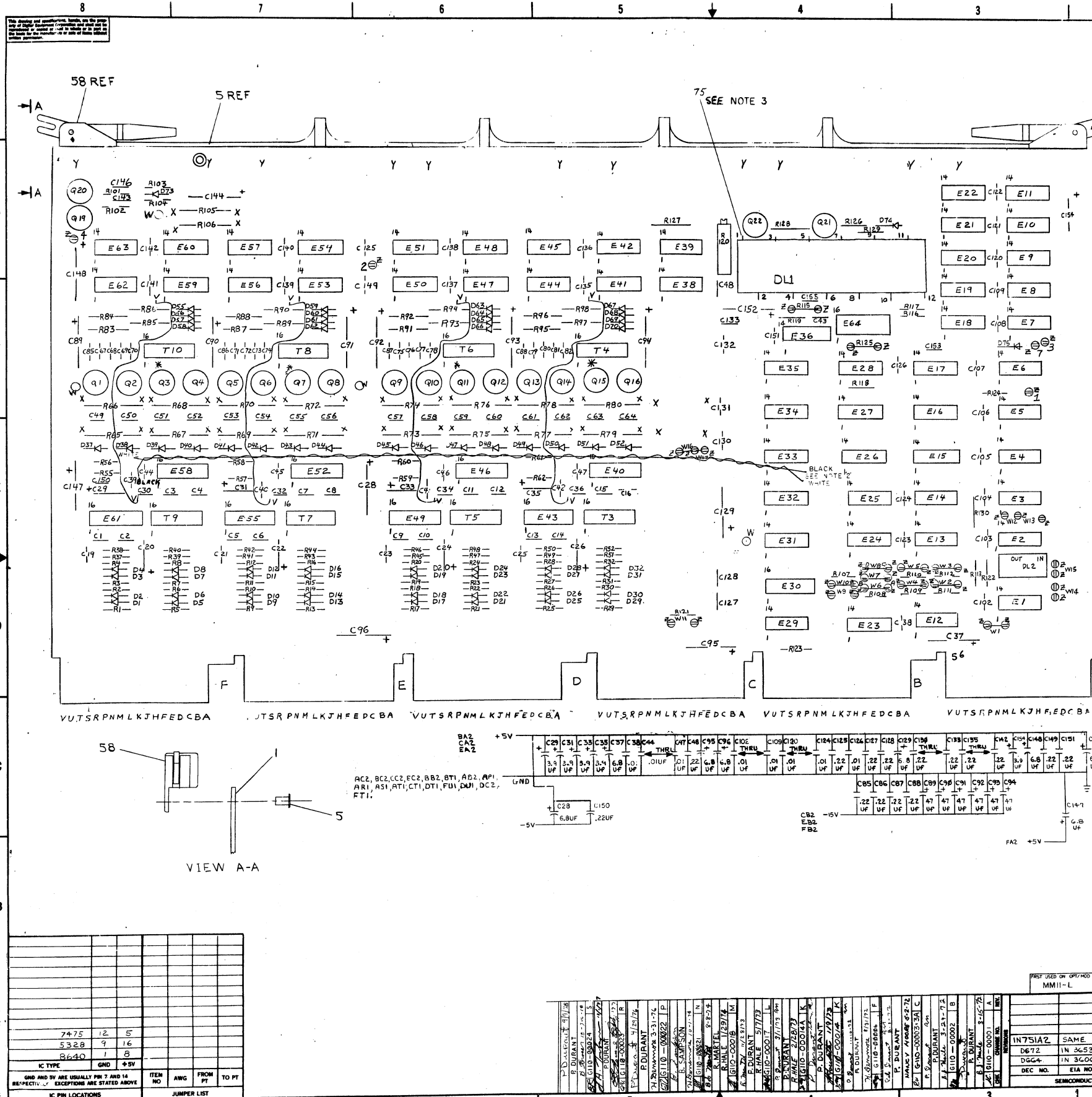


MD2 SD STACK DISCHARGE  
 ALL INPUTS FROM G231 SHEET 2 (MD2)  
 ALL OUTPUTS TO MEMORY STACK

REV	
CHANGE NO	
CHK	

FIRST USED ON OPTION/MODEL	QTY.	DESCRIPTION	PART NO.	ITEM NO.
PARTS LIST				
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES.		DRN <i>R. Carberry</i>	DATE 10/3/71	<b>digital</b> EQUIPMENT CORPORATION MAYNARD MASSACHUSETTS
TOLERANCES		CHKD <i>R. Carberry</i>	DATE 1-21-72	
DECIMALS	ANGLES	ENG. <i>R. Carberry</i>	DATE 1-25-72	TITLE <b>PDP-11 MEMORY DRIVER</b>
.xxx - .006	±0° 30'	PROJ. ENG. <i>R.D. Warrat</i>	DATE 1-25-72	
.xx - .02		PROD. <i>R. Carberry</i>	DATE 1-26-72	
REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY ✓				
MATERIAL	NEXT HIGHER ASSY.		SIZE CODE	NUMBER
FINISH	SCALE		DCS	G231-0-1
SHEET 5 OF 5		DIST.		REV R





ORIGINAL PART	PART NO.	SUBSTITUTE PART	PART NO.
10010001	10010001	10010001	10010001
10010002	10010002	10010002	10010002

**NOTES:**  
 1. R115 AND R125 MAY BE CHANGED AT FINAL TEST. RESISTANCE RANGE WILL BE FROM 5.11K TO 14.7K.  
 2. TO TWISTED WIRE PAIR WILL RUN UNDER THE THREE 66K. WHT. JUMPER WIRES THE RIN SHOULD BE LAYED BETWEEN THE LINE OF D6725 C89 THROUGH D52 AND THE LINE OF 4.95K R112, R126, R28, R62 AND R61.  
 3. USE SLEEVING 310728-00 OVER PIN1 OF DLI  
 4. DEC 74381 (191123-01) ACCEPTABLE SUBSTITUTION FOR 74381.  
 5. TRANSFADS USED UNDER Q1 THRU Q16, Q19, Q22.

QTY.	REF DESIGNATION	DESCRIPTION	PART NO.
1	E33	IC DEC 74140	190888
1	E34	IC DEC 7415	190889
1	E35	IC DEC 7409	190890
1	E11	IC DEC 74121	190891
1	E10	IC DEC 74121	190892
1	E9	IC DEC 74121	190893
1	E8	IC DEC 74121	190894
1	E7	IC DEC 74121	190895
1	E6	IC DEC 74121	190896
1	E5	IC DEC 74121	190897
1	E4	IC DEC 74121	190898
1	E3	IC DEC 74121	190899
1	E2	IC DEC 74121	190900
1	E1	IC DEC 74121	190901

QTY.	REF DESIGNATION	DESCRIPTION	PART NO.
2	E21	IC DEC 74121	190892
2	E22	IC DEC 74121	190893
2	E23	IC DEC 74121	190894
2	E24	IC DEC 74121	190895
2	E25	IC DEC 74121	190896
2	E26	IC DEC 74121	190897
2	E27	IC DEC 74121	190898
2	E28	IC DEC 74121	190899
2	E29	IC DEC 74121	190900
2	E30	IC DEC 74121	190901
2	E31	IC DEC 74121	190902
2	E32	IC DEC 74121	190903
2	E33	IC DEC 74121	190904
2	E34	IC DEC 74121	190905
2	E35	IC DEC 74121	190906

QTY.	REF DESIGNATION	DESCRIPTION	PART NO.
1	E1	IC DEC 74121	190901
1	E2	IC DEC 74121	190902
1	E3	IC DEC 74121	190903
1	E4	IC DEC 74121	190904
1	E5	IC DEC 74121	190905
1	E6	IC DEC 74121	190906
1	E7	IC DEC 74121	190907
1	E8	IC DEC 74121	190908
1	E9	IC DEC 74121	190909
1	E10	IC DEC 74121	190910

ETCH BOARD REV 1

IN751A2 SAME

D672 IN 3653

D644 IN 3604

DEC NO. EIA NO.

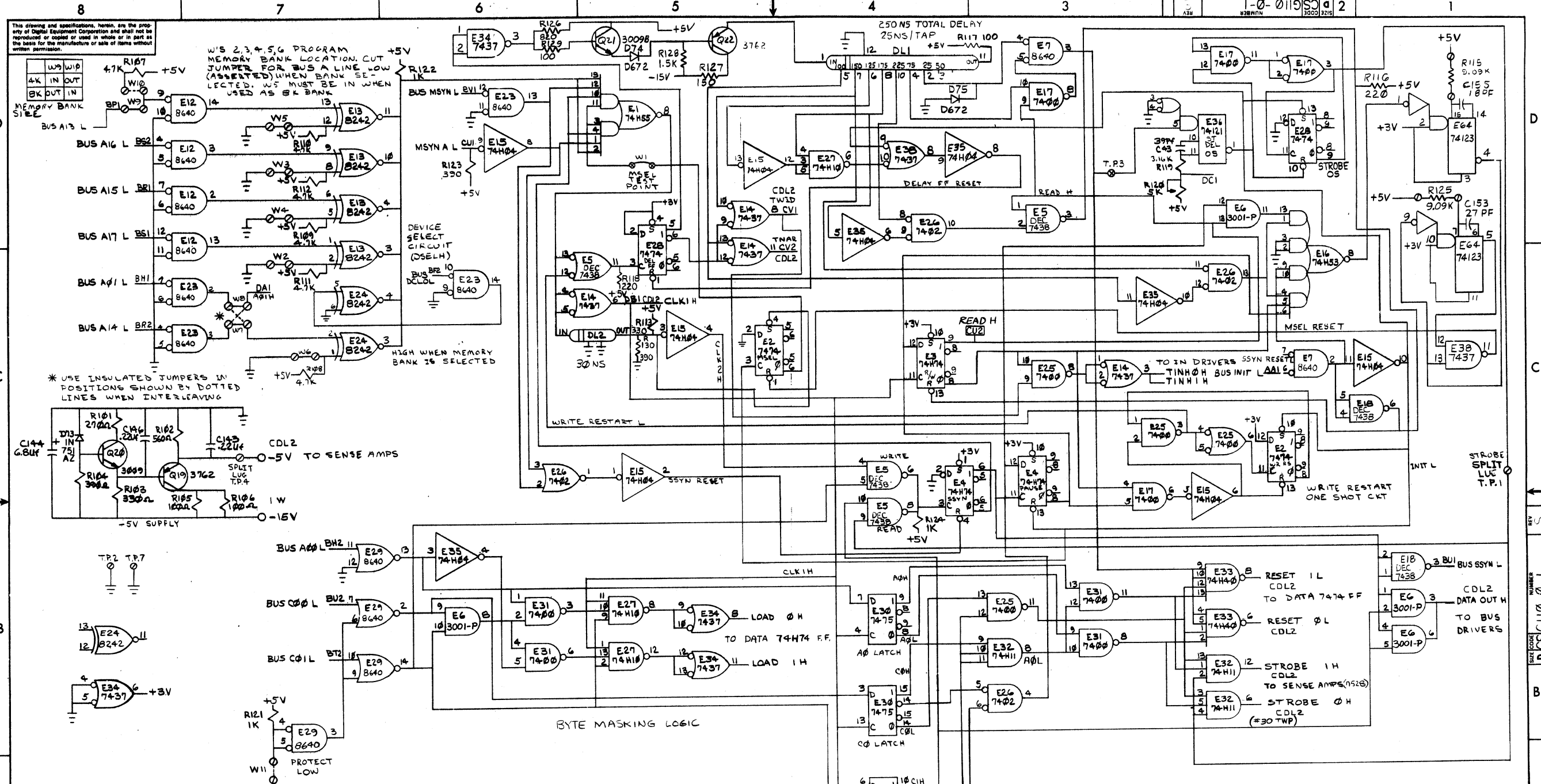
EQUIPMENT CORPORATION

CONTROL & DATA LOOPS

C.D.L. 1

SCALE 2/1

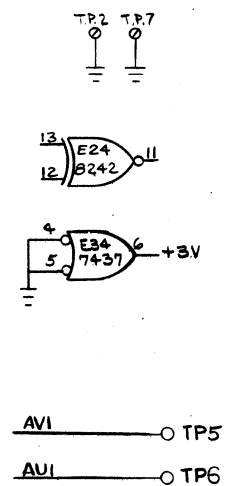
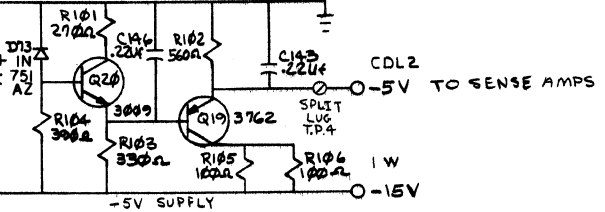
SEMICONDUCTOR CONVERSION CHART



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W15, 2, 3, 4, 5, 6 PROGRAM MEMORY BANK LOCATION. CUT JUMPER FOR BUS A LINE LOW (ASSEMBLED) WHEN BANK SELECTED. W5 MUST BE IN WHEN USED AS B BANK.

\* USE INSULATED JUMPERS IN POSITIONS SHOWN BY DOTTED LINES WHEN INTERLEAVING



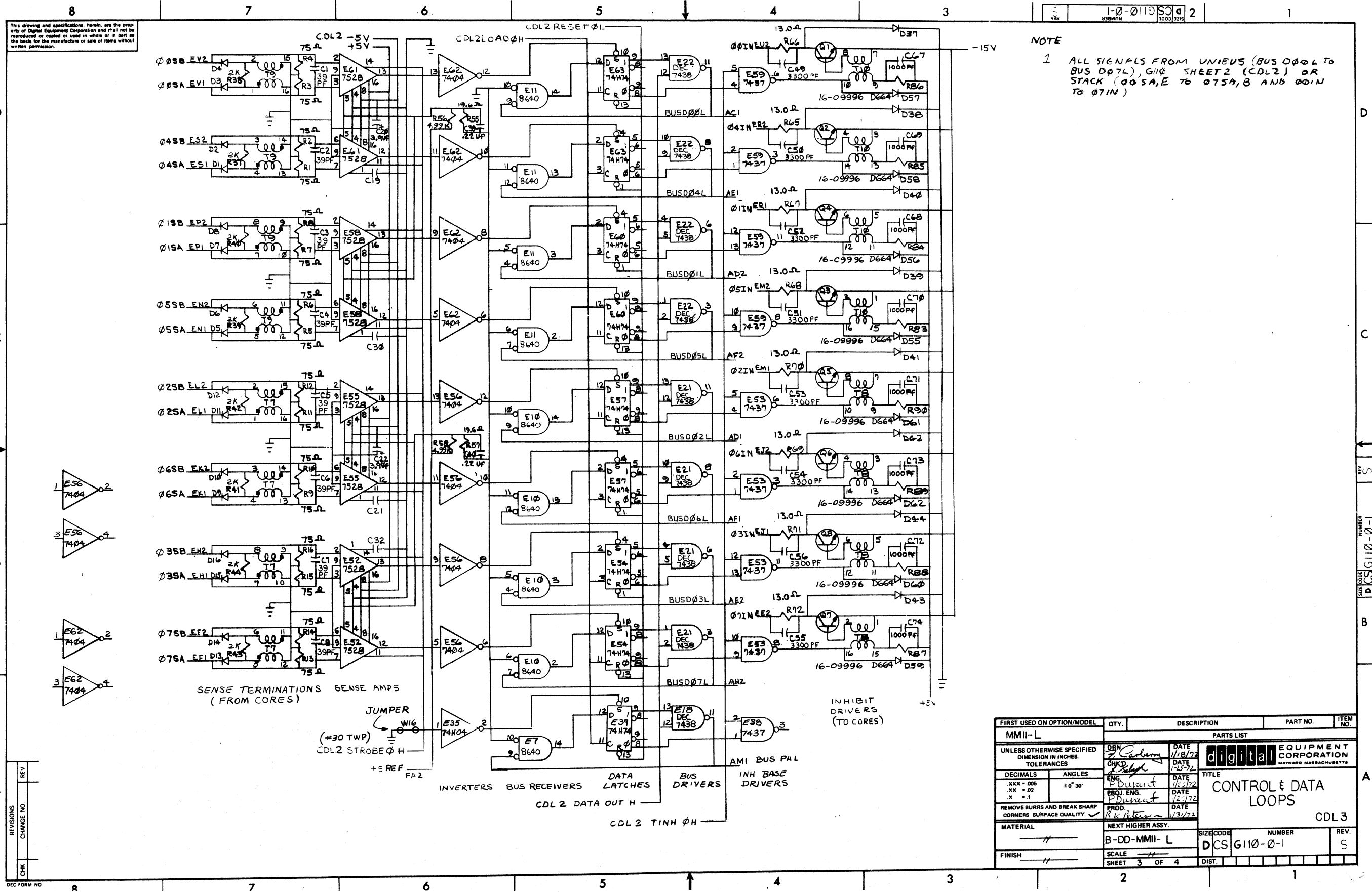
	W12	W13	W14	W15	W16	W17
G110	OUT	OUT	OUT	OUT	IN	IN

FIRST USED ON OPTION/MODEL	QTY.	DESCRIPTION	PART NO.	ITEM NO.
MMII-L		PARTS LIST		
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES TOLERANCES	DRAWN <i>J. Carberry</i> DATE 1/25/72	DIGITAL EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS		
DECIMALS .XXX = .005 .XX = .02 .X = .1	CHKD <i>P. Duvant</i> DATE 1-25-72	TITLE CONTROL & DATA LOOPS		
REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY	ENG. <i>P. Duvant</i> DATE 1/25/72	CDL 2		
MATERIAL	PROO. <i>K.K. Reiman</i> DATE 1-21-72	NEXT HIGHER ASSY.		
FINISH		B-DD-MMII-L	SIZE CODE DCS G110-0-1	NUMBER 1
		SCALE	DIST.	REV. S
		SHEET 2 OF 4		

REVISIONS	REV.
CHANGE NO.	
CHK	

DEC FORM NO





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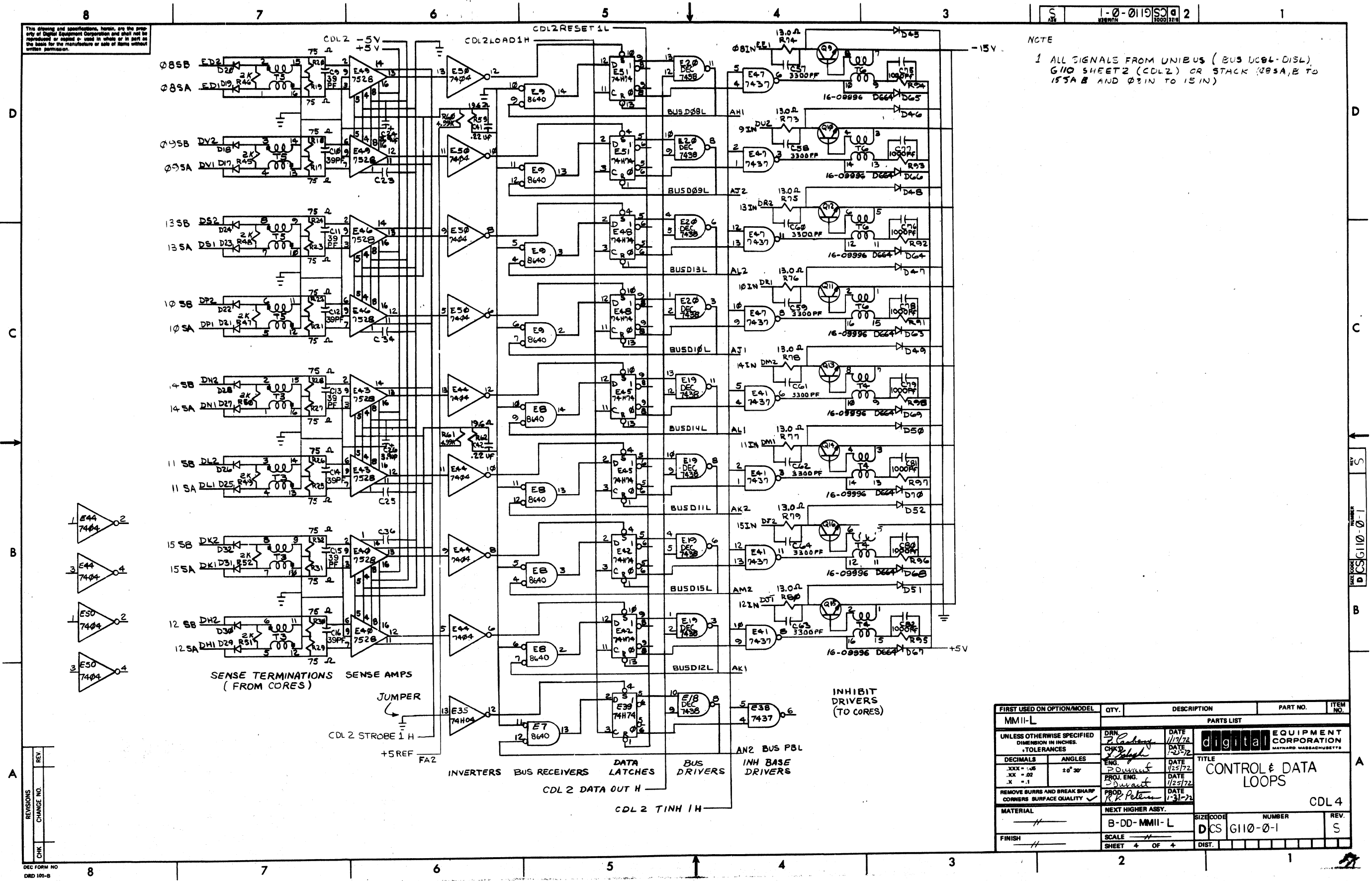
NOTE  
 1 ALL SIGNALS FROM UNIBUS (BUS D00L TO BUS D07L), GI08 SHEET 2 (CDL2) OR STACK (005A,E TO 075A,B AND 001N TO 071N)

REVISIONS	NO.	REV.
CHK	CHANGE NO.	
DEC FORM NO.		

FIRST USED ON OPTION/MODEL	QTY.	DESCRIPTION	PART NO.	ITEM NO.
MMII-L				
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES. TOLERANCES		DATE 1/18/72	PARTS LIST	
DECIMALS .XXX = .005	ANGLES ±0° 30'	CHKD 2/25/72	digital EQUIPMENT CORPORATION	
.XX = .02		ENG. P. Durant	MAYNARD MASSACHUSETTS	
.X = .1		PROJ. ENG. P. Durant	TITLE	
REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY		PROD. R. K. Peterson	CONTROL & DATA LOOPS	
MATERIAL	NEXT HIGHER ASSY.	DATE 1/31/72	CDL3	
FINISH	B-DD-MMII-L	SIZE CODE	NUMBER	REV.
	SCALE	DCS G110-0-1		S
	SHEET 3 OF 4	DIST.		

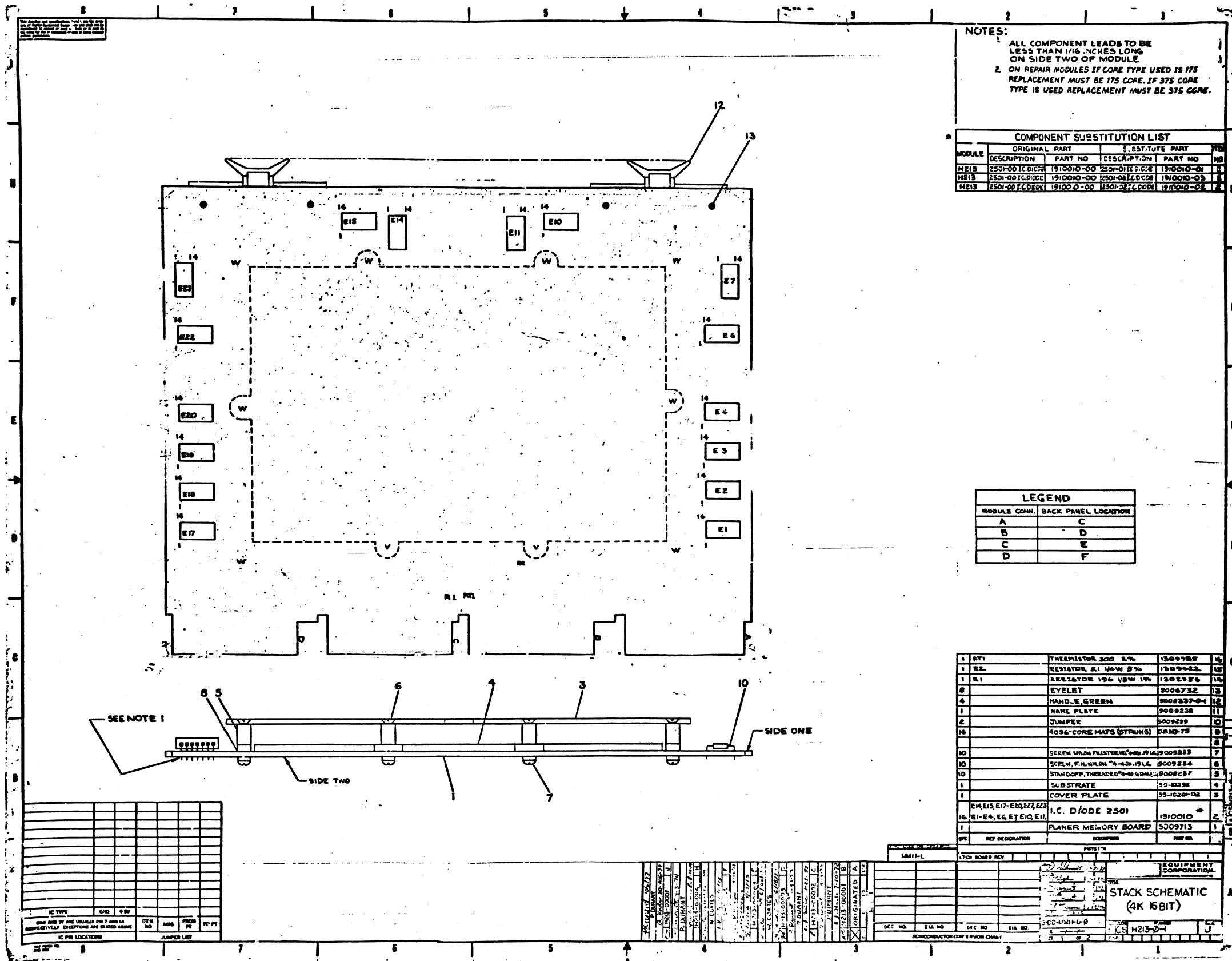
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NOTE  
 1 ALL SIGNALS FROM UNIBUS (BUS UC04-DISL), G110 SHEET 2 (CDL2 OR STACK (085A, B TO 155A B AND 03IN TO 15IN))



REV.	NO.
CHK	NO.
DEC FORM NO. DRD 101-B	

FIRST USED ON OPTION/MODEL	QTY.	DESCRIPTION	PART NO.	ITEM NO.
MM11-L				
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES. TOLERANCES				
DECIMALS	ANGLES	TITLE		
.XXX = .05	±0°30'	CONTROL & DATA LOOPS		
.XX = .02		CDL 4		
.X = .1				
REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY				
MATERIAL		NEXT HIGHER ASSY.	SIZE CODE	NUMBER
		B-DD-MM11-L	DCS	G110-0-1
FINISH		SCALE	SHEET	OF
			4	4
		DIST.		



**NOTES:**  
 1. ALL COMPONENT LEADS TO BE LESS THAN 1/16 INCHES LONG ON SIDE TWO OF MODULE  
 2. ON REPAIR MODULES IF CORE TYPE USED IS 175 REPLACEMENT MUST BE 175 CORE. IF 375 CORE TYPE IS USED REPLACEMENT MUST BE 375 CORE.

COMPONENT SUBSTITUTION LIST				
MODULE	ORIGINAL PART DESCRIPTION	PART NO	SUBSTITUTE PART DESCRIPTION	PART NO
M213	2501-00 I.C. DIODE	1910010-00	2501-01 I.C. DIODE	1910010-01
M213	2501-00 I.C. DIODE	1910010-00	2501-02 I.C. DIODE	1910010-02
M213	2501-00 I.C. DIODE	1910010-00	2501-03 I.C. DIODE	1910010-03

LEGEND	
MODULE CONN.	BACK PANEL LOCATION
A	C
B	D
C	E
D	F

1	E17	THERMISTOR 300 5%	1300182	14
1	R2	RESISTOR 51 1/4W 5%	1300422	17
1	R1	RESISTOR 150 1/4W 1%	1300276	16
8		EYELET	5006732	13
4		HANDLE GREEN	9008337-04	12
1		HANDLE PLATE	9008238	11
2		JUMPER	5008239	10
16		4096-CORE MATS (STRUNG)	DRM-75	9
				8
10		SCREEN MILN PLASTERING	9161909233	7
10		SCREEN P.H. NYLON 2-1/2" x 19 1/2"	9008234	6
10		STANDOFF, THERMOPLASTIC	9008237	5
1		SUBSTRATE	50-0296	4
1		COVER PLATE	55-1020-02	3
16	E15, E17, E20, E22, E23	I.C. DIODE 2501	1910010	2
1	E1-E4, E6, E7, E10, E11	PLANNER MEMORY BOARD	5009713	1

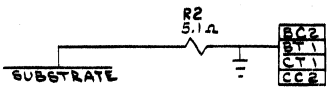
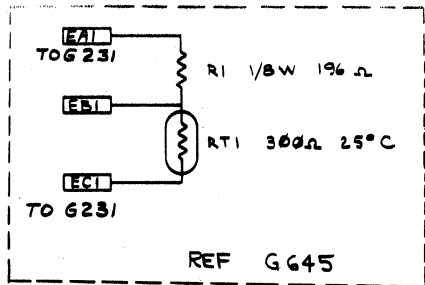
IC TYPE	QTY	POS	REF	ITEM NO	ANG	FROM	TO

REV	DATE	BY	CHKD	APP'D	DESCRIPTION
1					MM1-L

STACK SCHEMATIC  
(4K 16BIT)

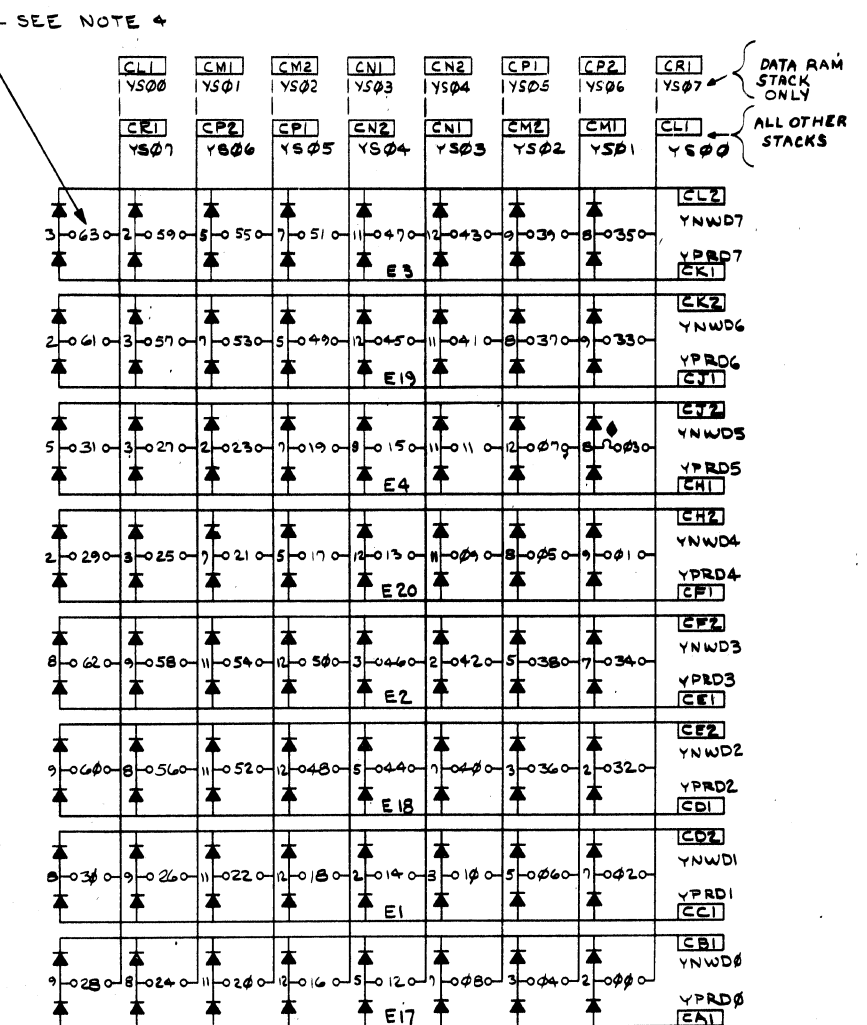
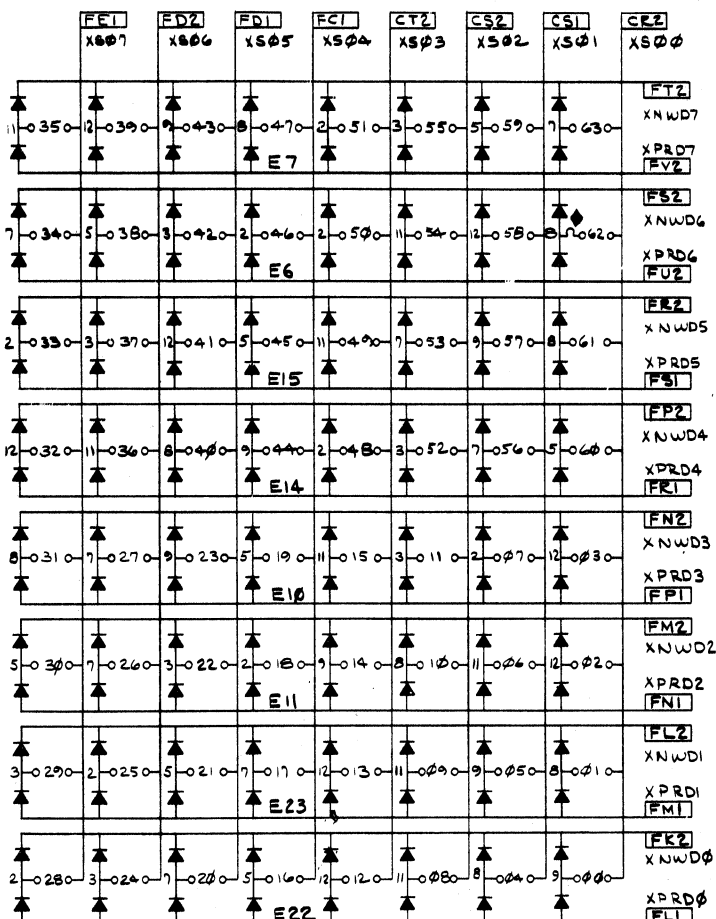
KS H213-D-1

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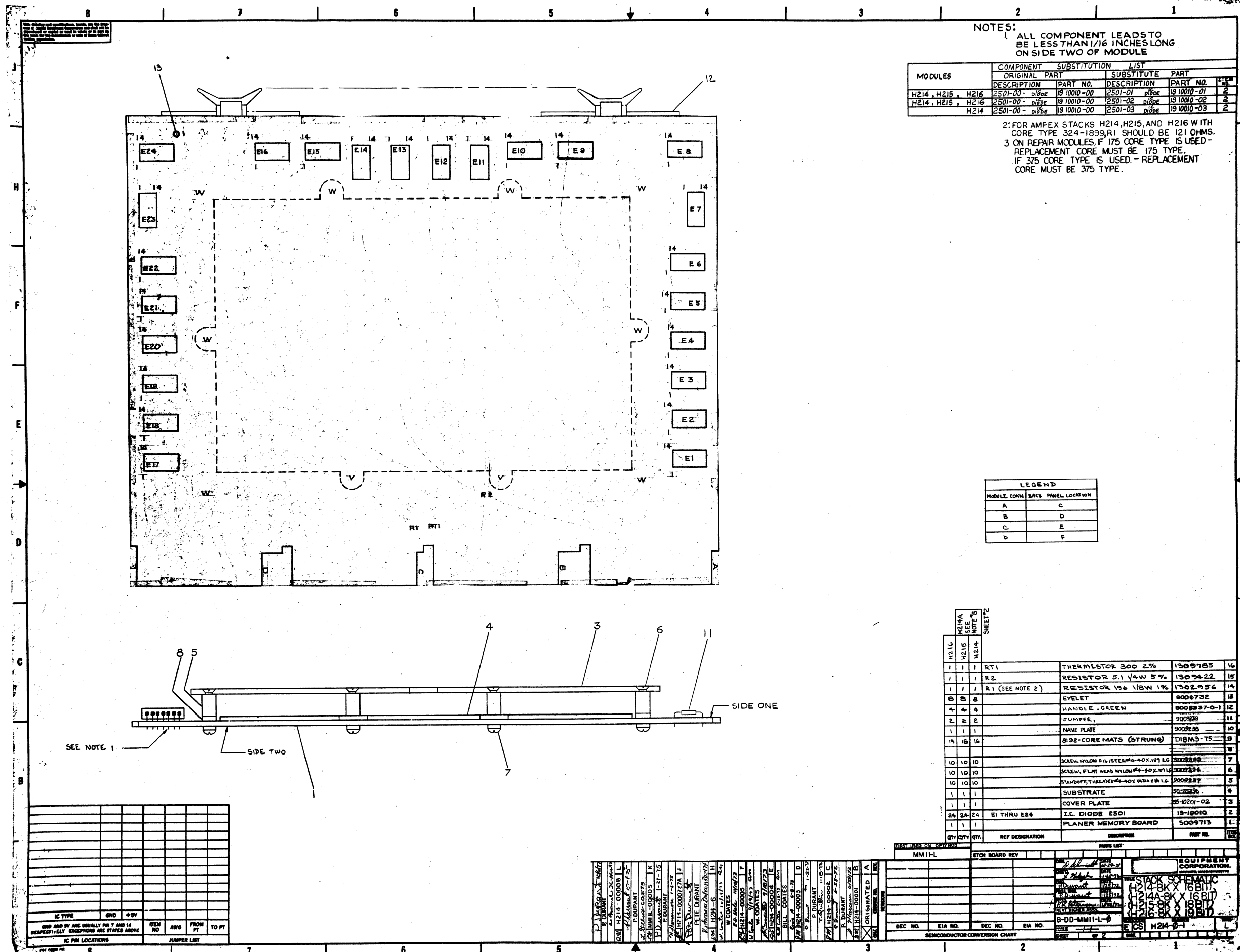
- NOTES  
 1. UNLESS OTHERWISE SPECIFIED:  
 ICS ARE TO BE DEC PART # 1910010  
 2. INDICATES STACK LINE NUMBER. (TYP)  
 3. INDICATES CURRENT LOOP.  
 4. INDICATES MAGNET WIRE TERMINATION (SOLDERED TO P.C. PAD).

- BIT 0: EA2, EB2, EC2, EV2, EV1, OSB, OSA
- BIT 4: EA2, EB2, EC2, EV2, EV1, OSB, OSA
- BIT 1: EA1, EB1, EC1, EV1, EV2, OSB, OSA
- BIT 5: EA2, EB2, EC2, EV2, EV1, OSB, OSA
- BIT 2: EA1, EB1, EC1, EV1, EV2, OSB, OSA
- BIT 6: EA2, EB2, EC2, EV2, EV1, OSB, OSA
- BIT 3: EA1, EB1, EC1, EV1, EV2, OSB, OSA
- BIT 7: EA2, EB2, EC2, EV2, EV1, OSB, OSA
- BIT 8: EA1, EB1, EC1, EV1, EV2, OSB, OSA
- BIT 9: EA2, EB2, EC2, EV2, EV1, OSB, OSA
- BIT 13: EA2, EB2, EC2, EV2, EV1, OSB, OSA
- BIT 0: EA1, EB1, EC1, EV1, EV2, OSB, OSA
- BIT 14: EA2, EB2, EC2, EV2, EV1, OSB, OSA
- BIT 11: EA1, EB1, EC1, EV1, EV2, OSB, OSA
- BIT 15: EA2, EB2, EC2, EV2, EV1, OSB, OSA
- BIT 12: EA1, EB1, EC1, EV1, EV2, OSB, OSA



REV.	CHANGE NO.

FIRST USED ON OPTION/MODEL	QTY.	DESCRIPTION	PART NO.	ITEM NO.
MM11-L				
PARTS LIST				
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES		DATE 12/1/71		
TOLERANCES		CHK'D. DATE 1-25-72		
DECIMALS	ANGLES	DATE 1-25-72		
.XX - .006	± 0° 30'	DATE 1-25-72		
.XX - .02		DATE 1-25-72		
X - .1		DATE 1-25-72		
REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY		DATE 1-26-72		
MATERIAL		NEXT HIGHER ASSY.		
FINISH		B-DD-MM11-L-0		
		SIZE CODE	NUMBER	REV.
		DCS H213-0-1		J
		SCALE		
		SHEET 2 OF 2		
		DIST.		

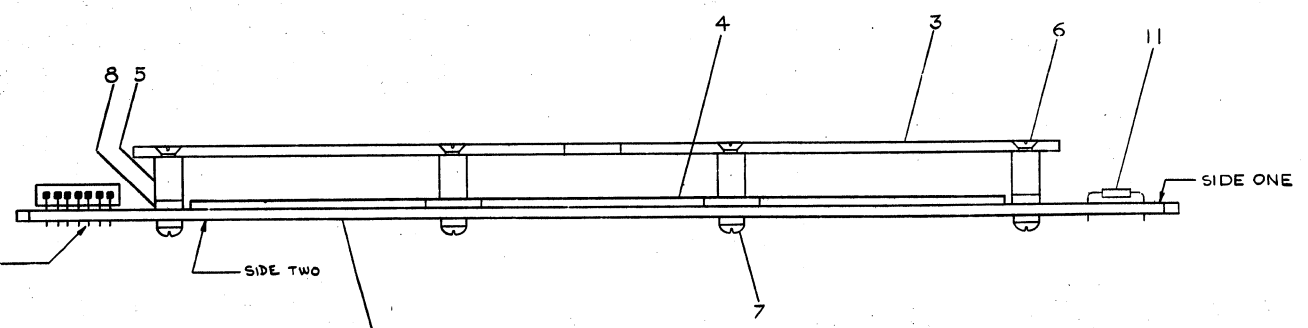


NOTES:  
 1. ALL COMPONENT LEADS TO BE LESS THAN 1/16 INCHES LONG ON SIDE TWO OF MODULE

MODULES	COMPONENT SUBSTITUTION LIST			
	ORIGINAL PART DESCRIPTION	PART NO.	SUBSTITUTE PART DESCRIPTION	PART NO.
H214, H215, H216	2501-00 - diode	19 10010-00	2501-01 diode	19 10010-01
H214, H215, H216	2501-00 - diode	19 10010-00	2501-02 diode	19 10010-02
H214, H215, H216	2501-00 - diode	19 10010-00	2501-03 diode	19 10010-03

2. FOR AMPLEX STACKS H214, H215, AND H216 WITH CORE TYPE 324-1899, R1 SHOULD BE 121 OHMS.  
 3. ON REPAIR MODULES, IF 175 CORE TYPE IS USED - REPLACEMENT CORE MUST BE 175 TYPE. IF 375 CORE TYPE IS USED - REPLACEMENT CORE MUST BE 375 TYPE.

LEGEND	
MODULE CONN.	BACK PANEL LOCATION
A	C
B	D
C	E
D	F



QTY	QTY	REF DESIGNATION	DESCRIPTION	PART NO.	REV
1	1	RT1	THERMISTOR 300 2%	1300785	16
1	1	R2	RESISTOR 5.1 1/4W 5%	1307422	15
1	1	R1 (SEE NOTE 2)	RESISTOR 196 1/8W 1%	1302956	14
4	4	B	EYELET	8006732	18
4	4	A	HANDLE, GREEN	800837-0-1	12
2	2	Z	JUMPER	907239	11
1	1	I	NAME PLATE	900238	10
1	1	16	BI92-CORE MATS (STRUNG)	DIBM3-75	9
10	10	10	SCREW NYLON FILISTEM 4-40X.197 LG	900239	7
10	10	10	SCREW, FLAT HEAD NYLON 4-40X.197 LG	900236	6
10	10	10	STANDOFF, THERMATED 4-40X.197 LG	900237	5
1	1	1	SUBSTRATE	50-70236	4
1	1	1	COVER PLATE	85-40201-02	3
24	24	E1 THRU E24	I.C. DIODE 2501	18-10010	2
1	1	1	PLANNER MEMORY BOARD	5009715	1

IC TYPE	QTY	QTY	QTY	QTY	QTY
IC PIN LOCATIONS	JUMPER LIST				

REV	DATE	BY	CHKD	DESCRIPTION
1				ORIGINAL
2				
3				
4				
5				
6				
7				
8				
9				
10				
11				
12				
13				
14				
15				
16				
17				
18				
19				
20				

MM11-L ETCH BOARD REV

DEC NO. EIA NO. DEC NO. EIA NO.

EQUIPMENT CORPORATION

STACK SCHEMATIC

1214-8K X 16BIT

1214A-8K X 16BIT

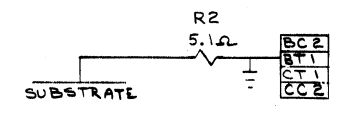
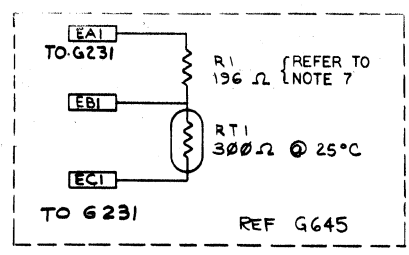
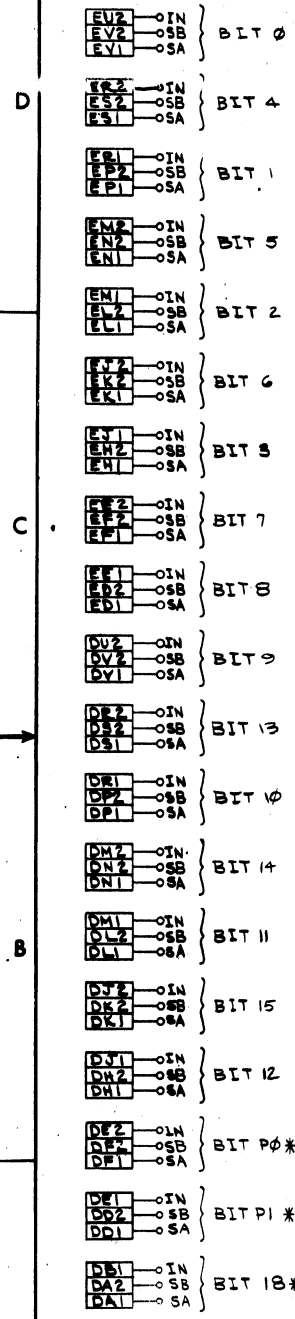
1215-8K X 16BIT

1215A-8K X 16BIT

B-DD-MM11-L-0

ECS H24-01

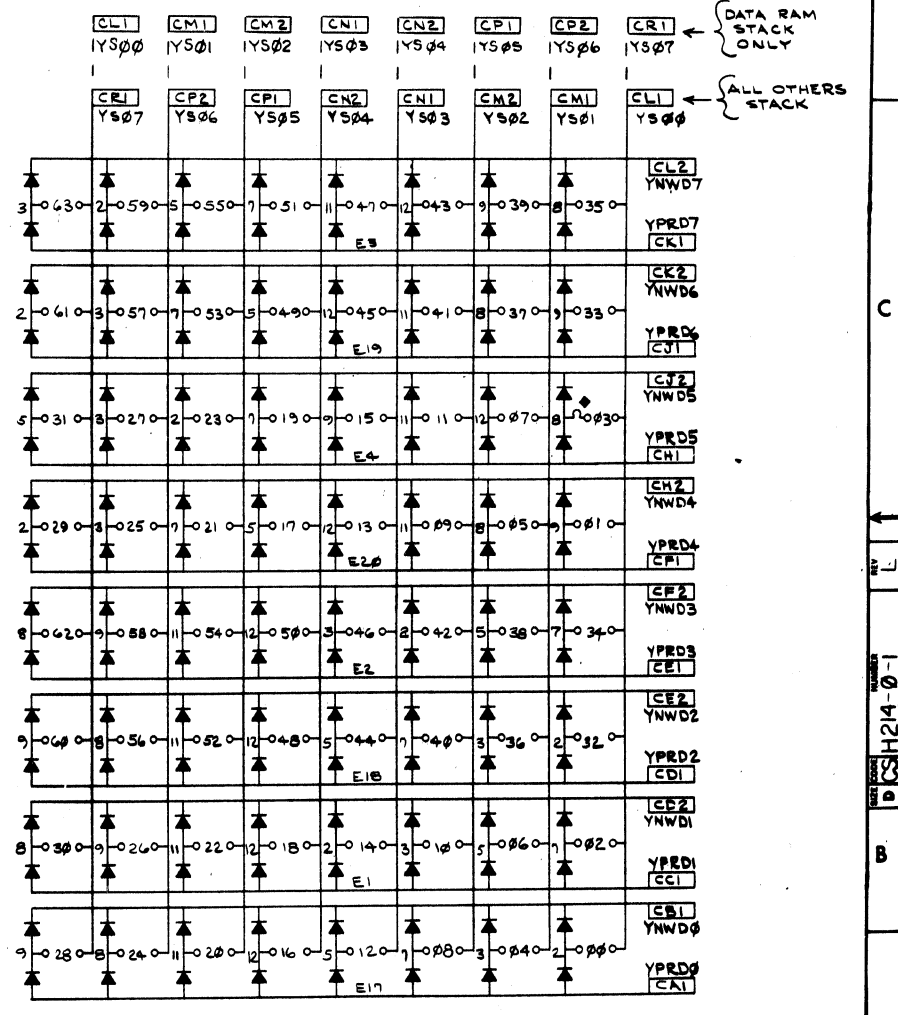
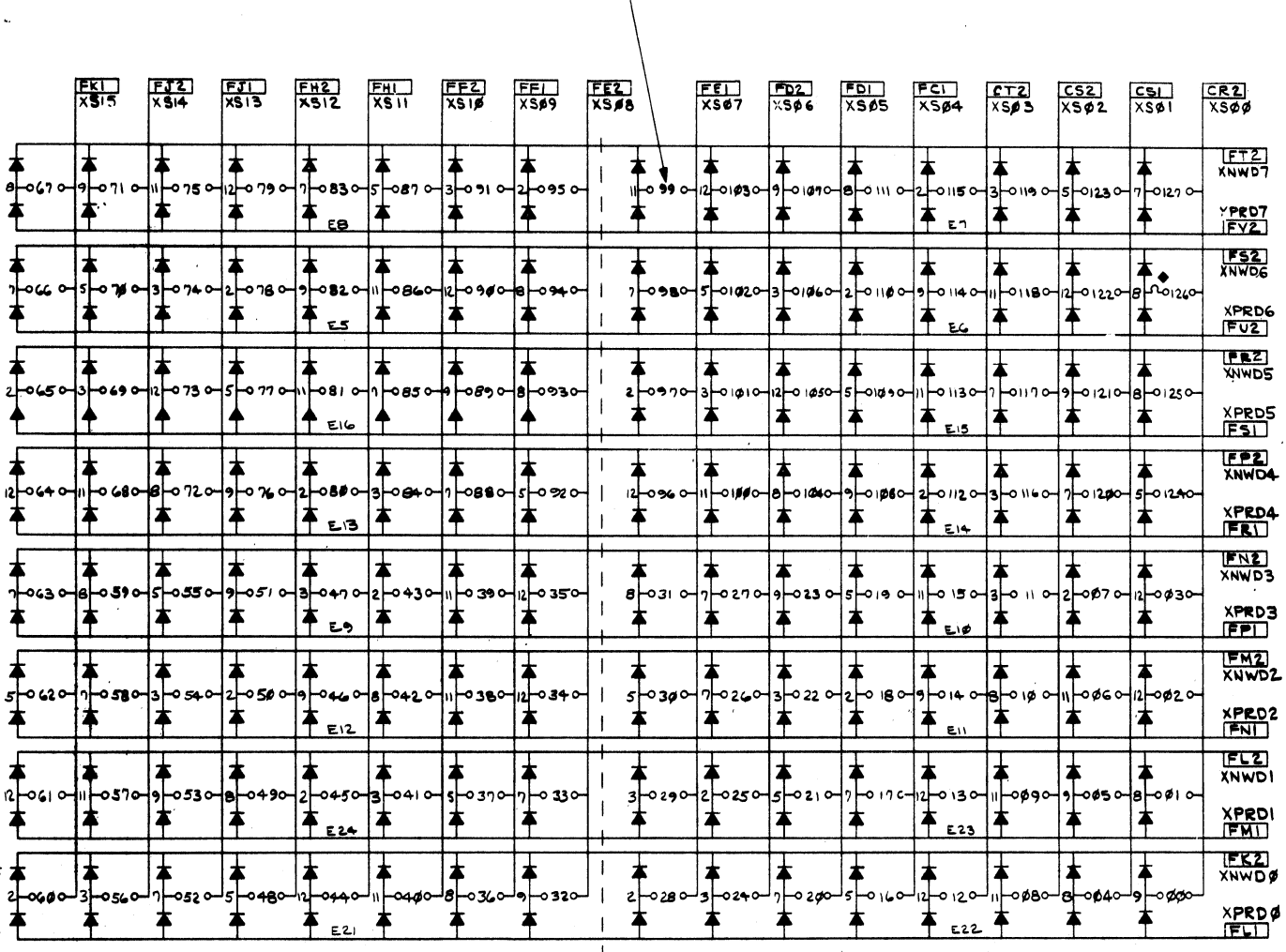
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7: FOR AMPLEX STACKS H214, H215, AND H216 WITH CORE TYPE 324-1899, R1 SHOULD BE 121 OHMS.

8: AN H214A IS DEFINED TO BE EITHER AN H214 OR H215 MODULE.

- NOTES:
- UNLESS OTHERWISE SPECIFIED, IC'S E1-E24 ARE TO BE DEC PART # 1910010
  - INDICATES STACK LINE NUMBER. (TYP)
  - INDICATES CURRENT LOOP
  - INDICATES MAGNET WIRE TERMINATION (SOLDERED TO RC. PAD).
  - FOR H215 & H216 ONLY. P0=PA, P1=PB
  - FOR H216 ONLY.



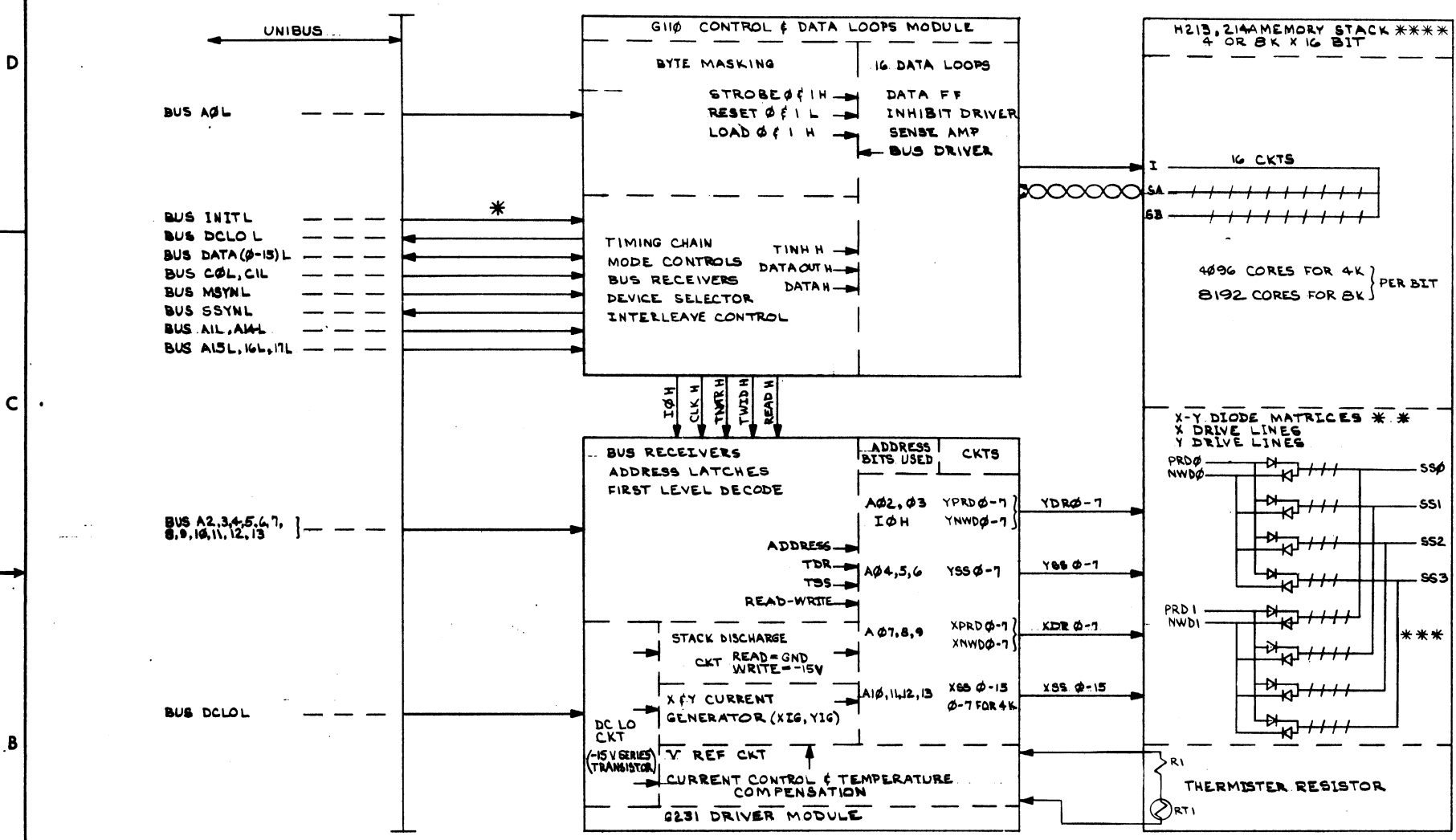
REV	CHG	NO

FIRST USED ON OPTION/MODEL	QTY.	DESCRIPTION	PART NO.	ITEM NO.
MM11-L				
PARTS LIST				
<b>TITLE</b> STACK SCHEMATIC (H214-8K X 16 BIT) (H214A-8K X 16 BIT) (H215-8K X 18 BIT) (H216-8K X 19 BIT)				
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES TOLERANCES DECIMALS ANGLES .XXX - .005 ±0°30' .XX - .02 .X - .1 REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY		DRN: <i>[Signature]</i> DATE: 11/22/71 CHK'D: <i>[Signature]</i> DATE: 12/2/71 ENG: <i>[Signature]</i> DATE: 12/2/71 PROJ. ENG: <i>[Signature]</i> DATE: 1/23/72 PROD: <i>[Signature]</i> DATE: 1-26-72 MATERIAL: _____ NEXT HIGHER ASSY.: _____ FINISH: _____ SCALE: _____ SHEET: ? OF 2		
SIZE CODE: B-DD-MM11-L-0 NUMBER: DCS H214-0-1 DIST.		REV.		

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DBD MM11-S-2 2

NOTES:  
 \* 1. ALL ARROWS SHOW SIGNAL FLOW DIRECTION.  
 \*\* 2. MATRIX SHOWN IS FOR ILLUSTRATION ONLY.  
 \*\*\* 3. ACTUAL MATRIX HAS  
 Y AXIS 8PRD, 8NRD, 8SS  
 X AXIS 4K 8PRD, 8NRD, 8SS  
 X AXIS 8K 8PRD, 8NRD, 16SS  
 \*\*\*\* 4. H214A IS A DESIGNATOR FOR EITHER AN H214 OR H215 MEMORY STACK, SEE E-CS-H214-0-1.



REV.	CHANGE NO.	DATE
A	MM11-S-00005	1/14/72
B	MM11-S-00075	3/30/72
C	MM11-S-00117	5/2/72

DEC FORM NO. 500 100-9

FIRST USED ON OPTION/MODEL	QTY.	DESCRIPTION	PART NO.	ITEM NO.
MM11-S				
PARTS LIST				
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES. TOLERANCES		DRN: <i>Carney</i> DATE: 1/14/72	digital EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS	
DECIMALS	ANGLES	CHKD: <i>Bligh</i> DATE: 2/14/72	TITLE: BLOCK DIAGRAM	
.XXX - .006	±0° 30'	ENG: <i>Bligh</i> DATE: 3/30/72		
.XX - .02		PROJ. ENG. DATE: 3/31/72		
.X - .1		PROD. DATE: 5/2/72		
REMOVE BURRS AND BREAK SHARP CORNERS. SURFACE QUALITY		NEXT HIGHER ASSY.		
MATERIAL		B-DD-MM11-S	SIZE CODE: DBD	NUMBER: MM11-S-2
FINISH		SCALE	SHEET 1 OF 2	REV. A

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REV. 2  
 SIZE CODE  
 DBCMM11-S-2  
 NUMBER

MEMORY BANK	MACHINE ADDRESS	W1 *	W5 A13 Δ	W6 □ A14 OR A81	W4 A15	W3 A16	W2 A17L	Δ W9 4K-8K	W10	W7-8 INTER LEAVE □	W11 PROTECT
0-4K	00000-017776	IN	IN	IN	IN	IN	IN				
4-8K	02000-037776	↑	OUT	IN	IN	IN	IN				
8-12K	04000-057776		IN	OUT	IN	IN	IN				
12-16K	06000-077776		OUT	OUT	IN	IN	IN				
16-20K	10000-117776		IN	IN	OUT	IN	IN				
20-24K	12000-137776		OUT	IN	OUT	IN	IN				
24-28K	14000-157776		IN	OUT	OUT	IN	IN				
28-32K	16000-177776		OUT	OUT	IN	IN	IN				
32-36K	20000-217776		IN	IN	IN	OUT	IN				
36-40K	22000-237776		OUT	IN	IN	OUT	IN				
40-44K	24000-257776		IN	OUT	IN	OUT	IN				
44-48K	26000-277776		OUT	OUT	IN	OUT	IN				
48-52K	30000-317776		IN	IN	OUT	OUT	IN				
52-56K	32000-337776		OUT	IN	OUT	OUT	IN				
56-60K	34000-357776	IN	IN	OUT	OUT	OUT	IN				
60-64K	36000-377776	↑	OUT	OUT	OUT	OUT	IN				
64-68K	40000-417776		IN	IN	IN	IN	OUT				
68-72K	42000-437776		OUT	IN	IN	IN	OUT				
72-76K	44000-457776		IN	OUT	IN	IN	OUT				
76-80K	46000-477776		OUT	OUT	IN	IN	OUT				
80-84K	50000-517776		IN	IN	OUT	IN	OUT				
84-88K	52000-537776		OUT	IN	OUT	IN	OUT				
88-92K	54000-557776		IN	OUT	OUT	IN	OUT				
92-96K	56000-577776		OUT	OUT	OUT	IN	OUT				
96-100K	60000-617776		IN	IN	IN	OUT	OUT				
100-104K	62000-637776		OUT	IN	IN	OUT	OUT				
104-108K	64000-657776		IN	OUT	IN	OUT	OUT				
108-112K	66000-677776		OUT	OUT	IN	OUT	OUT				
112-116K	70000-717776		IN	IN	OUT	OUT	OUT				
116-120K	72000-737776		OUT	IN	OUT	OUT	OUT				
120-124K	74000-757776		IN	OUT	OUT	OUT	OUT				
DEVICE USED AS 4K MEMORY Δ		IN	X	X	X	X	X	IN	OUT		

WHEN USED AS A 4K OR A NON INTER-LEAVED 8K JUMPERS ARE

W8

W7

WHEN USED AS AN 8K INTERLEAVED (2-8K BANKS REQUIRED) JUMPERS ARE

W8

W7

NOTES:

\*1. W1 IS FOR TEST PURPOSES ONLY

Δ2. WHEN USED AS AN 8K BANK, W5 AND W10 MUST BE INSTALLED AND W9 MUST BE OUT. WHEN USED AS A 4K BANK W10 MUST BE OUT, W9 MUST BE IN AND W5 DETERMINES THE BANKS LOCATION ON THE BUS.

□3. THIS MEMORY CAN ONLY BE INTERLEAVED AS 16K (TWO ADJACENT, CONTIGUOUS ADDRESS 8K BANKS). WHEN NOT INTER-LEAVED (SOLID JUMPERS ON W7 AND W8) THE DEVICE SELECT IS AS SHOWN IN TABLE 1 USING A14. WHEN TWO 8K BANKS ARE INTER-LEAVED W7 AND W8 MUST BE AS SHOWN IN DOTTED LINES IN TABLE 1. ALSO IN TABLE 1, A81 NOW GOES TO THE DEVICE SELECTOR GATE CONTROLLED BY W6. THE TWO BANKS MUST HAVE W6 IN ON ONE BANK AND OUT ON THE OTHER.

4. FIGURE 1 SHOWS THE PHYSICAL LOCATION OF THE JUMPERS ON THE G110 IF THE MODULE WERE LYING ON THE PRINT WITH COMPONENTS UP AND CONNECTORS TOWARD BOTTOM OF PRINT. W7 & W8 ARE AS SHOWN SCHEMATICALLY ON D-CS-G110-0-1

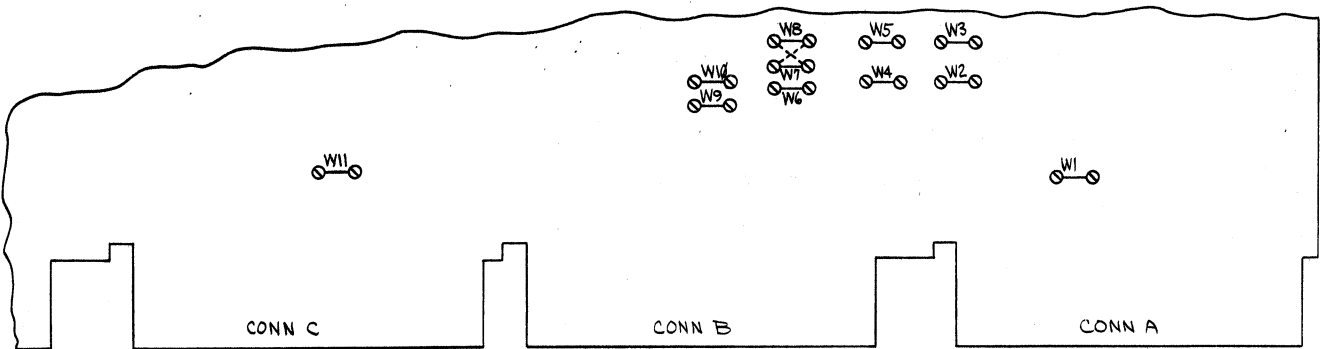


FIGURE 1  
 G110 JUMPER PHYSICAL LOCATION  
 SEE NOTE 4

FIRST USED ON OPTION/MODEL	QTY.	DESCRIPTION	PART NO.	ITEM NO.
MM11-S				
PARTS LIST				
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES.	DRN 5/15/72	DATE	digital EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS	
TOLERANCES	CHK'D 5-25-72	DATE	TITLE	
DECIMALS	ENG. 1/11/72	DATE	BLOCK DIAGRAM (DEVICE DECODING)	
ANGLES	PROJ. ENG.	DATE		
.XXX = .005	PROD.	DATE		
.XX = .02		DATE		
X = .1		DATE		
REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY		DATE		
MATERIAL	NEXT HIGHER ASSY.			
			SIZE CODE	NUMBER
FINISH			DBCMM11-S-2	A
	SCALE			
	SHEET 2 OF 2			

REVISIONS  
 CHANGE NO. REV.

DEC FORM NO. PD 100-A

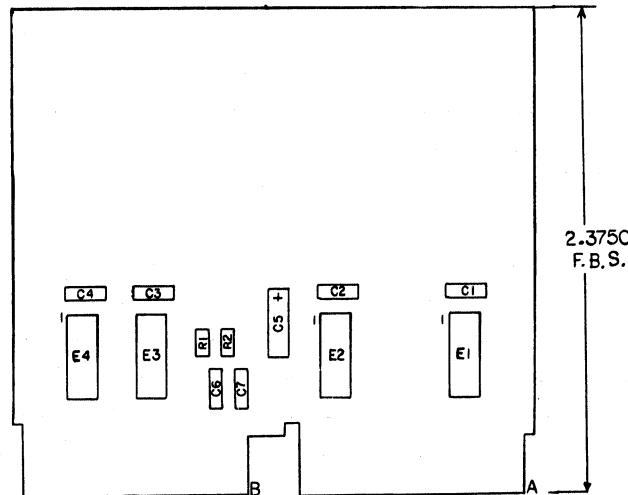


DIGITAL EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS			QUANTITY/VARIATION														
<b>PARTS LIST</b>																	
MADE BY	F. CARBERRY	CHECKED	<i>[Signature]</i>	SECTION													
DATE	1/5/72	DATE	1-25-72	ISSUED SECT.													
ENG	<i>P. Duvaux</i>	PROD	<i>R.K. Patton</i>														
DATE	1/25/72	DATE	1-26-72														
ITEM NO.	DWG NO. / PART NO.	DESCRIPTION			MM11-L	MM11-K											
1	S-00-H213-0	H213 MEMORY STACK (4K X 16)				1											
2	E-CS-G231-0-1	MEMORY DRIVER			1	1											
3	E-CS-G110-0-1	CONTROL & DATA LOOPS			1	1											
4	E-CS-H214-0-1	H214 MEMORY STACK (8K X 16)			1												
TITLE				ASSY NO.	SIZE	CODE	NUMBER				REV.	ECO NO.					
MEMORY, MM11				11	A	PL	MM11-L-0				A	MM11-00005					
SHEET 1 OF 1				DIST.													

DEC FORM DEC 16-(325)-1031-N870  
DRA 110

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**NOTES:**



REF	X-Y COORDINATE HOLE LOCATION	K-CO-M930-0-4	1
REF	ASSY/DRILLING HOLE LAYOUT	D-AH-M930-0-5	2
REF	MODULE ECO HISTORY	B-MH-M930-0-6	3
1	ETCHED CIRCUIT BOARD	5005696	4
4	C1 THRU C4	CAP .22 μF 50V CER	1010274-01 5
2	C6, C7	CAP 1000PF 250V 20% DISC	1000043 6
1	C5	CAP 39μF 10V 10% TANT	1000076 7
2	R1, R2	RES 303Ω 1/4W 1%	1305125 8
2	E1, E4	28 RES DIVIDER NETWORK	1312628-00 9
2	E2, E3	25 RES DIVIDER NETWORK	1312628-01 10

QTY	REF. DESIGNATION	DESCRIPTION	PART NO.	ITEM NO.

IC TYPE	GND	+5V	
GND AND 5V ARE USUALLY PIN 7 AND 14 RESPECTIVELY EXCEPTIONS ARE STATED ABOVE			
IC PIN LOCATIONS			

FIRST USED ON OPTION MODEL				PARTS LIST			
M930				ETCH BOARD REV. [E-P3]			
DRN. J. CARTER	DATE 7-9-75			TITLE			
CHK'D. E. Murphy	DATE 7-16-75			BUS TERMINATOR			
ENG. K. Sargent	DATE 7-11-75			SIZE CODE			
PROJ. ENG. K. Sargent	DATE 7-11-75			NUMBER			
PROD. K. Sargent	DATE 7-11-75	NEXT HIGHER ASSY		DIST.			
DEC NO.	EIA NO.	DEC NO.	EIA NO.	SCALE			
SEMICONDUCTOR CONVERSION CHART						SHELT OF 2	

M930 1-0-026W SCD 2

REV. E  
 M930-0-1  
 DCS  
 DATE

8

7

6

5

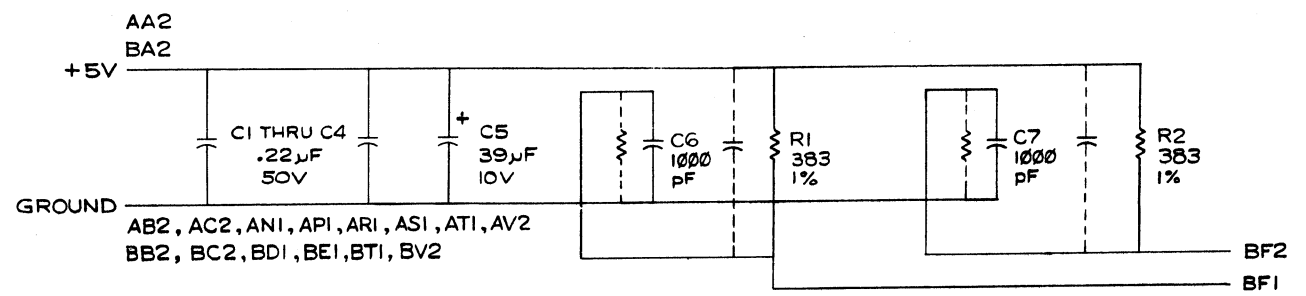
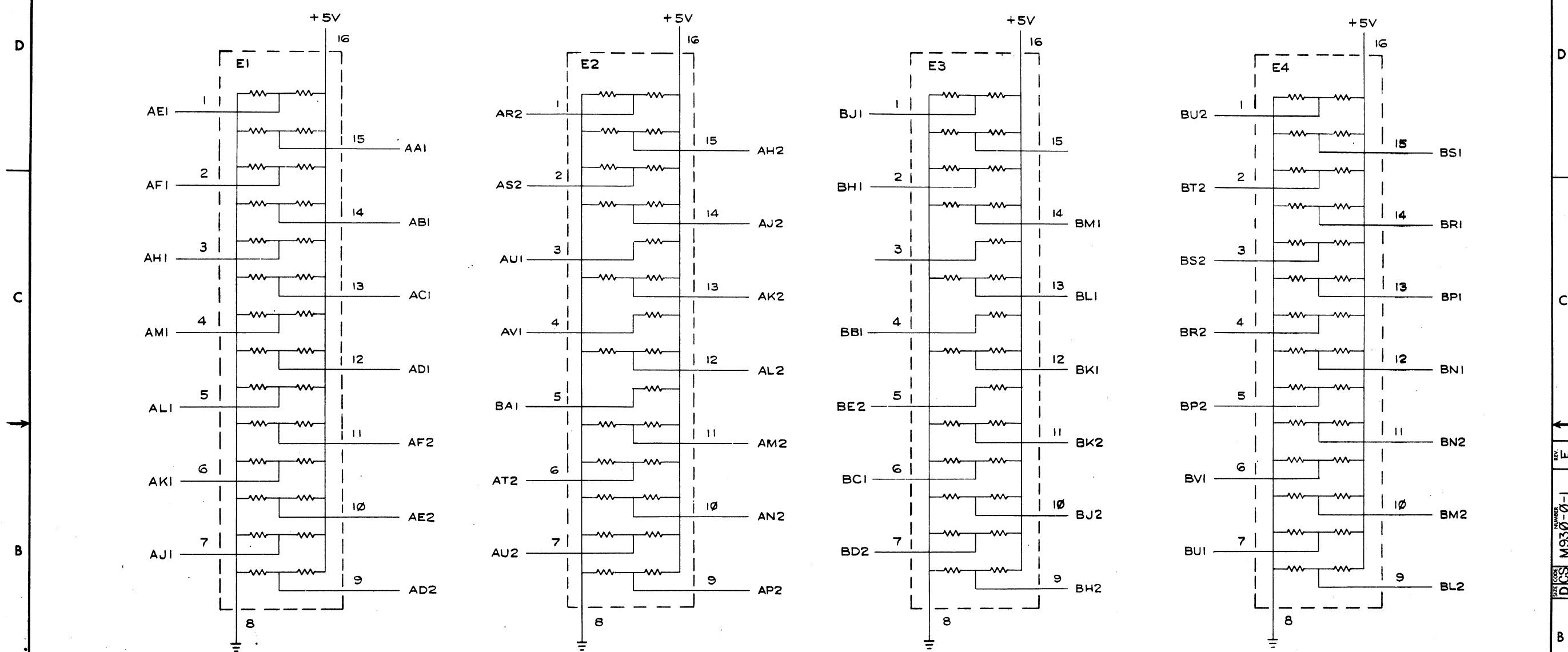
4

3

REV. E  
DCS M930-0-1  
1963 1215

1

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REVISIONS		
CHK	CHANGE NO.	REV.

TITLE: BUS TERMINATOR  
 SIZE CODE: DCS  
 NUMBER: M930-0-1  
 SHEET: 2 OF 2  
 DIST:  

DEC FORM NO. 100 100

8

7

6

5

4

3

2

1



## CUSTOMER PRINT SET INDEX

SEQUENCE

SEQUENCE

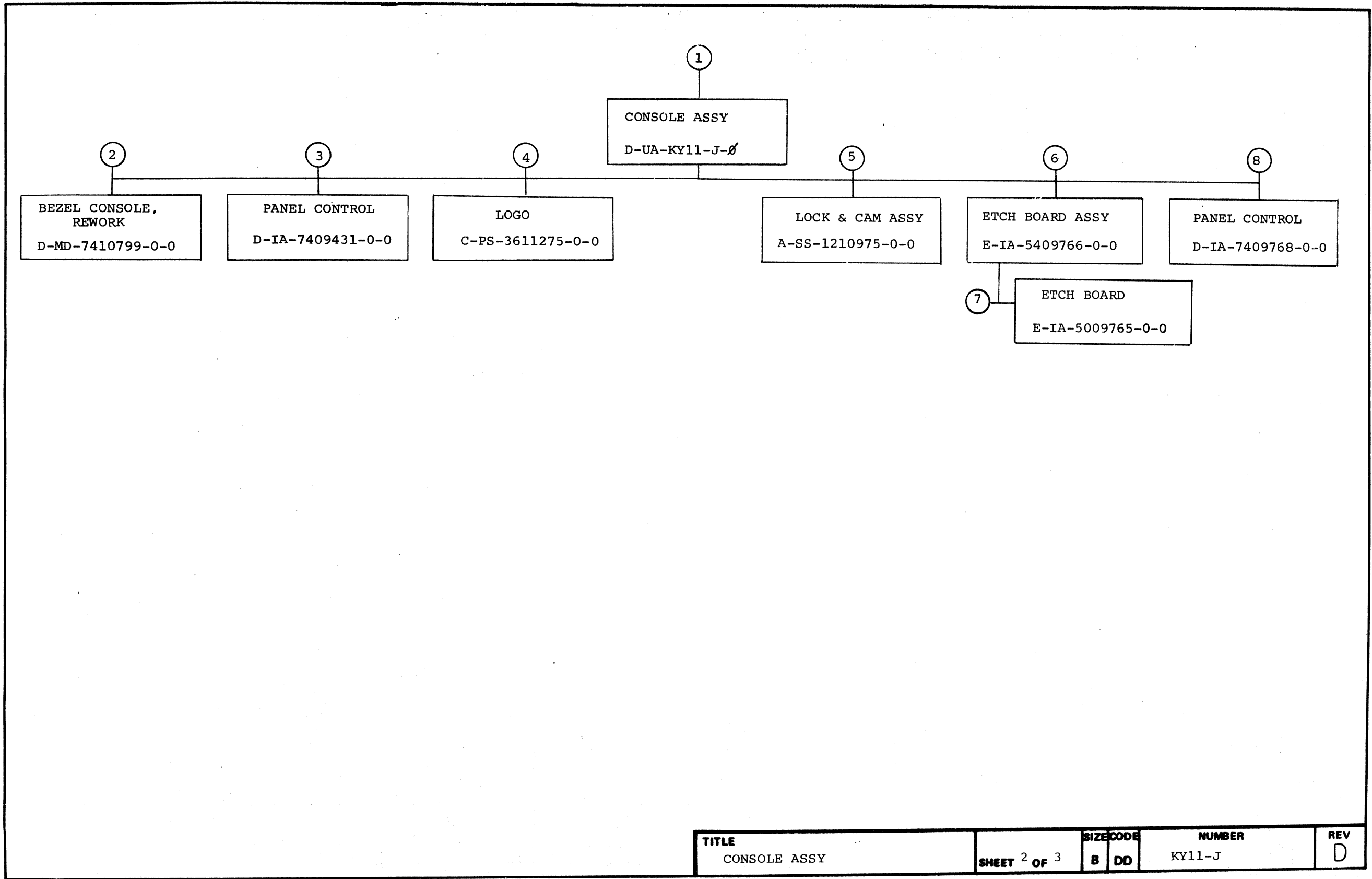
THIS IS PRINT SET

CONSOLE ASSY  
CONSOLE ASSY (PL)  
JUMPER POWER  
I/O CABLE  
CIRCUIT SCHEMATIC  
ETCH BOARD ASSY

D-UA-KY11-J-Ø  
A-PL-KY11-J-Ø  
B-IA-7409730-0-0  
C-UA-BCØ8R-Ø3-Ø  
D-CS-5409766-Ø-1  
E-IA-5409766-0-0

UNIT VARIATIONS		PRINT SET			
VAR	TITLE	KY11-J			
KY11-JA	CONSOLE ASSY 11/Ø5	X			
KY11-JB	CONSOLE ASSY 11/1Ø	X			
KY11-JC	CONSOLE ASSY GT4Ø	X			
KY11-JF	CONSOLE ASSY UNICHANNEL 15	X			
KY11-JD	CONSOLE ASSY 11/Ø5 (10.5)	X			
KY11-JE	CONSOLE ASSY 11/1Ø (10.5)	X			

REVISIONS	DATE	CHG. NO.	REV	USED ON OPTION/MODEL	DRN.	DATE	TITLE	SIZE	CODE	NUMBER	REV		
	2/74	KY11J-00004	A		11/Ø5	J. CAHILL						4-23-73	CONSOLE ASSY
	3/74	KY11J-00005	B			CHK'D.						DATE	
	3/74	KY11J-00006	C			J. CAHILL						4-23-73	
	3/74	KY11J-00007	D			PROJ ENG.						DATE	
					PROD.	8-29-73				D			
					FIELD SERV.	DATE							
				SHEET 1 OF 3		9-26-73	DIST						



<b>TITLE</b> CONSOLE ASSY	<b>SHEET 2 OF 3</b>	<b>SIZE CODE</b> B DD	<b>NUMBER</b> KY11-J	<b>REV</b> D
------------------------------	---------------------	--------------------------	-------------------------	-----------------

CUSTOMER PRINT SET		ELECTRICAL					CUSTOMER PRINT SET		MECHANICAL						
KY11-J	MFG. SET	FIND NO.	DRAWING NO.	REV	NO OF SHT	OPTION NO./FILE DATE	KY11-J	MFG. SET	FIND NO.	DRAWING NO.	REV	NO OF SHT	DESCRIPTION	OPTION NO./FILE DATE	
X		1	D-UA-KY11-J-Ø	N	1				1	D-UA-KY11-J-Ø	N	1	CONSOLE ASSY		
X			A-PL-KY11-J-Ø	N	2					A-PL-KY11-J-Ø	N	2	CONSOLE ASSY (PL)		
X			B-IA-7409730-0-0	#	1					C-MD-7409534-0-0		1	ACTUATOR (REWORK)		
X			C-UA-BCØ8R-Ø3-Ø	#	1					B-IA-7409444-0-0		1	DETENT		
										C-UA-BCØ8R-Ø3-Ø		1	I/O CABLE		
										B-MD-7409867-0-0		1	EXTENDED LEAF REWORK (ACTUATOR)		
										B-MD-7409868-0-0		1	SWITCH ADAPTER PLATE		
X		6	E-IA-5409766-0-0	#	1					C-MD-7411728-0-0		1	KEY LOCK SWITCH, REWORK		
X			D-CS-5409766-Ø-1	#	1										
			B-MH-5409766-Ø-6	#	1										
		7	E-IA-5009765-0-0		1										
										2			D-MD-7410799-0-0	1	BEZEL CONSOLE REWORK
													J-PS-1210922-0-0	1	BEZEL CONSOLE CASTING
										3			D-IA-7409431-0-0	1	PANEL CONTROL
													C-SS-7409431-0-1	1	SILK SCREEN (MAGENTA)
													C-SS-7409431-0-2	1	SILK SCREEN (BLK) REAR
													C-SS-7409431-0-3	1	SILK SCREEN (11Ø5) VERMILLION
										4			C-PS-3611275-0-0	1	LOGO
													A-SS-3611275-0-1	1	SILK SCREEN
													A-SS-3611275-0-2	1	SILK SCREEN
													A-SS-3611275-0-3	1	SILK SCREEN
													A-SS-3611275-0-4	1	SILK SCREEN
													A-SS-3611275-0-5	1	SILK SCREEN
										5			A-PS-1210975-0-0	1	LOCK & CAM ASSY
										6			E-IA-5409766-0-0	1	ETCH BOARD ASSY
										7			E-IA-5009765-0-0	1	ETCH BOARD
										8			D-IA-7409768-0-0	1	PANEL CONTROL
													C-SS-7409768-0-1	1	SILK SCREEN
													C-SS-7409768-0-2	1	SILK SCREEN
													C-SS-7409768-0-3	1	SILK SCREEN

CUSTOMER PRINT SET CODES  
X = PRINT OF DOCUMENT INCLUDED IN PRINT SET  
C = INCLUDES ALL PRINTS INDICATED ON DOCUMENT  
S = CONFIDENTIAL AUTHORIZED SIGNATURE REQUIRED

TITLE  
CONSOLE ASSY

SHEET 3 OF 3

SIZE CODE  
B DD

NUMBER  
KY11-J

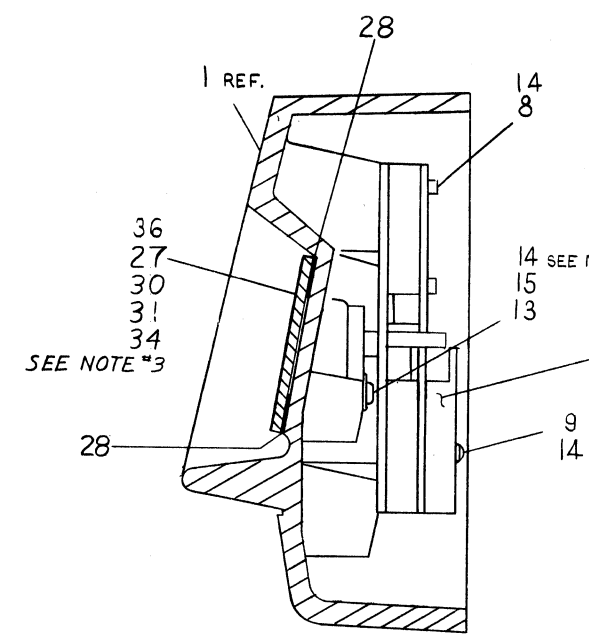
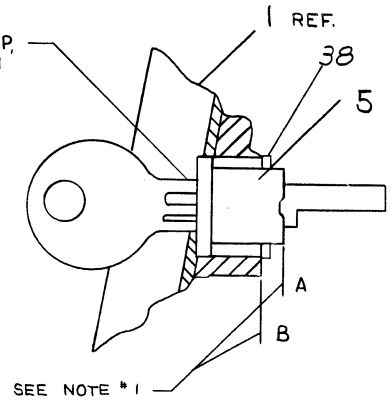
REV  
D

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1972

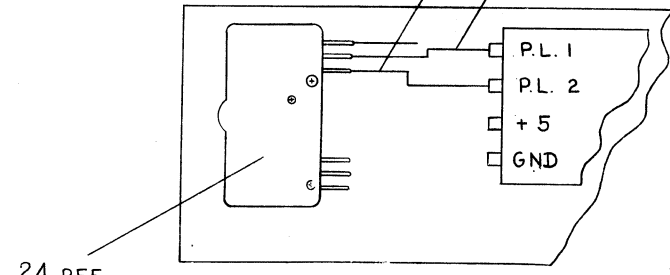
**LEGEND**

PART NO.	VARIATION
KY11-JA	1105 CONSOLE WITH L.E.D.S.
KY11-JB	1110 CONSOLE WITH L.E.D.S.
KY11-JC	VT40 CONSOLE WITH L.E.D.S.
KY11-JF	UC15 CONSOLE WITH L.E.D.S.
KY11-JD	10 1/2" 1105 CONSOLE WITH L.E.D.S.
KY11-JE	10 1/2" 1110 CONSOLE WITH L.E.D.S.
KY11-JJ	INDUSTIAL CONSOLE WITH L.E.D.S.

TEETH OF KEY UP, COMPUTER IS IN "OFF" POSITION



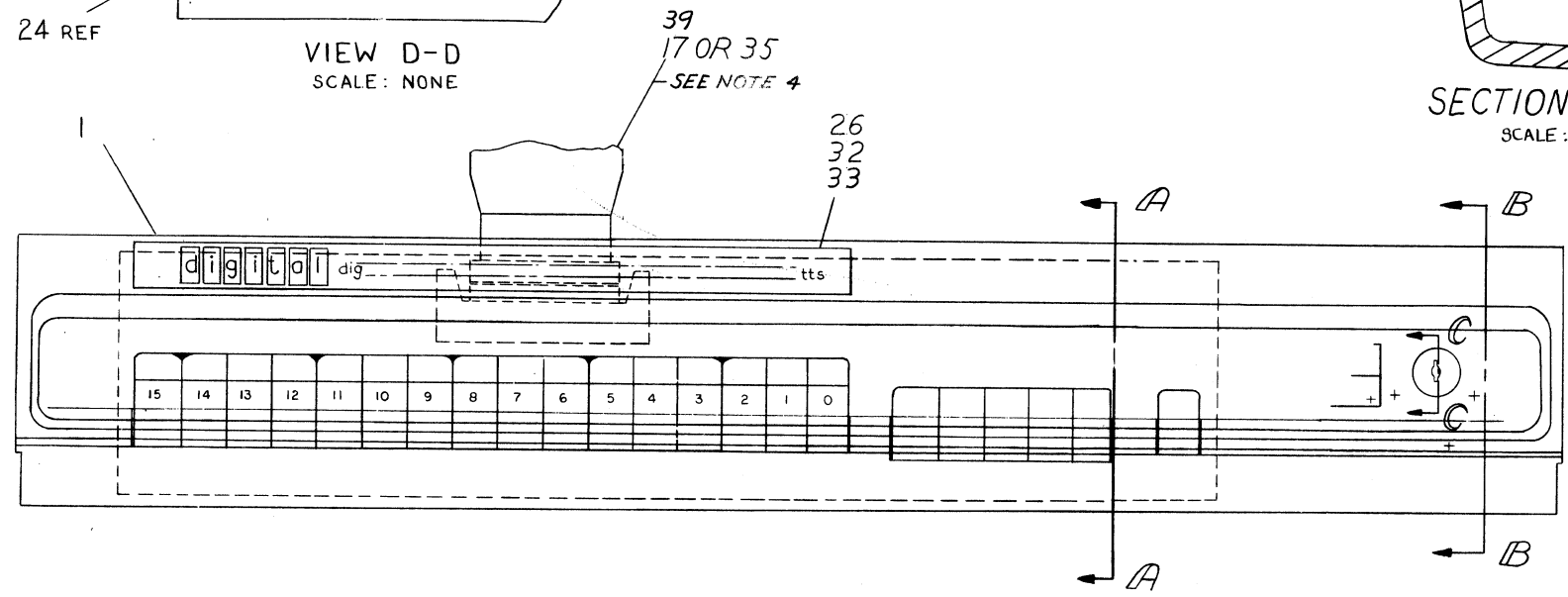
- NOTES:**
- CASTING KEY HOLE, SURFACES A & B TO BE COATED WITH LUBE (ITEM #18)
  - LOCK WASHER IS NOT USED ON SCREW UNDER SWITCH.
  - INSERT CONTROL PANEL (ITEM #27,30,31 OR 34) USING ADHESIVE TRANSFER TAPE (ITEM #29) ON REAR SURFACE & PRESS FIRMLY IN PLACE.
  - INSTALL 18 INCHES OF TAPE (ITEM #39) ON UNSHIELDED SIDE OF I/O CABLE BEGINNING AT FAR END OF CABLE FROM CONSOLE. ALL VARIATIONS EXCEPT JD AND JE.



**SECTION C-C**  
SCALE: 2/1

**SECTION B-B**  
SCALE: 2/1

**SECTION A-A**  
SCALE: 2/1



REV	DATE	BY	CHK'D	DESCRIPTION
1	11-28-72	G. GRAHAM		REVISED & REDRAWN
2	11-05-00040	M. TELBAUM		
3	11-19-73			
4	11-28-72	G. GRAHAM		
5	11-20-73			
6	11-20-73	F. ELIA		
7	11-20-73			
8	11-20-73			
9	11-20-73			
10	11-20-73			
11	11-20-73			
12	11-20-73			
13	11-20-73			
14	11-20-73			
15	11-20-73			
16	11-20-73			
17	11-20-73			
18	11-20-73			
19	11-20-73			
20	11-20-73			
21	11-20-73			
22	11-20-73			
23	11-20-73			
24	11-20-73			
25	11-20-73			
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27	11-20-73			
28	11-20-73			
29	11-20-73			
30	11-20-73			
31	11-20-73			
32	11-20-73			
33	11-20-73			
34	11-20-73			
35	11-20-73			
36	11-20-73			
37	11-20-73			
38	11-20-73			
39	11-20-73			

FIRST USED ON OPTION / MODEL PDP 1105		DO NOT SCALE DRAWING UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES TOLERANCES DECIMALS FRACTIONS ANGLES ± .005 ± 1/64 ± 90° FINAL SURFACE QUALITY REMOVE BURRS AND BREAK SHARP CORNERS		DRN. CAHILL DATE 11-16-71 CHK'D. TESCHNER DATE 4-19-72 ENG. GRAHAM DATE 4-5-72 PROJ. ENG. WEEKS DATE 3-5-72 PROD. PETERSON DATE 5-10-72	
MATERIAL ++		NEXT HIGHER ASSY D-UA-1105-0-0		PARTS LIST <b>digital</b> EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS	
FINISH ++		SCALE 1/1		TITLE <b>CONSOLE ASSY (PDP 1105)</b>	
SHEET 1 OF 1		SIZE CODE DUA		NUMBER KY11-J-0	
				REV N	

REV N  
NUMBER KY11-J-0  
SIZE CODE DUA



DIGITAL EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS				QUANTITY / VARIATION											
PARTS LIST				KY11-JA	KY11-JB	KY11-JC	KY11-JF	KY11-JD	KY11-JE	KY11-JJ					
MADE BY	CHECKED	SECTION	DATE												
R. ROBICHAUD	C. TESCHNER	1	12-8-71	5-1-72	1										
G. GRAHAM	R. K. PETERSON	1	4-5-72	5-10-72	1										
ITEM NO.	DWG NO. / PART NO.	DESCRIPTION													
1	D-MD-7410799-0-0	CONSOLE BEZEL REWORK			1	1	1	1	1	1	1				
<del>2</del>	<del>E-IA-7409374-3-0</del>	<del>BEZEL CONSOLE (11/10)</del>			<del>X</del>	<del>X</del>	<del>X</del>	<del>X</del>							
3	E-IA-5409766-0-0	CONSOLE ETCH BOARD ASSY			1	1	1	1	1	1	1				
<del>4</del>	<del>E-IA-5409766-2-0</del>	<del>CONSOLE ETCH BOARD ASSY (OPOGA)</del>			<del>X</del>	<del>X</del>	<del>X</del>								
5	A-PS-1210975-0-0	LOCK & CAM ASS'Y			1	1	1	1	1	1	1				
6	9006020-1	SCR, PHL PAN HD. #6-32 X 1/4 LG			6	6	6	6	6	6	6				
7	9006633	WASH INT TOOTH LOCK #6			6	6	6	6	6	6	6				
8	9006003-1	SCR PHL PAN HD #2-56 X 3/8 LG			2	2	2	2	2	2	2				
9	9008025-1	SCR PHL PAN HD #2-56 X 5/8 LG			2	2	2	2	2	2	2				
10	<del>1210790-0-0</del>	<del>SWITCH DPST N.O.</del>			<del>1</del>	<del>1</del>	<del>1</del>								
11	<del>E-MD-7409534-0-0</del>	<del>ACTUATOR REWORK</del>			<del>1</del>	<del>1</del>	<del>1</del>								
12	<del>1210905-1</del>	<del>INSULATOR</del>			<del>2</del>	<del>2</del>	<del>2</del>								
13	B-IA-7409444-0-0	DETENT			1	1	1	1	1	1	1				
14	9006686	WASH #2 SPLIT LOCK			5	5	5	6	5	5	5				
15	9006000-4	SCR BINDER HD #2-56 X3/16 LG			2	2	2	2	2	2	2				
16	<del>1210904-1</del>	<del>SWITCH TMD 5201 (COLD CONTACT-291-5201-00)</del>			<del>1</del>	<del>1</del>	<del>1</del>								
17	C-UA-BC08R-03	I/O CABLE (3'-0" LG)			1	1	1	1	X	X	1				
18	4901077	LUBE (FOR CAM LOCK)			A/R	A/R	A/R	A/R				A/R			
19	B-IA-7409730-0-0	JUMPER, POWER			2	2	2	2	2	2	2				
20	<del>B-MD-7409868-0-0</del>	<del>SWITCH ADAPTER PLATE</del>			<del>1</del>	<del>1</del>	<del>1</del>								
21	<del>B-MD-7409867-0-0</del>	<del>EXTENDER LEAF REWORK (ACTUATOR)</del>			<del>1</del>	<del>1</del>	<del>1</del>								
22	<del>9008449-2</del>	<del>SCR PHL FLAT HD #2-56 X 1/4 LG.</del>			<del>2</del>	<del>2</del>	<del>2</del>								
TITLE CONSOLE ASSY (PDP11/05)				ASSY NO. D-UA-KY11-J-0	SIZE A	CODE PL	NUMBER KY11-J-0				REV. N	ECO NO. KY11J-00007			
SHEET 1 OF 2				DIST. G											

DEC FORM DEC 76 (325) 1031 N870  
DRA 110

DIGITAL EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS				QUANTITY / VARIATION											
PARTS LIST				KY11-JA	KY11-JB	KY11-JC	KY11-JF	KY11-JD	KY11-JE	KY11-JJ					
MADE BY	CHECKED	SECTION	DATE												
R. ROBICHAUD	C. TESCHNER	1	12-8-71	5-1-72	1										
G. GRAHAM	R. K. PETERSON	1	4-5-72	5-10-72	1										
ITEM NO.	DWG NO. / PART NO.	DESCRIPTION													
<del>23</del>	<del>E-IA-7409374-4-0</del>	<del>BEZEL CONSOLE (VT40)</del>			<del>X</del>	<del>X</del>	<del>1</del>	<del>X</del>							
24	A-PS-1210982-0-0	KEY LOCK SWITCH			1	1	1	1		1	1				
<del>25</del>	<del>E-IA-7409374-5-0</del>	<del>BEZEL CONSOLE (UC15)</del>			<del>X</del>	<del>X</del>	<del>X</del>	<del>X</del>							
26	C-PS-3611275-0-0	LOGO (PDP-1105) NOTE VARIATIONS			1	1	X	X	1	1	1				
27	D-IA-7409431-1-0	PANEL CONTROL (1105)			1	X	X	X	1	X	X				
28	9009210-1	ADHESIVE TRANSFER TAPE 1/2 WIPE			A/R	A/R	A/R	A/R	A/R	A/R	A/R				A/R
<del>29</del>	<del>1211052</del>	<del>CONSOLE PROTECTIVE COVER</del>			<del>1</del>	<del>1</del>	<del>1</del>	<del>1</del>	<del>1</del>	<del>1</del>	<del>1</del>				
30	D-IA-7409431-3-0	PANEL CONTROL (1110)			X	1	X	X	X	1	X				X
31	D-IA-7409768-0-0	PANEL CONTROL (GT40)			X	X	1	X	X	X	X				X
32	C-PS-3611275-4-0	LOGO (DEC GRAPHIC)			X	X	1	X	X	X	X				X
33	C-PS-3611275-5-0	LOGO (UC15)			X	X	X	1	X	X	X				X
34	D-IA-7409431-4-0	PANEL CONTROL (UC15)			X	X	X	1	X	X	X				X
35	C-UA-BC08R-64	IØ CABLE (4'-0"LG)			X	X	X	X	1	1	X				X
36	D-IA-7411393-0-0	INDICATOR PANEL (INDUSTRIAL)			X	X	X	X	X	X	1				X
37	C-MD-7411728-0-0	KEY LOCK SWITCH, REWORK			X	X	X	X	1	X	X				X
38	9009589	RETAINING RING .025 THK			1	1	1	1	1	1	1				1
39	9009339	TAPE CLOTH WATERPROOF 2" WIDE GRAY			A/R	A/R	A/R	A/R	X	X	A/R				A/R
TITLE CONSOLE ASSY (PDP11/05)				ASSY NO. D-UA-KY11-J-0	SIZE A	CODE PL	NUMBER KY11-J-0				REV. N	ECO NO.			
SHEET 2 OF 2				DIST. G											

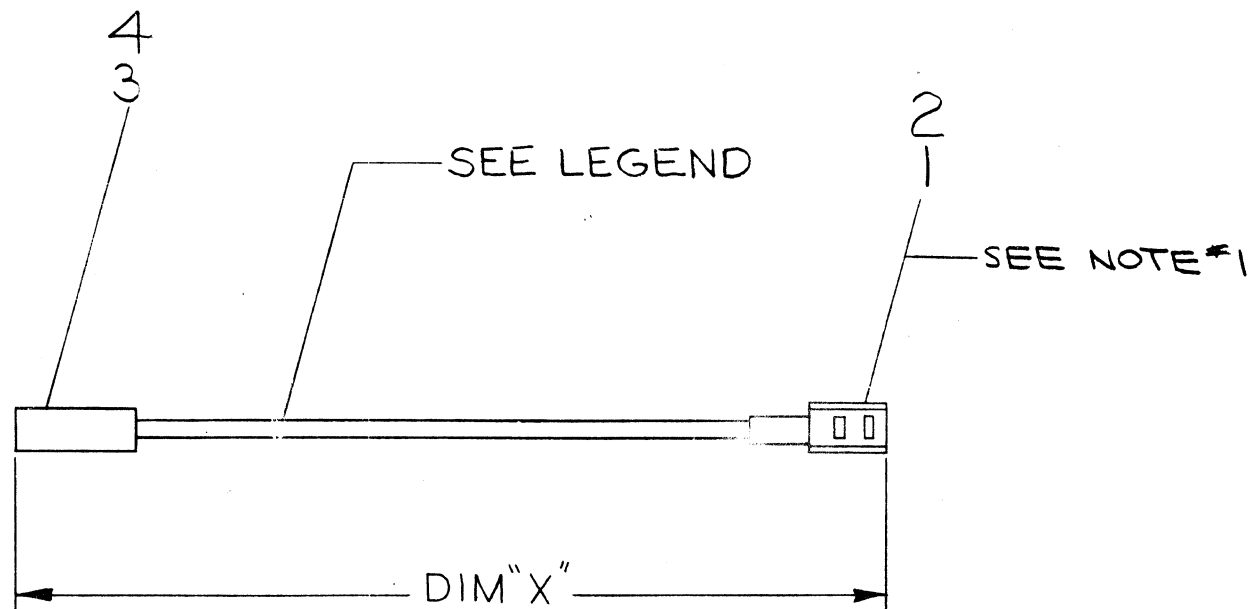
DEC FORM DEC 76 (325) 1031 N870  
DRA 110

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LEGEND			
NUMBER	DIM "X"	COLOR	ITEM
7409730-Ø1	5 1/2"	BLACK	5

NOTES:

1. ITEM #2 (SHRINK TUBING) TO COVER CONTACT AREA OF ITEM #1 (SOLDERLESS CONN.)



QTY.	DESCRIPTION	PART NO.	ITEM NO.
A/R	WIRE, #18 AWG, BLK	9107278-00	5
1	MINI FASTAB #60291-1	1210820-2	4
1	HOUSING, 1-480417-C	1210820-1	3
1	TUBING, SHRINK (WHITE)	9107252	2
1	SOLDERLESS CONN.-50902	9007917	1

FIRST USED ON OPTION MODEL  
PDP-11Ø5

DRN. <i>CB McCoy</i>	DATE 4-11-72
CHK'D. <i>Coteschner</i>	DATE 4-19-72
ENG. <i>G. Gushon</i>	DATE 4-19-72
PROJ/ENR. <i>B.D. White</i>	DATE 4-28-72
PROD. <i>R.K. Petersen</i>	DATE 4/27/72

**digital** EQUIPMENT CORPORATION  
MAYNARD, MASSACHUSETTS

TITLE  
JUMPER, POWER

NEXT HIGHER ASSEMBLY <i>/ /</i>		SIZE CODE B IA	NUMBER 7409730-0-0	REV.
SCALE NONE	SHEET 1 OF 1	DIST. <i>G</i>		

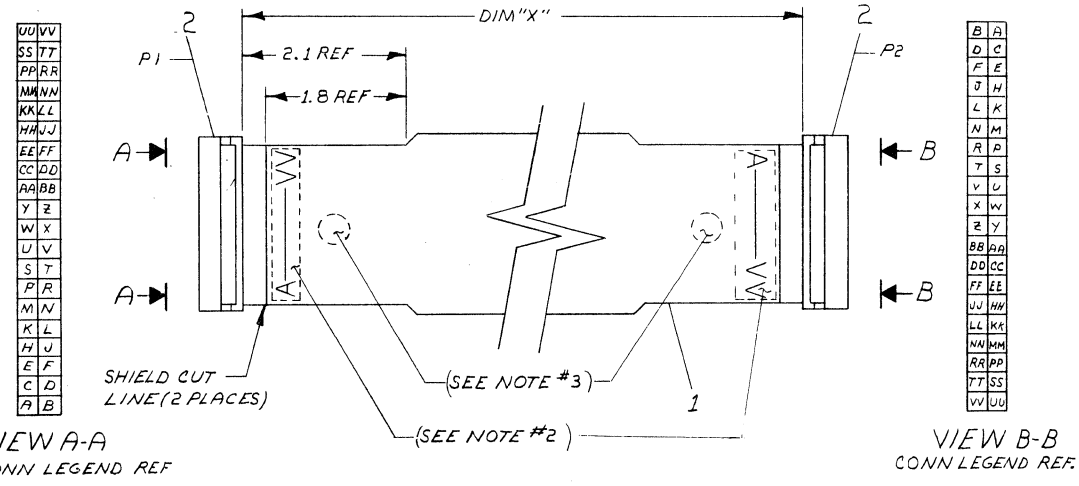
REVISIONS	REV.
CHANGE NO.	
CHK	

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FROM	TO	FROM	TO
P1-A	P2-VV	P1-Y	P2-X
P1-B	P2-UU	P1-Z	P2-W
P1-C	P2-TT	P1-AA	P2-V
P1-D	P2-SS	P1-BB	P2-U
P1-E	P2-RR	P1-CC	P2-T
P1-F	P2-PP	P1-DD	P2-S
P1-H	P2-NN	P1-EE	P2-R
P1-J	P2-MM	P1-FF	P2-P
P1-K	P2-LL	P1-HH	P2-N
P1-L	P2-KK	P1-JJ	P2-M
P1-M	P2-UU	P1-KK	P2-L
P1-N	P2-HH	P1-LL	P2-K
P1-P	P2-FF	P1-MM	P2-J
P1-R	P2-EE	P1-NN	P2-H
P1-S	P2-DD	P1-PP	P2-F
P1-T	P2-CC	P1-RR	P2-E
P1-U	P2-BB	P1-SS	P2-D
P1-V	P2-AA	P1-TT	P2-C
P1-W	P2-Z	P1-UU	P2-B
P1-X	P2-Y	P1-VV	P2-A

NUMBER	DIM "X"	PRECUT LENGTH
BC0BR-01	1.00 FT.	1.00 FT. 1.5 IN. ± .00 IN
BC0BR-02	2.00 FT.	2.00 FT. 1.5 IN. ± .00 IN
BC0BR-03	3.00 FT.	3.00 FT. 1.5 IN. ± .00 IN
BC0BR-04	4.00 FT.	4.00 FT. 1.5 IN. ± .00 IN
BC0BR-05	5.00 FT.	5.00 FT. 1.5 IN. ± .00 IN
BC0BR-06	6.00 FT.	6.00 FT. 1.5 IN. ± .00 IN
BC0BR-07	7.00 FT.	7.00 FT. 1.5 IN. ± .00 IN
BC0BR-08	8.00 FT.	8.00 FT. 1.5 IN. ± .00 IN
BC0BR-09	9.00 FT.	9.00 FT. 1.5 IN. ± .00 IN
BC0BR-10	10.00 FT.	10.00 FT. 1.5 IN. ± .00 IN
BC0BR-11	11.00 FT.	11.00 FT. 1.5 IN. ± .00 IN
BC0BR-12	12.00 FT.	12.00 FT. 1.5 IN. ± .00 IN
BC0BR-13	13.00 FT.	13.00 FT. 1.5 IN. ± .00 IN
BC0BR-14	14.00 FT.	14.00 FT. 1.5 IN. ± .00 IN
BC0BR-15	15.00 FT.	15.00 FT. 1.5 IN. ± .00 IN
BC0BR-16	16.00 FT.	16.00 FT. 1.5 IN. ± .00 IN
BC0BR-17	17.00 FT.	17.00 FT. 1.5 IN. ± .00 IN
BC0BR-18	18.00 FT.	18.00 FT. 1.5 IN. ± .00 IN
BC0BR-19	19.00 FT.	19.00 FT. 1.5 IN. ± .00 IN
BC0BR-20	20.00 FT.	20.00 FT. 1.5 IN. ± .00 IN
BC0BR-25	25.00 FT.	25.00 FT. 1.5 IN. ± .00 IN
BC0BR-30	30.00 FT.	30.00 FT. 1.5 IN. ± .00 IN
BC0BR-35	35.00 FT.	35.00 FT. 1.5 IN. ± .00 IN
BC0BR-50	50.00 FT.	50.00 FT. 1.5 IN. ± .00 IN
BC0BR-60	60.00 FT.	60.00 FT. 1.5 IN. ± .00 IN
BC0BR-75	75.00 FT.	75.00 FT. 1.5 IN. ± .00 IN
BC0BR-90	90.00 FT.	90.00 FT. 1.5 IN. ± .00 IN
BC0BR-A3	130.00 FT.	130.00 FT. 1.5 IN. ± .00 IN
BC0BR-A6	160.00 FT.	160.00 FT. 1.5 IN. ± .00 IN

1. ASSEMBLE THIS CABLE PER PROCESS SPEC #7606485-0-0.
2. CONNECTOR LEGEND IDENTIFICATION TO BE PLACED ON SHIELD SIDE OF CABLE IN THIS AREA AS SHOWN.
3. INSPECTION & TEST STAMPS TO BE PLACED AT EACH END OF THE CABLE ASSY.
4. STRIP LENGTH SHOULD BE  $.62 \pm 4/32$ .



REV.	CHANGE NO.	REV.
1	BC0BR-0000	L
2	BC0BR-0000	L
3	BC0BR-0000	L
4	BC0BR-0000	L
5	BC0BR-0000	L
6	BC0BR-0000	L
7	BC0BR-0000	L
8	BC0BR-0000	L

2	CONNECTOR, 4P SOCKET	12/1206	2
1	4P CABLE, 40 COND. FLAT W/SHIELD	1700004	1
DESCRIPTION		DWG./PART NO.	ITEM NO.
UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES			
ANGLES ±0° 30'	CLASS OF ACCURACY	NOMINAL DIMENSION RANGE INCHES	
SURFACE QUALITY IN	MEDIUM	OVER 0 TO 0.2	OVER 0.2 TO 0.4
		OVER 0.4 TO 0.6	OVER 0.6 TO 0.8
QUANTITY & VARIATION	PREFERRED	±.004	±.008
THIRD ANGLE PROJECTION	MICROINCHES	±.012	±.016
REMOVE BURRS AND BREAK SHARP CORNERS	DO NOT SCALE DWG	±.025	±.04
MATERIAL	SCALE	±.063	±.1
FINISH	SHEET	OF	DIST.

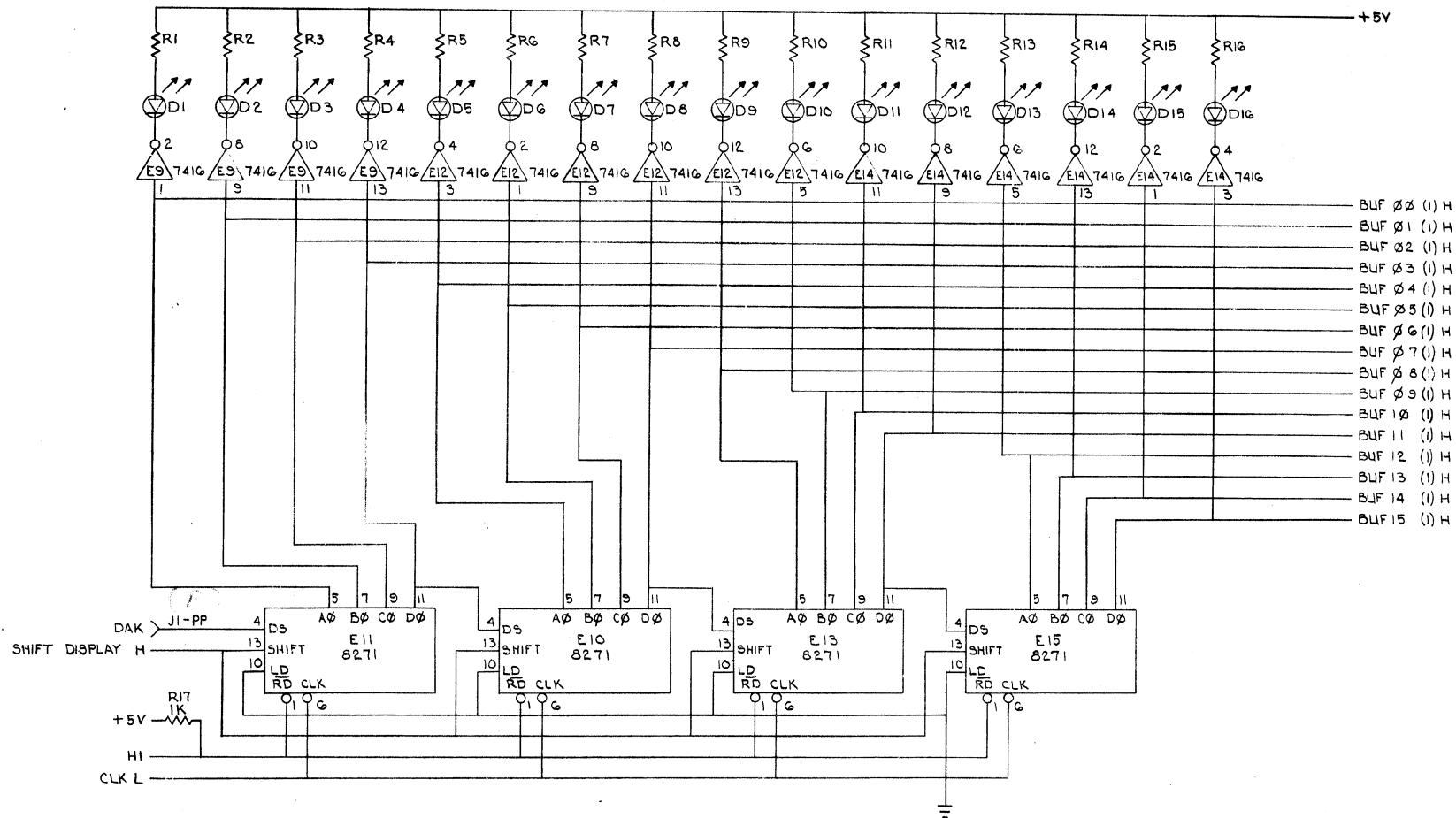
  

DRND. FONTAINE	5-28-70	FIRST USED ON	digital
CHK'D FLEMING	5-3-70	TITLE	BC0BR
ENGP GARDNER	5-3-70		TO CABLE
PROJ. ENG GARDNER	9-2-71		
PROD. DONALL	9-4-71		

REV. M  
NUMBER BC0BR-0-0  
SIZE CODE DUA

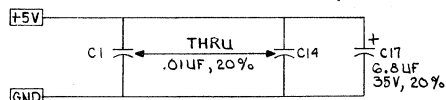
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CS 5409766-0-1



- BUF 00 (1) H
- BUF 01 (1) H
- BUF 02 (1) H
- BUF 03 (1) H
- BUF 04 (1) H
- BUF 05 (1) H
- BUF 06 (1) H
- BUF 07 (1) H
- BUF 08 (1) H
- BUF 09 (1) H
- BUF 10 (1) H
- BUF 11 (1) H
- BUF 12 (1) H
- BUF 13 (1) H
- BUF 14 (1) H
- BUF 15 (1) H

UNLESS OTHERWISE INDICATED:  
 RESISTORS= 1/4W, 5%  
 CAPACITORS= 100V, 5%  
 DIODES ARE LIGHT EMITTING  
 PIN 14=+5V, PIN 7=GND ON DEC 7404, 7416, 7417  
 PIN 14=+5V, PIN 7=GND ON DEC 8271, 74123, 74193



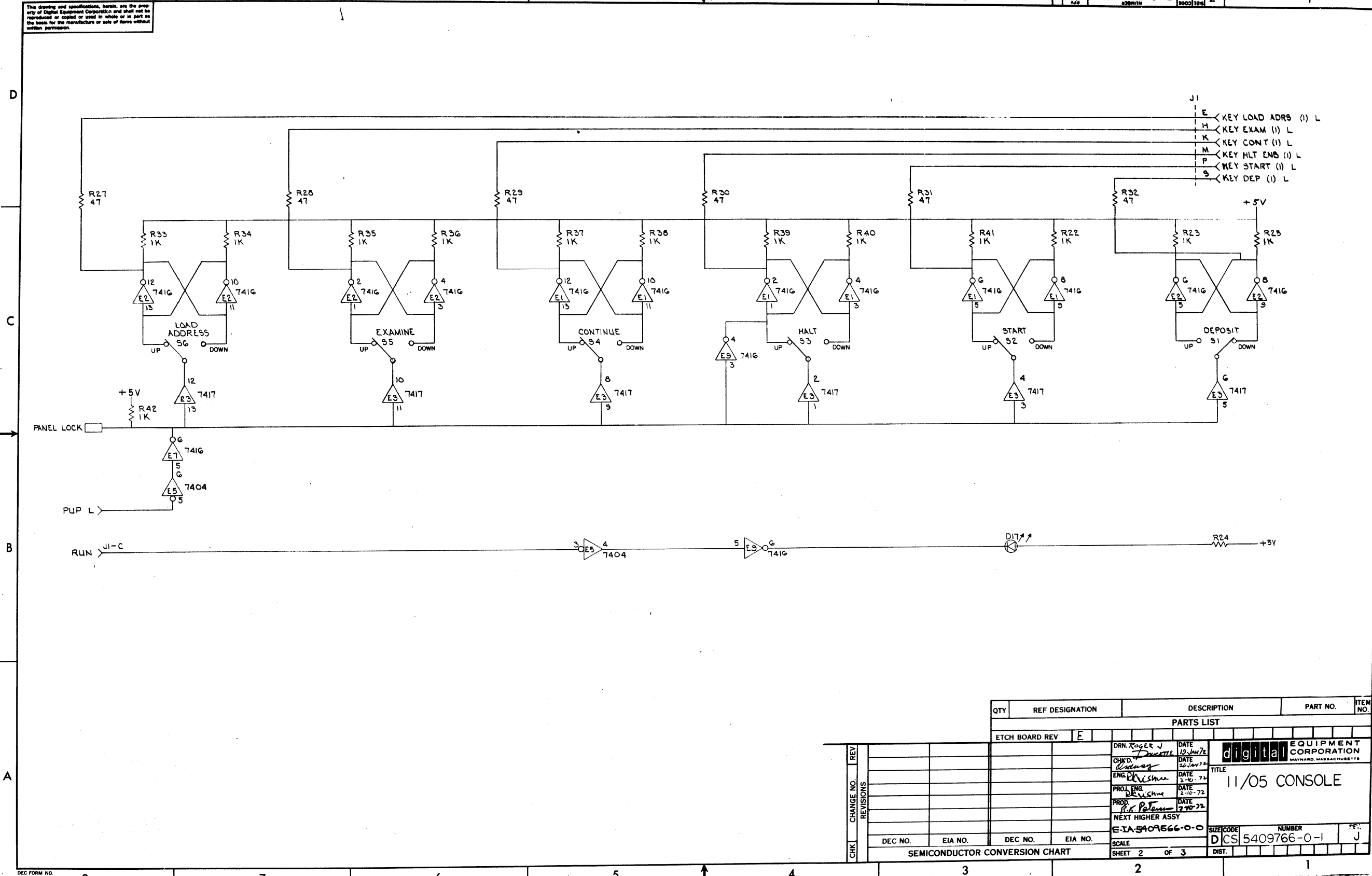
DESIGNED BY	B. ARMSTRONG
DATE	5/1/74
CHECKED BY	R. KRISHNA
DATE	10-25-72
ENGINEER	R. KRISHNA
DATE	10-25-72
PROJ. ENG.	R. KRISHNA
DATE	10-25-72
PROD. ENG.	R. KRISHNA
DATE	10-25-72
NEXT HIGHER ASSY	EIA5409766-0-0

QTY	REF DESIGNATION	DESCRIPTION	PART NO.	ITEM NO.
PARTS LIST				
ETCH BOARD REV E				
DRN. ROGER J. DATE 10-25-72		<b>digital EQUIPMENT CORPORATION</b> MAYNARD, MASSACHUSETTS TITLE <b>11/05 CONSOLE</b>		
CHKD. R. KRISHNA DATE 10-25-72				
ENGR. R. KRISHNA DATE 10-25-72				
PROJ. ENG. R. KRISHNA DATE 10-25-72				
PROD. ENG. R. KRISHNA DATE 10-25-72				
NEXT HIGHER ASSY EIA5409766-0-0		SIZE CODE DICS 5409766-0-1	NUMBER J	REV. J
SEMICONDUCTOR CONVERSION CHART				
DEC NO.	EIA NO.	DEC NO.	EIA NO.	
SCALE		SHEET OF 3		

DICS 5409766-0-1 J

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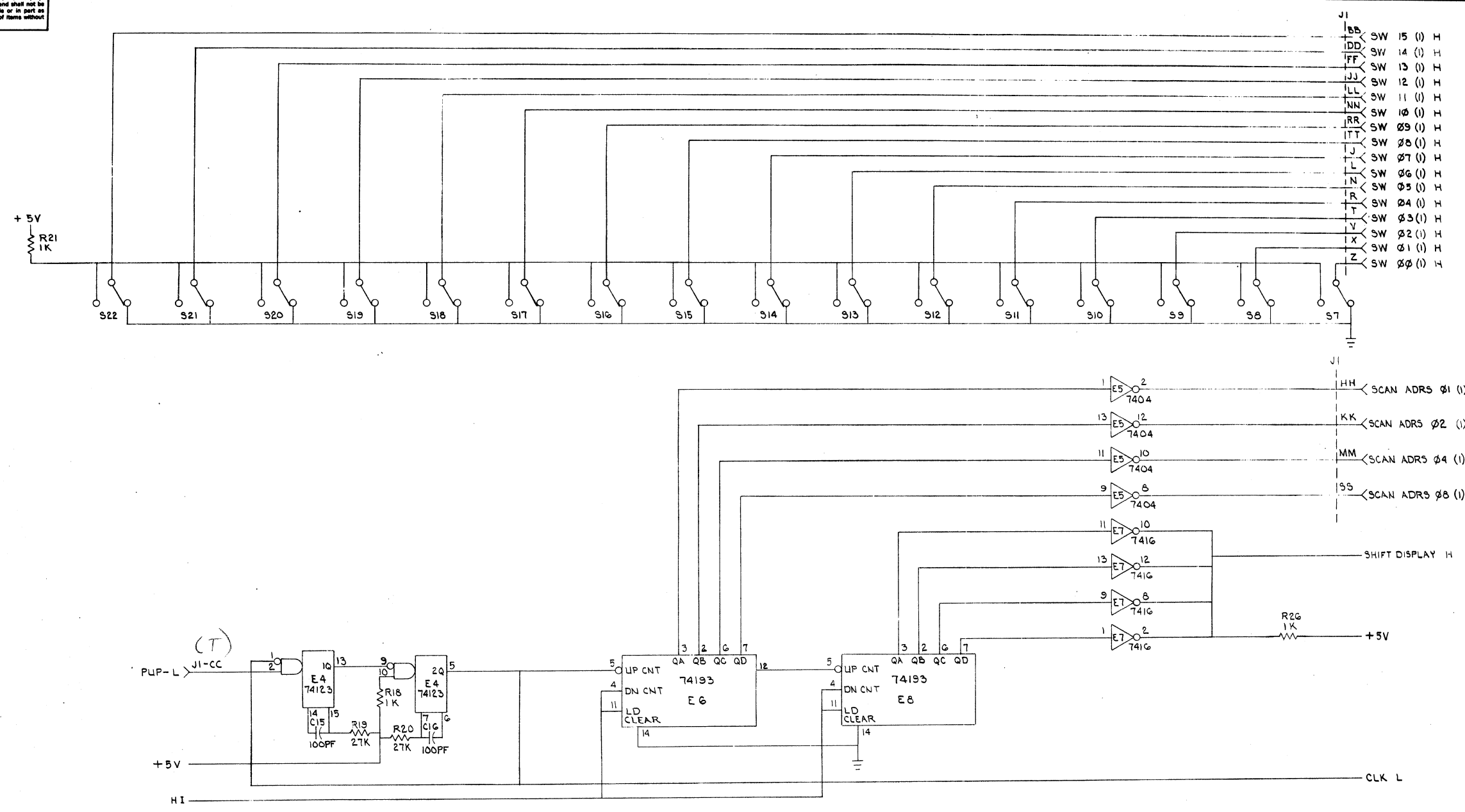
DIGITAL EQUIPMENT CORPORATION  
 5409766-0-1 J 2



QTY	REF DESIGNATION	DESCRIPTION	PART NO.	ITEM NO.
PARTS LIST				
ETCH BOARD REV E				
DRN. Roger J		DATE 19 JAN 72	<b>digital EQUIPMENT CORPORATION</b> MAYNARD, MASSACHUSETTS TITLE 11/05 CONSOLE	
CHKD. <i>[Signature]</i>		DATE 22 JAN 72		
ENG. <i>[Signature]</i>		DATE 2-10-72		
PROJ. ENG. <i>[Signature]</i>		DATE 2-10-72		
PROD. <i>[Signature]</i>		DATE 2-10-72		
NEXT HIGHER ASSY			E-1A 5409766-0-0	
DEC. NO.		EIA NO.	DEC. NO.	EIA NO.
SCALE		SHEET 2 OF 3		DIST.

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CS 5409766-0-1 J



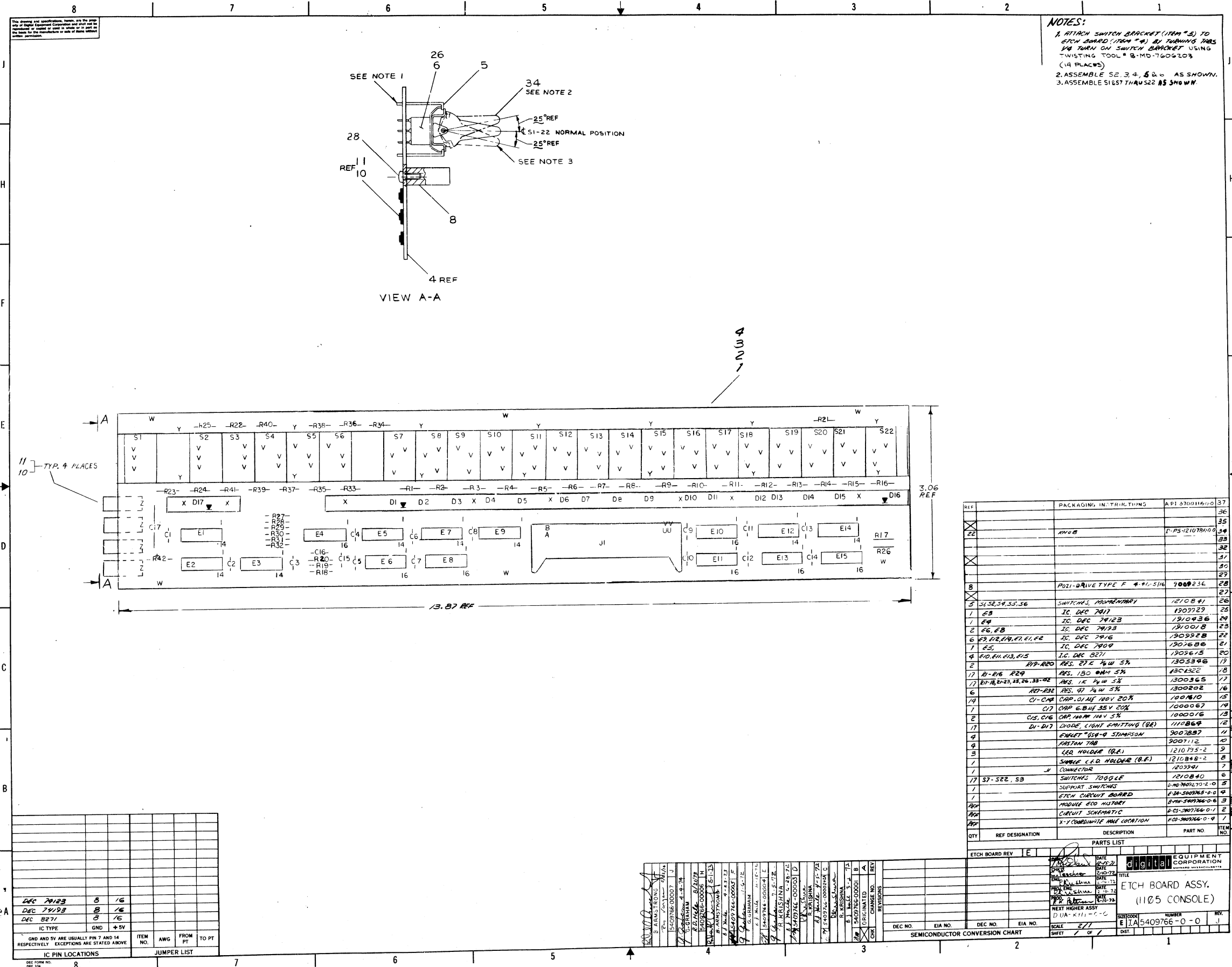
- J1 SW 15 ( ) H
- IDD SW 14 ( ) H
- FF SW 13 ( ) H
- JJ SW 12 ( ) H
- LL SW 11 ( ) H
- NN SW 10 ( ) H
- RR SW 09 ( ) H
- TT SW 08 ( ) H
- J SW 07 ( ) H
- L SW 06 ( ) H
- N SW 05 ( ) H
- R SW 04 ( ) H
- T SW 03 ( ) H
- V SW 02 ( ) H
- X SW 01 ( ) H
- Z SW 00 ( ) H

- J1 HH SCAN ADRS 01 ( ) L
- KK SCAN ADRS 02 ( ) L
- MM SCAN ADRS 04 ( ) L
- SS SCAN ADRS 08 ( ) L

QTY	REF DESIGNATION	DESCRIPTION	PART NO.	ITEM NO.
PARTS LIST				
ETCH BOARD REV E				
DRN. <i>Roger J. Poulette</i>		DATE 20 JAN 72	<b>digital EQUIPMENT CORPORATION</b> MAYNARD, MASSACHUSETTS TITLE <h1>11/05 CONSOLE</h1>	
CHKD. <i>Rodney</i>		DATE 26 JAN 72		
ENGR. <i>Chas. Shwa</i>		DATE 2-10-72		
PROJ. ENG. <i>Chas. Shwa</i>		DATE 2-10-72		
PROD. <i>R. Peters</i>		DATE 2-10-72		
NEXT HIGHER ASSY E-1A-5409766-0-0				
DEC NO.		EIA NO.	DEC NO.	EIA NO.
SCALE		SHEET 3 OF 3		DIST.

REV	CHANGE NO.	REVISIONS

SIZE CODE NUMBER  
 DCS 5409766-0-1 J



**NOTES:**  
 1. ATTACH SWITCH BRACKET (ITEM #5) TO ETCH BOARD (ITEM #8) BY TURNING TABS 1/8 TURN ON SWITCH BRACKET USING TWISTING TOOL # B-MD-7606203 (14 PLACES)  
 2. ASSEMBLE S2, 3, 4, 5 & 6 AS SHOWN.  
 3. ASSEMBLE S1 & S7 THRU S22 AS SHOWN.

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DEC NO.	REV.	DATE	BY	CHKD.	DESCRIPTION
DEC 79123	3	16			
DEC 79123	3	16			
DEC 8271	3	16			

IC TYPE	GND	+5V

ITEM NO.	AWG	FROM PT	TO PT

NO.	NAME	DATE	REV.
1	B. ARMSTRONG	12-17-73	1
2	S. J. MULLIS	12-17-73	1
3	R. MARSHALL	12-17-73	1
4	R. MARSHALL	12-17-73	1
5	R. MARSHALL	12-17-73	1
6	R. MARSHALL	12-17-73	1
7	R. MARSHALL	12-17-73	1
8	R. MARSHALL	12-17-73	1
9	R. MARSHALL	12-17-73	1
10	R. MARSHALL	12-17-73	1
11	R. MARSHALL	12-17-73	1
12	R. MARSHALL	12-17-73	1
13	R. MARSHALL	12-17-73	1
14	R. MARSHALL	12-17-73	1
15	R. MARSHALL	12-17-73	1
16	R. MARSHALL	12-17-73	1
17	R. MARSHALL	12-17-73	1
18	R. MARSHALL	12-17-73	1
19	R. MARSHALL	12-17-73	1
20	R. MARSHALL	12-17-73	1
21	R. MARSHALL	12-17-73	1
22	R. MARSHALL	12-17-73	1
23	R. MARSHALL	12-17-73	1
24	R. MARSHALL	12-17-73	1
25	R. MARSHALL	12-17-73	1
26	R. MARSHALL	12-17-73	1
27	R. MARSHALL	12-17-73	1
28	R. MARSHALL	12-17-73	1
29	R. MARSHALL	12-17-73	1
30	R. MARSHALL	12-17-73	1
31	R. MARSHALL	12-17-73	1
32	R. MARSHALL	12-17-73	1
33	R. MARSHALL	12-17-73	1
34	R. MARSHALL	12-17-73	1
35	R. MARSHALL	12-17-73	1
36	R. MARSHALL	12-17-73	1
37	R. MARSHALL	12-17-73	1
38	R. MARSHALL	12-17-73	1
39	R. MARSHALL	12-17-73	1
40	R. MARSHALL	12-17-73	1
41	R. MARSHALL	12-17-73	1
42	R. MARSHALL	12-17-73	1
43	R. MARSHALL	12-17-73	1
44	R. MARSHALL	12-17-73	1
45	R. MARSHALL	12-17-73	1
46	R. MARSHALL	12-17-73	1
47	R. MARSHALL	12-17-73	1
48	R. MARSHALL	12-17-73	1
49	R. MARSHALL	12-17-73	1
50	R. MARSHALL	12-17-73	1

REF	DESCRIPTION	PART NO.	QTY
37	PACKAGING INSTRUCTIONS	API 370016-0-0	1
36			1
35			1
34	KNOB	F-PS-21078-0-0	1
33			1
32			1
31			1
30			1
29			1
28	PULL-DRIVE TYPE F 4-11-516	9009236	1
27			1
26	SWITCHES, MOMENTARY	1210841	1
25	IC, DEC 7917	1209929	1
24	IC, DEC 79123	1210436	1
23	IC, DEC 79123	1210018	1
22	IC, DEC 7916	1209928	1
21	IC, DEC 7909	1207688	1
20	IC, DEC 8271	1205615	1
19	RES, 27K 1/4W 5%	1203396	1
18	RES, 150 OHM 5%	1201322	1
17	RES, 1K 1/4W 5%	1200365	1
16	RES, 91 1/4W 5%	1200202	1
15	CAP, 0.1UF 100V 20%	1200110	1
14	CAP, 6.8UF 35V 20%	1200067	1
13	CAP, 100UF 16V 5%	1200016	1
12	DIODE, LIGHT EMITTING (RED)	1110869	1
11	SOLENOID, 9V STIMPSON	9007897	1
10	FASTON TAB	9007112	1
9	LEO HOLDER (R.A.)	1210849-2	1
8	SAMPLE C.B. HOLDER (R.A.)	1203941	1
7	CONNECTOR	1210840	1
6	SWITCHES TOGGLE	1210840	1
5	SUPPORT SWITCHES	1210840	1
4	ETCH BOARD BOARD	1210840	1
3	MODULE 510 METER	1210840	1
2	CIRCUIT SCHEMATIC	1210840	1
1	X-Y COORDINATE MAP LOCATION	1210840	1

ETCH BOARD REV	DATE	BY	CHKD.	REV.
E	12-17-73	R. MARSHALL	R. MARSHALL	1

DEC NO.	EIA NO.	DEC NO.	EIA NO.

**DIGITAL EQUIPMENT CORPORATION**  
**ETCH BOARD ASSY.**  
 (1105 CONSOLE)

SCALE: 2/1  
 SHEET: 1 OF 1





# DRAWING DIRECTORY

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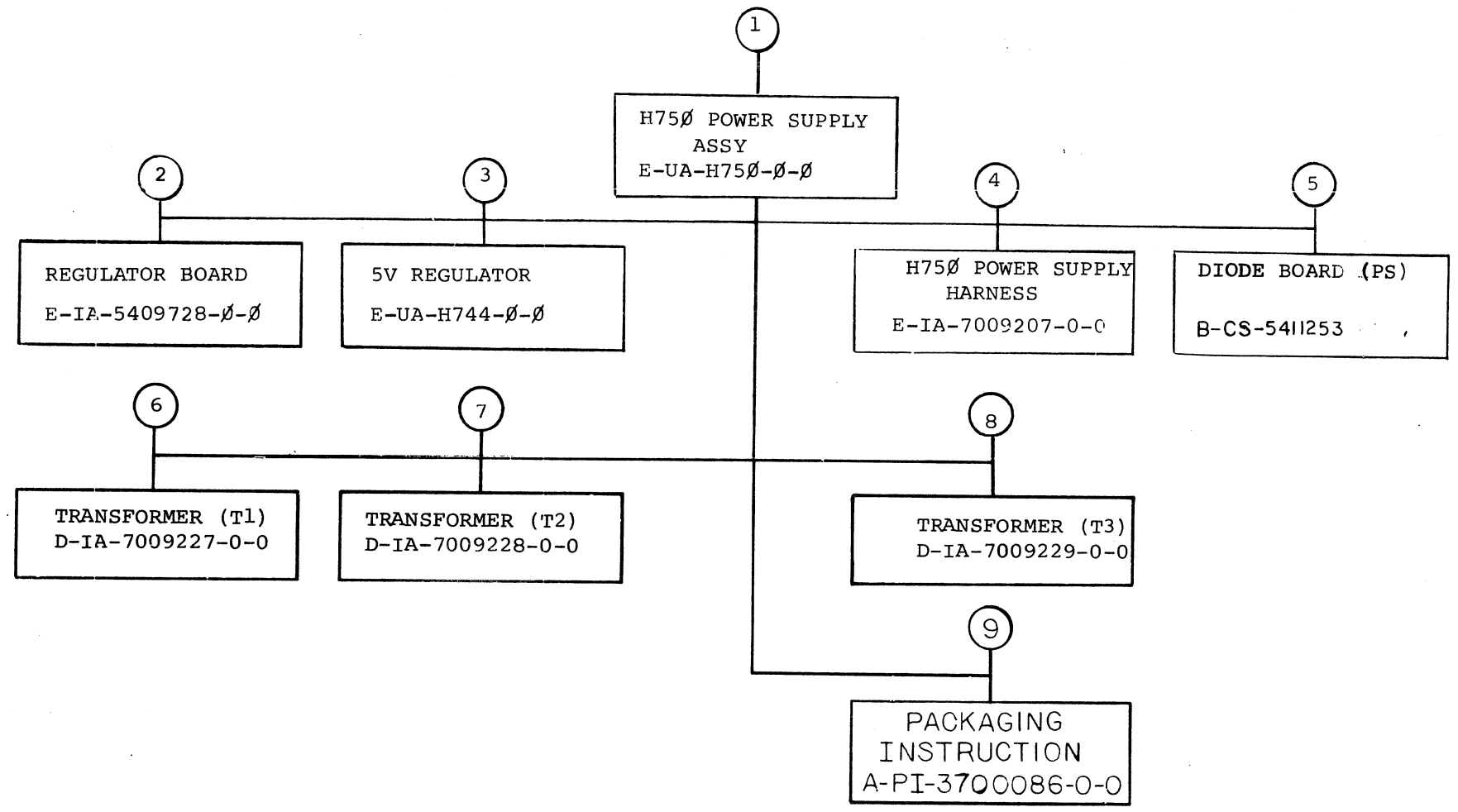
## CUSTOMER PRINT SET INDEX

THIS IS PRINT SET

	SEQUENCE		SEQUENCE
DRAWING DIRECTORY	B-DD-H750-0	MFG PRINT SET	A-PI-3700086-0-0
UNIT ASSEMBLY	E-UA-H750-0-0		
CIRCUIT SCHEMATIC	D-CS-H750-0-1	PACKAGING INSTRUCTION	
DIODE BOARD, P.S.			
CIRCUIT SCHEMATIC	B-CS-5411253		
REGULATOR BOARD	E-IA-5409728-0-0		
CIRCUIT SCHEMATIC	D-CS-5409728-0-1		
5V REGULATOR BOARD	E-UA-H744-0-0		
5V REGULATOR	D-CS-H744-0-1		
H750 POWER SUPPLY HARNESS	E-IA-7009207-0-0		

UNIT VARIATIONS		PRINT SET		
VAR	TITLE			
H750-0	H750 POWER SUPPLY	X		

REVISIONS		USED ON OPTION/MODEL		DRN.		DATE		TITLE	
DATE	CHG. NO.	REV		CHK'D	DATE	SIZE	CODE	NUMBER	REV
3-73	H750-00001	A	11/35	<i>Paul Cahill</i>	1-15-72	B	DD	H750-0	M
5-73	H750-2	B		<i>Paul Cahill</i>	1-16-72				
6-73	H750-3	C		<i>R.J. Wolf</i>	2/12/73				
7-73	H750-4	D		<i>H. Stinger</i>	2-21-73				
8-73	H750-5	E		<i>W. Drumlet</i>	2/13/73				
11-73	H750-6	F							
11-73	H750-7	H							
2-74	H750-8	J							
5-74	H750-9	K							
9-74	H750-11	L							
1-75	H750-13	M							
SHEET 1 OF 3									

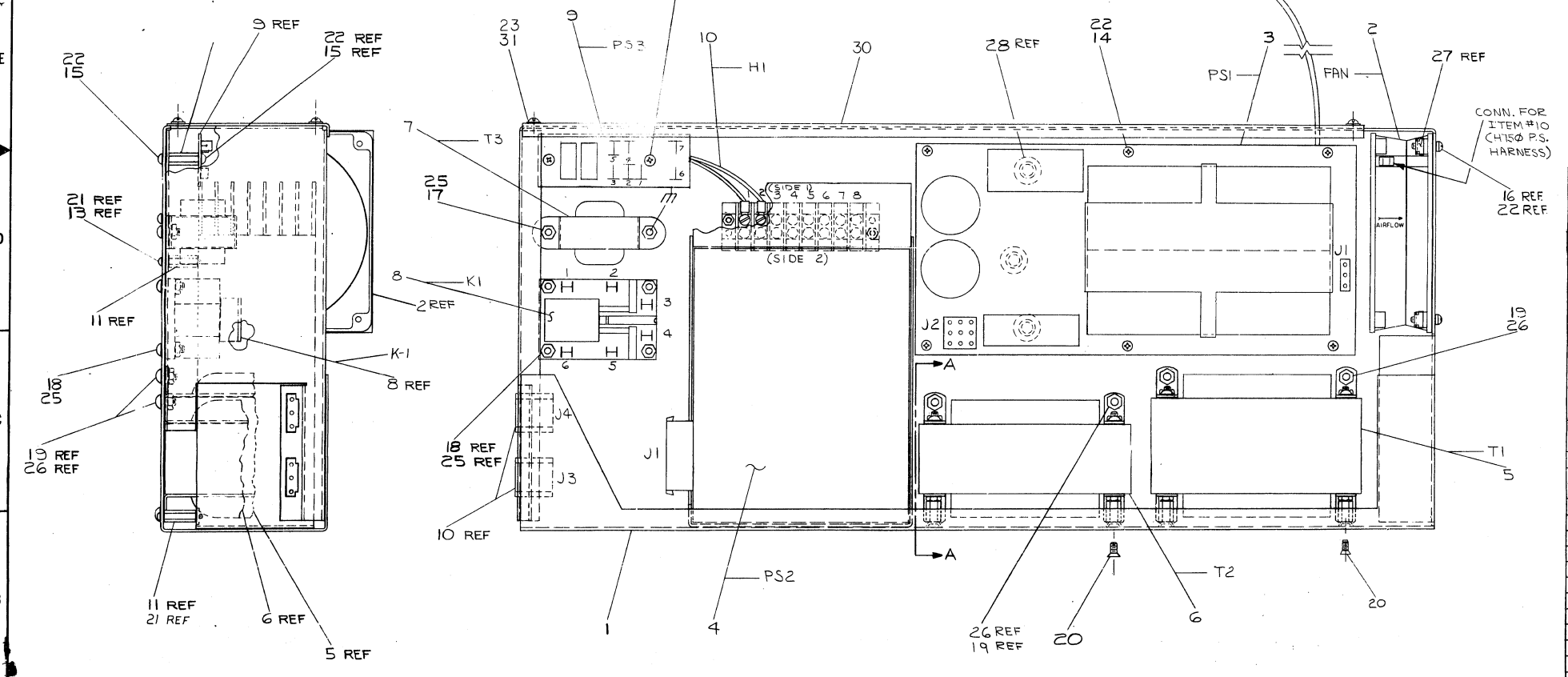
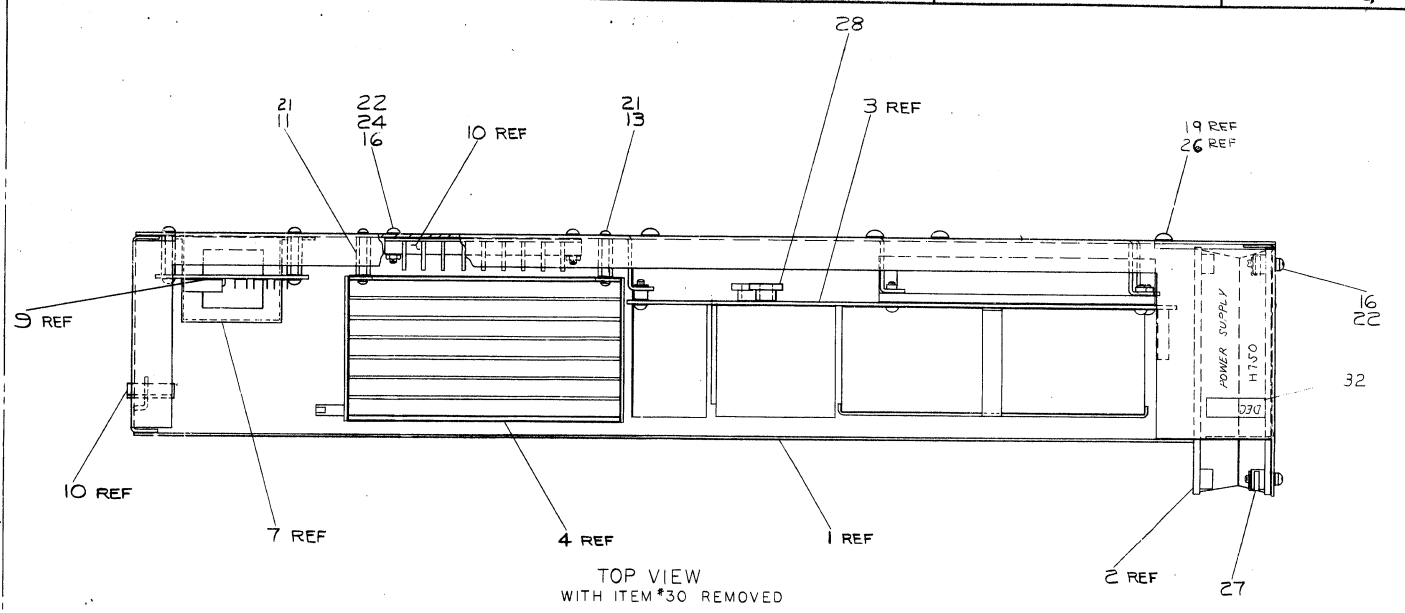
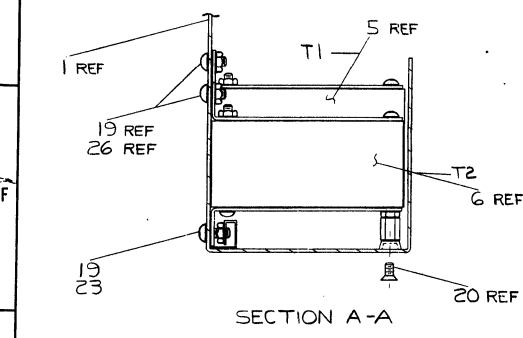


TITLE	SHEET 2 OF 3	SIZE CODE	NUMBER	REV
POWER SUPPLY H750		B DD	H750-0	M

CUSTOMER PRINT SET	ELECTRICAL					CUSTOMER PRINT SET	MECHANICAL					
	H75Ø-Ø	MFG. SET	FIND NO.	DRAWING NO.	REV NO OF SHT		OPTION NO./FILE DATE	H75Ø-Ø	MFG. SET	FIND NO.	DRAWING NO.	REV NO OF SHT
X	X	1	E-UA-H75Ø-Ø-Ø D-CS-H75Ø-Ø-1 A-SP-H75Ø-Ø-3 1211222	2 D A 1	H75Ø POWER SUPPLY ASSY CIRCUIT SCHEMATIC (H75Ø PS.) TEST PROCEDURE RELAY			X	1	E-UA-H75Ø-Ø-Ø E-IA-7409723-0-0 1209403 C-MD-7409722-0-0 A-DC-7411006-0-0	2 # 1 #	H75Ø POWER SUPPLY ASSY CHASSIS POWER SUPPLY FAN SUPER BOXER COVER, POWER SUPPLY DECAL
		2	E-IA-5409728-0-0 D-CS-5409728-0-1 K-CO-5409728-0-4 B-MH-5409728-0-6 5009727	# # 1 1 1	REGULATOR BOARD CIRCUIT SCHEMATIC X-Y COORDINATE HOLE LOC. MODULE ECO HISTORY ETCH BOARD				2	E-IA-5409728-0-0 D-CS-5409728-0-1 C-IA-5310126-0-0 C-MD-5309779-0-0 C-MD-5309781-0-0	# # 1 1 1	REGULATOR BOARD CIRCUIT SCHEMATIC HOLDER CAPACITOR CONTACT, COMMON CAPACITOR CONTACT, CAPACITOR
		3	E-UA-H744-Ø-Ø D-CS-H744-Ø-1 B-MH-H744-Ø-4 K-CO-H744-Ø-5 D-AH-H744-Ø-6 5009725	# # 1 1 1 1	5V REGULATOR CIRCUIT SCHEMATIC X-Y COORDINATE HOLE LOCATION ASSY/DRILLING HOLE LOCATION MODULE ECO HISTORY ETCH BOARD				3	E-UA-H744-Ø-Ø D-CS-H744-Ø-1	# #	5V REGULATOR CIRCUIT SCHEMATIC
		4	E-IA-7009207-0-0	#	H75Ø POWER SUPPLY HARNESS				4	E-IA-7009207-0-0	#	H75Ø POWER SUPPLY HARNESS
		5	B-CS-5411253 K-CO-5411253 B-MH-5411253 E-IA-5011252	# 1 1 1	DIODE BOARD PS. CIRCUIT SCHEMATIC X-Y COORDINATE HOLE LOC. MODULE ECO HISTORY ETCH CIRCUIT BOARD				5	B CS-5411253	#	DIODE BOARD P.S. CIRCUIT SCHEMATIC
		6	D-IA-7009227-0-0 1611224	# 1	TRANSFORMER REWORK (T1) TRANSFORMER				6	D-IA-7009227-0-0 1611224	# 1	TRANSFORMER REWORK (T1) TRANSFORMER
		7	D-IA-7009228-0-0 1611225	# 1	TRANSFORMER REWORK (T2) TRANSFORMER				7	D-IA-7009228-0-0 1611225	# 1	TRANSFORMER REWORK (T2) TRANSFORMER
		8	D-IA-7009229-0-0 1610150	# 1	TRANSFORMER REWORK (T3) TRANSFORMER				8	D-IA-7009229-0-0 1610150	# 1	TRANSFORMER REWORK (T3) TRANSFORMER
		9						X	9	A-PI-3700089-0-0 A PS 9905237-0-0 A PS 9905238-0-0 A PS 9905239-0-0	# 2 2 2 2	PACKAGING INSTRUCTION HALF SLOTTED CARTON INNER HALF SLOTTED CARTON OUTER LAMINATED PIECE

CUSTOMER PRINT SET CODES	X = PRINT OF DOCUMENT INCLUDED IN PRINT SET C = INCLUDES ALL PRINTS INDICATED ON DOCUMENT S = CONFIDENTIAL AUTHORIZED SIGNATURE REQUIRED	TITLE POWER SUPPLY H75Ø	SHEET 3 OF 3	SIZE CODE B DD	NUMBER H75Ø-Ø	REV M
--------------------------	--	----------------------------	--------------	-------------------	------------------	----------

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NOTES:  
1. FOR EASE IN HANDLING AND MANUFACTURING, ITEMS IN THE H750 POWER SUPPLY SHOULD BE ASSEMBLED AND INSTALLED IN THE CHASSIS (ITEM #1) IN THE FOLLOWING ORDER:  
A) THE RELAY (ITEM #8), TRANSFORMER REWORK (ITEM #7), DIODE BOARD (ITEM #9), HARNESS (ITEM #10), AND FAN (ITEM #2) SHOULD BE INSTALLED IN CHASSIS (ITEM #1) FIRST.  
B) TRANSFORMER (ITEM #5) AND TRANSFORMER (ITEM #6) TO CHASSIS.  
C) REGULATOR BOARD (ITEM #3) TO CHASSIS.  
D) REMOVE PLASTIC SHIELD AND SCR FROM ITEM #4 (H144) AND REPLACE WITH 4 ITEM #11, 8 ITEM #12, THEN INSTALL TO CHASSIS.

ITEM NO.	DESCRIPTION	PART NO.	QTY.
1	PACKAGING INSTRUCTION	A-PS-37000-0-0	33
1	DECAL	A-674100-6	33
4	SCREW PHIL TRUSS HD #0-32 X .30	9006070-3	31
1	COVER POWER SUPPLY	E-MD-7409722	30
REF	TEST PROCEDURE	A-SP-H750-0-1	29
3	BUSHING, NYLON FLANGE	9008426	28
4	CLIP, FAN MTC. #8	9009165	27
4	NUT, KEPS #10-32	9008565	26
8	NUT, KEPS #8-32	9008563	25
4	NUT, KEPS #6-32	9008560	24
8	WASHER, INT. TOOTHLOCK #10	9008635	23
18	WASHER, INT. TOOTHLOCK #8	9008633	22
4	WASHER, INT. TOOTHLOCK #4	9008632	21
4	SCREW, PHIL FLAT HD #8-32 X .25 L	9006035-2	20
8	SCREW PHIL TRUSS HD #10-32 X .44 L	9006072-3	19
4	SCREW PHIL TRUSS HD #8-32 X .62 L	9006040-3	18
2	SCREW PHIL TRUSS HD #6-32 X .38 L	9006037-3	17
18	SCREW, PHIL PAN HD #6-32 X .62 L	9006025-1	16
4	SCREW, PHIL TRUSS HD #6-32 X .38 L	9006022-3	15
6	SCREW, PHIL PAN HD #6-32 X .50 L	9006024-1	14
4	SCREW, PHIL PAN HD #4-40 X .31 L	9006010-1	13
2	SPACER #8-32 X .75 L	9006859	12
4	STANDOFF #4-40 X .75 HD, NILE & FEM	9009284	11
1	H750 POWER SUPPLY HARNESS (H1)	E-IA-700820P-0	10
1	DIODE BOARD P.S. (P53)	B-CS-541253	9
1	RELAY (K1)	1211222	8
1	TRANSFORMER REWORK (T3)	D-IA-7008229-0	7
1	TRANSFORMER REWORK (T2)	D-IA-7008228-0	6
1	TRANSFORMER REWORK (T1)	D-IA-7008247-0	5
1	SV REGULATOR (P52)	E-UA-W744-B-B	4
1	REGULATOR BOARD (P51)	E-IA-5488728-0	3
1	FAN, SUPER BOXER (118)	1208403-0	2
1	CHASSIS POWER SUPPLY	E-IA-7408722-0	1

**H750**  
UNLESS OTHERWISE SPECIFIED:  
DIMENSION IN INCHES  
TOLERANCES  
DECIMALS .005  
ANGLES 30° 30'  
X .1  
PROT. ENG. DATE 7/17/73  
DRN DATE 7/17/73  
REV. 1  
MATERIAL  
NEXT HIGHER ASSY.  
SEE PARTS LIST  
FINISH  
SCALE NONE  
SHEET 1 OF 2

**digital EQUIPMENT CORPORATION**  
TITLE  
**H750 POWER SUPPLY ASSY**  
PART NO. E-IA-H750-0-0  
REV. L

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WIRE TABLE				
ITEM NO.	DESCRIPTION	FROM CONNECTION	TO CONNECTION	REMARKS
		H1-P1		
		H1-J1	PS1-J3	
		H1-J2		
		H1-J3	TO CHASSIS	
		H1-J4	ITEM #1	
16	RED	H1-J5	TO PWR	
		H1-J6	ITEM #2	
10	WHT	H1-7	K1-6	
(11)	RED	H1-8	K1-3	
	WHT	H1-9	K1-4	
	ORN	H1-10	K1-5	
	VIO	H1-11	K1-2	
	RED	H1-12	K1-1	
	RED	H1-13	K1-1	
	VIO	H1-14	PS3-2	
	ORN	H1-15	PS3-3	
19	BLK	H1-16	PS3-5	
18	BLK	H1-17	PS3-2	
16	BLK	H1-49	PS3-5	
17	BLK	H1-50	T3-171	
		T1-J1	PS1-J1	
5	BLK	T1-1	H1-TBI-5	ALL CONN-
(71)	RED	T1-2	H1-TBI-6	CTIONS TO
	RED/WHT	T1-3	H1-TBI-7	H1-TBI ARE
14	BLK/WHT	T1-4	H1-TBI-8	ON SIDE #2.
		T2-P1		
6	BLK	T2-7	H1-TBI-5	ALL CONN-
(72)	RED	T2-8	H1-TBI-6	CTIONS TO
	RED/WHT	T2-9	H1-TBI-7	H1-TBI ARE
14	WHT	T2-10	H1-TBI-8	ON SIDE #2.
20	BRN	T3-1	H1-TBI-2	SIDE #1
	RED	T3-2	H1-TBI-5	SIDE #1
7	ORN	T3-3	H1-TBI-7	SIDE #1
(73)	YEL	T3-4	K1-6	
	GRN	T3-5	PS3-4	
	GRN	T3-6	PS3-1	
20	BLU	T3-7	H1-TBI-1	SIDE #2
18	BLK	K1-3	PS3-6	
(R1)	18	BLK	K1-4	PS3-7

FIRST USED ON OPTION MODEL	QTY.	DESCRIPTION	PART NO.	ITEM NO.
H750				
UNLESS OTHERWISE SPECIFIED DIMENSIONS IN INCHES TOLERANCES				
DECIMALS	ANGLES			
.001 - .010	± 30"			
.011 - .050	± 20"			
.051 - .100	± 15"			
REMOVE BURRS AND BREAK SHARP CORNERS. SURFACE QUALITY				
MATERIAL				
SEE PARTS LIST				
FINISH				
SCALE NONE				
SHEET 2 OF 2				

PARTS LIST

TITLE	DATE	DESIGNED BY	CHECKED BY	DATE	DATE	DATE
H750 POWER SUPPLY ASSY	1/15/73	K. V. J.		1/15/73		

digital EQUIPMENT CORPORATION

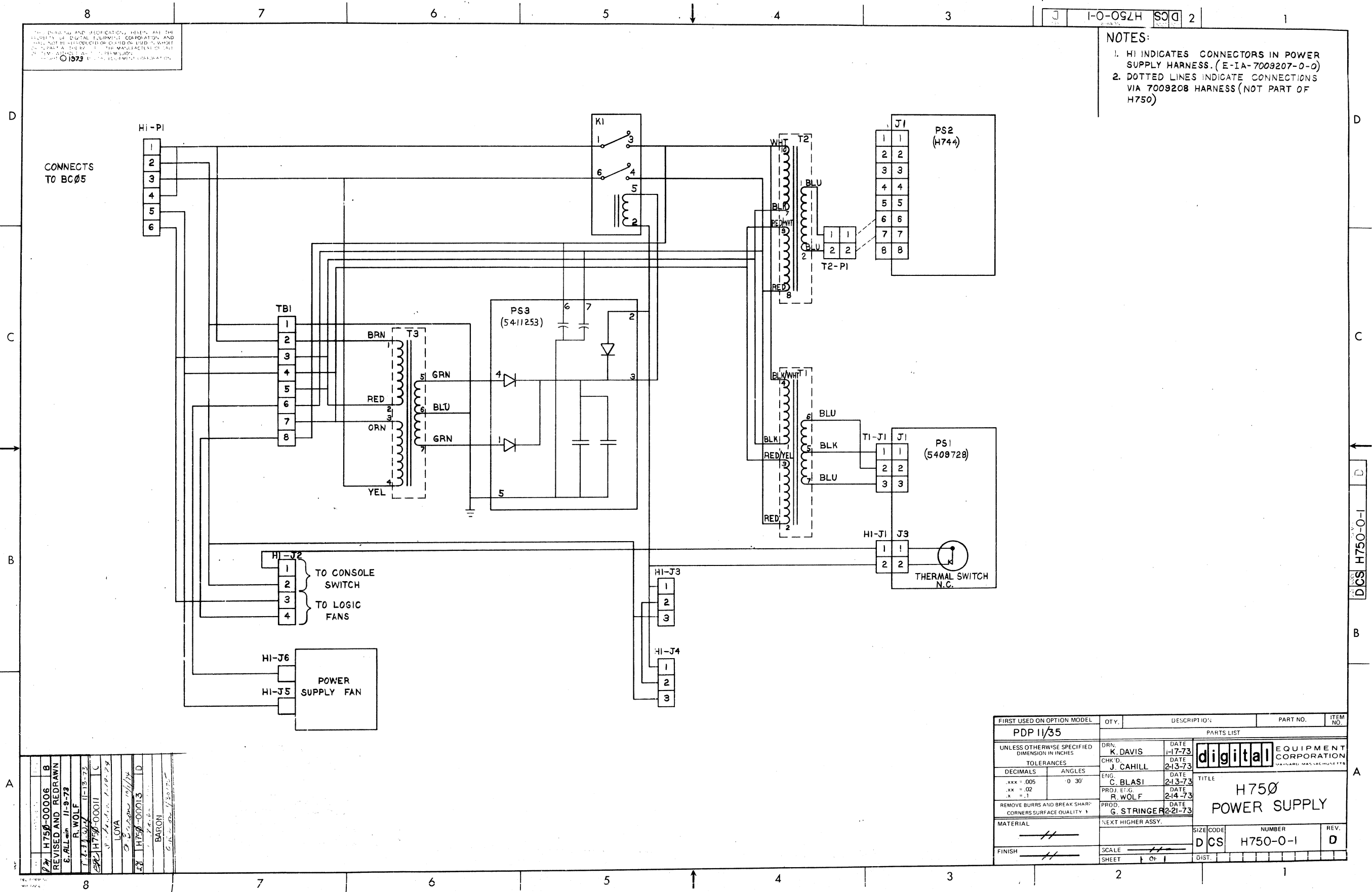
DRAWING NO. H750-0-0

REV. L

SHEET 2 OF 2

NOTES:

1. HI INDICATES CONNECTORS IN POWER SUPPLY HARNESS. (E-IA-7009207-0-0)
2. DOTTED LINES INDICATE CONNECTIONS VIA 7009208 HARNESS (NOT PART OF H750)



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REV. 1	H750-00006	B
REVISED AND REDRAWN		
E. ALLIN	11-9-73	
R. WOLF	11-13-73	
G. BROWN	11-13-73	
LOVA		
BARON		
REV. 2	H750-00013	D

FIRST USED ON OPTION MODEL	QTY.	DESCRIPTION	PART NO.	ITEM NO.
PDP 11/35				
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES				
TOLERANCES				
DECIMALS	ANGLES	DATE		
.xxx = .005	0 30'	DRN: K. DAVIS	1-17-73	
.xx = .02		CHK'D: J. CAHILL	2-13-73	
.x = .1		ENG: C. BLASI	2-13-73	
REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY 1				
NEXT HIGHER ASSY.				
MATERIAL				
FINISH				
SCALE		SHEET		
1 OF 1		DIST.		

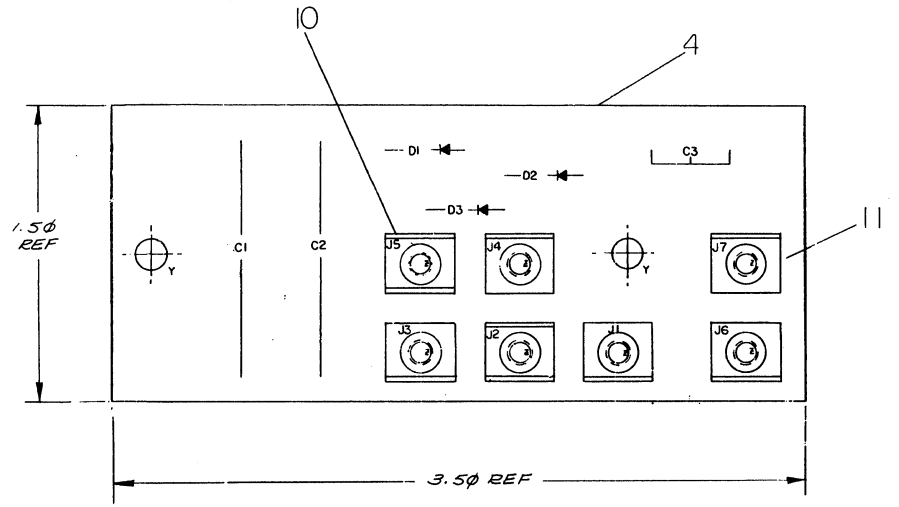
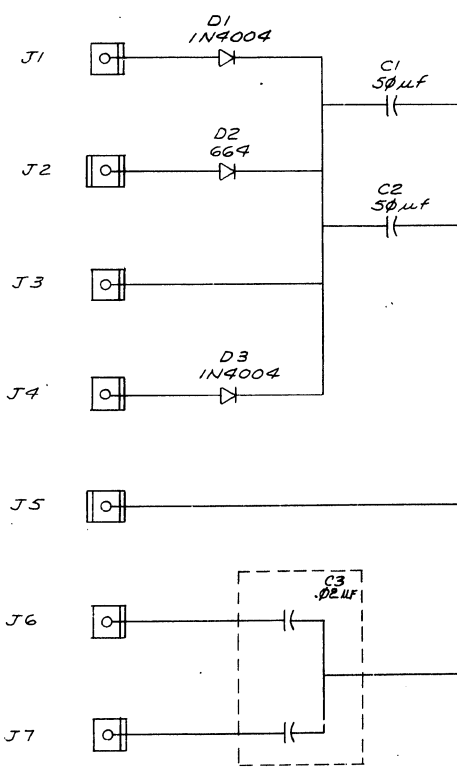
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H750 POWER SUPPLY

SIZE CODE: DCS NUMBER: H750-0-1 REV. D

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NOTES:



IC TYPE		GND	+5V

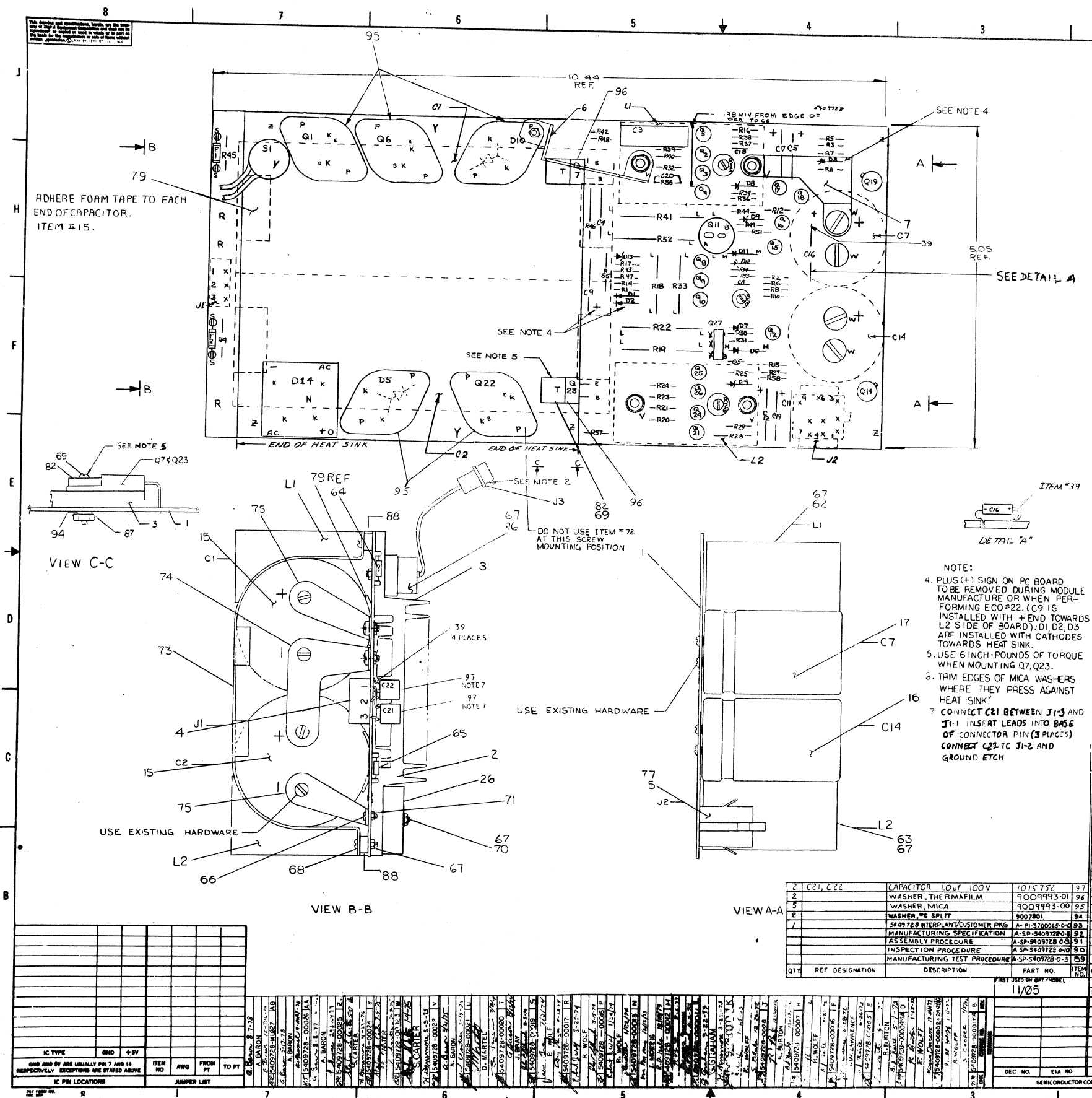
GND AND 5V ARE USUALLY PIN 7 AND 14 RESPECTIVELY EXCEPTIONS ARE STATED ABOVE

IC PIN LOCATIONS

REV	X-Y COORDINATE HOLE LOCATION	ITEM NO.
1		1
2		2
3		3
4		4
5		5
6		6
7		7
8		8
9		9
10		10
11		11

FIRST USED ON OPTION MODEL H750		ETCH BOARD REV. A		 TITLE DIODE BOARD	SIZE CODE D CS 5411253-0-1 NUMBER 1 REV. A
DRN. G. BROWN 2/10/74 DATE 2/10/74		DATE 2/10/74			
CHKD. G. BROWN DATE 10/24/74		DATE 10/24/74		SCALE SHEET 1 OF 1	
ENG. G. BROWN DATE 10/24/74		DATE 10/24/74		SEMICONDUCTOR CONVERSION CHART	
PROD. DATE NEXT HIGHER ASSY +		DATE DIST.			

DRAWING NUMBER: 5411253-0-1  
 REV: A  
 DATE CODE: DCS



NOTES:

- APPLY ITEM #8 (THERMAL COMPOUND) BETWEEN SI AND HEATSINK.
- TRIM LEADS ON ITEM #76 (THERMOSTAT) TO (5) INCHES AND ATTACH ITEM #84 (PINS) AND ITEM #83 (HOUSING) AS SHOWN.
- APPLY FLAT WASHER ITEM #72 BETWEEN SCR. HD. AND ETCHED BOARD WHEN MOUNTING COMPONENTS Q16, Q22, D5 + D10.

QTY	REF DESIGNATION	DESCRIPTION	PART NO.	ITEM NO.
2	C21, C22	CAPACITOR 1.0UF 100V	1015752	97
2		WASHER - THERMAFILM	900993-01	96
2		WASHER - MICA	900993-00	95
2		WASHER #5 SPLIT	900780	94
1		#49728 INTERPLANT/CUSTOMER PKG	A-SP-3409728-0-03	93
1		MANUFACTURING SPECIFICATION	A-SP-3409728-0-02	92
1		ASSEMBLY PROCEDURE	A-SP-3409728-0-01	91
1		INSPECTION PROCEDURE	A-SP-3409728-0-01	90
1		MANUFACTURING TEST PROCEDURE	A-SP-3409728-0-03	89
2		CAPACITOR 1.0UF 100V	1015752	97
1		WASHER - THERMAFILM	900993-01	96
1		WASHER - MICA	900993-00	95
1		WASHER #5 SPLIT	900780	94
1		#49728 INTERPLANT/CUSTOMER PKG	A-SP-3409728-0-03	93
1		MANUFACTURING SPECIFICATION	A-SP-3409728-0-02	92
1		ASSEMBLY PROCEDURE	A-SP-3409728-0-01	91
1		INSPECTION PROCEDURE	A-SP-3409728-0-01	90
1		MANUFACTURING TEST PROCEDURE	A-SP-3409728-0-03	89
1		WASHER #5 SPLIT	900780	94
1		WASHER - MICA	900993-00	95
1		WASHER - THERMAFILM	900993-01	96
1		CAPACITOR 1.0UF 100V	1015752	97
1		DIODE 1N4004	1105796	23
1		DIODE 1N53A 6.2V ZENER	1102481	22
1		DIODE 1N53B 24V ZENER	1101938	21
1		DIODE 1N484 3.9V ZENER	1100122	20
1		CAP 1000PF 100V DM	1000042	19
1		CAP 20UF 50V 5% STANT	1000002-01	18
1		CAP 6000UF 10V 10% AL EL	1010704	17
1		CAP 4500UF 25V 10% AL EL	1010703	16
2		CAP 2400UF 50V 10% AL EL	1010702	15
1		CAP 100UF 50V 10% AL EL	1002781	14
1		CAP 10UF 100V 10%	1011072	13
2		CAP 22UF 50V	1011072	12
2		CAP 6.8UF 35V 10% STANT	1005306	11
2		CAPACITOR 0.33UF 100V 10% MLAR	1000050	10
1		CAP 2200PF 250V	1000055	9
1		THERMAL COMPOUND	9009668	8
1		#2 THERMAL STRAP	C-MD-55098-1	7
1		#1 THERMAL STRAP	C-MD-58110-1	6
1		MATE-N-LOCK CONNECTOR	1209350-09	5
1		MATE-N-LOCK CONNECTOR	121242-03	4
1		HEAT SINK	1205996	3
1		SPLIT LUG	9007825	2
1		ETCHED BOARD	500927	1
1		MODULE ECO HISTORY	B-MH-3409728-0-04	REF
1		X-Y COORDINATE HOLE LOC	K-CD-9403728-0-04	REF
1		CIRCUIT SCHEMATIC	C-CS-3409728-0-1	REF

IC TYPE	QND	QBV	ITEM NO	AWG	FROM PT	TO PT
IC PIN LOCATIONS						
JUMPER LIST						
1						
2						
3						
4						
5						
6						
7						
8						

REGULATOR BOARD

EIA 5409728-0-0

ETCH BOARD REV #

11/05

SEMICONDUCTOR CONVERSION CHART

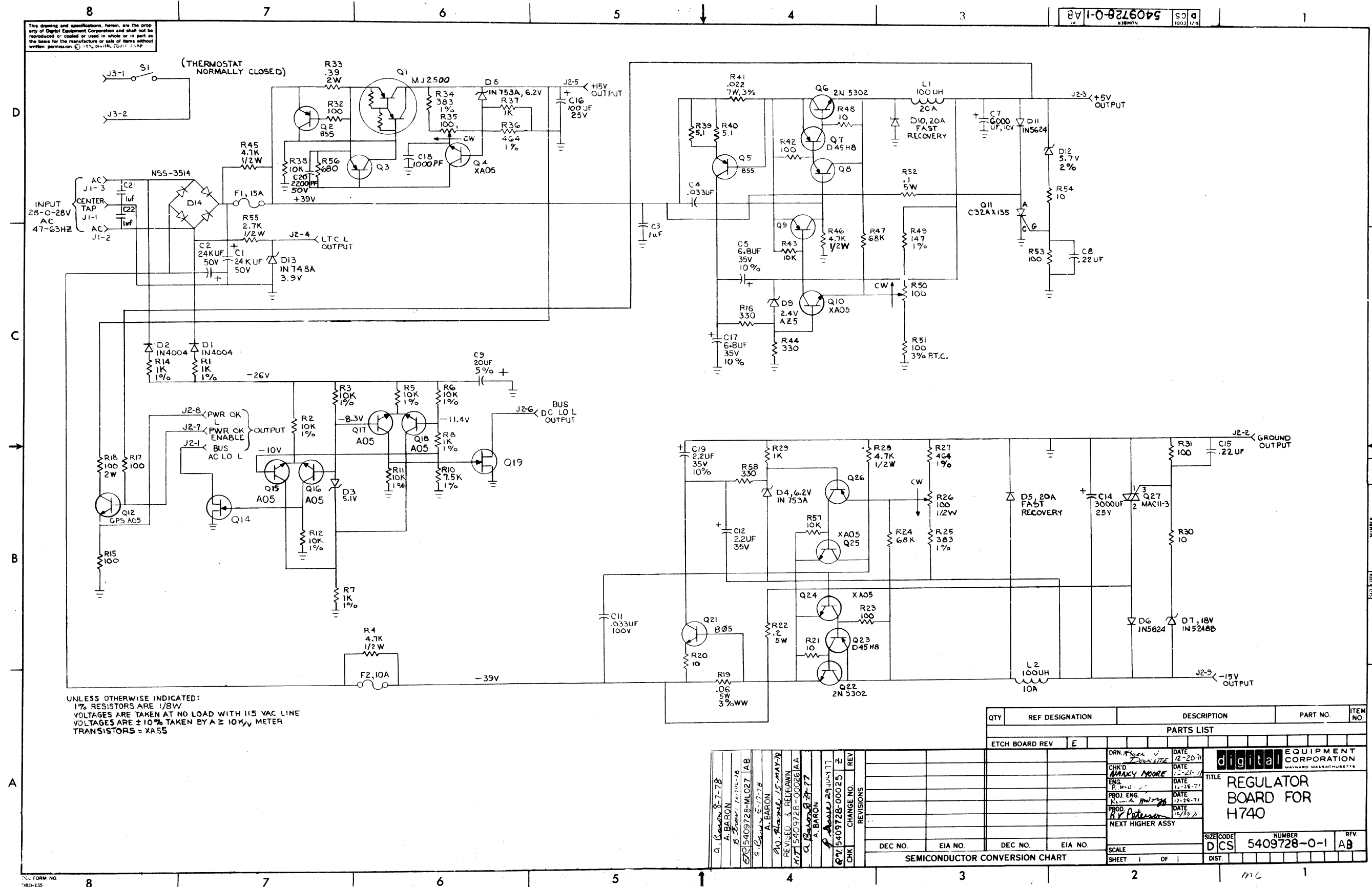
SCALE 2/1

DEC NO. EIA NO. DEC NO. EIA NO.

REGULATOR BOARD



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UNLESS OTHERWISE INDICATED:  
 1% RESISTORS ARE 1/8W  
 VOLTAGES ARE TAKEN AT NO LOAD WITH 115 VAC LINE  
 VOLTAGES ARE ± 10% TAKEN BY A ≥ 10k<sub>v</sub> METER  
 TRANSISTORS = XA55

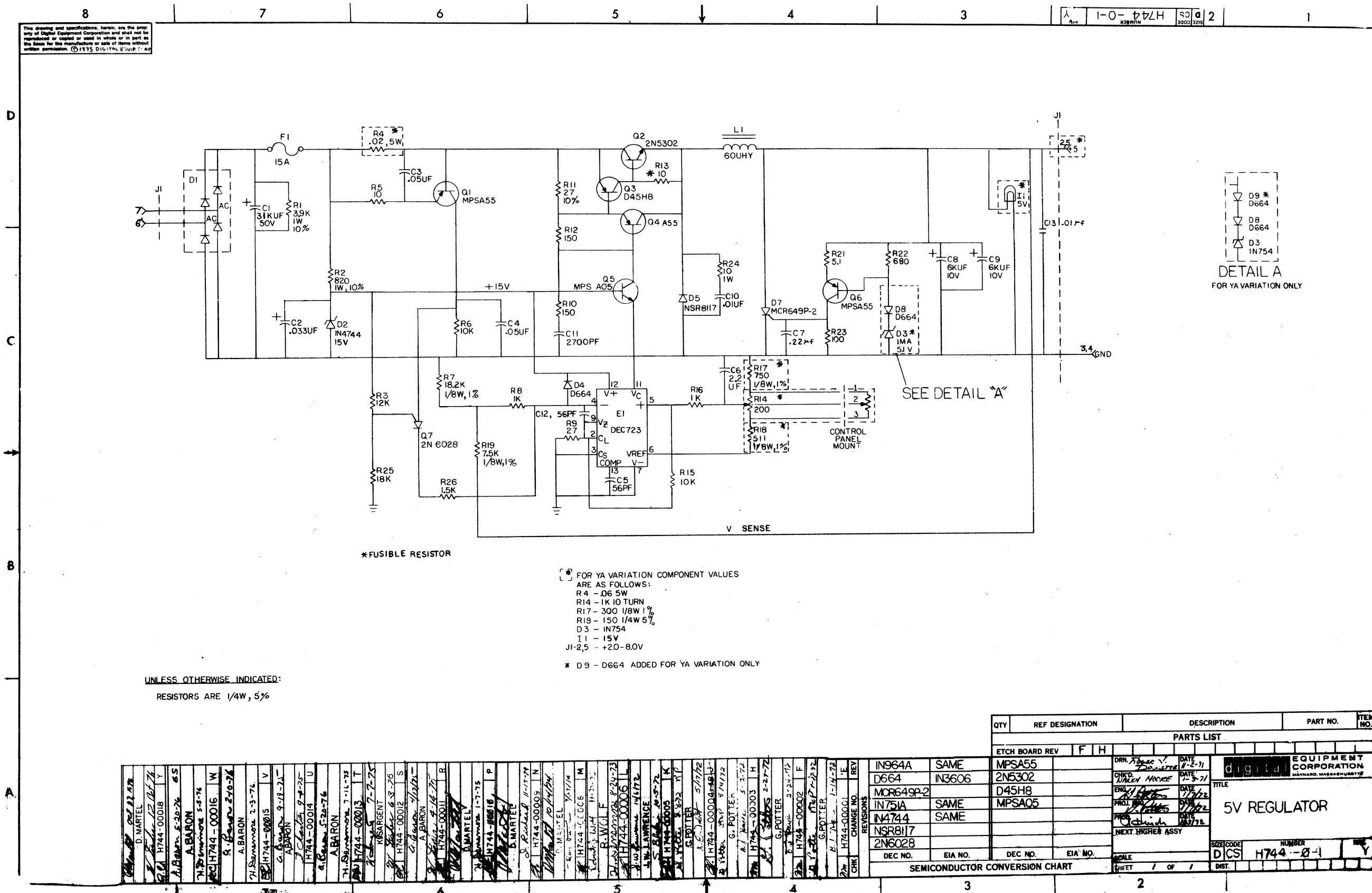
Q. BARON 8-7-78	DATE	12-20-77
A. BARON	DATE	1-21-78
B. BARON 10-14-78	DATE	11-15-77
C. BARON 5-17-78	DATE	12-24-77
A. BARON	DATE	1-13-77
A. BARON 15-MAY-79	DATE	
REVISED & REDRAWN	DATE	
KIT 5409728-00061A	DATE	
C. BARON 8-27-77	DATE	
A. BARON	DATE	
REVISED 25 JUNE 77	DATE	
REV 5409728-00025	DATE	
CHK CHANGE NO. REV	DATE	

QTY	REF DESIGNATION	DESCRIPTION	PART NO.	ITEM NO.																		
PARTS LIST																						
ETCH BOARD REV E																						
<table border="1"> <tr> <td>DRN. BY</td> <td>DATE</td> <td>12-20-77</td> </tr> <tr> <td>CHK'D</td> <td>DATE</td> <td>1-21-78</td> </tr> <tr> <td>ENG.</td> <td>DATE</td> <td>11-15-77</td> </tr> <tr> <td>PROJ. ENG.</td> <td>DATE</td> <td>12-24-77</td> </tr> <tr> <td>PROD.</td> <td>DATE</td> <td>1-13-77</td> </tr> <tr> <td colspan="3">NEXT HIGHER ASSY</td> </tr> </table>					DRN. BY	DATE	12-20-77	CHK'D	DATE	1-21-78	ENG.	DATE	11-15-77	PROJ. ENG.	DATE	12-24-77	PROD.	DATE	1-13-77	NEXT HIGHER ASSY		
DRN. BY	DATE	12-20-77																				
CHK'D	DATE	1-21-78																				
ENG.	DATE	11-15-77																				
PROJ. ENG.	DATE	12-24-77																				
PROD.	DATE	1-13-77																				
NEXT HIGHER ASSY																						
TITLE			REGULATOR BOARD FOR H740																			
SIZE CODE			DICS 5409728-0-1 AB																			
SCALE			SHEET 1 OF 1																			
SEMICONDUCTOR CONVERSION CHART			DIST.																			

NUMBER 5409728-0-1 AB  
 SHEET 1 OF 1  
 DICS 5409728-0-1 AB  
 REV. AB  
 ml 1



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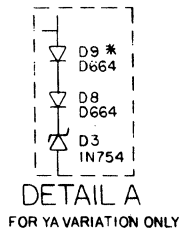


\*FUSIBLE RESISTOR

FOR YA VARIATION COMPONENT VALUES ARE AS FOLLOWS:  
 R4 - .06 5W  
 R14 - 1K 10 TURN  
 R17 - 300 1/8W 1%  
 R18 - 150 1/4W 5%  
 D3 - IN754  
 I1 - 15V  
 J1-2,5 - +2.0-8.0V  
 \* D9 - D664 ADDED FOR YA VARIATION ONLY

UNLESS OTHERWISE INDICATED:  
 RESISTORS ARE 1/4W, 5%

QTY	REF DESIGNATION	DESCRIPTION	PART NO.	ITEM NO.
PARTS LIST				
	ETCH BOARD REV	F	H	
	IN964A	SAME	MPSA55	
	D664	IN3606	2N5302	
	MCR649P-2		D45H8	
	IN751A	SAME	MPSA05	
	IN4744	SAME		
	NSR8117			
	2N6028			
	DEC NO.	EIA NO.	DEC NO.	EIA NO.
SEMICONDUCTOR CONVERSION CHART				
DRN. DATE	1-2-71	DATE	1-2-71	
CHKD. DATE	1-3-71	DATE	1-3-71	
ENG. DATE	1-3-71	DATE	1-3-71	
PROJ. DATE	1-3-71	DATE	1-3-71	
PROJ. NO.		DATE	1-3-71	
PROJ. NAME		DATE	1-3-71	
NEXT HIGHER ASSY		DATE	1-3-71	
SCALE		SCALE		
SHEET	1	OF	1	
DIST.		DIST.		



SEE DETAIL "A"

V SENSE

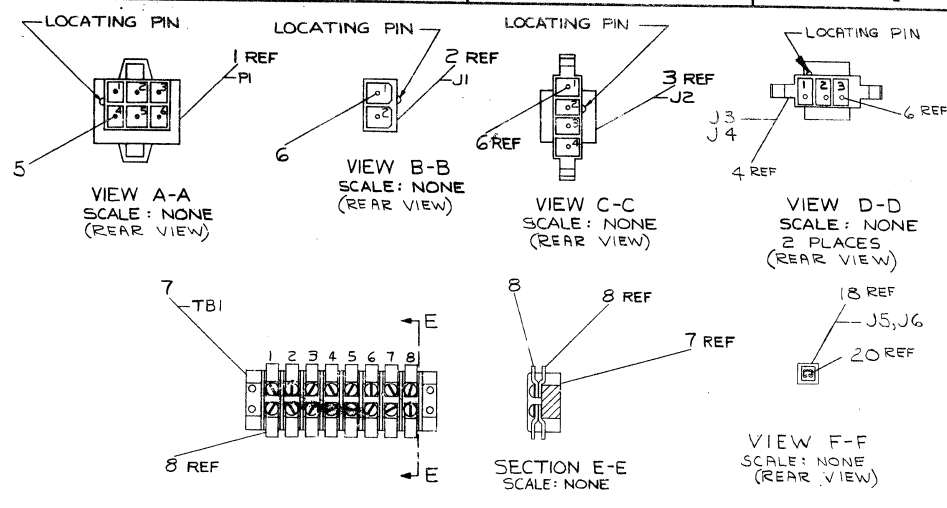
TITLE  
 5V REGULATOR

SIZE CODE  
 DICS H744-0-1

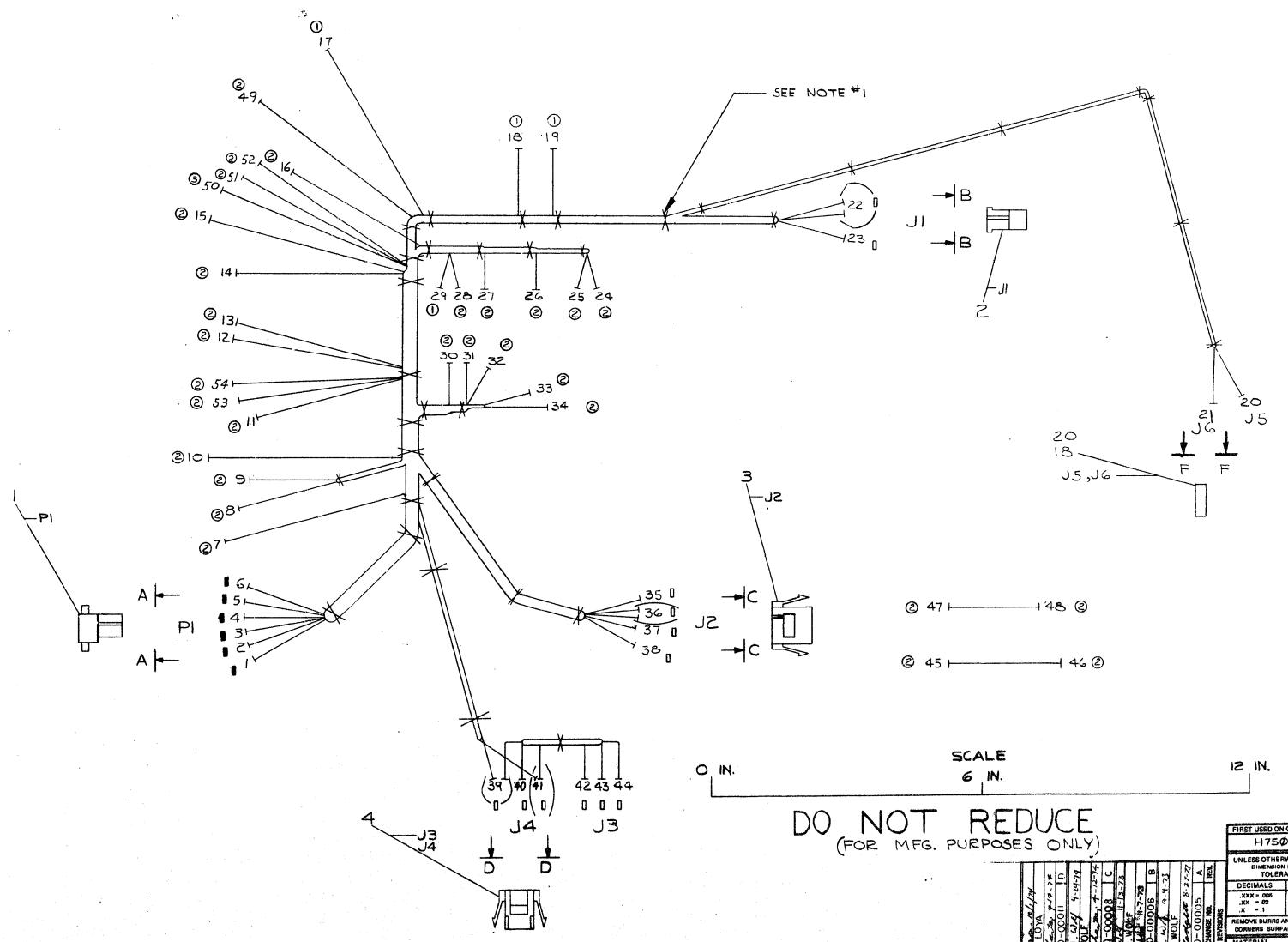
NUMBER  
 H744-0-1

Y

WIRE TABLE										
ITEM NO.	AWG	COLOR	POINT	FROM CONN	TERM POINT	TO CONN	TERM POINT	WIRE LENGTH	SIGNAL	
12	14	RED	1	PI-1	5	12	10	12		
11	14	BLK	2	PI-2	30	TBI-1	10	9		
14	14	WHT	3	PI-3	7			10		
12	14	RED	4	PI-4	32	TBI-2	10	9.5		
12	14	RED	5	PI-5	34	TBI-4	10	10.5		
14	14	WHT	6	PI-6	5	TBI-3	10	9.7		
17	14	RED	8		10	TBI-8	10	13.5		
14	14	WHT	9		10	TBI-6	10	12		
21	14	ORN	10		10		15	13.5		
13	14	VIO	11		10		14	9.2		
12	14	RED	13		10	TBI-2	10	7		
11	14	BLK	16		10	TBI-1	10	5.2		
15	18	BLK	17		9		22	14		
15	18	BLK	39	J4-1	6	J1-1	6	22.2		
15	18	BLK	39	J4-1	6	J3-1	6	3.5		
17	18	RED	18	TBI-4	9	J5	18/20	23.5		
17	18	WHT	19	TBI-6	9	J6	18/20	22		
19	18	BLU	23	J1-2	6	J2-1	6	22		
12	14	RED	25	TBI-8	10	J2-4	6	16		
14	14	WHT	27	TBI-3	10	J2-3	6	13		
15	18	BLK	29	TBI-1	9	J2-2	6	17		
15	18	BLK	41	J4-3	6	J3-3	6	3.5		
12	14	RED	45	TBI-4	10	TBI-7	10	2.5		
14	14	WHT	47	TBI-3	10	TBI-5	10	2.0		
15	18	BLK	49	J4-2	6	J3-2	6	3.5		
11	14	BLK	49		10	50	22	6		
15	18	BLK	53	K1-3	10	PS3-6	10	6		
15	18	BLK	59	K1-4	10	PS3-7	10	6		



NOTES:  
 1. USE TIE WRAPS (X) (ITEM #16) APPROXIMATELY EVERY THREE (3) INCHES WHEN NECESSARY AND AT BREAKOUT POINTS.



QTY	DESCRIPTION	PART NO.	ITEM NO.
22	TERM. SOLDERLESS #50364	9007928	22
21	WIRE #14 STRD, ORN	9107370-33	21
20	MINI-FASTABS #60291-1	1210820-2	20
19	WIRE #18AWG STRD, BLU	9107360-66	19
18	HOUSING, 1-480477-0	1210820-1	18
17	WIRE #18AWG STRD TWP RED WHT	9107430-29	17
16	TIE WRAP, PANDUIT #55TIM	3007031	16
15	WIRE #18 STRD, BLK	9107360-00	15
14	WIRE #14 STRD, WHT	9107370-99	14
13	WIRE #14 STRD, VIO	9107370-77	13
12	WIRE #14 STRD, RED	9107370-22	12
11	WIRE #14 STRD, BLK	9107370-00	11
10	CONN. SOLDERLESS, ARKLESS #50906	3007919	10
9	CONN. SOLDERLESS, ARKLESS #50902	3007917	9
8	TERM. 1 UG, OFFSET, AMP #6045-1	3007112	8
7	TERM. BLOCK, CNCH #8-540	9006909	7
6	SOCKET TERM. CONTACT (FEMALE)	1209379-01	6
5	PIN CONTACT (MALE)	1209378-01	5
4	CONN. MATE-N-LOCK, 3 PIN	1209350-03	4
3	CONN. MATE-N-LOCK, 4 PIN	1209350-04	3
2	CONN. MATE-N-LOCK, 2 PIN	1210221-02	2
1	CONN. MATE-N-LOCK, 6 PIN	1209351-06	1

SYN H750

UNLESS OTHERWISE SPECIFIED DIMENSIONS IN INCHES. TOLERANCES: DECIMALS .005 ANGLES 1/2 30

REMOVE BURRS AND BREAK BURRS. CONFIRM SURFACE QUALITY.

MATERIAL: SEE PARTS LIST

FINISH: SEE PARTS LIST

DATE: 8/27/77

BY: [Signature]

APPROVED: [Signature]

digital EQUIPMENT CORPORATION

TITLE: H750 POWER SUPPLY HARNESS

REV: 1

SCALE: 1/1

SHEET: 1 OF 1

# DRAWING DIRECTORY

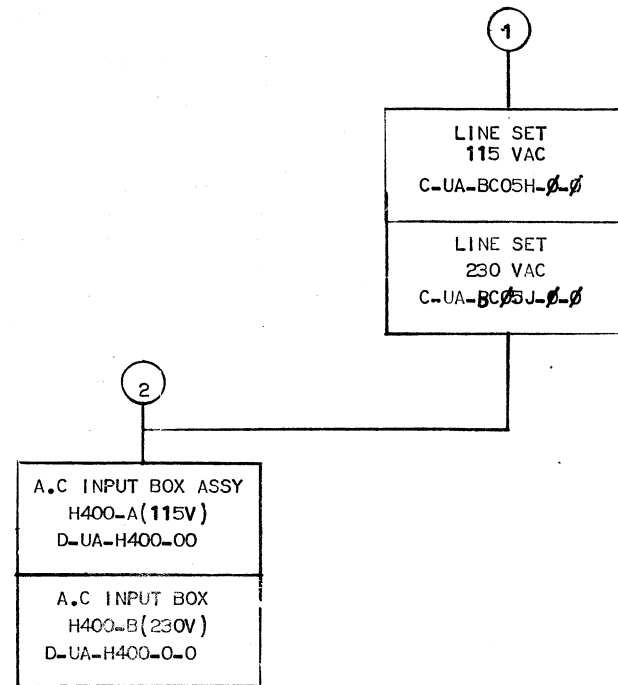
## CUSTOMER PRINT SET INDEX

THIS IS PRINT SET [ ] [ ] [ ] [ ] [ ]

SEQUENCE		SEQUENCE
DWG. DIRECTORY	B-DD-BC <del>5</del> H- <del>0</del>	
LINE SET 115V	C-UA-BC <del>5</del> H- <del>0</del>	
LINE SET 230V	C-UA-BC <del>5</del> J- <del>0</del>	
A.C INPUT BOX	D-UA-H4 <del>0</del> - <del>0</del>	
A.C INPUT BOX (P.L)	A-PL-H4 <del>0</del> - <del>0</del>	
PWR CONTROL BD. 115V	C-1A-5409824- <del>0</del>	
PWR CONTROL BD. 230V	C-1A-5409825- <del>0</del>	

UNIT VARIATIONS		PRINT SET TYPE			
VARIATION	TITLE	B	DD		
BC <del>5</del> H	LINE SET 115VAC 7 AMP			X	
BC <del>5</del> J	LINE SET 230VAC 5 AMP			X	

REVISIONS			USED ON OPTION/MODEL	DRN.	DATE	TITLE	SIZE	CODE	NUMBER	REV
DATE	CHG. NO.	REV					B	DD		
			11/67	D. FONTAINE	4/5/72	LINE SET				
				CHK'D	DATE					
				PROJ ENG.	DATE					
				PROD.	DATE					
				FIELD SERV.	DATE					
SHEET 1 OF 3				DIST						



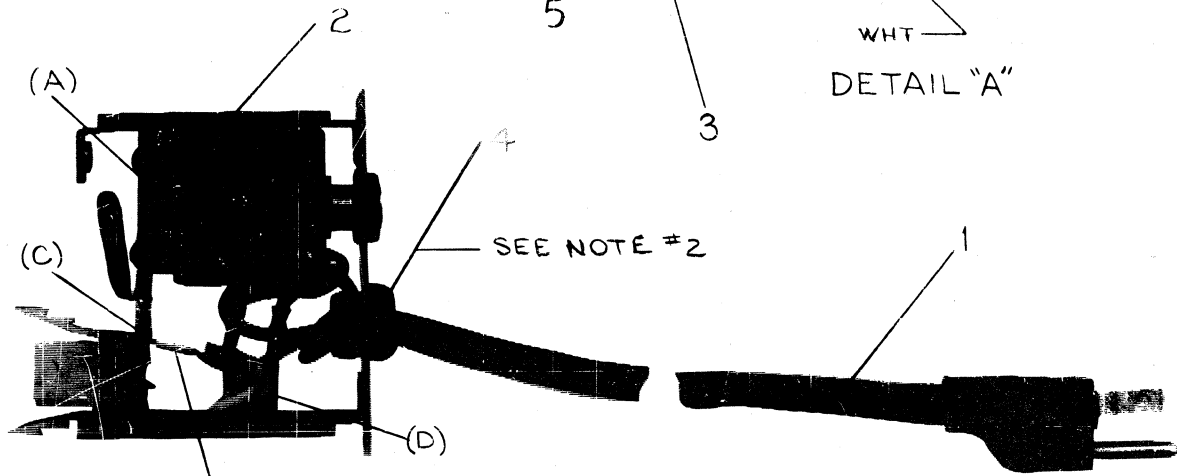
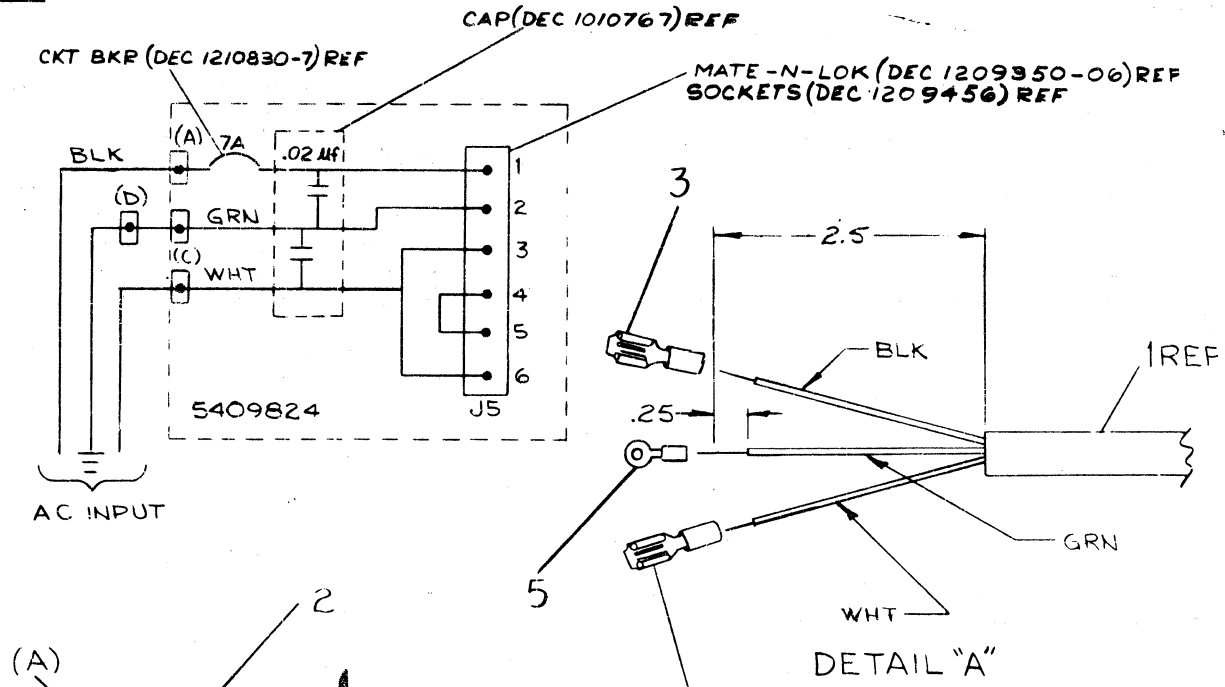
TITLE	SHEET	OF	SIZE	CODE	NUMBER	REV
LINE SET	2	OF	3	B DD	BC05H-φ	

CUSTOMER PRINT SET				ELECTRICAL					CUSTOMER PRINT SET				MECHANICAL								
BCOE-H-1				MFG. SET	FIND NO.	DRAWING NO.	REV	NO OF SHT	DESCRIPTION	OPTION NO.	BCOE-H-1				MFG. SET	FIND NO.	DRAWING NO.	REV	NO OF SHT	DESCRIPTION	OPTION NO.
											X					1.	C-UA-BC <del>5H</del> - <del>1-1</del>		1	LINE SET 115V 7 AMP	BC <del>5H</del>
											X						C-UA-BC <del>5J</del> - <del>1-1</del>		1	LINE SET 230V 5 AMP	BC <del>5J</del>
X					2.	C-1A-5409824-0-0		1	POWER CONTROL BD 115V	H400											
X						C-1A-5409825-0-0		1	POWER CONTROL BD 230V	H400											
											X					2.	D-UA-H4 <del>00</del> - <del>1-1</del>		1	AC INPUT BOX	H4 <del>00</del>
											X						A-PL-H4 <del>00</del> - <del>1-1</del>		1	A.C INPUT BOX PARTS LIST	H4 <del>00</del>
																	D-1A-5309845-0-0		1	BOX	H4 <del>00</del>
																	C-MD-5309849-0-0		1	COVER	H4 <del>00</del>
																	A-DC-5309899-0-0		1	PWR CONTROL DECAL 115V	H4 <del>00</del>
																	A-DC-5309900-0-0		1	PWR CONTROL DECAL 230V	H4 <del>00</del>

TITLE	SHEET 3 OF 3	SIZE CODE	NUMBER	REV
LINE SET		B DD	BC <del>5H</del> - <del>1</del>	

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- NOTES:**
1. CONNECT ITEM #1 (POWER CORD) AND ITEM #2 (AC INPUT BOX) PER CIRCUIT SCHEMATIC.
  2. FOR INSTALLATION USE HEYCO #29 STRAIN RELIEF PLIERS



SEE DETAIL 'A'  
3 REF  
SHOWN WITHOUT COVER

QTY.	DESCRIPTION	PART NO.	ITEM NO.
1	TERMINAL, RING, RED	9007929-0	5
1	STRAIN RELIEF SR-6N3-4	9008492-2	4
2	CONNECTOR, FASTON, RED	9007970	3
1	AC INPUT BOX H400A	D-UA-H400-0-0	2
1	POWER CORD 120V	1700015-6	1

REV.	CHG	NO.	BY	DATE
A		1	R. WOLFF	3-29-72
B		2	R. BURTON	5-13-72
C		3	R. BURTON	5-25-72
D		4	R. BURTON	5-31-72
E		5	R. BURTON	5-30-72

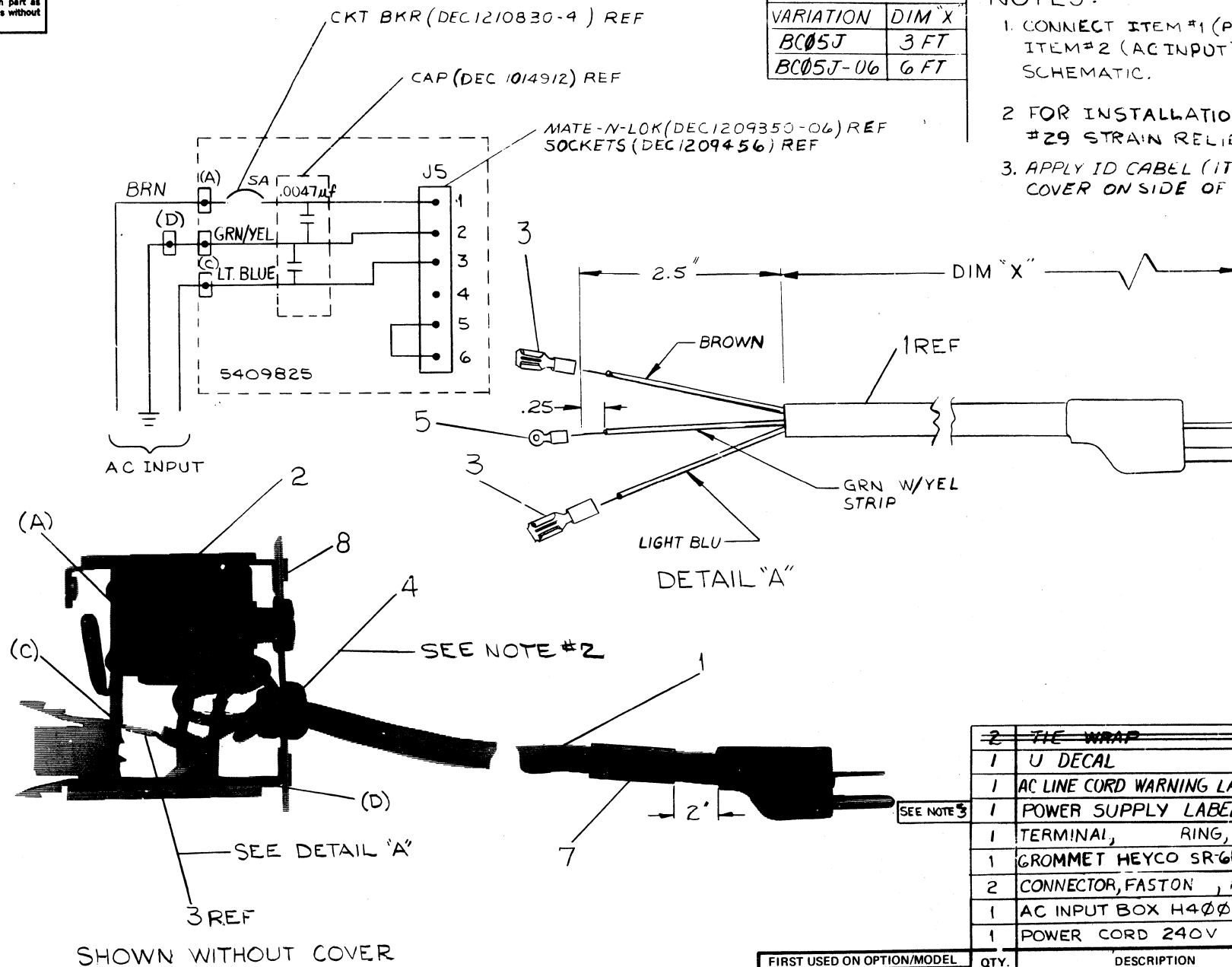
FIRST USED ON OPTION/MODEL 11/05	QTY.	DESCRIPTION	PART NO.	ITEM NO.
PARTS LIST				
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES TOLERANCES	DRN T. Quillin	DATE 12-27-71	digital EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS	
DECIMALS XXX = .005 XX = .02 X = .1	CHK'D C. Antone	DATE 1-8-72	TITLE LINE SET	
ANGLES ± 0° 30'	ENG. David DeMondelle	DATE 1-4-72	115V AC 7 AMP	
REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY ✓	PROJ. ENG. R. H. Perry	DATE 1-7-72	SIZE CODE C UA	
MATERIAL + + +	PROD. R. K. Pitarone	DATE 1/2/72	NUMBER BC05H-0-0	
FINISH + + +	NEXT HIGHER ASSY.	SCALE + + +	REV. E	
		SHEET 1 OF 1	DIST. 1	



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LEGEND	
VARIATION	DIM "X"
BC05J	3 FT
BC05J-06	6 FT

- NOTES:
- CONNECT ITEM #1 (POWER CORD) AND ITEM #2 (AC INPUT) PER CIRCUIT SCHEMATIC.
  - FOR INSTALLATION USE HAYCO #29 STRAIN RELIEF PLIERS.
  - APPLY ID CABEL (ITEM #6) TO BACK COVER ON SIDE OF UNIT.



QTY.	DESCRIPTION	PART NO.	ITEM NO.
2	TIE WRAP	3007031	9
1	U DECAL	3612063	8
1	AC LINE CORD WARNING LABEL	3611448	7
1	POWER SUPPLY LABEL	9009255-01	6
1	TERMINAL, RING, RED	9007929-0	5
1	GROMMET HEYCO SR6N3-4	9008492-2	4
2	CONNECTOR, FASTON, RED	9007970	3
1	AC INPUT BOX H40ØB	DUA-H40Ø-Ø-Ø	2
1	POWER CORD 240V	1700043	1

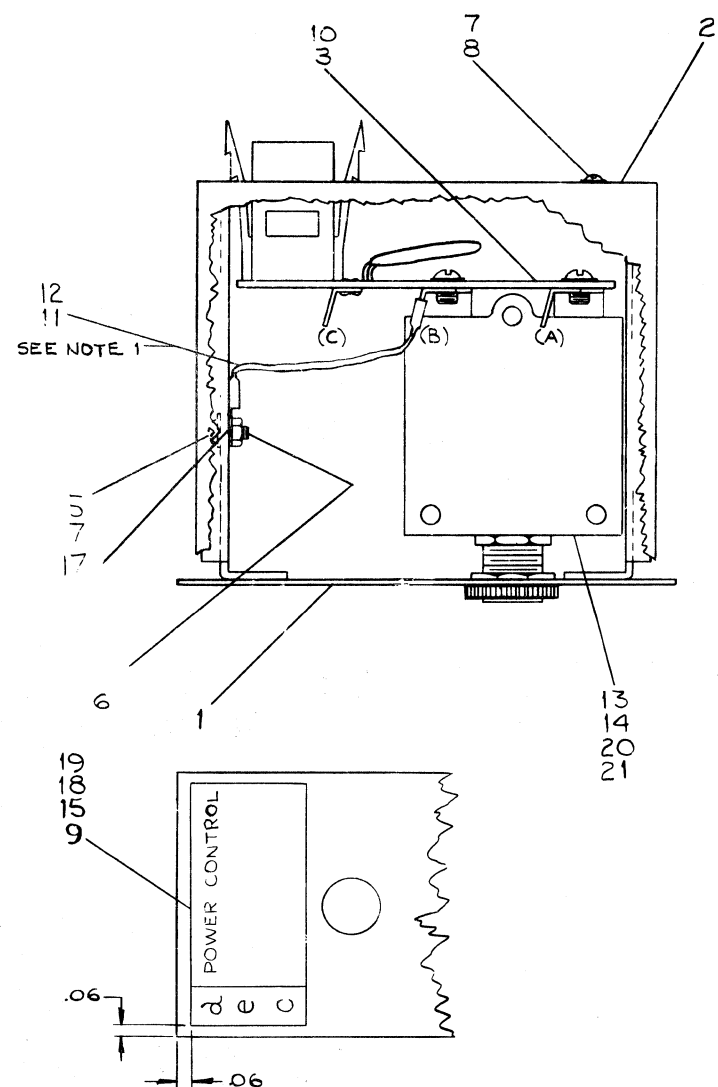
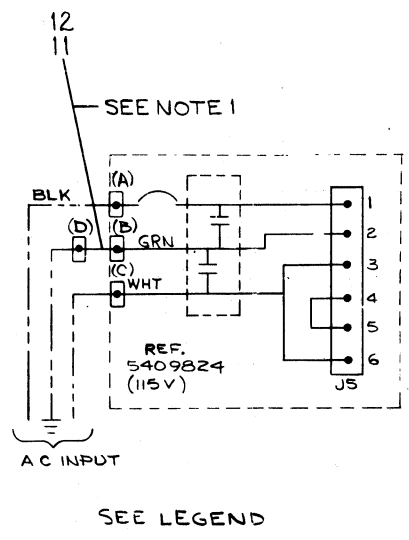
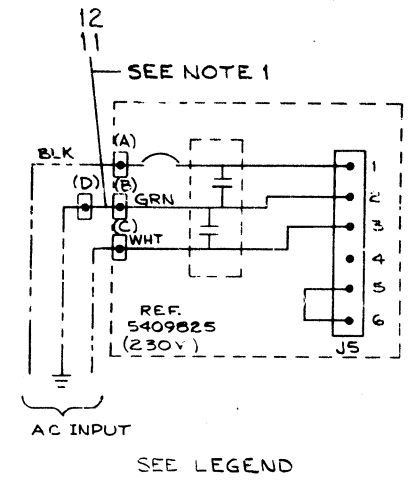
FIRST USED ON OPTION/MODEL		PARTS LIST	
11/Ø5		UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES. TOLERANCES	
DECIMALS	ANGLES	DRN T. Guillin 12-27-71	DATE 12-27-71
.XXX = .005	±0° 30'	CHKD C. J. ...	DATE 1-4-72
.XX = .02		ENG. David D. ...	DATE 1-7-72
.X = .1		PROJ. ENG. R. ...	DATE 1-7-72
REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY ✓		PROD. R. K. ...	DATE 1/7/72
MATERIAL	NEXT HIGHER ASSY.	TITLE LINE SET 230V AC 4 AMP	
FINISH	SCALE	SIZE CODE C UA	NUMBER BC05J-Ø-Ø
	SHEET 1 OF 1	DIST.	REV. H

CHK	CHANGE NO.	REV.
	BC05J-00001	A
	H400-00002	B
	BC05J-00002	C
	BC05J-00003	D
	BC05J-00005	E
	BC05J-00006	F
	BC05J-00007	H
	BC05J-00008	I
	BC05J-00009	J

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LEGEND		
NUMBER	VARIATION	USED ON
H400-A	115 VAC 7AMP	BC05H
H400-B	230 VAC 4AMP	BC05J
H400-C	115 VAC 10AMP	BC05T
H400-D	230 VAC 5AMP	BC05U

NOTES:  
1. ITEM #11 (WIRE) AND TWO OF ITEM #12 (FASTON TABS) ARE TO BE CONNECTED FROM POINT D TO POINT B.



FIRST USED ON OPTION/MODEL	QTY.	DESCRIPTION	PART NO.	ITEM NO.
D-UA-BC05H-0-0		PARTS LIST		
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES TOLERANCES		DRN. T. Quill	DATE 12-23-71	<b>digital</b> EQUIPMENT CORPORATION <small>MILFORD MASSACHUSETTS</small> TITLE <b>AC INPUT BOX ASSY</b>
DECIMALS	ANGLES	ENG. J. Burton	DATE 12-21-71	
.XXX - .005	20° 30'	PROJ. ENG. R. Peterson	DATE 1-4-72	
.XX - .01		PROD. R. Peterson	DATE 1-7-72	
.X - .1			DATE 1/2/72	
MATERIAL	C-UA-BC05H-0-0	NEXT HIGHER ASSY.	SIZE CODE	NUMBER
FINISH			DUA	H400-0-0
			SCALE	REV. F
			SHEET 1 OF 1	DIST. 1

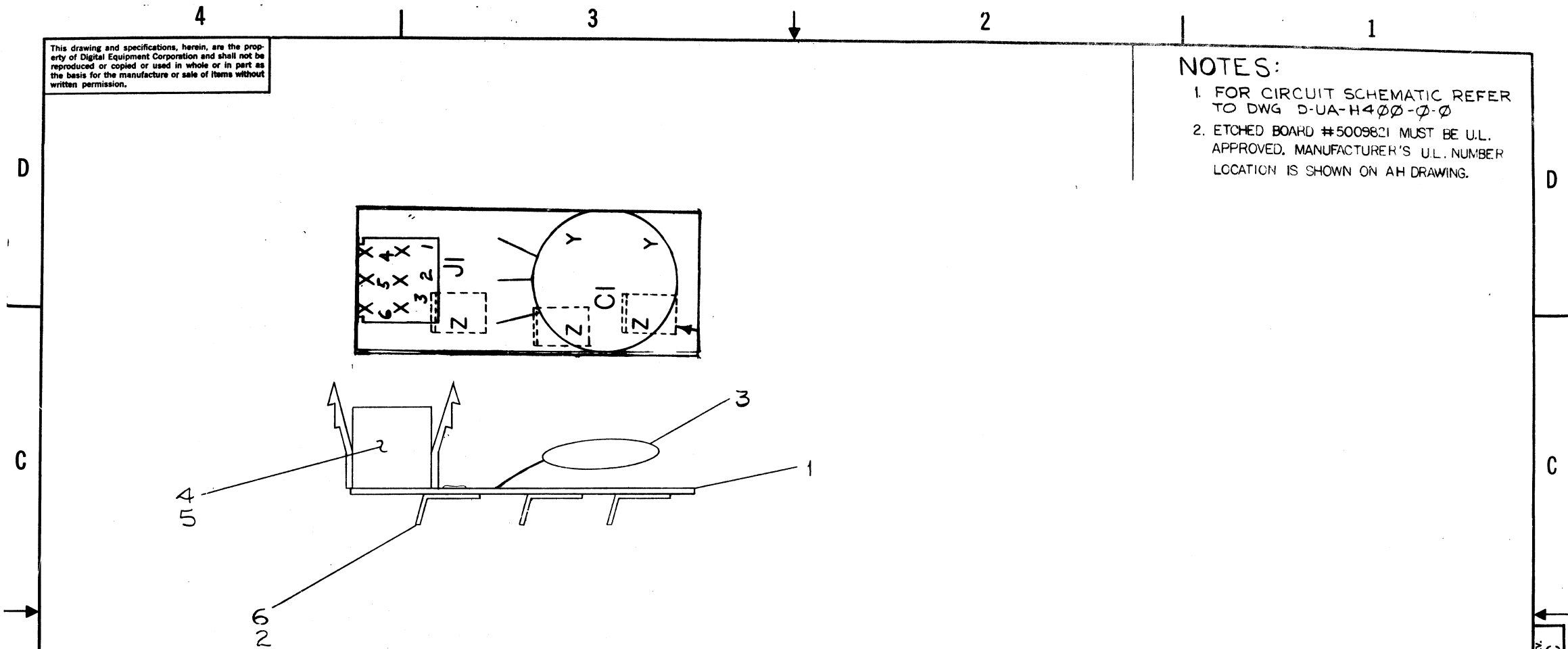
REVISIONS	CHANGE NO.	REV.
CHK	H400-00002	A
	5-25-72	
	R. BURTON	
	5-31-72	B
	H400-00003	
	5-31-72	
	R. BURTON	
	11-9-72	C
	H400-00004	
	12-21-72	
	R. BURTON	
	12-23-71	D
	H400-00005	
	11-18-73	
	R. BURTON	
	2-2-73	E
	H400-00007	
	6-14-73	
	R. BURTON	
	6-21-73	F
	H400-00008	
	12-21-72	
	R. WOLF	
	10-12-74	

DIGITAL EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS			QUANTITY / VARIATION														
PARTS LIST			H400-A (115V)	H400-B (230V)	H400-C (115V)	H400-D (230V)											
MADE BY TYRONE QUILLIN		CHECKED <i>T. Quinn</i>	SECTION														
DATE 12-1-71		DATE 1-9-72	ISSUED SECT.														
ENG <i>David L. Moynihan</i>		FRG <i>R.K. Peterson</i>															
DATE 1-4-72		DATE 1/2/72															
ITEM NO.	DWG NO. / PART NO.	DESCRIPTION															
1	D-IA-5309845-0-0	BOX	1	1	1	1											
2	C-MD-5309849-0-0	COVER	1	1	1	1											
3	C-IA-5409825-0-0	POWER CONTROL BOARD (230V)	-	1	-	1											
4	<del>9007113</del>	<del>DOUBLE FASTAB</del>	<del>1</del>	<del>1</del>	<del>1</del>	<del>1</del>											
5	9006022-1	SCR PHL PAN HD #6-32 x 3/8 LG	1	1	1	1											
6	9006185	KEP NUT HEX HD #6-32	1	1	1	1											
7	9006633	WASHER INT. #6	2	2	2	2											
8	9006020-1	SCR PHL PAN HD #6-32 x 1/4 LG	1	1	1	1											
9	A-DC-5309899-0-0	PWR CONTROL DECAL 115V	1	-	-	-											
10	C-IA-5409824-0-0	POWER CONTROL BOARD (115V)	1	-	1	-											
11	9107360-55	#18 AWG STRD <del>3FT INS</del> (GRN 3 IN. LG)	1	1	1	1											
12	9007917	FASTON TABS	1	1	1	1											
13	1210830-4	CRK BREAKER (WOOD 4 AMP)	-	1	-	-											
14	1210830-7	CRK BREAKER (WOOD 7 AMP)	1	-	-	-											
15	A-DC-5309900-0-0	PWR CONTROL DECAL (230V)	-	1	-	-											
16	<del>9006632</del>	<del>WASHER - INTERNAL TOOTH</del>	<del>1</del>	<del>1</del>	<del>1</del>	<del>1</del>											
17	9007929-01	CRIMP-ON EYELET	1	1	1	1											
18	A-DC-7410727-0-0	PWR CONT DECAL	-	-	1	-											
19	A-DC-7410726-0-0	PWR CONT DECAL	-	-	-	1											
20	1210830-5	CKT BREAKER (WOOD 5 AMP)	-	-	-	1											
21	1210830-10	CKT BREAKER (WOOD 10 AMP)	-	-	1	-											
TITLE		AC INPUT BOX ASS'Y	ASSY NO.		D-UA-H400-0-0		SIZE	CODE	NUMBER		H400-0-0		REV.	ECO NO.		H400-00008	
			SHEET		1 OF 1		DIST.	G									

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**NOTES:**

1. FOR CIRCUIT SCHEMATIC REFER TO DWG D-UA-H400-0-0
2. ETCHED BOARD #5009821 MUST BE U.L. APPROVED. MANUFACTURER'S U.L. NUMBER LOCATION IS SHOWN ON AH DRAWING.



QTY.	REF DESIGNATION	DESCRIPTION	PART NO.	ITEM NO.
		ASSY/DRILLING HOLE LAYOUT	D-AH-5409824-05	REF
3		FASTON TAB	9008219	6
6		P.C. SOCKETS FEMALE	1209456	5
1	J1	MATE-N-LOCK 6-PIN	1209350-06	4
1	C1	CAP INPUT .02 MF	1010767	3
3		EYELET # GS4-5	9009000	2
		ETCHED CIRCUIT BD	5009821	1
		MODULE ECO HISTORY	B-MH-5409824-0-6	REF
		X-Y COORDINATE HOLE LOCATION	K-CO-5409824-0-0	REF

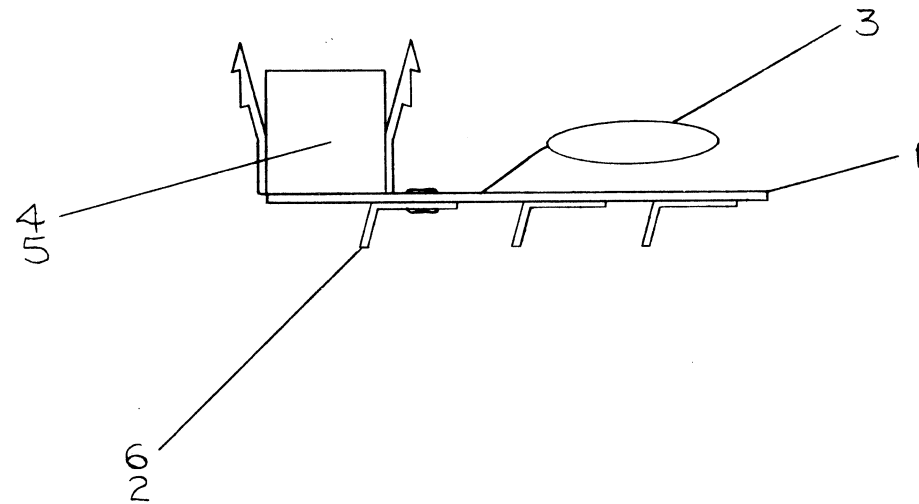
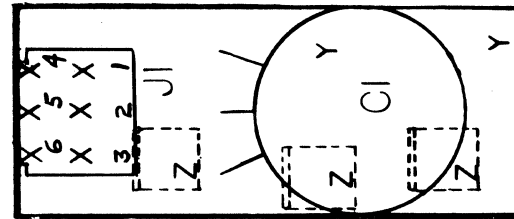
FIRST USED ON OPTION/MODEL		PARTS LIST	
H400 A		UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES. TOLERANCES	
DECIMALS	ANGLES	DRN. DATE	<b>digital</b> EQUIPMENT CORPORATION MAYNARD MASSACHUSETTS TITLE <b>POWER CONTROL BOARD (115 V)</b>
XXX = .005	±0° 30'	CHK'D DATE	
XX = .02		ENG. DATE	
X = .1		PROJ. ENG. DATE	
		PROD. DATE	
REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY ✓			
MATERIAL	NEXT HIGHER ASSY.	C-IA	NUMBER
FINISH	SCALE	5409824-0-0	REV.
	SHEET	1	C

REV.	CHANGE NO.	REV.
A	5409824-00001	
B	5409824-00002	
C	5409824-00003	

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NOTES:

- FOR CIRCUIT SCHEMATIC REFER TO DWG D-UA-H400-0-0.
- ETCHED BOARD # 5009822 MUST BE U.L. APPROVED. MANUFACTURER'S U.L. NUMBER LOCATION IS SHOWN ON AH DRAWING.



CHK	REVISIONS	CHANGE NO.	REV.
1	5409825-0000	A	R. WOLF
2	5409825-0000	B	R. WOLF
3	5409825-0000	C	R. WOLF
4	5409825-0000	D	A. BARON
5	5409825-0000	E	A. BARON

QTY.	REF DESIGNATION	DESCRIPTION	PART NO.	ITEM NO.
		ASSY/DRILLING HOLE LAYOUT	D-AH-5409825-0-5	REF
3		FASTON TAB	9008219	6
6		P.C. SOCKETS MALE	1209456	5
1	J1	6-PIN MATE-N-LOCK	1209350-06	4
1	C1	CAP. INPUT .0047UF	1014912	3
3		EYELET #654-5	9009000	2
1		ETCHED CIRCUIT BOARD	5009822	1
		MODULE ECO HISTORY	B-MH-5409825-0-6	REF
		X-Y COORDINATE HOLE LOCATION	KCO-5409825-0-4	REF

FIRST USED ON OPTION/MODEL		PARTS LIST	
H400 B		digital EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS	
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES. TOLERANCES		DRN. T. Guilin 12-9-71	DATE 12-9-71
DECIMALS .XXX = .005 .XX = .02 .X = .1		CHK'D. J. Lister 1-4-72	DATE 1-4-72
ANGLES ±0° 30'		ENG. R. Peterson 1-7-72	DATE 1-7-72
REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY ✓		PROD. R. Peterson 1-7-72	DATE 1-7-72
MATERIAL		TITLE POWER CONTROL BOARD (230V)	
FINISH		NEXT HIGHER ASSY. C-UA-H400-0-0	
SCALE		SIZE CODE	NUMBER
SHEET OF 1		CIA	5409825-0-0
DIST.		REV.	E

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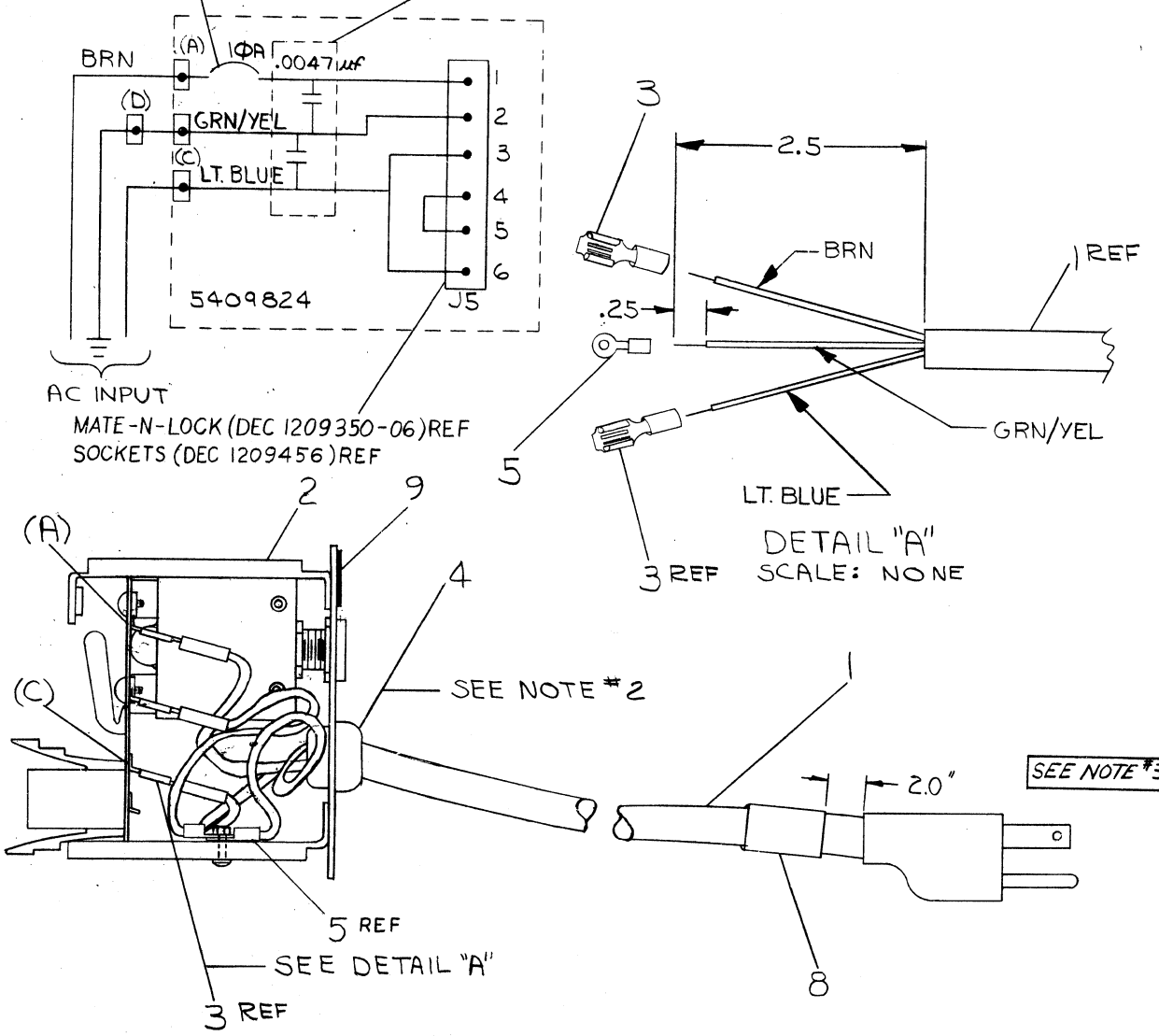
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4 3 2 1

CKT BKR (DEC 1210830-10) REF  
CAP (DEC 1014912) REF

LEGEND	
NUMBER	VARIATION
BC05T-06	ITEM NO. 1
BC05T-09	1700015-6
	1700015-9

- NOTES:
- CONNECT ITEM #1 (POWER CORD) AND ITEM #2 (AC INPUT BOX) PER CIRCUIT SCHEMATIC.
  - FOR INSTALLATION USE HEYCO #20 STRAIN RELIEF PLIERS.
  - APPLY ID LABEL (ITEM #10) TO BACK COVER.



SHOWN WITHOUT COVER

QTY.	DESCRIPTION	PART NO.	ITEM NO.
1	LABEL, POWER SUPPLY	9009255-01	10
1	U DECAL	3612063	9
1	AC LINE CORD WARNING LABEL	3611448	8
<del>1</del>	<del>CABLE LABEL</del>	<del>9009532</del>	<del>7</del>
<del>2</del>	<del>TIE WRAP (SM)</del>	<del>9007031</del>	<del>6</del>
1	RING TERMINAL RED	9007929-0	5
1	STRAIN RELIEF GN3-4	9008492-2	4
2	FAST ON RED	9007970	3
1	AC INPUT BOX H400C	H400-C	2
1	POWER CORD 120V	SEE LEGEND	1

CHK	CHANGE NO.	REV.
P7	BC05T-00001	A
	018-00001	4-6-73
	C. BLASI	
	CAB	4-9-72
P7	BC05T-00002	B
	Ernie A.	6-11-73
	C. BLASI	
	CAB	6-15-73
P7	BC05T-00003	C
	J. Chatter	11-23-75
	R. KENNEDY	
	H. Kennedy	10-15-75
P7	BC05T-00004	D
	D. Chatter	30 DEC 77
	A. BARON	
	G. Baron	1-9-78
P7	BC05T-00005	E
	J. Chatter	27 MAR 78
	A. BARON	
	A. Baron	4-4-78

FIRST USED ON OPTION/MODEL 11/35	QTY.	DESCRIPTION	PART NO.	ITEM NO.
PARTS LIST				
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES. TOLERANCES	DRN. K. Davis	DATE 11-21-72	digital EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS	
DECIMALS .XXX = .005 .XX = .02 .X = .1	CHKD. J. Blasi	DATE 2-5-73	TITLE	
ANGLES ±0° 30'	ENG. J. Blasi	DATE 1-16-73	LINE SET	
REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY	PROD. J. Blasi	DATE 1-16-73	115V A.C. 10AMP.	
MATERIAL	NEXT HIGHER ASSY.	SIZE CODE	NUMBER	REV.
FINISH	E-AD-BALL-D-0	C	UA BC05T-0-0	E
	SCALE NONE	SHEET	OF	
		1		

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CKT BKR (DEC 1210830-5) REF

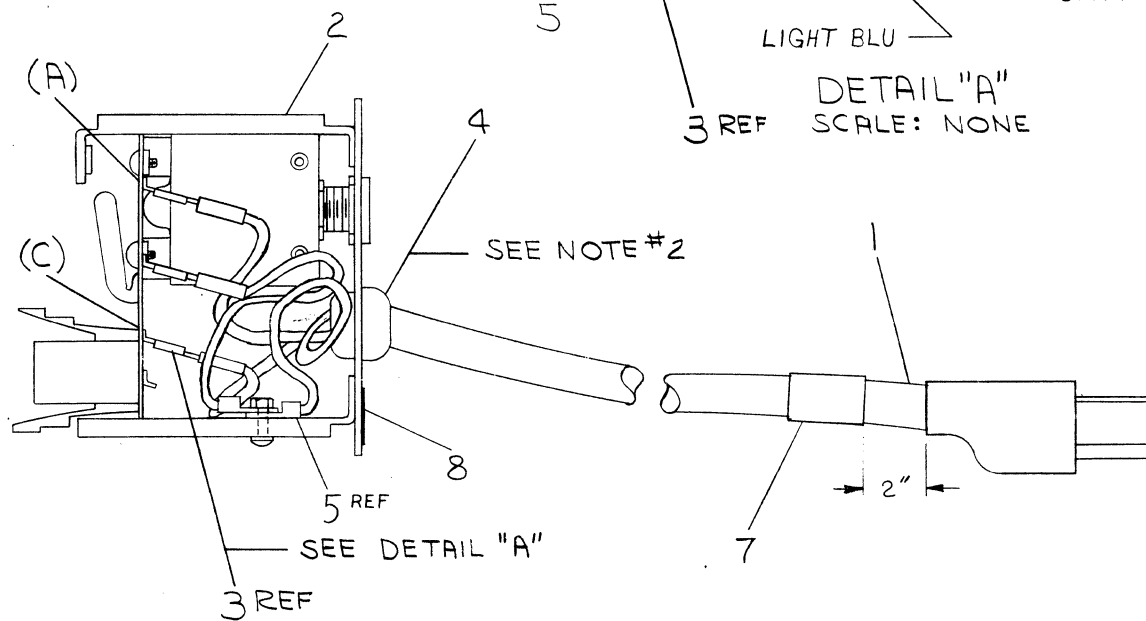
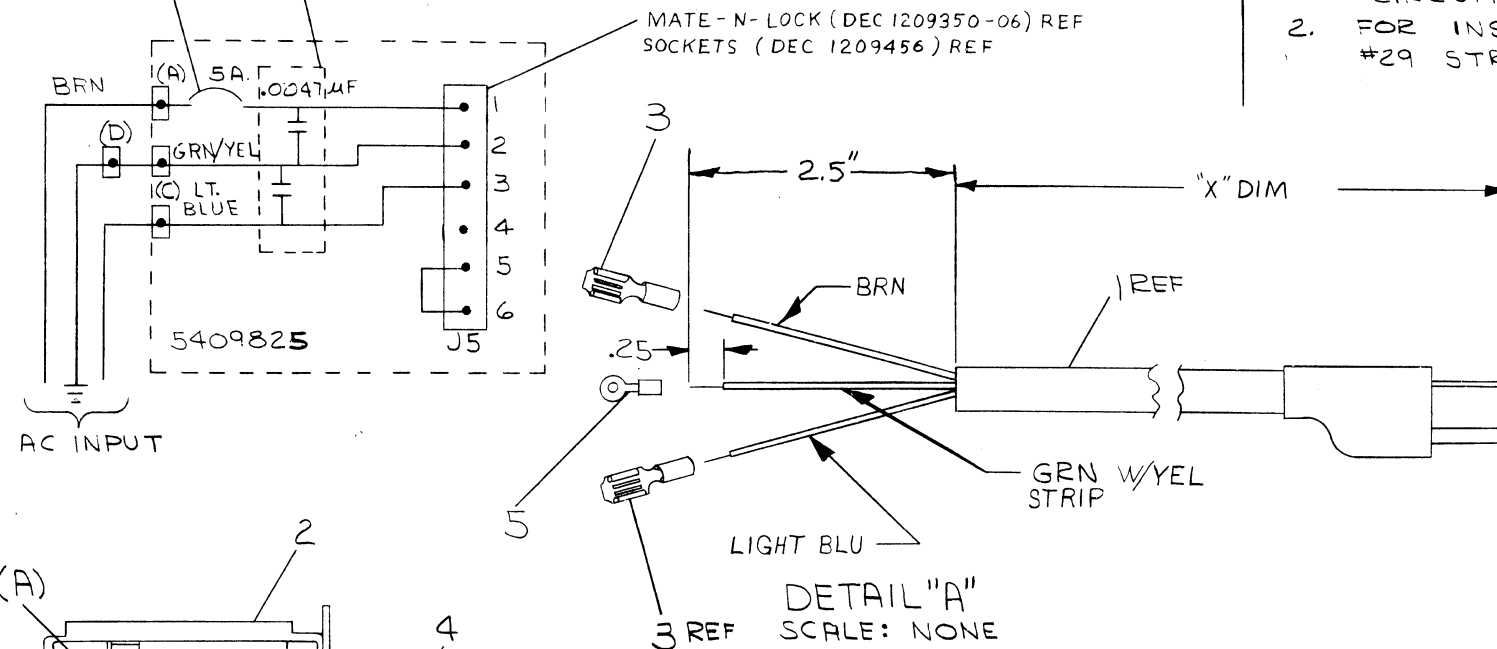
CAP (DEC 1014912) REF

LEGEND

NUMBER	DIM. "X" VARIATION
BC05U-06	6 FEET
BC05U-09	9 FEET

NOTES:

- CONNECT ITEM #1 (POWER CORD) AND ITEM #2 (AC INPUT BOX) PER CIRCUIT SCHEMATIC.
- FOR INSTALLATION USE HEYCO #29 STRAIN RELIEF PLIERS.



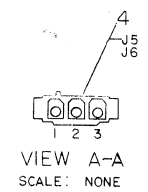
SHOWN WITHOUT COVER

QTY.	DESCRIPTION	PART NO.	ITEM NO.
1	U DECAL	3612063	8
1	AC LINE CORD WARNING LABEL	3611448	7
1	POWER SUPPLY LABEL	9009255-01	6
1	TERMINAL, RING, RED	9007929-0	5
1	STRAIN RELIEF 6N3-4	9008492-2	4
2	CONNECTOR, FASTON, RED	9007970	3
1	AC INPUT BOX H400D	H400-D	2
1	POWER CORD 240V	1700043	1

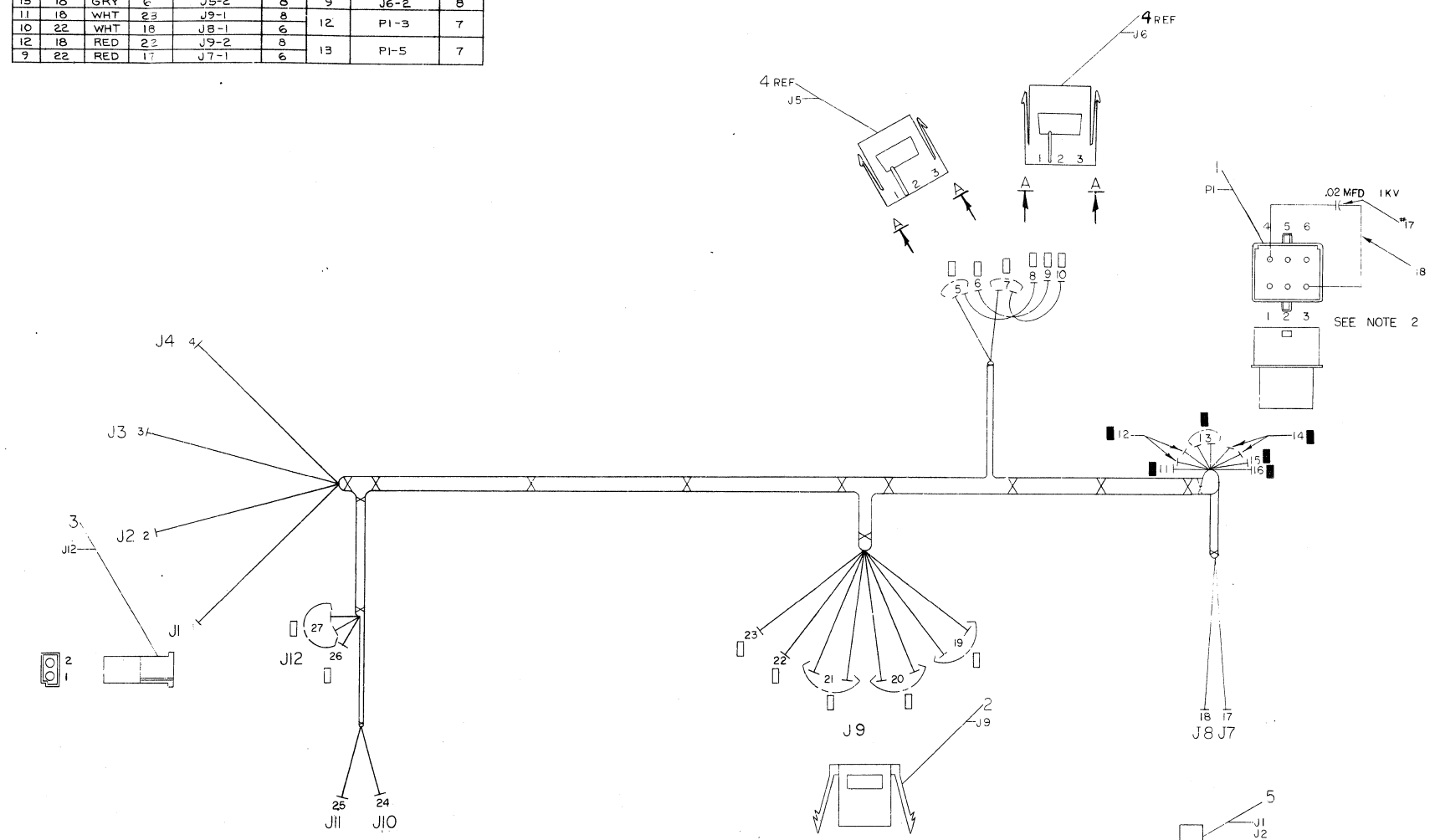
FIRST USED ON OPTION/MODEL	QTY.	DESCRIPTION	PART NO.	ITEM NO.
11/35				
PARTS LIST				
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES. TOLERANCES		DRN. <i>K. Davis</i>	DATE 11-21-72	<b>digital</b> EQUIPMENT CORPORATION MAYNARD MASSACHUSETTS TITLE LINE SET 230V AC. 5 AMP
DECIMALS	ANGLES	CHK'D <i>[Signature]</i>	DATE 12-1-72	
.xxx = .005	± 0° 30'	ENG. <i>[Signature]</i>	DATE 1-16-73	
.xx = .02		PROJ. ENG. <i>[Signature]</i>	DATE 1-16-73	
.x = .1		PROD. <i>[Signature]</i>	DATE 2-21-73	
REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY				
MATERIAL		NEXT HIGHER ASSY.		
FINISH		E-AD-BALL-D-Ø	SIZE CODE C U A B C Ø 5 U - Ø - Ø	NUMBER
		SCALE NONE		REV. D
		SHEET 1 OF 1	DIST.	

CHK	CHANGE NO.	REV.
<i>[Signature]</i>	BC05U-00001	A
<i>[Signature]</i>	<i>[Signature]</i>	A-6-73
<i>[Signature]</i>	C. BLASI	4-13-73
<i>[Signature]</i>	BC05U-00002	B
<i>[Signature]</i>	Ernie A.	6-11-73
<i>[Signature]</i>	C. BLASI	6-15-73
<i>[Signature]</i>	BC05U-00003	C
<i>[Signature]</i>	<i>[Signature]</i>	2-DEC-73
<i>[Signature]</i>	R. KENNEDY	9 DEC 73
<i>[Signature]</i>	BC05U-00004	D
<i>[Signature]</i>	<i>[Signature]</i>	6 JAN 78
<i>[Signature]</i>	A. BARON	1-9-78

WIRE TABLE								
ITEM NO.	AWG	COLOR	POINT	CONNECTION	TERM	TO POINT	CONNECTION	TERM
9	22	RED	24	J10-1	6	19	J9-3	8
12	18	RED	11	PI-6	7	27	J12-2	8
9	22	RED	25	J11-1	6	16	PI-4	7
12	18	RED	20	J9-4	8	1	J1-1	6
12	18	RED	26	J12-1	8	15	PI-1	7
12	18	RED	2	J2-1	6	5	J5-1	8
13	18	VIO	3	J3-1	6	21	J9-5	8
13	18	VIO	8	J6-1	6	7	J5-3	8
14	18	BLK	4	J4-1	6	9	J6-2	8
14	18	BLK	14	PI-2	7	12	PI-3	7
14	18	BLK	10	J6-3	8	13	PI-5	7
15	18	GRY	6	J5-2	8			
11	18	WHT	23	J9-1	8			
10	22	WHT	18	J8-1	6			
12	18	RED	22	J9-2	8			
9	22	RED	17	J7-1	6			



NOTES:  
 1. USE TIE WRAPS (ITEM #16) APPROXIMATELY EVERY THREE INCHES WHEN NECESSARY AND AT EVERY BREAKOUT POINT.  
 2. CAPACITOR SHOULD BE CRIMPED TOGETHER WITH PART 12 & 16. LEADS SHOULD BE INSULATED AND NOT LONGER THAN 1 INCH.



**DO NOT REDUCE DRAWING  
 NOT TO BE USED FOR PRODUCTION**

DO NOT REDUCE  
 SCALE  
 6 IN 12 IN

A/R	DESCRIPTION	PART NO.	ITEM NO.
A/R	TEFLON TUBING	9107278-01	18
X	CAP .02 MFD 1000V 20%	10-10767	17
A/R	TIE WRAP, 5/8" X 1/4" PAN DUIT	9007031	16
A/R	WIRE, #18 AWG GRY	9107360-88	15
A/R	WIRE, #18 AWG BLK	9107360-00	14
A/R	WIRE, #18 AWG VIO	9107360-77	13
A/R	WIRE, #18 AWG RED	9107360-22	12
A/R	WIRE, #18 AWG WHT	9107360-99	11
A/R	WIRE, #22 AWG WHT	9107350-99	10
A/R	WIRE, #22 AWG RED	9107350-22	9
13	SOCKET, TERM. CONTACT	1209379-01	8
6	PIN, TERM. CONTACT	1209378-01	7
8	MINI-FASTABS #60291-1	1210820-2	6
8	HOUSING, I-480417-0	1210820-1	5
2	CONN, MATE-N-LOK, 3 PIN	1209350-03	4
1	CONN, MATE-N-LOK, 2 PIN	1210821-2	3
1	CONN, MATE-N-LOK, 6 PIN	1209350-06	2
1	CONN, MATE-N-LOK, 6 PIN	1209351-06	1

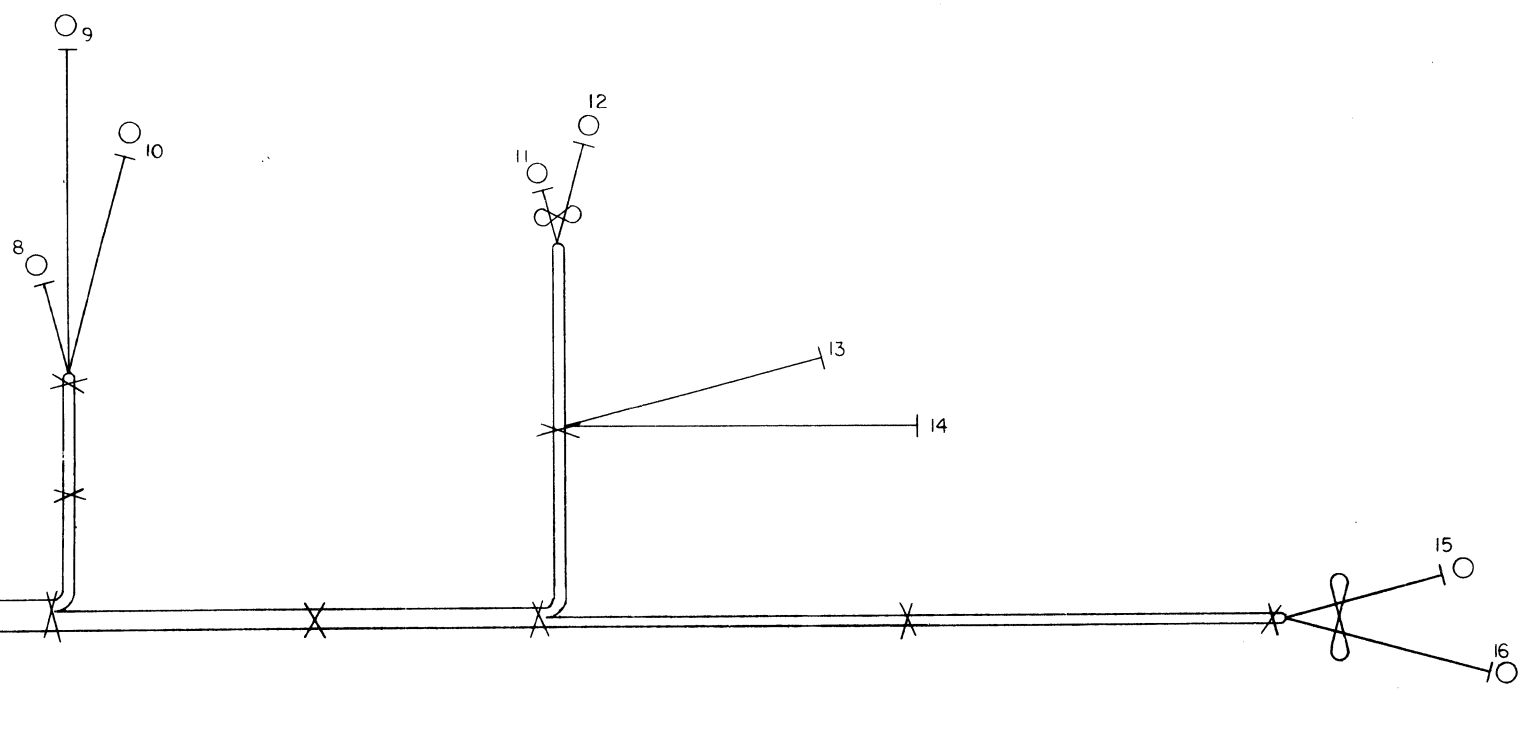
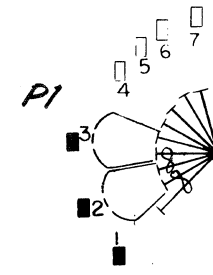
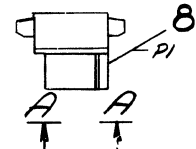
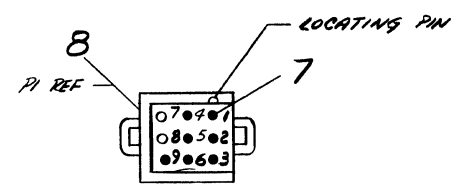
FIRST USED ON OPTION MODEL PDP-1105	DWN DATE DESIGNED BY DATE CHECKED BY DATE DRAWN BY DATE MATERIALS DATE FINISH DATE	EQUIPMENT CORPORATION TITLE AC INPUT HARNESS (PDP1105) NUMBER 7008713-0-0 REV. D
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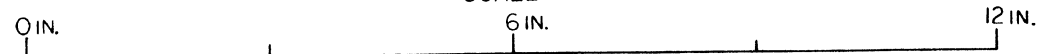
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ITEM NO.	DESCRIPTION		FROM			TO			REMARKS
	AWG	COLOR	POINT	CONNECTION	TERM	POINT	CONNECTION	TERM	
5	*18	BLU	1	PI-9	7	12	—	6	
	TWP	BLK	2	PI-2	↑	11	—	↑	
4	*18	BLK	2	PI-2	↓	15	—	↑	
	TWP	RED	3	PI-3	↓	16	—	↑	
3	*18	RED	3	PI-3	7	9	—	↑	
1	*22	YEL	4	PI-1	13	10	—	↑	
2	*22	VIO	5	PI-6	↑	8	—	6	
10	*22	BRN	6	PI-4	↓	14	—	12	
11	*22	ORN	7	PI-5	13	13	—	12	

NOTES:  
1. USE CABLE TIES (X) ITEM #9 AS SHOWN AND AT BREAK OUT POINTS WHERE NECESSARY.



DO NOT REDUCE SCALE



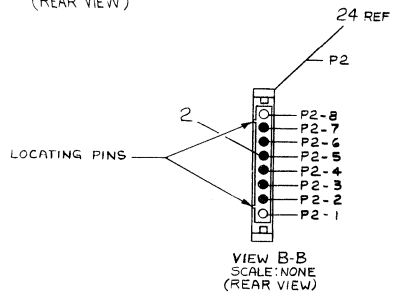
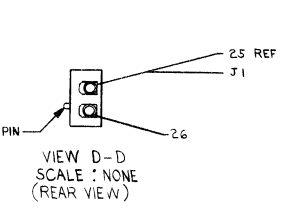
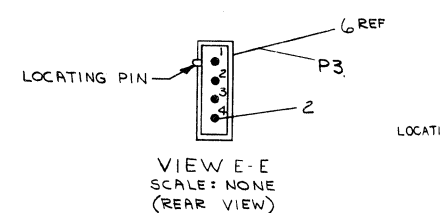
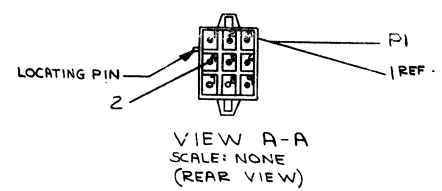
QTY.	DESCRIPTION	PART NO.	ITEM NO.
4	PIN MATE-N-LOCK MALE	1209378-03	13
2	TERMINAL AMP #85952-3	9007655	12
1/R	WIRE #22 AWG STRD (ORN)	9107350-33	11
1/R	WIRE #22 AWG STRD (BRN)	9107350-11	10
X	A/R TIE, CABLE PANDUIT SST 15M	9007880	9
PI	1 CONN., MATE-N-LOCK 9 PIN MALE	1209351-09	8
3	PIN, MATE-N-LOCK MALE	1209378-03	7
7	TERM., AMP TAB .RED	9007917	6
A/R	WIRE, #18 AWG STRD TWP <sup>BLK</sup>	9107430-06	5
A/R	WIRE, #18 AWG STRD TWP <sup>RED</sup>	9107430-02	4
A/R	WIRE, #18 AWG STRD RED	9107360-22	3
A/R	WIRE, #22 AWG STRD VIO	9107350-77	2
A/R	WIRE, #22 AWG STRD YEL	9107350-44	1

FIRST USED ON OPTION/MODEL <b>PDP 1105</b>		QTY.		DESCRIPTION		PARTS LIST	
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES. TOLERANCES		DRN KOBICHAUD	DATE 2-28-72	digital EQUIPMENT CORPORATION MAYNARD MASSACHUSETTS		TITLE	
DECIMALS .XXX - .006	ANGLES ±0° 30'	CHK'D C. T. W. HAYES	DATE 5-7-72	HARNESS DC (PDP 1105)		REV. A	
REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY		PROJ. ENG. R. K. WILSON	DATE 5-24-72	MATERIAL NEXT HIGHER ASSY.		SIZE CODE DIA	NUMBER 7008856-0-0
FINISH		SEE PARTS LIST		SCALE 1/1		SHEET 1 OF 1	

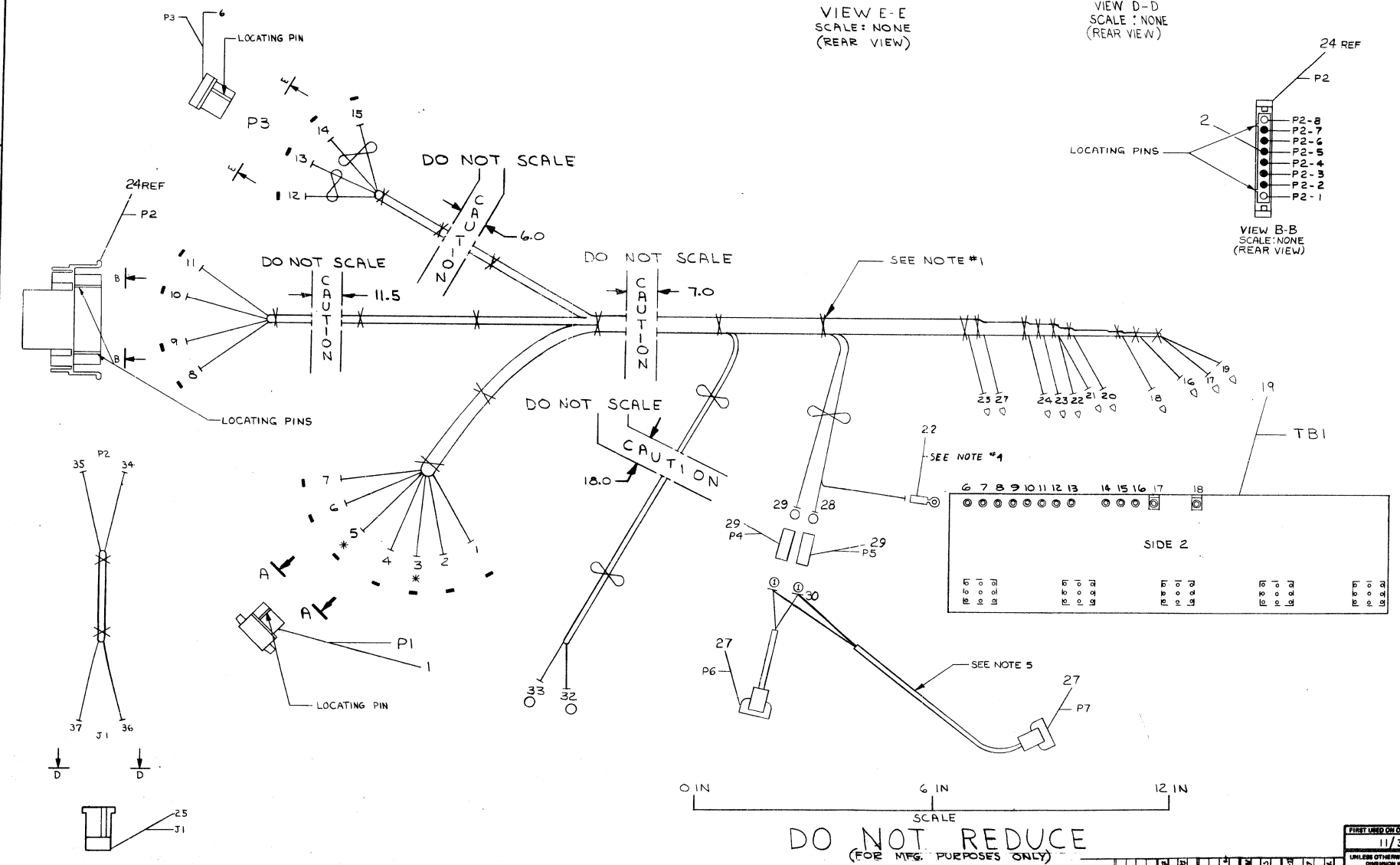
REV.	CHANGE NO.	DESCRIPTION
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2	7008856-0002	A
3	7008856-0003	A
4	7008856-0004	A
5	7008856-0005	A
6	7008856-0006	A
7	7008856-0007	A
8	7008856-0008	A

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SEE NOTE #2

WIRE TABLE										
ITEM NO	DESCRIPTION	AVG COLOR	POINT	FROM CONN	ITEM POINT	TO CONN	ITEM	LENGTH	SIGNAL NAME	
17	14	BLU	1	P1-4	2	TB1-14	SOLDER	27.50	-15V	
16	14	VIO	2	P1-4	31	TB1-8	SOLDER	29.80	DCLO	
21	12	BLK	3	P1-2	2*	TB1-7	SOLDER	25.50	GND(1)	
18	18									
13	12	RED	3	P1-3	31	TB1-13	SOLDER	26.25	ACLO	
20	14	GRY	4	P1-5	2*	TB1-12	SOLDER	27.25	+5V(1)	
15	14	ORN	6	P1-2	2	TB1-11	SOLDER	27.00	-15V	
30	14	RED	8	P2-2	2	TB1-10	SOLDER	26.75	LTC	
14	14	RED	11	P2-3	2	TB1-17	SOLDER	42.75	+5V	
14	14	BLK	9	P2-3	2	TB1-16	SOLDER	42.25		
14	14	BLK	10	P2-4	2	TB1-18	SOLDER	42.25	GND2	
23	20	BLK	23	P3-2	12	P3-4	SOLDER	31.50		
20	TWP	WHT	23	P3-2	13	P3-3	SOLDER	31.50		
18	TWP	RED	23	P3-2	13	P3-3	SOLDER	31.50		
18	TWP	WHT	23	P3-2	13	P3-3	SOLDER	31.50		
10	18	BLK	14	P3-2	31	P3-3	SOLDER	50.25		
17	14	BLU	34	P2-6	2	J1-1	SOLDER	50.25		
17	14	BLU	35	P2-7	2	J1-2	SOLDER	6.00		
27	18	BLK	30	P4	23	P6	SOLDER	2.50		
27	18	BLK	31	P4	29	P6	SOLDER	2.50		
27	18	BLK	37	P4	29	P7	SOLDER	9.00		



- NOTES:
- USE TIE WRAPS (ITEM #1006) APPROXIMATELY EVERY THREE (3) INCHES WHEN NECESSARY, AND AT EVERY BREAKOUT POINT.
  - ITEM #21 (12GR BLK) AND ITEM #16 (10GR VIO) ARE SEPARATE WIRES BUT A TWISTED PAIR SHALL BE MADE OF THEM BEFORE PLACING THEM IN THE HARNESS.
  - SHIELDED GND FOR WIRE TO FANS TO BE CONNECTED ON P4, P5 END ONLY.
  - SOLDER ITEM 22 TO SHIELD WIRE. MAKE SHIELD WIRE AT LEAST 2" LONG.
  - ITEMS 9, 27, 28 AND 29 CAN BE ORDERED AS A SEPARATE SUB-ASSEMBLY ITEM. DRAWING AND PART NUMBER C-1A-7009208-0-0.
  - SOLDER AFTER TERMINATE.



QTY.	DESCRIPTION	PART NO.	REF.
4	MATE-N-LOCK PIN	1209378-08	31
A/R	WIRE #14WG STD, RED	9107370-22	30
2	TERMINAL ADAPTOR, DOUBLE MALE PINS	9009110	29
2	CONN, SOLDERLESS #30906	9007969	28
2	FLUG-LOAD ASSEMBLY	1210283	27
2	BEST TERMINAL CONTACT TERMINAL	1209379-8	26
1	CONN, MATE-N-LOCK, 8 PIN	1209381-81	25
1	CONN, MATE-N-LOCK, 8 PIN	1209348-01	24
A/R	WIRE #20SHIELDED TWP BLK/WHT	9107701	23
1	TERMINAL, SHAKEPROOF	9008150	22
A/R	WIRE #12 STD, BLK	9107380-00	21
A/R	WIRE #14 STD, GRY	9107370-88	20
1	POWER DIST. BOARD	D059007-0-0	19
A/R	WIRE #18 STD, YEL	9107360-44	18
A/R	WIRE #14 STD, BLU	9107370-66	17
A/R	WIRE #16 STD, VIO	9107360-77	16
A/R	WIRE #14 STD, ORN	9107370-33	15
A/R	WIRE #14 STD, BLK	9107370-00	14
A/R	WIRE #12 STD, RED	9107380-22	13
A/R	WIRE #16 STD, GRN	9107360-55	12
A/R	WIRE #16 STD, BLK	9107360-00	11
A/R	WIRE #16 STD TWP, BLK/BLU	9107430-04	10
2	WIRE #12 STD, TWP, RED/BLK	9107380-24	9
Y	7 TIE WRAP, PAN/DUT #58-24	9007032	8
X	20 TIE WRAP, PAN/DUT #58-14	9007031	7
1	CONN, MATE-N-LOCK 4 PIN	1209381-04	6
4	HOUSING, 1-40041-0	1210820-1	5
4	WIRE #12 STD, GRN	9107360-33	4
4	CONN, SOLDERLESS #30902	9007970-00	3
13	PIN, CONTACT (MALE)	1209378-00	2
SYN	1 CONN, MATE-N-LOCK 9 PIN	1209381-09	1

FIRST USED ON OPTION MODEL 11/35

UNLESS OTHERWISE SPECIFIED DIMENSIONS IN INCHES TOLERANCES DECIMALS ANGLES

REWORK PARTS AND GREAT QUALITY

SEE PARTS LIST

DATE: 9/21/74  
BY: [Signature]  
CHECKED: [Signature]  
APPROVED: [Signature]

REVISIONS: 1, 2, 3, 4, 5, 6, 7, 8, 9, 10, 11, 12, 13, 14, 15, 16, 17, 18, 19, 20, 21, 22, 23, 24, 25, 26, 27, 28, 29, 30, 31, 32, 33, 34, 35, 36, 37, 38, 39, 40, 41, 42, 43, 44, 45, 46, 47, 48, 49, 50, 51, 52, 53, 54, 55, 56, 57, 58, 59, 60, 61, 62, 63, 64, 65, 66, 67, 68, 69, 70, 71, 72, 73, 74, 75, 76, 77, 78, 79, 80, 81, 82, 83, 84, 85, 86, 87, 88, 89, 90, 91, 92, 93, 94, 95, 96, 97, 98, 99, 100

DATE: 11/35  
TITLE: PDP 11/35 POWER TO DISTRIBUTION BOARD HARNESS  
PART NO.: 11A7009208-0-0  
REV: E

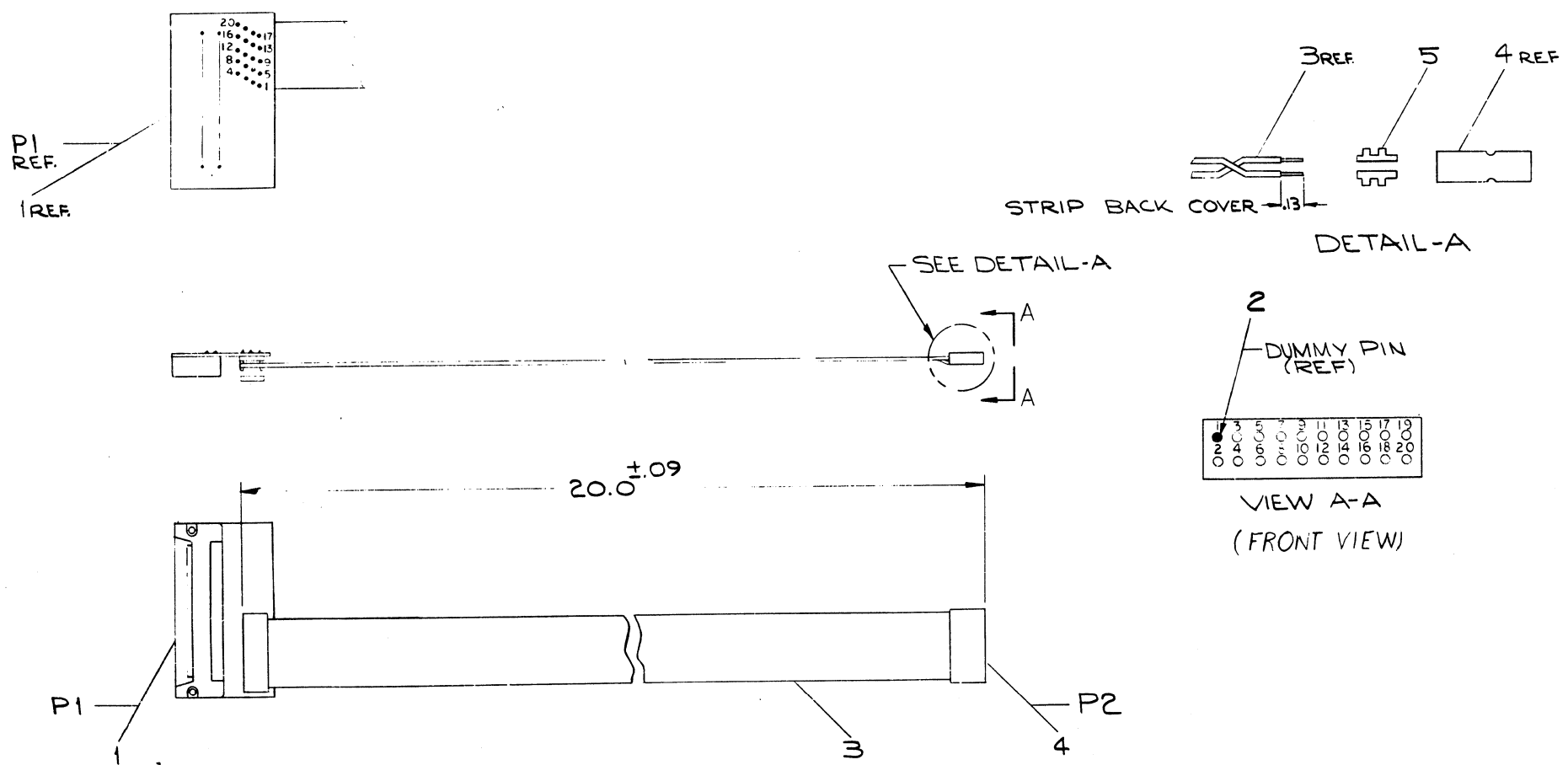
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0-0-028800Z  
REV. 1/72

NOTES:

- CONNECTORS P1 AND P2 ARE TO BE WIRED POINT TO POINT (P1-1 TO P2-1 P1-2 TO P2-2 ECT.)

D  
C  
B  
A



QTY.	DESCRIPTION	PART NO.	ITEM NO.
19	MINI-TERMINAL #4783 BERG	1210089-0	5
1	RECEPTACLE 20 PIN #G2043-027 BERG	1210918-027	4
A/R	CABLE SCOTCHFLEX #3350 3M	9107747	3
1	DUMMY PIN #4700 BERG	9009190	2
1	DC. DISTRIBUTION MODULE	5409949-00	1

FIRST USED ON OPTION/MODEL	QTY.	DESCRIPTION	PART NO.	ITEM NO.
PDP1105				

UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES TOLERANCES	DECIMALS	ANGLES	REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY	DATE	DATE	DATE	DATE	DATE	DATE
.XXX - .005	.XX - .02	20° 30'		2-14-72	3-17-72	5-24-72	5-24-72	5-24-72	5-24-72

digital EQUIPMENT CORPORATION		TITLE	
MASSACHUSETTS		HEADER CABLE ASSY	

MATERIAL	NEXT HIGHER ASSY.	SIZE CODE	NUMBER	REV.
SEE PARTS LIST	DUR-1105-0-0	DIA	7008820-0-0	A

FINISH	SCALE	SHEET	OF	DIST.
	NONE	1	1	

REVISIONS	CHANGE NO.	REV.
CHK	7008820-0001A	A
DESIGNED BY	DEBORAHVILLE	
DATE	5-24-72	

DEC FORM NO. DRD 100-A

DIA 7008820-0-0

A


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J1 BERG HEADER ON P.C. CARD	J2 20 PIN 3M CONNECTOR	SIGNAL	OUTPUT CABLE (REFERENCE)
VV	9-12	GROUND	GROUND (A)
UU	9-12	GROUND	GROUND (B)
W	9-12	GROUND	GROUND (UU)
A	9-12	GROUND	GROUND (VV)
RR	16	SERIAL IN (TTL)	E
NN	15	20 MA INTERLOCK	I
LL	14	SERIAL IN (+20MA)	K
DD	5	SERIAL IN (-20MA)	S
Y	7	SERIAL OUT (+20MA)	AA
T	6	CLOCK IN (TTL)	CC
R	4	READER RUN (-20MA)	EE
Z	3	CLOCK DISABLE (TTL)	HH
L	2	SERIAL OUT (-20MA)	KK
F	10	READER RUN (+20MA)	PP
D	9	SERIAL OUT (TTL)	SS
C	20	+5 VOLTS	TT
BB	0	+15 VOLTS	U

D  
C  
B  
A

D  
C  
B  
A

REV.	
CHANGE NO.	
CHK	

FIRST USED ON OPTION/MODEL PDP-1105	QTY.	DESCRIPTION	PART NO.	ITEM NO.
PARTS LIST				
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES. TOLERANCES	DRN. C. Teschner	DATE 4-6-72	 <b>digital</b> EQUIPMENT CORPORATION <small>MAYNARD, MASSACHUSETTS</small>	
DECIMALS .XXX = .005 XX = .02 X = .1	ENG. C. Teschner	DATE 4-6-72		
ANGLES ±0° 30'	PROJ/ENG. C. Teschner	DATE 4-13-72		
REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY ✓	PROD. ✓	DATE 4-13-72		
MATERIAL / /	NEXT HIGHER ASSY. D-IA-5409949-P-P	SIZE CODE C CS	NUMBER 5409949-0-1	REV. A
FINISH / /	SCALE / /	SHEET 1 OF 1		



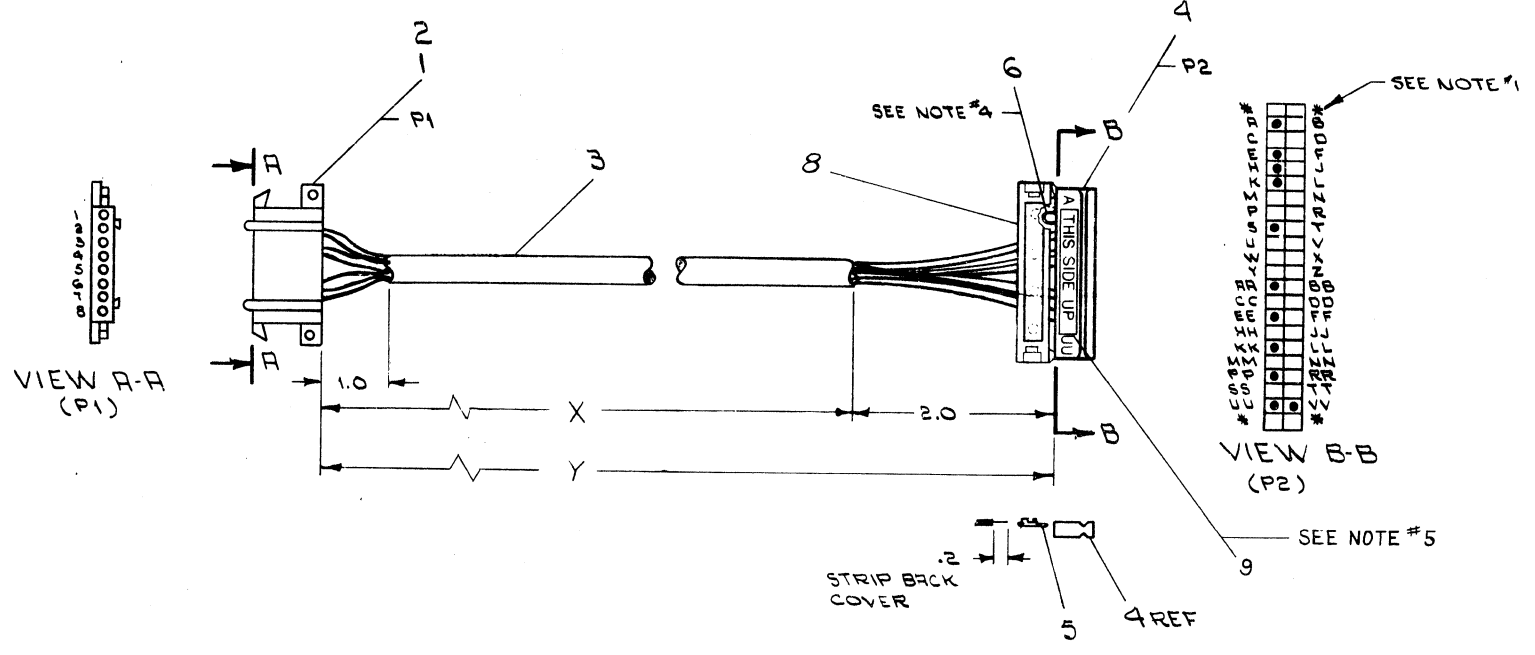
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WIRE TABLE							
ITEM NO.	AWG	COLOR	PAIR	FROM CONNECTION	WITH CONNECTION	TO CONNECTION	WITH CONNECTION
3	22	BLK		P1-2	2	P2-KK	5
3		RED	1	P1-3	2	P2-S	
3,7		SHIELD		SEE NOTE #2	-	P2-R(NOTE#3)	
3		BLK		P1-4	2	P2-EE	
3		WHT	2	P1-5	2	P2-RR	
3,7		SHIELD		SEE NOTE #2	-	P2-UV(NOTE#3)	
3		BLK		P1-6	2	P2-PP	
3		GRN	3	P1-7	2	P2-K	
3,7		SHIELD		SEE NOTE #2	-	P2-VV(NOTE#3)	
6	22	BLK	-	P2-E	5	P2-H	5

LEGEND		
VARIATION	LENGTH	
	X	Y
7008360-0	25IN±1.0	27IN±1.0
7008360-1	46IN±1.0	48IN±1.0
7008360-9	9FT±2IN	9FT2IN±2IN

H 0-0-098800Z VIT d 2

- NOTES:
- \* ASTERISKS INDICATE CAVITIES NOT USED OR DESIGNATED BY LETTERS.
  - DRAIN WIRES TO BE CUT BACK TO OUTER INSULATION ON P1 END OF CABLE ONLY. SHIELDS TO BE CUT BACK TO OUTER INSULATION ON BOTH ENDS OF CABLES.
  - DRAIN WIRES ON P2 END OF CABLE TO BE EACH ENCLOSED WITH ITEM #7 (TUBING) FROM END OF CABLE JACKET TO POINT WHERE THEY ENTER P2 CONNECTOR.
  - ITEM #6 (WIRE) TO BE APPROXIMATELY ONE(1) INCH LONG.
  - PLACE ITEM #9 ("THIS SIDE UP" STICKER) ON LETTERED SIDE OF ITEM #4 (BERG HOUSING) AS SHOWN.



QTY.	DESCRIPTION	PART NO.	ITEM NO.
1	LABEL, THIS SIDE UP	3611567	9
1	STRAIN RELIEF	1211166	8
R/R	TUB. #18 TEF. THINWALL NAT	9107278-11	7
R/R	WIRE #22 AWG STRD TEF BLK	9107350-00	6
11	SOCKET, CRIMP #47216	1210089-07	5
1	HOUSING, BERG #65043-015	1210918-15	4
R/R	CABLE, BELDEN #877-3PR SHLD	9107725-0	3
6	CONTACT MATE-N-LOCK (FEMALE)	1209379-03	2
1	CONN. MATE-N-LOCK (FEMALE)	1209340-00	1

CHK	REVISIONS	CHANGE NO.	REV
	A	6-E-00002	
	B	KLBE-00008	
	C	7008360-00001	
	D	7008360-00002	
	E	10-28-73	
	F		
	G		
	H		

FIRST USED ON OPTION / MODEL PDP-8E

DO NOT SCALE DRAWING  
 UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES  
 TOLERANCES: ANGLES ± 0°30', FINISH SURFACE QUALITY 1, REMOVE BURRS AND BREAK SHARP CORNERS  
 MATERIAL: SEE PARTS LIST  
 FINISH: NONE  
 DATE: 4-8-71  
 PROJ. ENG: J. Mcnamara  
 DATE: 4-8-71  
 DATE: 11/1/71

**digital EQUIPMENT CORPORATION**  
 WATUARD MASSACHUSETTS  
**CABLE ASSEMBLY (KL8E)**  
 SCALE: NONE  
 SIZE CODE: DIA 7008360-0-0  
 REV: H

DIA 7008360-0-0

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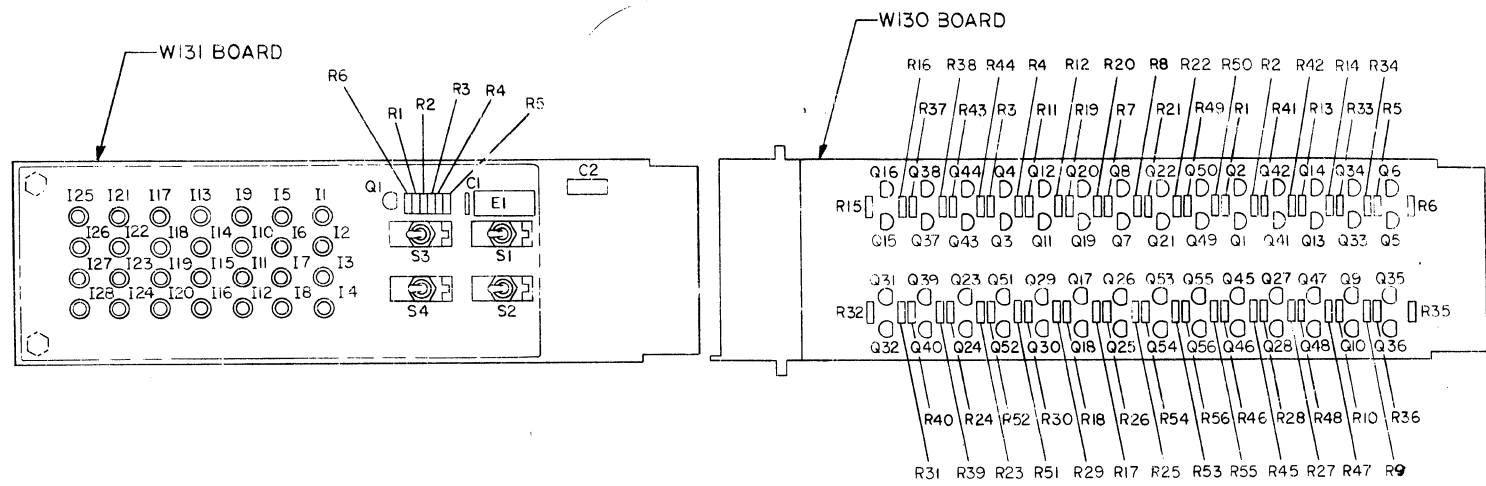
W130 PARTS REFERENCE

ITEM NO	DRAWING REFERENCE	DESCRIPTION	PART NUMBER	QUANTITY
1	R1, R3, R5, R7, R9, R11, R13, R15, R17, R19, R21, R23, R25, R27, R29, R31, R33, R35, R37, R39, R41, R43, R45, R47, R49, R51, R53, R55	15K, 1/4W, 5%	RES. 1300496	28
2	R2, R4, R6, R8, R10, R12, R14, R16, R18, R20, R22, R24, R26, R28, R30, R32, R34, R36, R38, R40, R42, R44, R46, R48, R50, R52, R54, R56	470, 1/4W, 5%	RES. 1300316	28
3	Q1-Q56	DEC 3009B TRANSISTOR	1503100	56
4	P1	H607 BLOCK, CONNECTOR	1209123	1

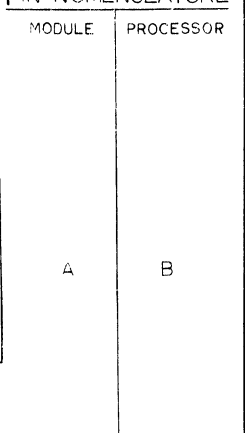
W131 PARTS REFERENCE

ITEM NO	DRAWING REFERENCE	DESCRIPTION	PART NUMBER	QUANTITY
1	E1	DEC 7400N IC	1905575	1
2	C1	.01 MFD, 10CV, 20% DC CAP.	1001610	1
3	C2	6.8 MFD, 35V, 20% ST. CAP.	1000057	1
4	R1, R2, R3, R4, R5	3K, 1/4W, 5%	RES. 1300432	5
5	R6	330, 1/4W, 5%	RES. 1300295	1
6	Q1	DEC 3009B TRANSISTOR	1503100	1
7	I1-I28	LAMP, HUDSON, BLUE *2309G	1209219	28
8	S1-S4	SWITCH, TOGGLE, SPST, 6ATI-T2	1201168	4

COMPONENT PLACEMENT



PIN NOMENCLATURE

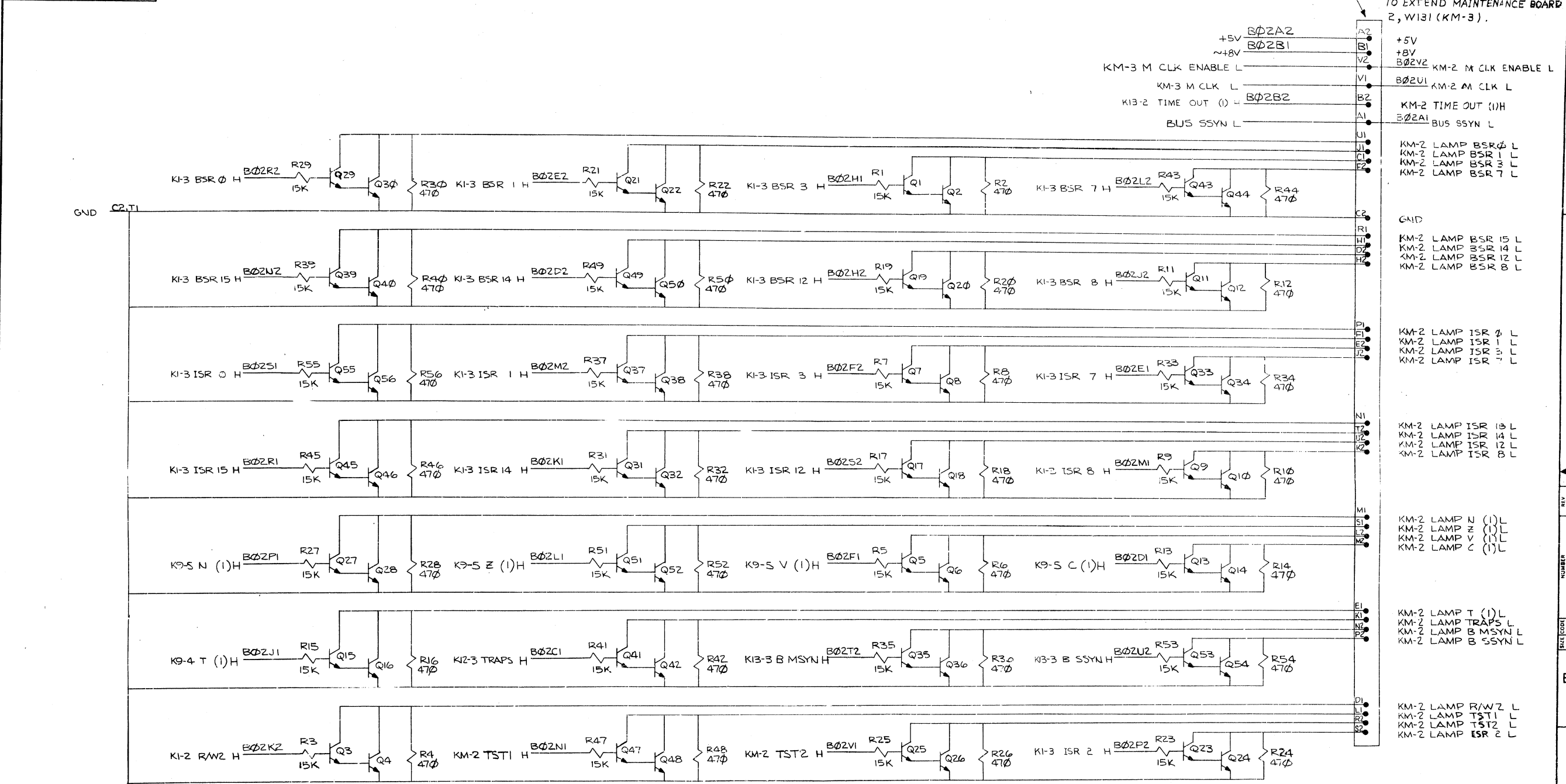


- NOTES:
1. THE KM11 IS A TWO MODULE (#130, W131) OPTION TO THE KA11 TO AID MAINTENANCE. THIS PREWIRED OPTION IS INSTALLED BY INSERTING THE W130 MODULE INTO LOCATION D02 AND INSERTING THE W131 MODULE INTO THE W130. NOTE THAT THE SWITCHES AND LIGHTS FACE TOWARD AND EXTEND BELOW THE CONSOLE. THE BOTTOM COVER MUST BE REMOVED WITH THE CHASSIS EXTERNAL TO THE CABINET.
  2. LABELS FOR THE INTERNAL MACHINE STATES LAMPS ARE NOTED ON THE W131 ETCH BOARD. SWITCHES PROVIDE A MANUAL CLOCK AND BUS RESPONSE AND ARE ACTIVE WHEN THE TOGGLE IS TOWARD THE NAME. NORMAL MACHINE OPERATION REQUIRES THAT ALL SWITCHES BE IN THE OFF POSITION.
  3. "M CLK ENABLE" AND "M CLK" PROVIDE A MANUAL CLOCK FOR THE KA11. "M CLK ENABLE" IS ACTIVATED WHILE THE PROCESSOR IS HALTED. EACH TOGGLE OF "M CLK" THEN STEPS THE PROCESSOR THROUGH THE SMALLEST PROCESSOR CLOCK INTERVALS, THE R/W STATES, THE NEXT HIGHEST CLOCK INTERVAL (S CLK) IS PROVIDED BY FOUR TOGGLES (2 COMPLETE SWITCH CYCLES) AND INDICATED BY THE R/W2 LAMP. R/W2 IS THE LAST (OR REST) R/W STATE IN A "S CLK" INTERVAL, NORMAL OPERATION IS RESUMED WHEN "M CLK" AND THEN "M CLK ENABLE" ARE RETURNED TO OFF.
  4. "NO TIME OUT" AND "SSYN" PROVIDE A MANUAL BUS RESPONSE TO THE PROCESSOR. IT IS USED WHEN OTHER DEVICES ARE NOT AVAILABLE. "NO TIME OUT" IS ACTIVATED, WHILE THE PROCESSOR IS HALTED, TO ELIMINATE AN ERROR TRAP ON MANUAL "SSYN". AT THE APPROPRIATE TIMES IN A BUS TRANSFER "SSYN" IS ACTIVATED AND DEACTIVATED.

REV.	CHG.	NO.

QTY.	DESCRIPTION	PART NO.	ITEM NO.
PARTS LIST			
FIRST USED ON OPTION/MODEL PDP11		DO NOT SCALE DRAWING	
CHK'D	DATE	digital EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS	
ENG.	DATE	TITLE MAINTENANCE BOARD (1&2) KM-1	
PRO. ENG.	DATE	SIZE CODE D	
PROD.	DATE	NUMBER KM11-0-MB	
MATERIAL	NEXT HIGHER ASSY	REV.	
FINISH	A-ML-KM11-0	SCALE 1/1	
SHEET 1 OF 3		DIST.	

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CONNECTOR AT REAR OF MODULE TO EXTEND MAINTENANCE BOARD 2, W131 (KM-3).

A2 +5V B02A2  
 B1 +5V B02B1  
 V2 ~+8V B02V2  
 VI KM-3 M CLK ENABLE L B02U1 KM-2 M CLK ENABLE L  
 VI KM-3 M CLK L B02U1 KM-2 M CLK L  
 B2 KI3-2 TIME OUT (1) B02B2 KM-2 TIME OUT (1)H  
 A1 B02A1 BUS SSYN L  
 U1  
 J1  
 C1  
 E1

KM-2 LAMP BSR 0 L  
 KM-2 LAMP BSR 1 L  
 KM-2 LAMP BSR 3 L  
 KM-2 LAMP BSR 7 L  
 GND  
 KM-2 LAMP BSR 15 L  
 KM-2 LAMP BSR 14 L  
 KM-2 LAMP BSR 12 L  
 KM-2 LAMP BSR 8 L  
 KM-2 LAMP ISR 0 L  
 KM-2 LAMP ISR 1 L  
 KM-2 LAMP ISR 3 L  
 KM-2 LAMP ISR 7 L  
 KM-2 LAMP ISR 10 L  
 KM-2 LAMP ISR 14 L  
 KM-2 LAMP ISR 12 L  
 KM-2 LAMP ISR 8 L  
 KM-2 LAMP N (1) L  
 KM-2 LAMP Z (1) L  
 KM-2 LAMP V (1) L  
 KM-2 LAMP C (1) L  
 KM-2 LAMP T (1) L  
 KM-2 LAMP TRAPS L  
 KM-2 LAMP B MSYN L  
 KM-2 LAMP B SSYN L  
 KM-2 LAMP R/W2 L  
 KM-2 LAMP TST1 L  
 KM-2 LAMP TST2 L  
 KM-2 LAMP ISR 2 L

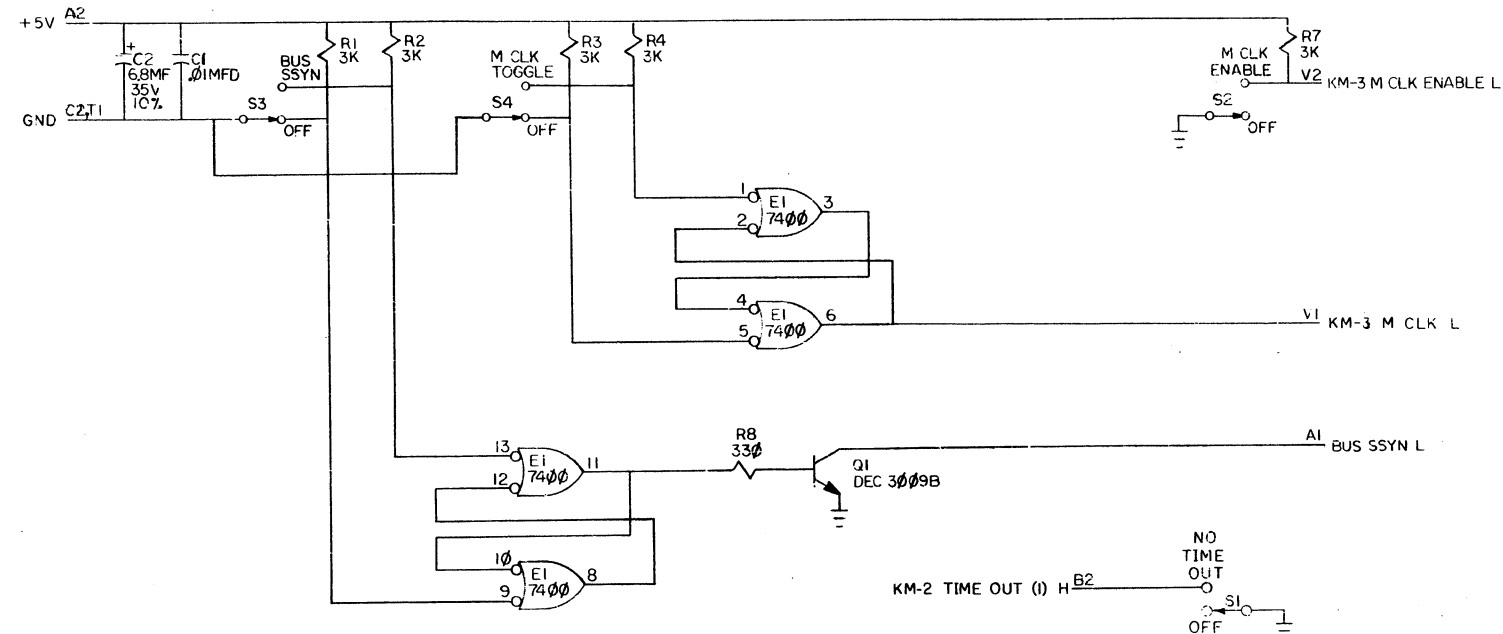
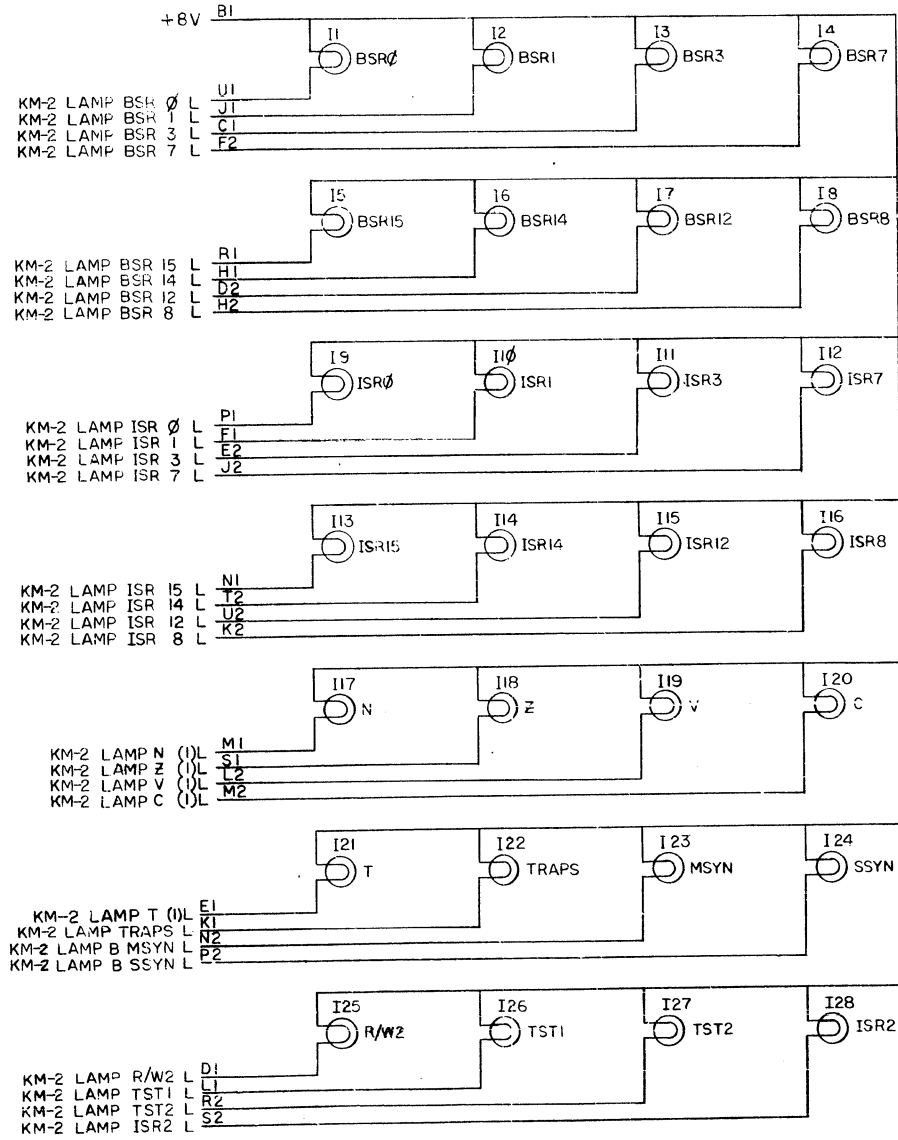
REV.	
CHG.	
NO.	

FIRST USED ON OPTION/MODEL	QTY.	DESCRIPTION	PART NO.	ITEM NO.
PDP 11				
PARTS LIST				
UNLESS OTHERWISE SPECIFIED	DRN	DATE	digital EQUIPMENT CORPORATION	
UNLESS OTHERWISE SPECIFIED	CHK'D	DATE	WATUARD MASSACHUSETTS	
DIMENSION IN INCHES	ENG	DATE	TITLE	
TOLERANCES	PROJ. ENG.	DATE	MAINTENANCE BOARD (I)	
DECIMALS FRACTIONS ANGLES	PROJ.	DATE	W130 KM-2	
= .005 = 1/64 = 0.130			SIZE CODE NUMBER REV.	
FINAL SURFACE QUALITY			DBS KMI-0-MB	
REMOVE BURRS AND BREAK SHARP CORNERS			SCALE NONE	
MATERIAL			SHEET 2 OF 3	
NEXT HIGHER ASSY			DIST.	
FINISH				



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DBS KM11-0-MB 2

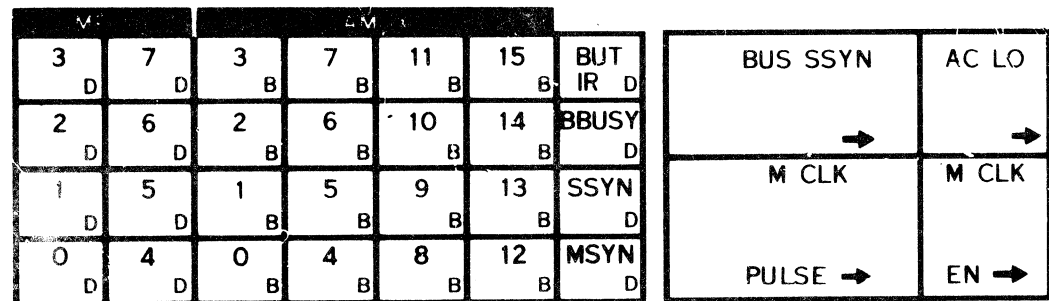


REV	NO	DATE	BY

DEC FORM NO 102A

FIRST USED ON OPTION/MODEL PDP 11	QTY.	DESCRIPTION	PART NO.	ITEM NO.
UNLESS OTHERWISE SPECIFIED				
UNLESS OTHERWISE SPECIFIED	DRN	DATE	PARTS LIST	
DIMENSION IN INCHES	J. Budis	7-7-69	digital EQUIPMENT CORPORATION	
DECIMALS FRACTIONS ANGLES	CHK'D	DATE	MAINTENANCE BOARD(2)	
± .005 ± 1/64 ± 0°30'	11	3-21-70	WI31 KM-3	
FINAL SURFACE QUALITY / REMOVE BURRS AND BREAK SHARP CORNERS	ENG.	DATE	SIZE CODE NUMBER REV	
		3-6-78	DBS KM11-0-MB	
MATERIAL	PROJ. ENGR.	DATE	SCALE NONE	
		3/5/70	SHEET 3 OF 3	
FINISH	PROD.		DIST.	

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D-DIM WHEN ASSERTED  
B-BRIGHT WHEN ASSERTED

5509081-0-9

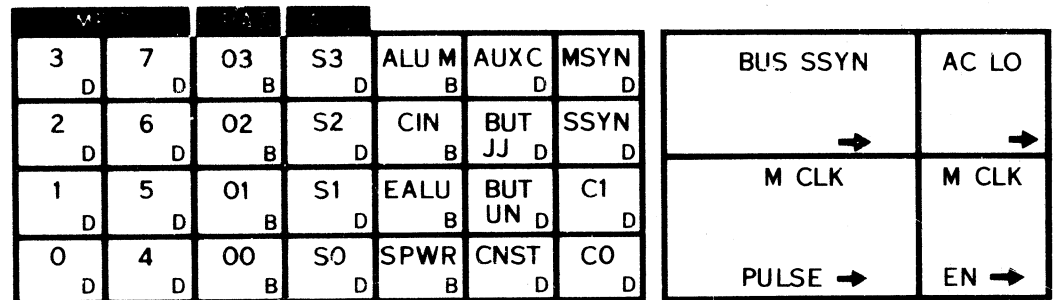
FIRST USED ON OPT 1110P  
11/05

REVISIONS	CHANGE NO.	REV.
	CHK	

SPEC\*9200100-94 (BLACK)

DRN. <i>D. Mattam</i>	DATE 5-11-72	<b>digital</b> EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS
CHK'D. <i>[Signature]</i>	DATE	
ENG. <i>[Signature]</i>	DATE	TITLE
PROJ. ENG. <i>[Signature]</i>	DATE 5-22-72	MAINT MODULE OVERLAY (11/05 - KM1)
PROD. <i>RK Peterson</i>	DATE 5/27/72	
NEXT HIGHER ASSY C-IA-5509081-0-0		SIZE CODE A 55
SCALE		NUMBER 5509081-0-9
SHEET	OF	DIST. 6

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D-DIM WHEN ASSERTED  
B-BRIGHT WHEN ASSERTED

5509081-0-10

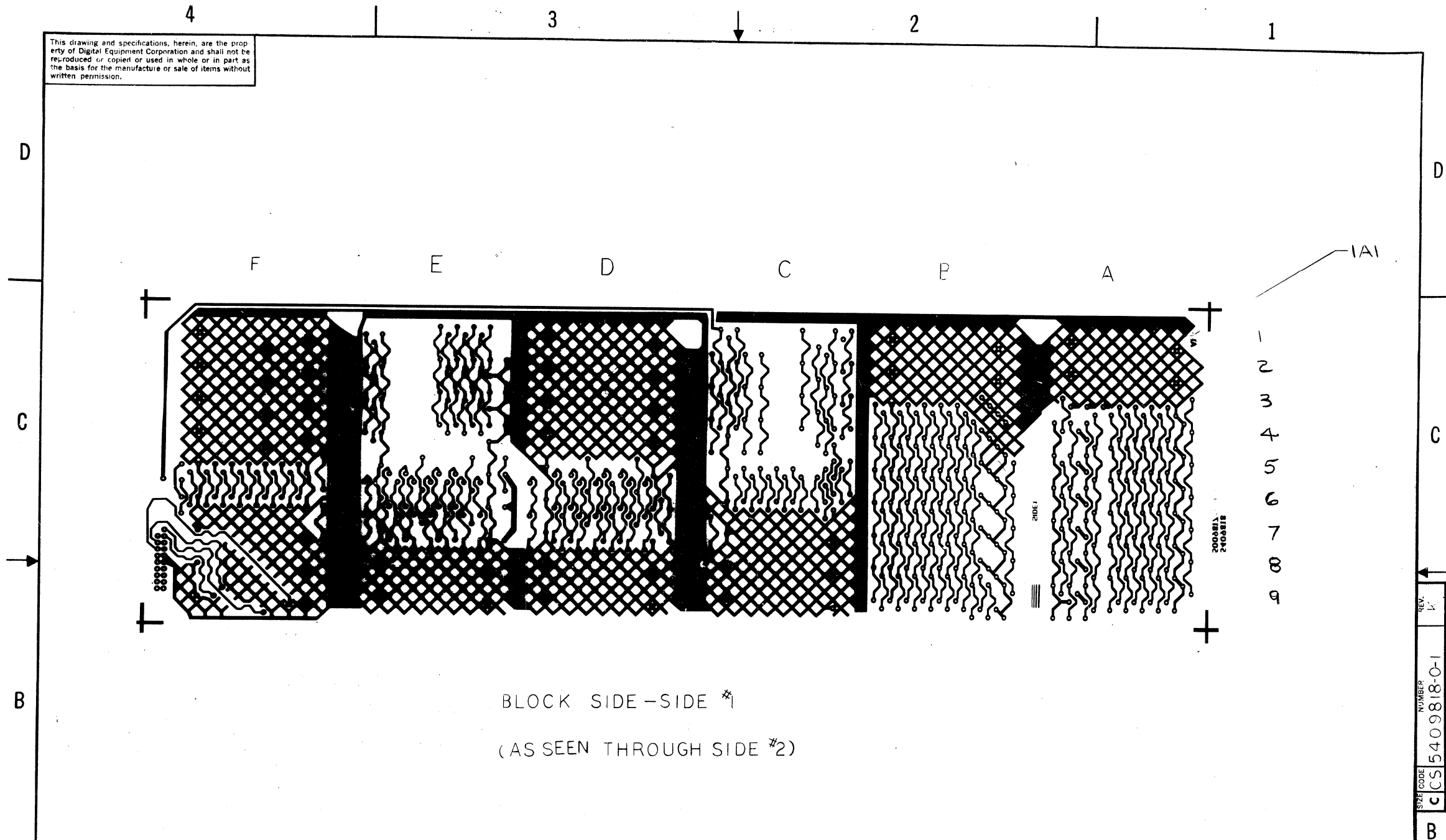
FIRST USED ON DPT/MOD  
11/05

REVISIONS	REV.	
	CHANGE NO.	
CHK		

SPEC # 9200150-94 (BLACK)

DRN. <i>D. Mattson</i>	DATE	<b>digital</b> EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS
CHK'D. <i>...</i>	DATE	
ENG. <i>...</i>	DATE	TITLE
PROJ. ENG. <i>A. Teicher</i>	DATE 5-23-72	MAINT MODULE OVERLAY (11/05-KM2)
PROD. <i>R.V. Peterson</i>	DATE 5/24/72	
NEXT FIGURE 1334		
C-IA-5509081-0-0	SIZE CODE	NUMBER
SCALE	A SS	5509081-0-10
SHEET	OF	DIST. 6

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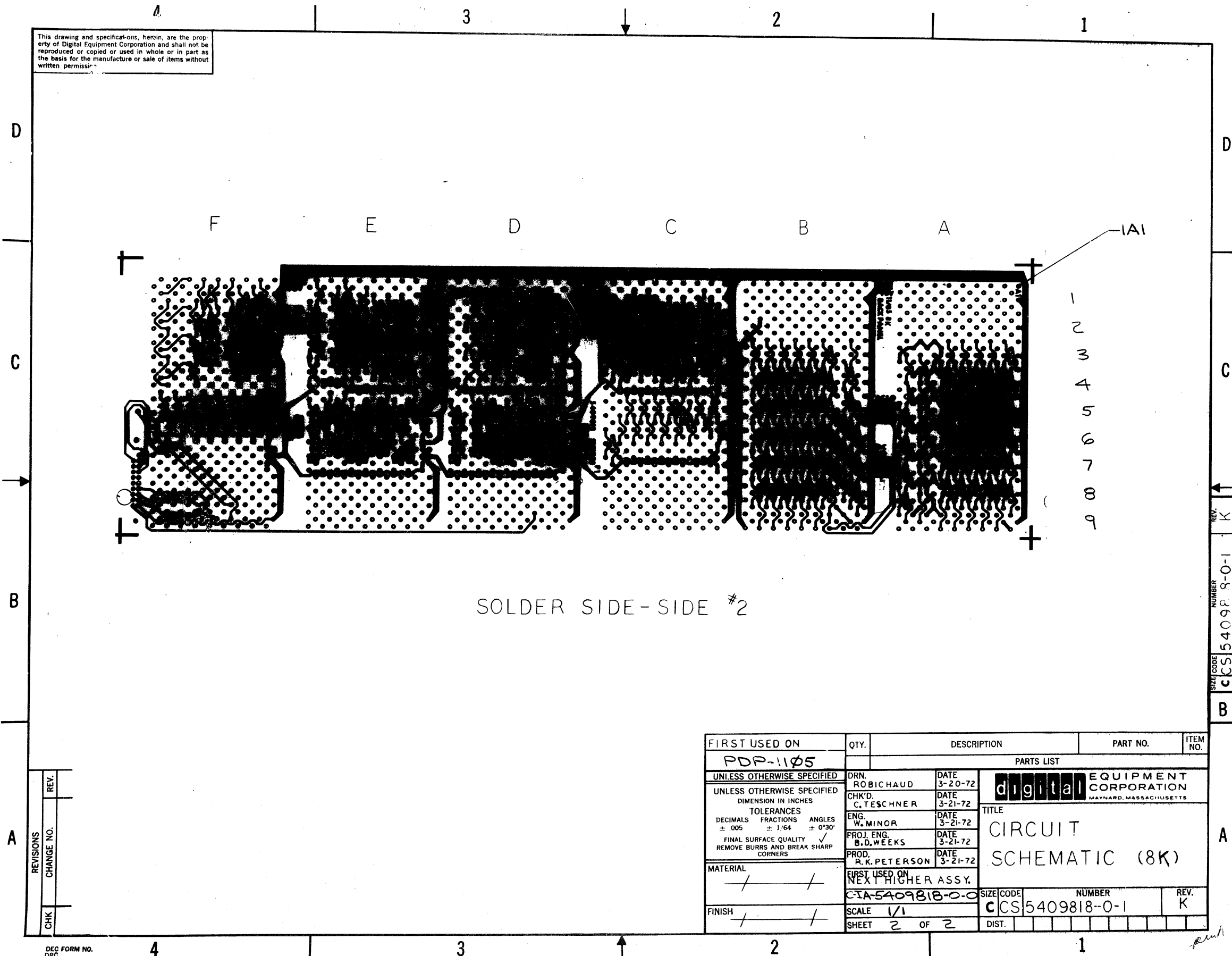
BLOCK SIDE-SIDE #1  
(AS SEEN THROUGH SIDE #2)

REV.	CHANGE NO.	DATE	BY
D	5409818-00001	5-7-72	B. MINOR
E	5409818-00002	2/5/73	G. GRAHAM
F	5409818-00003	3/27/73	D. RANDALL
H	5409818-00004	5-29-73	B. MINOR
J	5409818-00005	6-1-73	G. GRAHAM
K	5409818-00006	12-2-73	B. MINOR
L	5409818-00007	4-19-74	B. MINOR

FIRST USED ON <b>PDP-1105</b>	QTY.	DESCRIPTION	PART NO.	ITEM NO.
UNLESS OTHERWISE SPECIFIED				
UNLESS OTHERWISE SPECIFIED	DRN.	DATE	PARTS LIST	
DIMENSION IN INCHES	ROBICHAUD	8-20-72	<b>digital</b> EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS	
TOLERANCES	CHK'D.	DATE	TITLE	
DECIMALS FRACTIONS ANGLES	C. TESCHNER	3-21-72	CIRCUIT SCHEMATIC (8K)	
+ .005 + .064 + 0°30'	ENG.	DATE	SIZE CODE NUMBER REV.	
FINAL SURFACE QUALITY	W. MINOR	3-21-72	C	CS 5409818-0-1
REMOVE BURRS AND BREAK SHARP CORNERS	PROJ. ENG.	DATE	K	
	B.D. WEEKS	3-21-72	DIST.	
MATERIAL	PROD.	DATE	SHEET 1 OF 2	
FINISH	R. K. PETERSON	3-21-72	FIRST USED ON NEXT HIGHER ASSY.	
			C-IA-5409818-0-0	

REV. L  
NUMBER 5409818-0-1  
SIZE CODE CCS

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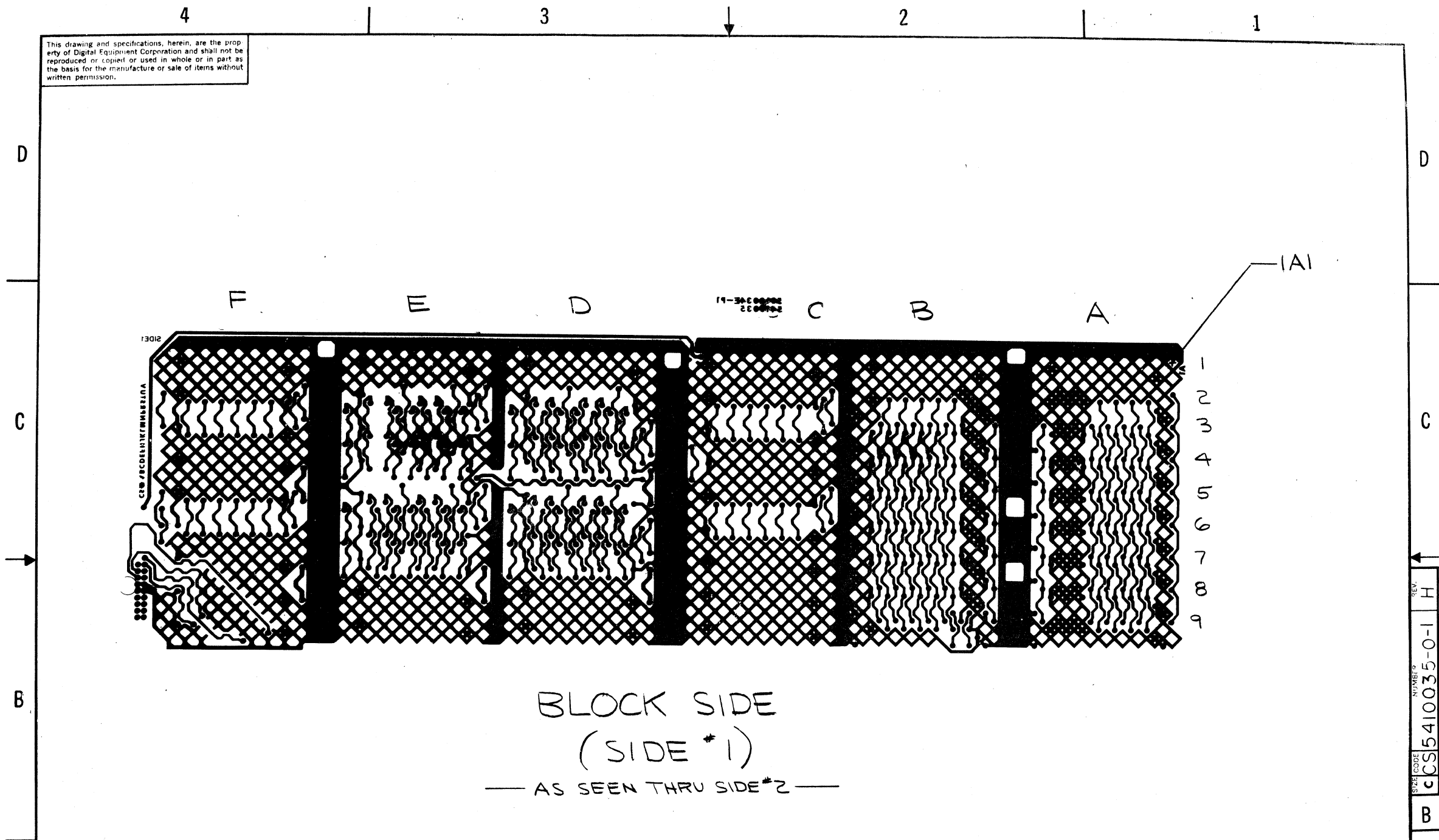
SOLDER SIDE-SIDE #2

REV.	
CHANGE NO.	
CHK	

FIRST USED ON PDP-1105	QTY.	DESCRIPTION	PART NO.	ITEM NO.
PARTS LIST				
UNLESS OTHERWISE SPECIFIED	DRN. ROBICHAUD	DATE 3-20-72	digital EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS	
UNLESS OTHERWISE SPECIFIED	CHK'D. C. TESCHNER	DATE 3-21-72	TITLE	
DIMENSION IN INCHES	ENG. W. MINOR	DATE 3-21-72	CIRCUIT	
TOLERANCES	PROJ. ENG. B. D. WEEKS	DATE 3-21-72	SCHEMATIC (8K)	
DECIMALS ± .005	PROD. R. K. PETERSON	DATE 3-21-72		
FRACTIONS ± 1/64				
ANGLES ± 0°30'				
FINAL SURFACE QUALITY ✓	FIRST USED ON	NEXT HIGHER ASSY.		
REMOVE BURRS AND BREAK SHARP CORNERS	CIA-5409818-0-0	SIZE CODE	NUMBER	REV.
MATERIAL		CCS	5409818-0-1	K
FINISH	SCALE 1/1	DIST.		
	SHEET 2 OF 2			

REV. NUMBER  
K  
8-0-1  
54098  
CS

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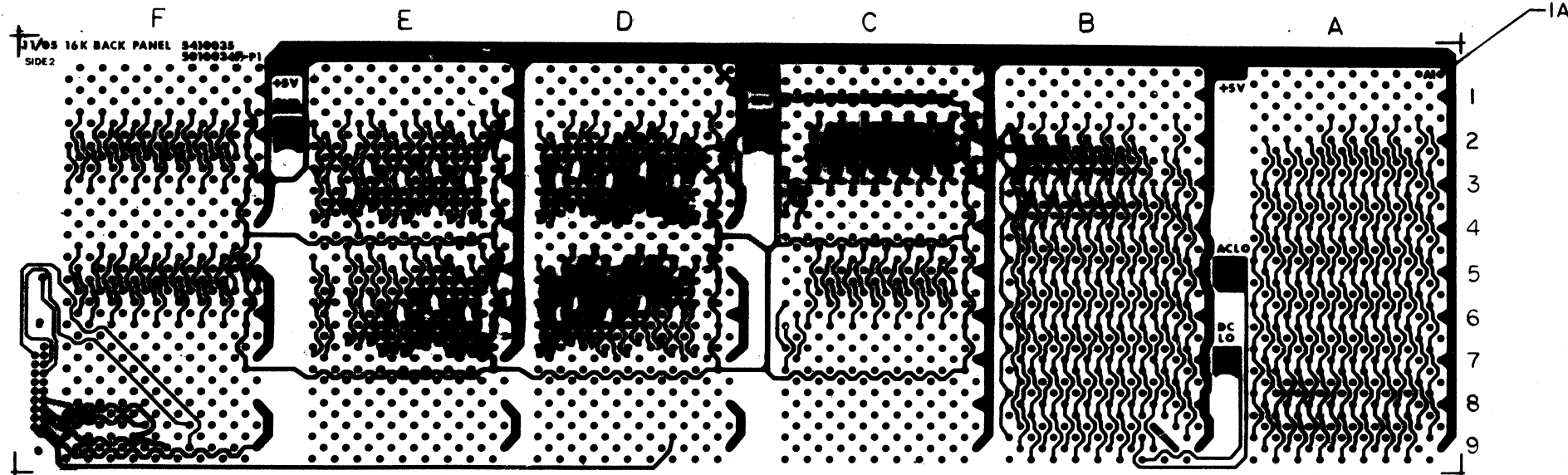
BLOCK SIDE  
(SIDE #1)  
— AS SEEN THRU SIDE #2 —

CHK	REVISIONS	CHANGE NO.	REV.
		5410035-00001	B
		2/1/73	
	G. GRAHAM		
	G. Graham	2-13-73	
		5410035-00002	C
		3/9/73	
	D. RANDALL		
	D. Randall	3/12/73	
		5410035-00003	D
	B. F. Hilde	5-25-73	
	G. GRAHAM		
	G. Graham	6-1-73	
		5410035-00004	E
	D. Randall	7/1/73	
	D. Randall	7/26/73	
		5410035-00005	F
	B. MINOR	11-15-73	
	B. Minor	11-21-73	
		5410035-00006	H
	H. Denmore	4-23-74	
	B. MINOR		
	B. E. Minor	7-17-74	

FIRST USED ON OPTION/ MODEL	QTY.	DESCRIPTION	PART NO.	CELL NO.
PDP-1105				
PARTS LIST				
UNLESS OTHERWISE SPECIFIED	DATE	digital EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS		
UNLESS OTHERWISE SPECIFIED	5-11-72	TITLE CIRCUIT SCHEMATIC (16K)		
DIMENSION IN INCHES	5-11-72			
TOLERANCES	5-11-72			
DECIMALS FRACTIONS ANGLES	5-12-72			
+ .005 + .01 + 0/20	5/15/72	SIZE CODE NUMBER REV. CCS 5410035-0-1 H		
FINAL SURFACE QUALITY REMOVE RIBBS AND BREAK SHARP CORNERS		NEXT HIGHER ASSY. C-1A-54100350-0		
MATERIAL		SCALE 1/1		
FINISH		SHEET 1 OF 2		
		DIST.		

SIZE CODE NUMBER REV.  
CCS 5410035-0-1 H

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SOLDER SIDE  
(SIDE 2)

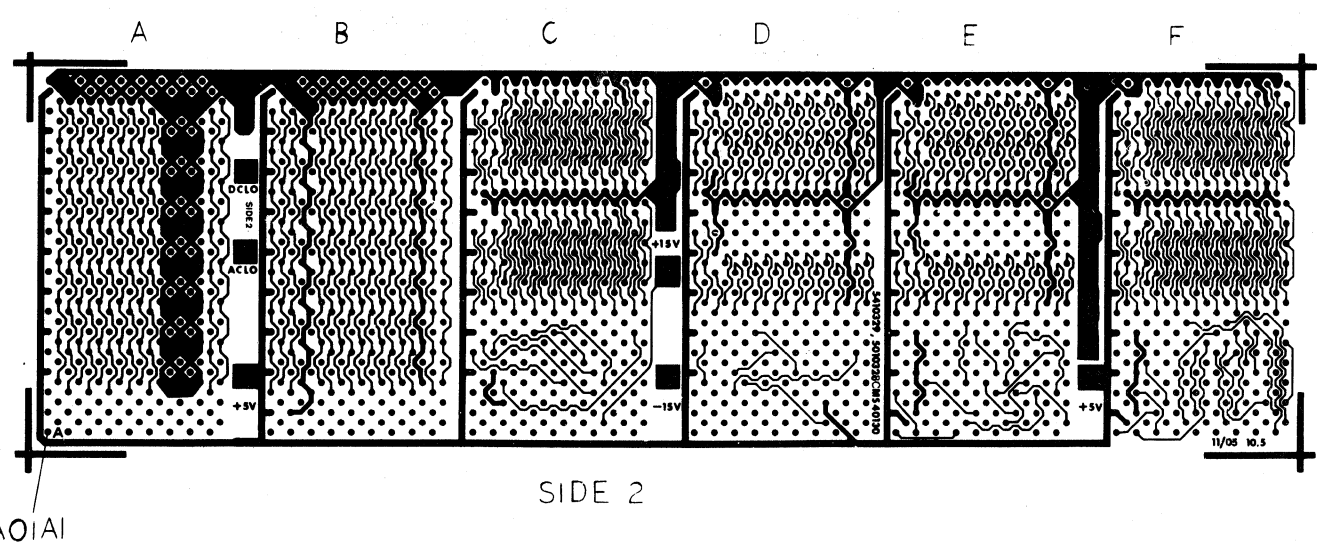
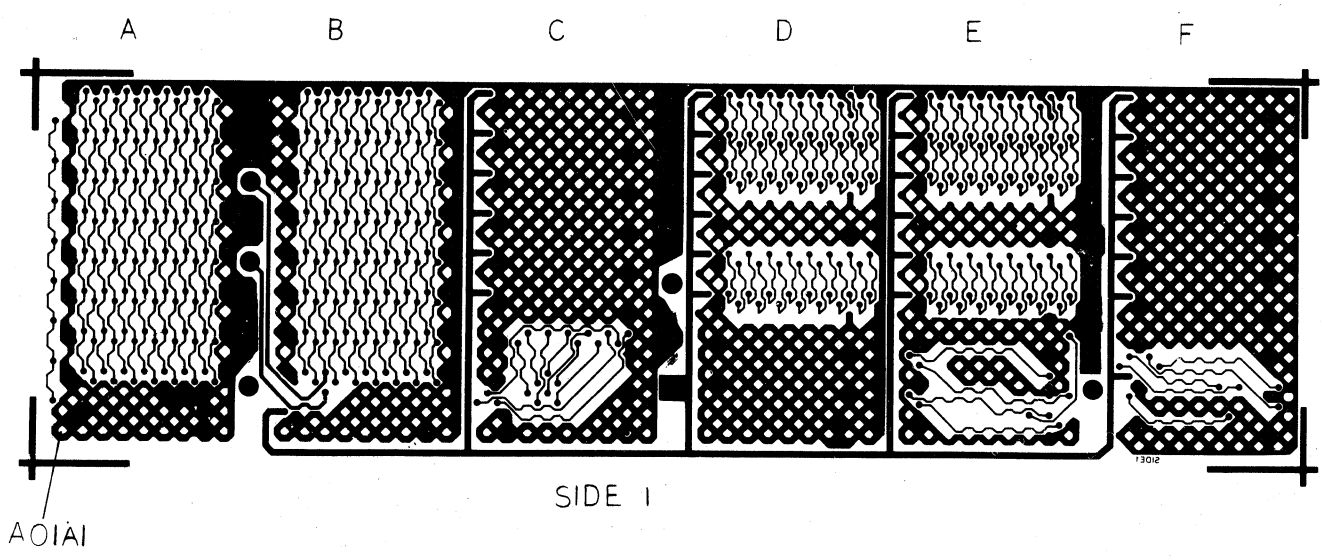
REV.	
CHANGE NO.	
CHK	

FIRST USED ON OPTION/MODEL PD11/05	QTY.	DESCRIPTION	PART NO.	ITEM NO.
PARTS LIST				
UNLESS OTHERWISE SPECIFIED	DRN. <i>R. Cook</i>	DATE <i>5/17/72</i>	digital EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS	
UNLESS OTHERWISE SPECIFIED	CHK'D. <i>C. Teschner</i>	DATE <i>5-11-72</i>	TITLE	
DIMENSION IN INCHES	ENG. <i>W. Minor</i>	DATE <i>5-14-72</i>	CIRCUIT SCHEMATIC (16K)	
TOLERANCES	PROJ'G. <i>B. D. Wilbur</i>	DATE <i>5-11-72</i>		
DECIMALS FRACTIONS ANGLES	PROD. <i>R. Peterson</i>	DATE <i>5/17/72</i>		
+ .005 + 1/64 + 0°30'	FIRST USED ON ASSY			
FINAL SURFACE QUALITY ✓	E-IA-5410035-0-0	SIZE CODE	NUMBER	REV.
REMOVE BURRS AND BREAK SHARP CORNERS	SCALE <i>H</i>	CCS	5410035-0-1	H
MATERIAL <i>H</i>	SHEET 2 OF 2	DIST.		
FINISH <i>H</i>				

REV. H  
NUMBER CCS5410035-0-1  
B

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**NOTES:**



REF	DESCRIPTION	PART NO.	ITEM NO.
	ETCH WIRE LIST	K-WL-1105-0-B	5
1	ETCHED CIRCUIT BOARD	5010328	4
REF	MODULE ECO HISTORY	B-MH-5410329-0-1	3
REF	ASSY/DRILLING HOLE LAYOUT	D-AH-5410329-0-1	2
REF	X-Y COORDINATE HOLE LOCATION	K-CO-5410329-0-1	1

FIRST USED ON OPTION MODEL		ETCH BOARD REV		PARTS LIST	
1105		C			
DRN	DATE	digital EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS			
CHK	DATE	TITLE			
ENGR	DATE	CIRCUIT SCHEMATIC			
PROJ. ENGR	DATE	(1105 10.5)			
PROJ. MGR	DATE	SIZE CODE NUMBER REV.			
NEXT HIGHER ASSY		D IA-7009241-0-0		D CS 5410329-0-1 B	
SCALE NONE		SHEET 1 OF 1		DIST.	

CHK	CHANGE NO.	REV

DEC NO.	EIA NO.	DEC NO.	EIA NO.

IC TYPE	GND	+5V

GND AND 5V ARE USUALLY PIN 7 AND 14 RESPECTIVELY. EXCEPTIONS ARE STATED ABOVE.

IC PIN LOCATIONS

