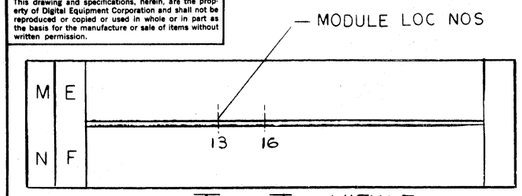
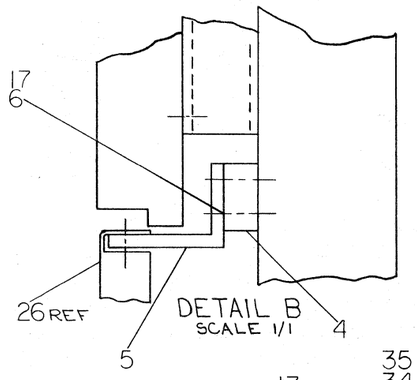
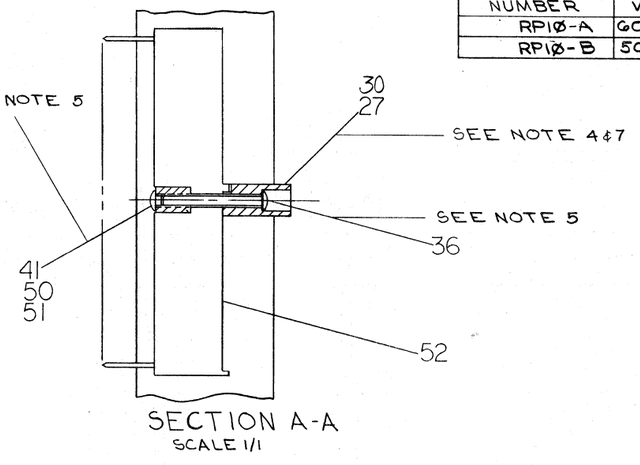
LEGEND	
NUMBER	VARIATION
RPIØ-A	60 HZ
RPIØ-B	50 HZ

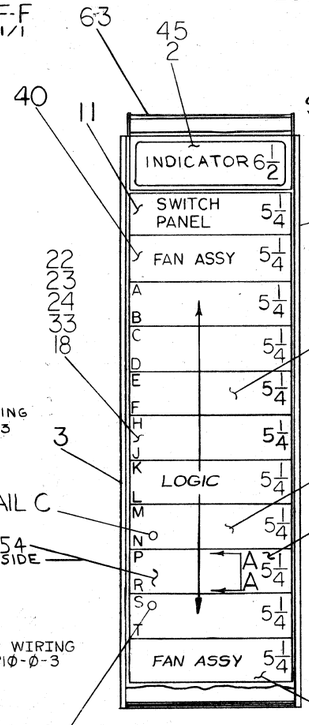
NOTES:

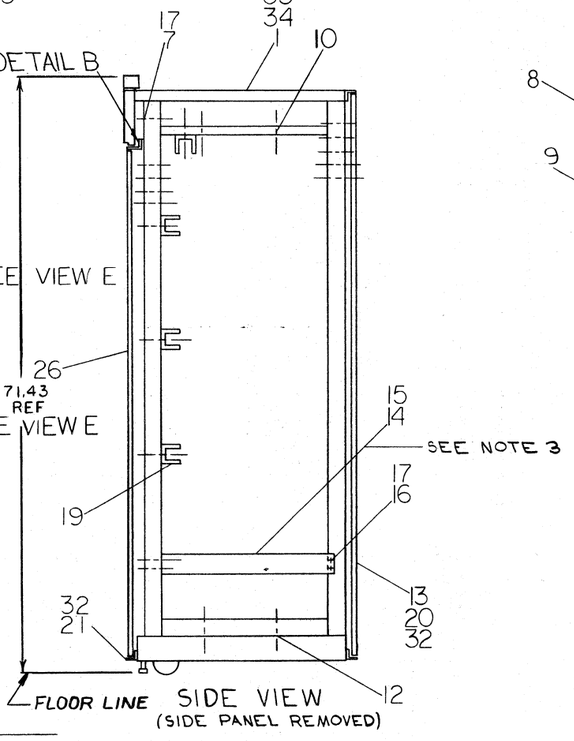
1. LENGTH OF PART # 15, 18, 29 & 48 TO BE DETERMINED BY SYSTEM.
2. REFER TO DWG INDEX LIST D-DI-RPIØ-Ø-1.
3. ITEMS #14 & 15 ARE ON LEFT HAND SIDE OF CABINET.
4. FOR LOCATIONS OF ITEM # 27 & 30 REFER TO MODULE UTILIZATION LIST SPECIFIED ON MASTER DWG LIST, USED AT ALL W851 & W852 PL'S
5. ITEM NO. 36 USED WITH ITEM NO. 27 ITEMS, NO. 41, 50, 51, USED WITH NO. 30.
6. PARTIAL VIEW, REAR OF MARGINAL CHECK PANEL P&R.
7. USE L.H. BLOCK #30 IN MODULE LOCATIONS 1 & 2, 9 & 10, 21 & 22. USE DOUBLE BLOCK #27 AT 13 THRU 20 ALL ABOVE BLOCKS ARE LOCATED IN LOGIC RACK P-R REFER TO D-MU-RPIO-0-PR
8. FOR MTG OF SKID & SHIPPING INFO REFER TO D-MD-7605120-0-0

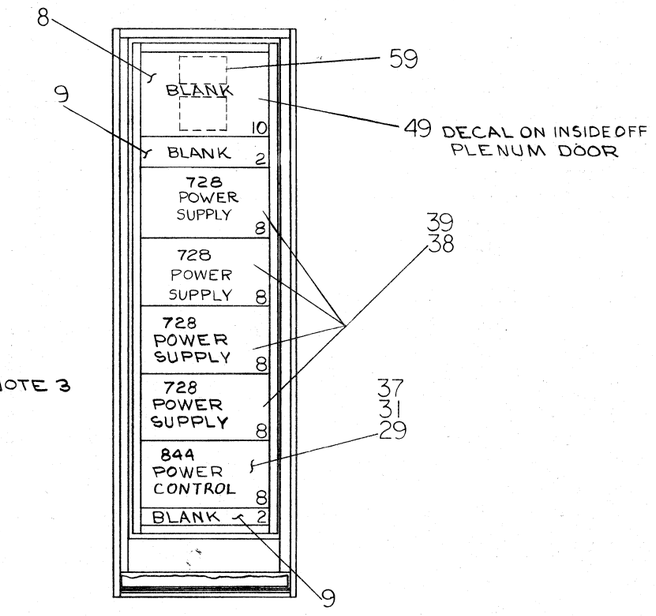












REVISIONS	REV	CHK	CHANGE NO.	
	A		RPIO-00011	J. GREEN 4/2/70
	B	FV	RPIO-00020	D. IVES 6/10/70
	C		RPIO-00024	T. Quilty 8-18-70
	D	FV	MISC-00079	IVES 10-12-70
	E		RPIO-00035	KENT 1/28/71
	F		RPIO-00038	WALSH 1/28/72
	G		RPIO-00038	WASH 3/24/72

FIRST USED ON OPTION / MODEL	QTY.	DESCRIPTION	PART NO.	ITEM NO.
RPIØ-A		PARTS LIST		

UNLESS OTHERWISE SPECIFIED

UNLESS OTHERWISE SPECIFIED
DIMENSION IN INCHES
TOLERANCES
DECIMALS ± .005 FRACTIONS ± 1/64 ANGLES ± 0°30'
FINAL SURFACE QUALITY / REMOVE BURRS AND BREAK SHARP CORNERS

MATERIAL

FINISH

DRN.	C Fontaine	DATE 2-3-69
CHK'D	R. Healy	DATE 3/11/69
ENG.	R. Mentz	DATE 10/28/69
PROJ. ENG.	John C Green Jr	DATE NOV 69
PROG.	G. M. Shea	DATE 10-10-69
NEXT HIGHER ASSY	A-ML-RPIØ-A	
SCALE	1/8	
SHEET	1 OF 1	

digital EQUIPMENT CORPORATION
MAYNARD, MASSACHUSETTS

TITLE
DISK FILE INTERFACE RPIØ

SIZE	CODE	NUMBER	REV.
D	UA	RPIØ-0-0	F

DIST. 67

DEC FORM NO. DRD 100

DIGITAL EQUIPMENT CORPORATION
MAYNARD, MASSACHUSETTS

PARTS LIST

MADE BY D. FONTAINE	CHECKED D. HEALY	SECTION
DATE 2/3/69	DATE 2/17/69	1
ENG D. Fontaine	PROD O. McLean	ISSUED SECT.
DATE 10/20/69	DATE 11-6-69	1

ITEM NO.	DWG NO. / PART NO.	DESCRIPTION	QUANTITY / VARIATION RP10-A (60HZ)	RP10-B (50HZ)
45	C-AD-7006195-0-0	CABLE SET RP10	1	1
46	9007870	SPACER, 3/8 A.F. X 1-1/4 LG #8 HOLE AL	4	4
47	9006045-1	SCR PHL HD PAN #8-32 X 1-1/2 SST	4	4
48	D-AD-7006463-0-0	I/O CABLE (LGTH SPEC BY ENG)	1	1
49	B-DC-7407694-0-0	DECALS-RP10	1	1
50	9006659	WASH FLAT #6	3	3
51	9007801	WASH SPLIT LOCK	3	3
52	A-PL-RP10-0-MC	MODULE COUNT	1	1
53	A-DC-7407413-0-0	DECAL (ANALOG)	1	1
54	D-AD-7006549-0-0	BAR, HOLD DOWN	1	1
55	9006634	WASH, INT TOOTH #8	4	4
56	1201168	SWITCH, #6AT1-T2 MICROSWITCH	2	2
57	B-MD-169-0-1-0-1	SWITCH MTG BKT #7405269	2	2
58	B-DC-7407694-1-0	DECAL - RP10	1	1
59	9007193	ARKLESS CONNECTOR #3000-541B	A/R	A/R
60	9107370-66	#14 AWG WIRE STRD TEF BLU	A/R	A/R
61	9107370-00	#14 AWG WIRE STRD TEF BLK	A/R	A/R
62	9007880	CABLE TIE SST-1.5M	A/R	A/R
63	D-AD-7008833-0-0	LOGO ASSY (19 1/4)	1	1

TITLE	ASSY NO.	SIZE	CODE	NUMBER	REV.	ECO NO.
DISK FILE INTERFACE (RP10)	D-UA-RP10-0-0	A	PL	RP10-0-0	F	
	SHEET 3 OF 3	DIST. 5				

DEC FORM NO.
DRA 110

MECHANICAL

FIND NO.	DESCRIPTION	PART NO.	PROD	CUST	F/C
1	DISK FILE INTERFACE 60 HZ	D-UA-RP1Ø-A-Ø			
	DISK FILE INTERFACE 50 HZ	D-UA-RP1Ø-B-Ø			
	DISK FILE INTERFACE (P.L.)	A-PL-RP1Ø-Ø-Ø			
	END PANEL ASSY	E-IA-7405092-2-0			
	SPACER	A-MD-7405960-0-0			
	BRACKET (DOOR PIVOT)	B-MD-7405961-0-0			
	PANEL, BLANK #7402037	B-5111			
	PANEL, BLANK #7402033	B-5111			
	MC/CABLE/REMOTE CONT	C-UA-BC1Ø8-Ø-Ø			
	CABLE ASSY BC10A	D-UA-BC1ØA-Ø-Ø			
	PLATE, RIGHT END	C-MD-5302486-0-0			
	BLOCK RETAINER	C-IA-7406047-0-0			
	BLOCK RETAINER LH	B-MD-7406258-0-0			
	CABLE TWIST LOCK 25FT	C-AD-7005427-0-0			
	CABLE ASSY W821-W921	C-IA-7405524-0-0			
	PANEL, BLANK #7402036	B-MD-5111			
	TRIM STRIP BOTTOM	D-MD-7405862-0-0			
	SCOTCHCALS	A-DC-7406255-0-0			
	CABLE SET	C-AD-7006195-0-0			
	I/O CABLE (LGTH SPEC BY ENG)	D AD-7006463-0-0			
	DECAL ANALOG	A-DC-7407413-0-0			
	DECAL (RP1Ø)	B-DC-7407694-0-0			
	BAR, HOLD DOWN	D-AD-7006549-0-0			
	SWITCH MTG BKT #7405269	B-MD-169-0-1-0-1			
	ENGINEERING SPECIFICATION	A-SP-RP1Ø-0-CDPD			
	ENGINEERING SPECIFICATION	A-SP-RP1Ø-0-SAS			
	ENGINEERING SPECIFICATION	A-SP-RP1Ø-0-DPD			
	ENGINEERING SPECIFICATION	A-SP-RP1Ø-0-MTP			
2	CABINET FRAME REWORK	E-IA-7406273-0-0			
3	CAB FRAME ASSY 19-3/4	E-AD-7403864-0-0			
	CAB FRAME ASSY 19-3/4 (PL)	A-PL-7403864-0-0			
	DWG INDEX LIST (CAB FRAME ASSY 19-3/4)	D-DI-7403864-0-1			
4	PANEL, INDICATOR ASSY	D-AD-7006193-0-0			
	PANEL, INDICATOR ASSY (PL)	A-PL-7006193-0-0			
	SUPPORT GLASS	D-IA-7406192-0-0			
	BEZEL INDICATOR (REWORK)	D-MD-7405729-2-0			
	PLATE STRIKER	A-MD-7405899-0-0			
	RETAINER FT PANEL	B-MD-7405931-0-0			
	BRKT, PANEL MTG	B-IA-7405929-0-0			
5	PANEL INDICATOR	D-IA-7407188-0-0			
	SILK SCREEN (GREY)	C-SS-7407188-0-2			
	SILK SCREEN (BLK)	C-SS-7407188-0-1			
6	LIGHT BOARD ASSY	C-IA-5404311-0-0			
	RECEPTACLE	B-MD-5503954-0-0			
7	ETCH BOARD	C-IA-5004310-0-0			
	FLIP CHIP MODULE BLANK	D-MD-1402230-0-0			
	ETCH PATTERN	PC-5404311			

MECHANICAL

FIND NO.	DESCRIPTION	PART NO.	PROD	CUST	F/C
8	PANEL SWITCH ASSY	D-AD-7006173-0-0			
	PANEL SWITCH ASSY (PL)	D-AD-7006173-0-0			
9	PANEL SWITCH	D-IA-7407147-0-0			
	SILK SCREEN (BLK)	C-SS-7407147-0-1			
10	TOGGLE SWITCH BRKT ASSY #1	D-AD-7005621-0-0			
	TOGGLE SW BRKT ASSY #1 (PL)	A-PL-7005621-0-0			
	TOGGLE SWITCH BRKT	C-MD-7406323-0-0			
11	TOGGLE SWITCH BRKT ASSY	D-AD-7006179-0-0			
	TOGGLE SW BRKT ASSY #5 (PL)	A-PL-7006179-0-0			
	TOGGLE SWITCH BRKT	C-MD-7406323-0-0			
12	TOGGLE SWITCH BRKT ASSY #6	D-AD-7006179-0-0			
	TOGGLE SW BRKT ASSY #6 (PL)	A-PL-7006179-0-0			
	TOGGLE & SWITCH BRKT	C-MD-7406323-0-0			
13	TOGGLE SWITCH BRKT ASSY #7	D-AD-7006180-0-0			
	TOGGLE SW BRKT ASSY #7 (PL)	A-PL-7006180-0-0			
	TOGGLE SW BRKT	C-MD-7406323-0-0			
14	DOOR, FULL ASSY	D-AD-7005358-4-0			
	DOOR, FULL ASSY (PL)	A-PL-7005358-0-0			
	FULL DOOR	D-IA-7405741-2-0			
	HINGE PIN, TOP	B-MD-20400-7			
	DOOR SPACER, BOTTOM	A-MD-7405312-0-0			
	HINGE PIN, BOTTOM	B-MD-20400-8			
15	POWER CONNECTOR BRKT ASSY	D-AD-7005467-0-0			
	POWER CONN BRKT ASSY (PL)	A -PL-7005467-0-0			
	BRACKET, POWER CONN	D-MD-7406024-0-0			
	SCOTCHCAL	A-SS-7406051-0-0			
16	WIRED ASSY	D-AD-7006194-0-0			
	WIRED ASSY (PL)	A-PL-7006194-0-0			
	PANEL SCOTCHCALS	A-DC-7406371-0-0			
17	1943D MTG PANEL	D-AD-1943-D-Ø			
	1943D MTG PANEL (PL)	A-PL-1943-D-Ø			
18	1943 CASTING PLUS PINS	D-AD-5302483-0-0			
	1943 CASTING PLUS PINS (PL)	A-PL-5302483-0-0			
	1943 FRAME CASTING	E-MD-1202885-0-0			

MECHANICAL

FIND NO	DESCRIPTION	PART NO.	PROD	CUST	F/C
19	MARGINAL CHECK PANEL ASSY	C-IA-5402526-0-0			
	PANEL MARGINAL CHECK	C-MD-5302484-0-0			
	SCOTCHCAL	C-SS-10801			
20	FAN HOUSING ASSY	E-AD-7005474-0-0			
	FAN HOUSING ASSY (PL)	A-PL-7005474-0-0			
	HOUSING FAN	D-MD-7406032-0-0			
	PANEL FRONT FAN	D-MD-7406030-0-0			
	COVER PROTECTION	B-MD-7404721-0-0			
	SCREEN FAN	C-MD-7404881-0-0			
21	FRONT DOOR ASSY	D-AD-7005361-4-0			
	FRONT DOOR ASSY (PL)	A-PL-7005361-0-0			
	FRONT DOOR	D-IA-7405877-2-0			
	HINGE PIN, TOP	B-MD-7405865-0-0			
	DOOR SPACER, BOTTOM	A-MD-7405312-0-0			
	HINGE PIN, BOTTOM	B-MD-20400-8			
22	944 POWER CONTROL	D-UA-944-0-0			
	944 POWER CONTROL	A-PL-944-0-0			
23	728 POWER SUPPLY 60 HZ	D-UA-728-0-1			
	728 POWER SUPPLY 60 HZ (PL)	A-PL-728-0-1			
	728A POWER SUPPLY 50 HZ	D-UA-728A-0-1			
	728A POWER SUPPLY 50 HZ (PL)	A-PL-728A-0-1			
24	LOGO ASSEMBLY (19 1/4)	D-AD-7008833-0-0			
	FRAME LOGO	D-MD-7409593-2-0			
	PLATE END LOGO L.H.	C-MD-7409105-1-0			
	PLATE END LOGO R.H.	C-MD-7409105-2-0			
	LOGO INLAY, BLANK	D-IA-7409224-4-0			

ELECTRICAL

FIND NO	DESCRIPTION	PART NO.	PROD	CUST	F/C
1	DISK FILE INTERFACE 60 HZ	A-ML-RP1Ø-A			
	DISK FILE INTERFACE 50 HZ	A-ML-RP1Ø-B			
	WIRED ASSY	D-AD-7006194-0-0			
	WIRED ASSY (PL)	A-PL-7006194-0-0			
	CABLE SET	C-AD-7006195-0-0			
	WIRE LIST	K-WL-RP1Ø-Ø-2			
	POWER WIRING	D-IC-RP1Ø-Ø-3			
	C E PACK TRACKS	D-SP-RP1Ø-0-CEPT			
	ASSY REGISTER	D-BS-RP1Ø-Ø-AR			
	ASSY REGISTER DATA GATE	D-BS-RP1Ø-Ø-ARD			
	CHANNEL CONTROL	D-BS-RP1Ø-Ø-CC			
	CONDITION REGISTER	D-BS-RP1Ø-Ø-CXR			
	DATA ADDRESS REGISTER	D-BS-RP1Ø-Ø-uAR			
	DISK SIGNAL BUS CONNECTORS	D-BS-RP1Ø-Ø-DSBC			
	DATA TRANSFER CONTROL	D-BS-RP1Ø-Ø- DTC			
	HEADER COMPARE	D-BS-RP1Ø-Ø-HCDE			
	I/O BUS CONTROL	D-IC-RP1Ø-Ø-IBC			
	INTER CONNECTING CABLES	D-IC-RP1Ø-Ø-IC			
	INDICATORS	D-BS-RP1Ø-Ø-INDC			
	IOB TRANSMITTERS	D-BS-RP1Ø-Ø-IOB			
	IOB DATA RECEIVERS	D-BS-RP1Ø-Ø-IOBD			
	LONGITUDINAL PARITY REG.	D-BS-RP1Ø-Ø-LPR			
	MULTIPLEXOR	D-BS-RP1Ø-Ø-MPX			
	SHIFT REGISTER	D-BS-RP1Ø-Ø-SR			
	SWITCH PANEL	D-BS-RP1Ø-Ø-SWP			
	READ DAA SEPERATOR	D-BS-RP1Ø-Ø-RDS			
	WORD COUNTERS	D-BS-RP1Ø-Ø-WDC			
	SECTOR COUNTER	D-BS-RP1Ø-Ø-SC			
	SECTOR COUNTER BUFFER	D-BS-RP1Ø-Ø-SCB			
	SECTOR COUNTER CONTROL PULSES	D-BS-RP1Ø-Ø-SCC			
	SECTOR COUNTER MULTIPLEXOR	D-BS-RP1Ø-Ø-SCM			
	PULSE AND LEVEL TERMINATIONS	D-CL-RP1Ø-Ø-TERM			
	MODULE UTILIZATION A-D	D-MU-RP1Ø-Ø-AD			
	MODULE UTILIZATION E-J	D-MU-RP1Ø-Ø-EJ			
	MODULE UTILIZATION K-N	D-MU-RP1Ø-Ø-KN			
	MODULE UTILIZATION P-T	D-MU-RP1Ø-Ø-PT			
	MODULE COUNT	A-PL-RP1Ø-Ø-MC			
	FLOW CHART CHANNEL CONTROL	D-FD-RP1Ø-Ø-FCCC			
	FLOW CHART CHANNEL INTERFACE	D-FD-RP1Ø-Ø-FCCI			
	FLOW CHART DISK CONTROL	D-FD-RP1Ø-Ø-FCDI			
	FLOW CHART END	D-FD-RP1Ø-Ø-FCE			
	FLOW CHART LOCAL	D-FD-RP1Ø-Ø-FCL			
	DATA TRANSFER CONTROL MICRO 1	D-FD-RP1Ø-Ø-FCM1			
	DATA TRANSFER CONTROL MICRO 2	D-FD-RP1Ø-Ø-FCM2			
	FLOW CHART READ/WRITE	D-FD-RP1Ø-Ø-FCRW			
	FLOW CHART START	D-FD-RP1Ø-Ø-FCS			
	FLOW CHART SEARCH	D-FD-RP1Ø-Ø-FCSC			
	FLOW CHART WR HEADERS & DATA	D-FD-RP1Ø-Ø-FCWH			
6.	LIGHT BOARD ASSY	C-IA-5404311-0-0			
	CIRCUIT SCHEMATIC	B-CS-4304311-0-1			
10.	TOGGLE SW BKT ASSY	D-AD-7005621-0-0			
12	TOGGLE SWITCH BRKT ASSY #6	D-AD-7006179-0-0			
13	TOGGLE SWITCH BRKT ASSY #7	D-AD-7006180-0-0			
15	POWER CONNECTOR BRKT ASSY	D-AD-7005467-0-0			
	MARGINAL CHK PANEL	C-IA-5402526-0-0			
19	CIRCUIT SCHEMATIC	B-CS-1943-0-1			
20	FAN HOUSING ASSY	E-AD-7005474-0-0			
22	CIRCUIT SCHEMATIC 944	B-CS-944-0-1			
23	CIRCUIT SCHEMATIC 728	B-CS-728-0-1			
	CIRCUIT SCHEMATIC 728A	B-CS-728A-0-1			
24	FAN HOUSING ASSY	E-MA-100295-1			

QTY.	DESCRIPTION	PART NO.	ITEM NO.

PARTS LIST

REVISIONS	REV.	CHANGE NO.	CHK

UNLESS OTHERWISE SPECIFIED

UNLESS OTHERWISE SPECIFIED
DIMENSION IN INCHES
TOLERANCES
DECIMALS ± .005 | FRACTIONS ± 1/64 | ANGLES ± 0°30'
FINAL SURFACE QUALITY ✓
REMOVE BURRS AND BREAK SHARP CORNERS

MATERIAL

FINISH

		DATE
DRN.		11/29/67
CHK'D		10/26/67
ENG.		10/26/67
PROJ. ENG.		11/20/69
PROD.		11-6-67

FIRST USED ON

SCALE NONE

SHEET 2 OF 2

digital EQUIPMENT CORPORATION
MAYNARD, MASSACHUSETTS

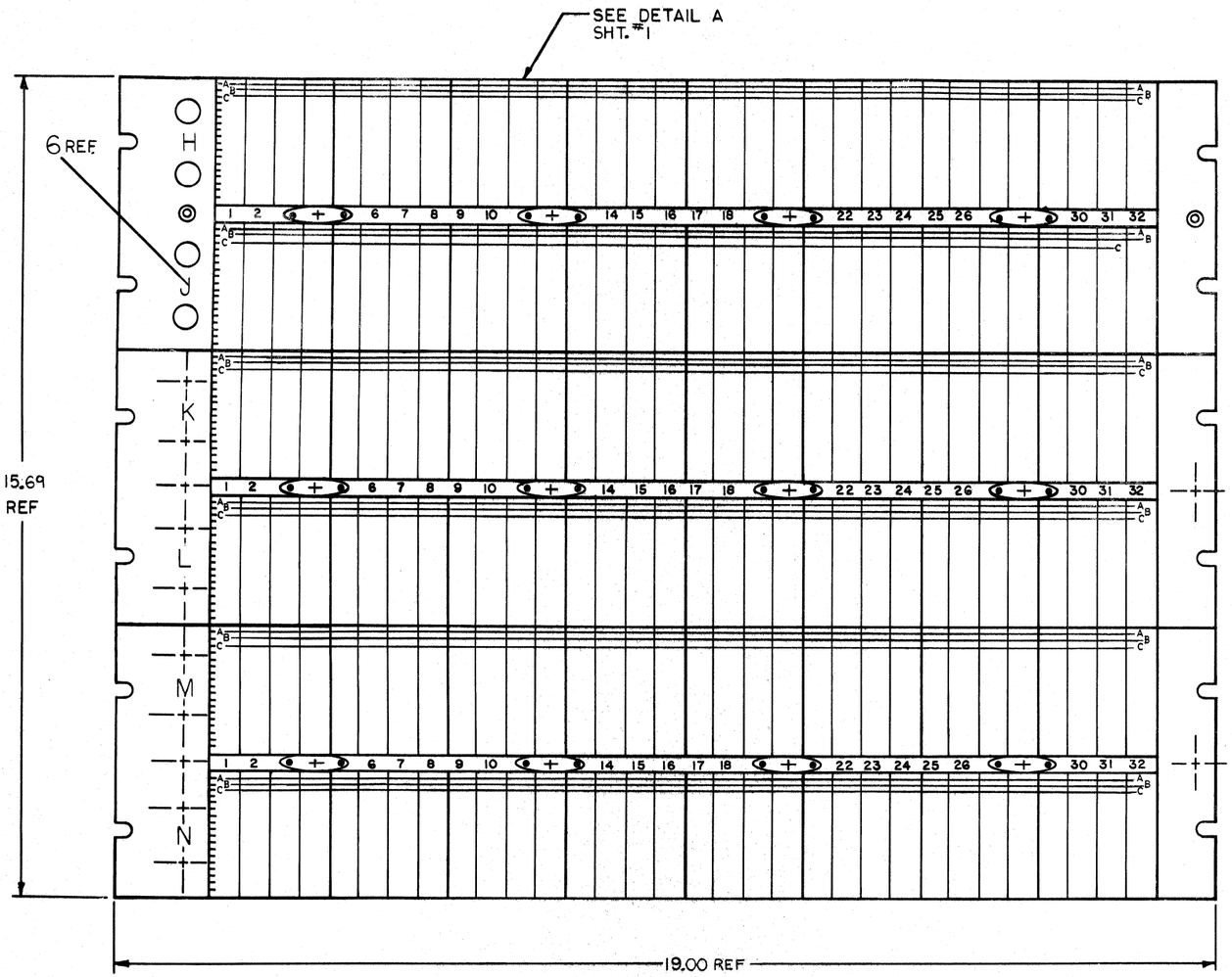
TITLE
DRAWING INDEX
LIST RPIØ

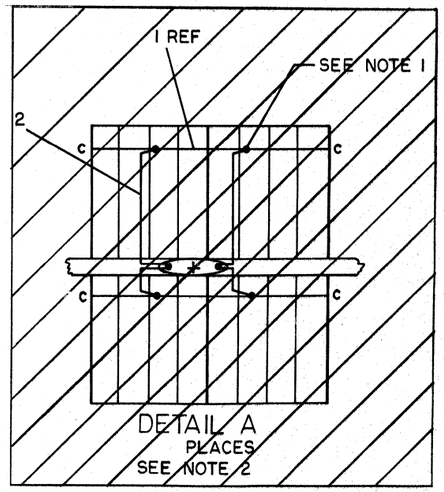
SIZE	CODE	NUMBER	REV.
D	DI	RPIØ-Ø-1	C

DIST

DEC FORM NO. DRD 100

NOTES:
1. CONNECTIONS ON ITEMS #1 & #2 TO BE SOLDERED AND LOCATED AT MINIMUM PRACTICAL HEIGHT ABOVE BOARD.
2. ALL CONN BLOCKS TO BE GROUNDED TO GND LUGS AS SHOWN.
3. USE YELLOW WIRE (ITEM #3) FOR MACHINE WRAPPED & BLUE WIRE (ITEM #4) FOR HAND WRAPPED WIRING.





REVISIONS	REV.	CHANGE NO.	CHK.

DEC FORM NO. DNS 107

QTY.	DESCRIPTION	PART NO.	ITEM NO.
PARTS LIST			

UNLESS OTHERWISE SPECIFIED
UNLESS OTHERWISE SPECIFIED
DIMENSION IN INCHES
TOLERANCES
~~DECIMALS~~ ~~FRACTIONS~~ ANGLES
~~.xx ±.005~~ ~~x/xx ±1/64~~ ± 0°30'
FINAL SURFACE QUALITY √
REMOVE BURRS AND BREAK SHARP CORNERS

MATERIAL

FINISH

DRN. D. Fontaine	DATE	1-30-69
CHK'D W. Hoover	DATE	10/29/69
ENG. M. Clark	DATE	10/30/69
PROJ. ENG. John C. Green Jr.	DATE	4 NOV 69
PROD.	DATE	11-6-69

FIRST USED ON

SCALE NONE
SHEET 2 3

digital EQUIPMENT CORPORATION
MAYNARD, MASSACHUSETTS

TITLE
WIRED ASSY
(RPIØ)

SIZE	CODE	NUMBER	REV.
D	AD	7006194-0-0	E

DIST.

DIGITAL EQUIPMENT CORPORATION
MAYNARD, MASSACHUSETTS

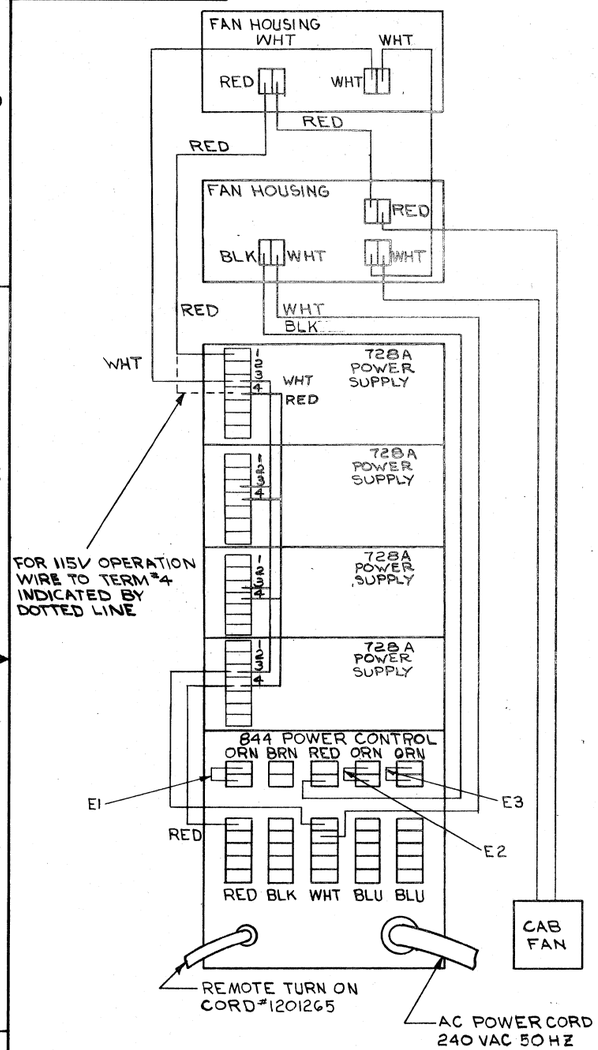
PARTS LIST

MADE BY D. FONTAINE	CHECKED D. HEALY	SECTION
DATE 1/31/69	DATE 2/17/69	1
ENG D. Murphy	PROD D. McGhee	ISSUED SECT.
DATE 10/3/69	DATE 11-6-69	1

ITEM NO.	DWG NO./PART NO.	DESCRIPTION	QUANTITY/VARIATION 7006194-0
1	1202188	CHAIN VOLTAGE	A/R
2	9107560-1	#22 WIRE BUS	A/R
3	9107470-5	#24 AWG SOLID KYNAR. (YEL)	A/R
4	9107470-10	#24 AWG SOLID KYNAR. (BLU)	A/R
5	D-AD-1943-D-0	1943 MTG. PANEL ASSY	8
6	A-DC-7406371-0-0	PANEL SCOTCHCALS	A/R
7	9107265	#22 TUBING WHT	A/R
8	9107450-55	#24 AWG TEFLON INS STRD GRN	A/R
9	7405961	CAPACITOR .02 MFD TERMI POINTS	12
10	7406364	CAPACITOR .002 MFD TERMI POINTS	2
11	7405969	RESISTOR 1K, ¼W, 5% CC W/TERMI PTS	2
12	7404647	RESISTOR 3K, ¼W, 5% CC W/TERMI PTS	6
13	7406632	RESISTOR 6.8K, ¼W, 5% CC W/TERMI PTS	2
14	7406917	RESISTOR 3.3K, ¼W, 5% CC W/Termi Pts	1
15	7405973	RESISTOR 15K, ¼W, 5% CC W/TERMI PTS	1
16	7404648	RESISTOR 10K, ¼W, 5% CC W/TERMI PTS	1
17	7405974	RESISTOR 20K, ¼W, 5% CC W/TERMI PTS	8
18	7404644	RESISTOR 100 OHMS, ¼W, 5% CC W/TERMI PTS	3
19	9107497-09	#24 AWG SOLID TEF WHT/BLK TWP	A/R
20	7407633	DIODE 1N4001 W/TERMI PTS	2
REF	K-WL-RP10-0-2	WIRELIST	

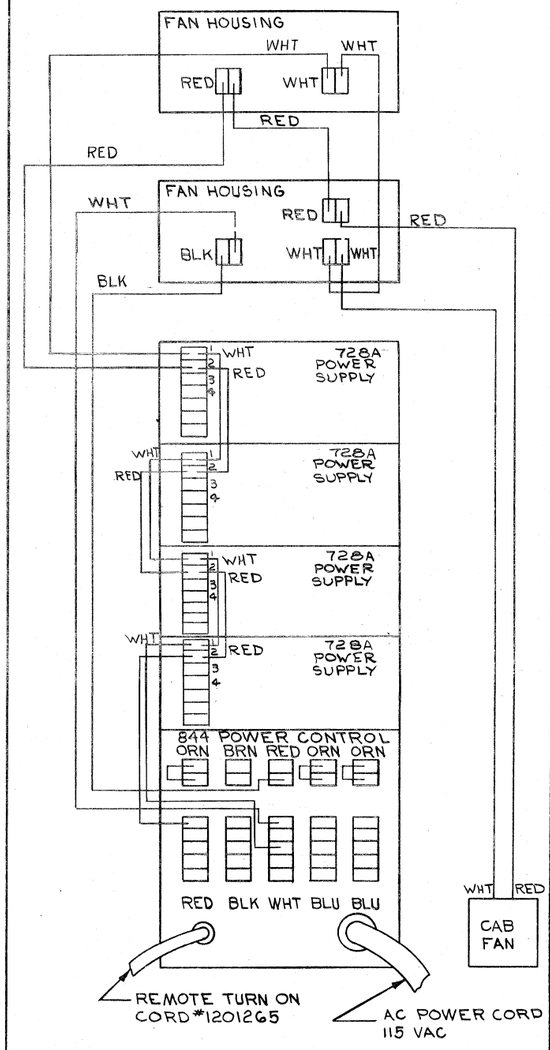
TITLE	ASSY NO.	SIZE	CODE	NUMBER	REV.	ECO NO.
WIRED ASS'Y RP10	D-AD-7006194-0-0	A	PL	7006194-0-0	E	RP10-00033
	SHEET 1 OF 1	DIST.	G			

DEC FORM NO.
DRA 110

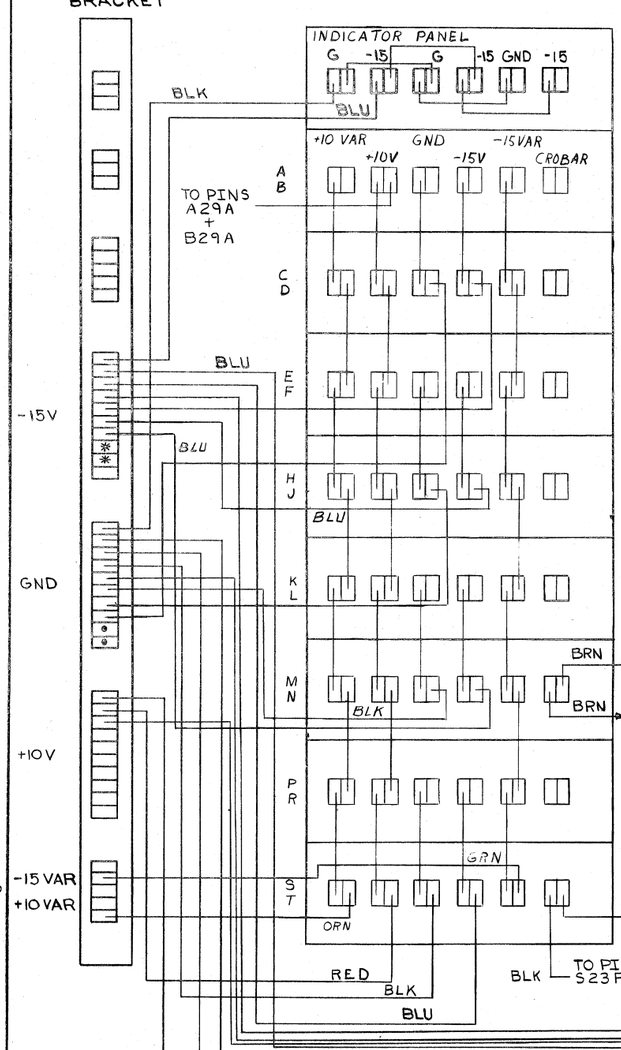


NOTE:
REMOVE JUMPERS E1, E2, & E3 FROM 844 PC FOR 230V 50 HZ OPERATION.

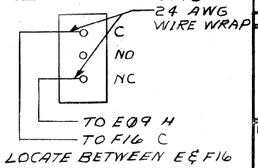
115 OR 230 50 HZ AC WIRING

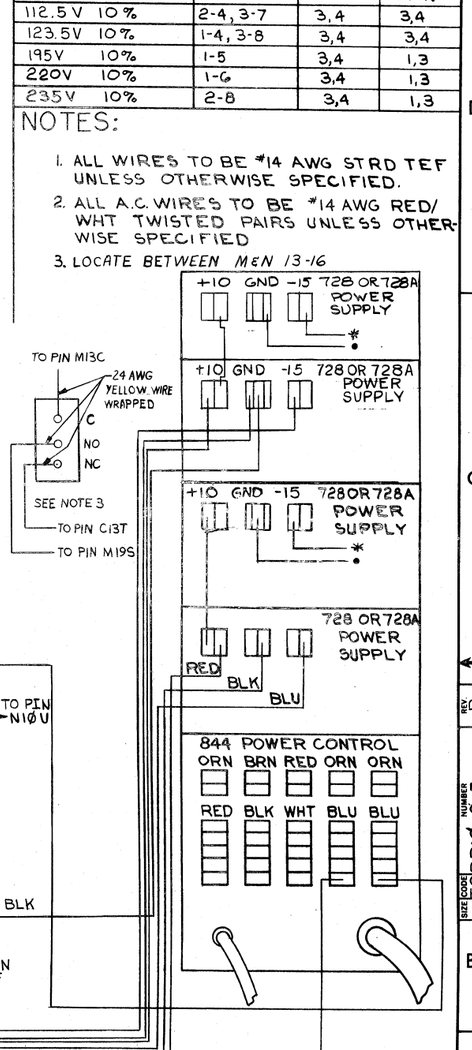


115 60 HZ AC WIRING



50 HZ OR 60 HZ DC WIRING





728A JUMPERING

INPUT VOLTAGE	JUMPER	LINE	FAN
112.5 V 10%	2-4, 3-7	3,4	3,4
123.5V 10%	1-4, 3-8	3,4	3,4
195V 10%	1-5	3,4	1,3
220V 10%	1-6	3,4	1,3
235V 10%	2-8	3,4	1,3

NOTES:

1. ALL WIRES TO BE #14 AWG STRD TEF UNLESS OTHERWISE SPECIFIED.
2. ALL A.C. WIRES TO BE #14 AWG RED/WHT TWISTED PAIRS UNLESS OTHERWISE SPECIFIED
3. LOCATE BETWEEN M&N 13-16

REVISIONS	REV	CHANGE NO.	CHK	
	A	RP10-00011	J. GREEN	4/2/70
	B	RP10-00035	WALSH	1/29/72

FIRST USED ON OPTION/MODEL	QTY.	DESCRIPTION	PART NO.	ITEM NO.
RP10-A		PARTS LIST		

UNLESS OTHERWISE SPECIFIED	DRN.	DATE 2-3-69
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES TOLERANCES DECIMALS .005 FRACTIONS ± 1/64 ANGLES ± 0°30' FINAL SURFACE QUALITY / REMOVE BURRS AND BREAK SHARP CORNERS	CHKD.	DATE 2-25-69
	ENG.	DATE
	PROJ. ENG.	DATE 4 NOV 69
	PROD.	DATE 11-6-69
MATERIAL	NEXT HIGHER ASSY	A-ML-RP10-A
FINISH	SCALE	NONE
	SHEET	1 OF 1

digital EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS

TITLE: WIRING POWER DC & AC

SIZE	CODE	NUMBER	REV
D	IC	RP10-0-3	B

DIST.

DEC FORM NO. DRD 100

DRWG NO	REVLTR
K-WL-RPIØ-0-2	V

REV LTR	ECO NO	DATE	ENG
A	RPIØ-00007	FEB 70	BR
B	RPIØ-00008	2-12-70	DJ
C	RPIØ-00009	2/18-70	DJ
D	RPIØ-00012	4-1-70	DJ
E	RPIØ-00013	4-20-70	DJ
F	RPIØ-00016	5-11-70	DJ
H	RPIØ-00017	5-27-70	DJ
J	RPIØ-00021	6-9-70	DJ
K	RPIØ-00022	6-22-70	DJ
L	RPIØ-00027	12-70	DJ
M	RPIØ-00028	2-18-71	DJ
N	RPIØ-00029	3-10-71	DJ
P	RPIØ-00030	5-4-71	D.J.
R	RPIØ-00031	6-3-71	D.W.
S	RPIØ-00032	6-14-71	B.W.
T	RPIØ-00033	11-2-71	B.W.
U	RPIØ-00035	12-22-71	B.W.
V	RPIØ-ØØØ39	4-14-72	B.W.

REVISIONS

FIRST USED ON OPT/MODEL	DRAWN	DATE
RPIØ-A	J. Ferguson	2-7-69
	CHECKED	DATE
	[illegible]	12/9/69
	ENG	DATE
	John C Brown Jr	8JAN70
	PROJ ENG	DATE
	John C Brown Jr	8JAN70
	PROD	DATE
	A. McLean	1-9-70

digital
EQUIPMENT
CORPORATION
MAYNARD, MASSACHUSETTS

TITLE: WIRE LIST (RPIØ)

FOR TAPE* FILE*

ASSY NO NEXT HIGHER ASSY: A-ML-RPIØ-A

SIZE	CODE	DWG. NO.	REV LTR
K	WL	RPIØ-Ø-2	V

SCALE NONE | SHEET 1 OF 1 | DIST.

	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44
	W012	B311	B172	B133	B165	S203	B152	W010	B165	G700	R201	RQ13	P201	R201	B133	R201	R201	W010	S602	B165	S202	B134	S203	S602	R201	S203	S203	G704	R201	B134	B133	B172												
USAGE	CABLE																																											
E	DTC ATTN Ø-7 CXR DISK WD PE SEC PE CXR CHAN CONT PE CXR CH DATA PE	DTC DATA STROBE	CXR PI ENABLE	DTC OP CODE 2 DTC SHIFT CT Ø PLS HCDE CYL DTC SHIFT CT Ø PLS DTC CLR AR	IBC LOCAL START DTC LD BUF FM SR DLY DTC DATA STROBE DTC SHIFT CT Ø PLS IBC LOCAL START	DTC OP CODE Ø DTC OP CODE 1 DTC OP CODE 2	DTC COM DE-CODER	D THRU V	DTC WRITE GAP DTC READ ALL DTC SEARCH DTC SET READ DTC SET WRITE	D THRU H M THRU V	LPR 33	DTC LOAD LPR CXR DISK WD PE DTC WRITE DATA	LPR 30	LPR 31	HCDE CYL COMP DAR CYL 256 DTC RSAD DATA	LPR 32	LPR 34	D THRU T U	DTC BUS LINE STROBE	HCDE DRIVE DES ER DTC BUS LINE STROBE CXR SEC PAR ER HCDE DRIVE DES ER WDC BYTE SHIFT =Ø	HCDE SEC DES ER HCDE SURF DES ER	HCDE SEC DES ER HCDE DRIVE DES ER	CXR PI Ø CXR PI 1 CXR PI 2	DTC CLEAR SHIFCT	CXR ILL WRITE	CXR DIS DS PE STOP CXR DIS CD PE STOP CXR DIS DW PE STOP	DAR DRIVE EN DTC READ ALL CXR WRITE EVEN PAR	D THRU N P THRU U	DTC WRITE DATA	DTC READ IMAGE INDEX DTC LOAD LPR DTC CLR HDR	DTC FINISH DTC OVER RUN SET DTC INDEX HCDE DES ER	DTC RP01												
	W012	W012	B134	R302	R201	B165	B135	B134	RG13	B163	R302	B214	B214	B165	B133	RG13	R220	B135	E311	W990	B137	B152	S202	S202	S202	S202	S202	RG13	B163	R302	R302	R302												
USAGE	CABLE	CABLE																																										
F	DTC OP CODE Ø, 1, 2 READ IMAGE, LPR, DATA, REFUSE GAP HDR WRITE HDR, GAP, DATA	DTC WRITE LPR WDC DATA WD CT ØØ-Ø6 WDC BYTE SHIFCT ØØ-Ø2 WDC BIT CT ØØ	DTC OVER RUN DTC CLR AR A & B DTC SHIFT AR A, B & C IBC LOAD ICWA	DTC LD BUF FM SR DLY DTC OVER RUN	DTC SUPR BUF LD	GEN CLR DTC SET ERASE IOBD 32 IOBD 33	DTC PARITY DTC PARITY DTC PAR CONT	DTC PAR CONT CC WRITE 8 DTC READ ALL DTC SET ERASE	DTC LOAD LPR DTC LD BUF FM SR DLY DTC SHIFT LPR C	SR 5 SR 4 SR 3 SR 2 SR 1 SR Ø	DTC CLR ERASE BUF IBC CLEAR ATTN	SR BUF 2 SR BUF 3 SR BUF 4 SR BUF 5	SR BUF Ø SR BUF 1	DSBC UNIT E Ø CLV CXR DIST NOT RDY DTC RESTORE WDC BYTE CT + 1 BUF SWP OP ØØ	DTC LD BUF FM AR DTC WRITE DATA DTC READ HEADER DTC WRITE GAP	DTC SET READ DATA DTC READ HEADER DTC SHIFT LPR B	WDC BYTE SHIFCT Ø WDC BYTE SHIFCT 1 WDC BYTE SHIFCT 2	WDC BYTE SHIFCT =Ø DTC WRITE DATA CC PREV START	DTC CLEAR AR A & B	DAR SEL DRIVE 1 THRU 7	HCDE DRIVE DES ERROR CC PREV START	CXR IOB PI 1 THRU 7	CXR DISK WD PE CXR DISK SEC PE	CXR NON EX MEM CXR CHAN DATA PE	CXR CHAN CONT PE CXR CW XFER COMP	CXR ILL COM WL BUSY CXR OVER RUN	CXR SEARCH ER 1 CXR SEARCH ER 2	DTC READ IMAGE INDEX DTC SEARCH IBC SET DAR ETC A	DSBC 6 DSBC 7 DSBC 6 DSBC 7 DSBC 2 DSBC 3	DTC TAG LINE STROBE DTC BUS LINE STROBE	DTC WRITE GAP DTC SECTOR DLY	DTC READ GAP DTC READ GAP												
	W012	G796	R201	BG11	BG11	BG11	B133	B163	B163	B163	B163	B163	B163	W010	B163	B163	S202	S202	S202	S202	B134	B135	B135	S602	R302	B311	R302	B165	P302	R302	R302	R302												
USAGE	CABLE	CABLE																																										
H	SR Ø THRU SR 5 SR BUF Ø THRU SR BUF 5	SWP OP Ø, 1, 2 SWP LOCAL SET SWP LOCAL CLR SWP CLEAR IBC STOP SWP START	IBC GEN CLR	DTC DATA STROBE DTC CLR AR DTC READ CLOCK DLY IBC GEN CLR	DTC DATA CLOCK DLY IBC GEN CLR DTC LD SR FM LPR DTC PAR CONT	DTC LD BUF FM AR HCDE HDR RD ER DTC LD BUF FM SR HDR RD ER	DTC PARITY CC INH DTC PARITY WDC WD CT +1A	D THRU U	SR BUF 2 SR BUF 2 SR BUF 2 SR BUF 3 SR BUF 4 SR BUF 4	SR BUF Ø SR BUF Ø SR BUF 1 SR BUF 1 SR BUF 5 SR BUF 5	SR BUF Ø SR BUF 2 SR BUF 3 SR BUF 4 SR BUF 5 SR BUF 5	-ARD 3Ø ARD 3Ø -ARD 31 ARD 31 -ARD 32 ARD 32	-ARD 3Ø ARD 3Ø -ARD 31 ARD 31 -ARD 32 ARD 32	-ARD 33 ARD 33 -ARD 34 ARD 34 -ARD 35 ARD 35	-ARD 33 ARD 33 -ARD 34 ARD 34 -ARD 35 ARD 35	DTC ERASE BUF DTC WRITE LPR	DTC WRITE GAP DTC WRITE HEADER	DTC READ DATA DTC READ LPR	DTC READ GAP DTC READ HEADER	DTC DATA CLOCK DLY DTC READ GAP DTC READ DTC SHIFT LPR	DSBC Ø DSBC 1 DSBC 4 DSBC 5	DSBC 4 DSBC 1 DSBC 5 DSBC Ø	IBC INITIAL CLEAR C IBC INITIAL CLEAR B	DTC SET DAR ECT DLY DTC SRCH COMP STRB	DTC SHIFT LPR	CXR DISK SEC PE DTC BUS LINE STROBE	SCC Ø CHANGE COUNT SCC 1 CHANGE COUNT SCC 2 CHANGE COUNT SCC 3 CHANGE COUNT SCC 4 CHANGE COUNT	SCC SEC PLS Ø DLY SCC SEC PLS 1 DLY	SCC SEC PLS 2 DLY SCC SEC PLS 3 DLY	SCC SEC PLS 4 DLY SCC SEC PLS 5 DLY	SCC SEC PLS 6 DLY SCC SEC PLS 7 DLY												
	W012	B165	BG85	B311	B163	B611	B311	B135	R302	B165	B165	S202	B163	B163	B163	B134	R201	R201	B165	B135	B133	B165	B141	B611	W010	B611	B133	R613	B152	B165	B134	W990												
USAGE	CABLE																																											
J	IBC BUSY, DONE CXR ILL WR, ILL COM, CW XF COMP, WE EVEN CXR OVER RUN CXR TRSK NOT RDY, NON EX MEM OR PE Ø, 1, 2	IOBD 27 THRU 31	DTC DATA RCVR EN A, B, C IBC LOAD TEST A, B	CC SHIFT CT Ø PLS DLY	DTC PARITY DTC PARITY DTC SHIFT BIT CNT DTC OP CODE Ø1 DTC OP CODE ØØ	DTC PARITY SR 5 DTC SHIFT BIT CNT IBC GEN CLR	CC RESET	CC INHIBIT DTC LD BUF FM AR CC TERM	DTC SEARCH SYNC CC PREVENT TEST	DTC ATTN Ø DTC ATTN 1 DTC ATTN 2 DTC ATTN 3 DTC ATTN 4	DTC ATTN 5 DTC ATTN 6 DTC ATTN 7 CC SHIFCT Ø PLS DTC SEARCH SYNC	DTC READ IMAGE DTC READ REFUSE	SR 5 SR 4 SR 3 SR 2 SR 1 SR Ø	SR 5 SR 4 SR 3 SR 2 SR 1 SR Ø	SR 5 SR 4 SR 3 SR 2 SR 1 SR Ø	DTC WRITE DATA WDC WD CT +1 DTC RD IM	1 IBC BUSY	IBC DONE	IBC SET DAR ETC DLY WDC BIT SHFCT =Ø CXR PI ENABLE IC CHAN BUSY IN CC PREVENT START	DTC LD SR FM AR CXR DISK NOT RDY CC ILLEGAL WRITE	DTC CLK ∧ PAR CONT CXR ILL COM WDC BIT SHFCT =Ø DSBC 3	DSBC 2 DSBC 3 DSBC 6 DSBC 7 DAR DRIVE EN	DTC READ IMAGE INDEX	DTC SET READ DATA DTC LD SR FM AR DTC SR JAM DTC WRITE CLOCK DTC SR JAM	D THRU S U BV	DTC CLOCK NO PAR CONT DTC READ IMAGE INDEX DTC LD SR FM AR DTC WR HDR V DATA	DTC READ HEADER DTC ST RD IMAGE TIME OUT DTC READ DATA CXR SEARCH ER	IBC CON SET IBC CON CLR IBC SET DAR ETC B	DAR SEL DRIVE Ø THRU 7	DSBC SEL UNIT SEL PLS SCC 5 CHANGE COUNT SCC 6 CHANGE COUNT SCC 7 CHANGE COUNT DTC LD SR FM AR	DTC RP01 EN DTC RP02 EN DTC READ GAP CLR DTC PAR CONT	DAR SEL DRIVE Ø THRU 7												

FOR INFORMATION ONLY
DO NOT BUILD TO THIS DRAWING

REVISIONS	CHK	CHANGE NO.	
		RPIØ-00008	A
		GREEN	B
	FV	RPIØ-00013	B
		J. GREEN	C
		RPIØ-00017	C
		IVES	D
		RPIØ-00028	D
		IVES	E
		RPIØ-00033	E
		B. WALSH	F
		RPIØ-00036	F
		B. WALSH	

FIRST USED ON OPTION / MODEL: PDPIØ

UNLESS OTHERWISE SPECIFIED
UNLESS OTHERWISE SPECIFIED
DIMENSION IN INCHES
TOLERANCES
DECIMALS ± .005 FRACTIONS ± 1/64 ANGLES ± 0°30'
FINAL SURFACE QUALITY
REMOVE BURRS AND BREAK SHARP CORNERS

MATERIAL

FINISH

		DATE
DRN.	W. Stephenson	3/2/69
CHK'D		11/10/69
ENG.		DATE
PROJ. ENG.		DATE 11/10/69
PROD.		DATE 11-18-69

NEXT HIGHER ASSY: A-ML-RPIØ-Ø

SCALE

SHEET 1 OF 1

digital EQUIPMENT CORPORATION
MAYNARD, MASSACHUSETTS

TITLE
MODULE UTILIZATION E-J

SIZE	CODE	NUMBER	REV
D	DMU	RPIØ-Ø-EJ	F

DIST.

DEC FORM NO. DRD 105

MODULE UTILIZATION P-T

Row P

Slot	Module	Usage	Signals
1	W85	CABLE	AR CHND ØØ TO 17
2	W851	CABLE	AR CHND 18 TO 35
3	W1Ø2		AR ØØ; CHND ØØ; AR Ø2; CHND Ø2; AR Ø4; CHND Ø4; AR Ø6; CHND Ø6
4	W1Ø2		AR Ø1; CHND Ø1; AR Ø3; CHND Ø3; AR Ø5; CHND Ø5; AR Ø7; CHND Ø7
5	W1Ø2		AR 18; CHND 18; AR 2Ø; CHND 2Ø; AR 22; CHND 22; AR 24; CHND 24
6	W1Ø2		AR 19; CHND 19; AR 21; CHND 21; AR 23; CHND 23; AR 25; CHND 25
7	B611		CC DATA STROBE A; CC DATA STROBE; CC DATA STROBE; CC DATA STROBE E
8	G7ØØ		D THRU P
9	W851	CABLE	AR CHND ØØ TO 17
10	W851	CABLE	AR CHND 18 TO 35
11	WØ21	CABLE	IC CHAN START IN; IC SA WR IN; CC DEVICE PULSE; CC WR CON WD REQ; CC CHAN RESET; CC WRITE EVEN PAR
12	WØ21	CABLE	IC CHAN PULSE; IC CHAN BUSY IN; IC DATA PAR ERROR; IC CON PAR ERROR; IC WR CON WD COMP; IC NO SUCH MEM
13	W851	CABLE	IOB ØØ THRU IOB 17
14	W851	CABLE	IOB 18 THRU IOB 35
15	W851	CABLE	IC IOB RESET; IC IOS Ø3 THRU IC IOS Ø9
16	W851	CABLE	IC IOB - DATAO CLR; DATAO SET; CONO CLR; CONO SET; DATAI; CONI
17	W851	CABLE	IOB ØØ THRU IOB 17
18	W851	CABLE	IOB 18 THRU IOB 35
19	W851	CABLE	IC IOB RESET; IC IOS Ø3 THRU IC IOS Ø9
20	W851	CABLE	IC IOB - DATAO CLR; DATAO SET; CONO CLR; CONO SET; DATAI; CONI
21	W852	CABLE	DSBC UB Ø TO UB 7; DSBC CYL ADR REG 1 2 4 8 16 32 64 128
22	W852	CABLE	DSBC SEQUENCE OUT; SET HD TAG LINE; SET CYL TAG LINE; CONTR TAG LINE; SEL UNIT RD ONLY
23	W591		DSBC CYL ADR REG 1; DSBC CYL ADR REG 2; DSBC CYL ADR REG 4; DSBC CYL ADR REG 8; DSBC CYL ADR REG 16
24	WØ1Ø		D & E
25	B133		MPX RAW DATA; MPX RAW DATA; WRITE DATA 6; MPX RAW DATA; WRITE DATA 7; MPX RAW DATA
26	W591		READ DATA Ø; READ DATA 1; READ DATA 2; READ DATA 3
27	B133		MPX WRITE DATA Ø; MPX WRITE DATA 1; MPX WRITE DATA 2; MPX WRITE DATA 3
28	W692		MPX WRITE DATA Ø; MPX WRITE DATA 1; MPX WRITE DATA 2; MPX WRITE DATA 3
29	WØ21	CABLE	READ DATA Ø; DAR SU Ø; ATTN Ø; MPX SEC PLS Ø; MPX WRITE DATA Ø; MPX +5V TO TERM
30	WØ21	CABLE	READ DATA 1; DAR SU 1; ATTN 1; MPX SEC PLS 1; MPX WRITE DATA 1; MPX +5V TO TERM
31	WØ21	CABLE	READ DATA 2; DAR SU 2; ATTN 2; MPX SEC PLS 2; MPX WRITE DATA 2; MPX +5V TO TERM
32	WØ21	CABLE	READ DATA 3; DAR SU 3; ATTN 3; MPX SEC PLS 3; MPX WRITE DATA 3; MPX +5V TO TERM
33–44			

Row R

Slot	Module	Usage	Signals
1			
2			
3	W1Ø2		AR Ø8; CHND Ø8; AR 1Ø; CHND 1Ø; AR 12; CHND 12; AR 14; CHND 14
4	W1Ø2		AR Ø9; CHND Ø9; AR 11; CHND 11; AR 13; CHND 13; AR 15; CHND 15
5	W1Ø2		AR 26; CHND 26; AR 28; CHND 28; AR 3Ø; CHND 3Ø; AR 16; CHND 16
6	W1Ø2		AR 27; CHND 27; AR 29; CHND 29; AR 31; CHND 31; AR 17; CHND 17
7	W1Ø2		AR 32; CHND 32; AR 33; CHND 33; AR 34; CHND 34; AR 35; CHND 35
8–10			
11	WØ21	CABLE	CC CHAN OUT; CC SA WR OUT; CC DEVICE PULSE; CC WR CON WD REQ; CC CHAN RESET; CC WRITE EVEN PAR
12	WØ21	CABLE	IC CHAN PULSE; CC CHAN BUSY OUT; IC DATA PAR ERROR; IC CON PAR ERROR; IC WR CON WD COMP; IC NO SUCH MEM
13–15			
16			CXR IOB - PI 1 TO PI 7
17–19			
20			CXR IOB - PI 1 TO PI 7
21			
22			SEL UNIT READY; SEL UNIT ON LINE; SEL INDEX; SEL FILE UNSAFE; SEL UNIT SK INC; SEL UNIT SEC PL; DSBC SEQUENCE OUT
23	W591		CYL ADR REG 32; CYL ADR REG 64; CYL ADR REG 128; SEL UNIT RD ONLY
24	W591		SEL UNIT RDY; SEL UNIT ON LINE; SEL INDEX; SEL FILE UN-SAFE; SEL UNIT SK INC
25	B133		MPX RAW DATA; MPX RAW DATA; MPX RAW DATA; MPX RAW DATA
26	W591		READ DATA 4; READ DATA 5; READ DATA 6; READ DATA 7
27	B133		MPX WRITE DATA 4; MPX WRITE DATA 5; MPX WRITE DATA 6; MPX WRITE DATA 7
28	W692		MPX WRITE DATA 4; MPX WRITE DATA 5; MPX WRITE DATA 6; MPX WRITE DATA 7
29	WØ21	CABLE	READ DATA 4; DAR SU 4; ATTN 4; MPX SEC PLS 4; MPX WRITE DATA 4; MPX +5V TO TERM
30	WØ21	CABLE	READ DATA 5; DAR SU 5; ATTN 5; MPX SEC PLS 5; MPX WRITE DATA 5; MPX +5V TO TERM
31	WØ21	CABLE	READ DATA 6; DAR SU 6; ATTN 6; MPX SEC PLS 6; MPX WRITE DATA 6; MPX +5V TO TERM
32	WØ21	CABLE	READ DATA 7; DAR SU 7; ATTN 7; MPX SEC PLS 7; MPX WRITE DATA 7; MPX +5V TO TERM
33–44			

Row S

Slot	Module	Usage	Signals
1	S2Ø2		SC Ø BIT Ø; SC Ø BIT 1
2	S2Ø2		SC 1 BIT Ø; SC 1 BIT 1
3	S2Ø2		SC 2 BIT Ø; SC 2 BIT 1
4	S2Ø2		SC 3 BIT Ø; SC 3 BIT 1
5	S2Ø2		SC 4 BIT Ø; SC 4 BIT 1
6	S2Ø2		SC 5 BIT Ø; SC 5 BIT 1
7	S2Ø2		SC 6 BIT Ø; SC 6 BIT 1
8	S2Ø2		SC 7 BIT Ø; SC 7 BIT 1
9	S2Ø2		SC Ø BIT 2; SC Ø BIT 3
10	S2Ø2		SC 1 BIT 2; SC 1 BIT 3
11	B163		SCB BUFF Ø; SCB BUFF 1; SCB BUFF 2
12	B163		SCB BUFF 3; SCB BUFF 4
13	B214		SCB BUFF Ø TO SCB BUFF 3
14	B214		SCB BUFF 4
15	B611		SCB FM SR REG JM; RDS CLOCK DLY; SCM CHANGE COUNT
16	G7ØØ		D THRU H; M THRU T
17	B312		DTC WRITE CLOCK FLOP
18	W8ØØ		DSBC SEQ OUT; DSBC CNTRL GND
19	W99Ø		DSBC SEQ OUT
20			
21	B212		DTC WRITE CLOCK FLOP
22	B41Ø		RDS VCO SLOW
23	B589		RDS VCO SLOW
24	B312		RPØ1 DATA WIND
25	B136		RDS DATA IN; RDS DIVIDE 2
26	B611		RDS DATA WIND; RDS DATA WIND; RDS ERR A
27	G7ØØ		D THRU H; M THRU V
28	B212		RDS DIVIDE 2; RDS DATA WIND
29	B311		RDS DATA WIND
30	B611		RDS DATA WIND; RDS CLOCK DLY; RDS DIVIDE 2
31			
32	B135		RDS DATA OUT H; RDS CLOCK [illegible] H
33–44			

Row T

Slot	Module	Usage	Signals
1	S2Ø2		SC 2 BIT 2; SC 2 BIT 3
2	S2Ø2		SC 3 BIT 2; SC 3 BIT 3
3	S2Ø2		SC 4 BIT 2; SC 4 BIT 3
4	S2Ø2		SC 5 BIT 2; SC 5 BIT 3
5	S2Ø2		SC 6 BIT 2; SC 6 BIT 3
6	S2Ø2		SC 7 BIT 2; SC 7 BIT 3
7	S2Ø2		SC Ø BIT 4; SC 1 BIT 4
8	S2Ø2		SC 2 BIT 4; SC 3 BIT 4
9	S2Ø2		SC 4 BIT 4; SC 5 BIT 4
10	S2Ø2		SC 6 BIT 4; SC 7 BIT 4
11	B169		SCM BIT Ø; SCM BIT 1
12	B169		SCM BIT Ø; SCM BIT 1
13	B169		SCM BIT 2; SCM BIT 3
14	B169		SCM BIT 2; SCM BIT 3
15	B169		SCM BIT 4; SCM CHANGE COUNT
16	B169		SCM BIT 4; SCM CHANGE COUNT
17–21			
22	B41Ø		RDS VCO FAST
23	G59Ø		RDS VCO FAST
24	B212		RDS ERR A; RDS ERR B
25	B312		RDS CLOCK DLY
26	B312		RDS CLOCK DLY
27	B611		RDS CLOCK DLY; RDS DIVIDE 2; RDS ERR B
28	B133		RDS DATA IN; RDS CLOCK DLY; RDS DATA WIND; RDS DATA WIND
29	B312		RDS DATA IN
30	B611		RDS DATA IN; RDS DATA OUT H; RDS CLOCK DLY; RDS DATA OUT
31	B611		RDS CLK WIND
32	B212		RDS CLK WIND
33–44			

FOR INFORMATION ONLY
DO NOT BUILD TO THIS DRAWING

REV.	CHANGE NO.	CHK
A	RP10 00009 GREEN	FV
B	RP10-00017 IVES	
C	RP10-00027 IVES	FV

FIRST USED ON OPTION / MODEL: PDP1Ø

UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES TOLERANCES DECIMALS ±.005 FRACTIONS ±1/64 ANGLES ±0°30' FINAL SURFACE QUALITY REMOVE BURRS AND BREAK SHARP CORNERS

MATERIAL

FINISH

NEXT HIGHER ASSY: A-ML-RP10-Ø

digital EQUIPMENT CORPORATION MAYNARD MASSACHUSETTS

TITLE: MODULE UTILIZATION P-T

SIZE D CODE MU NUMBER RPIØ-Ø-PT REV C

SHEET 1 OF 1

DIGITAL EQUIPMENT CORPORATION
MAYNARD, MASSACHUSETTS

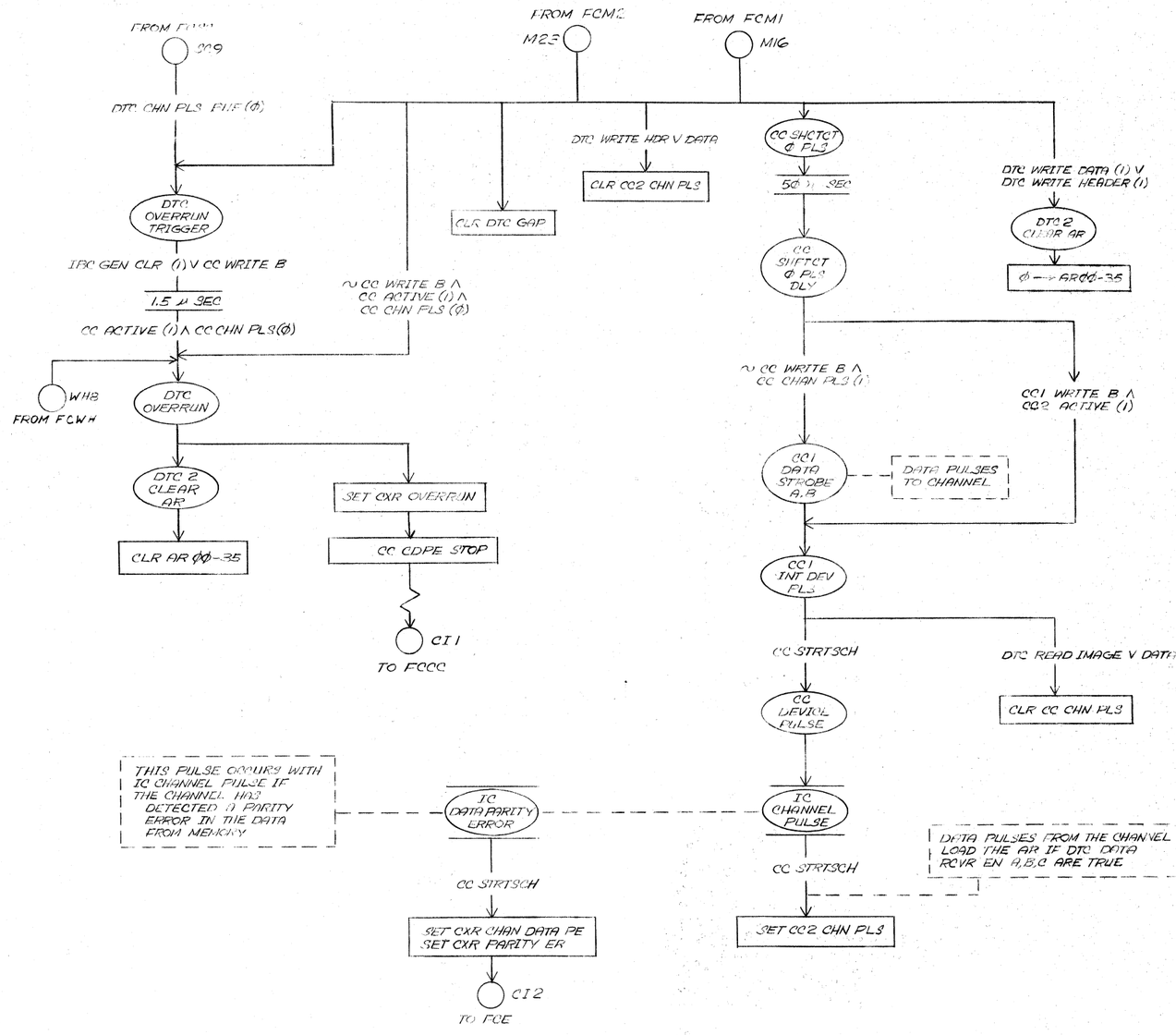
PARTS LIST

MADE BY J Longo DATE 11-15-69	CHECKED J Longo DATE 11-15-69	SECTION
ENG John C Green Jr 24NOV69 DATE	PROD G McClure DATE 11-16-69	ISSUED SECT.

ITEM NO.	DWG NO. / PART NO.	DESCRIPTION	QUANTITY / VARIATION 60 Hz	50 Hz
	W107	BUS RECEIVER	7	7
	W505	LOW VOLTAGE DETECTOR	1	1
	W591	CONVERTER	9	9
	W692	CONVERTER	7	7
	W800	RELAY	1	1
	W990	SPLIT LUG BOARD	3	3

TITLE DISK FILE INTERFACE	ASSY NO.	SIZE A	CODE PL	NUMBER RP10-0-MC	REV. E / ECO NO.
	SHEET 3 OF 3	DIST.			

DEC FORM NO.
DRA 110



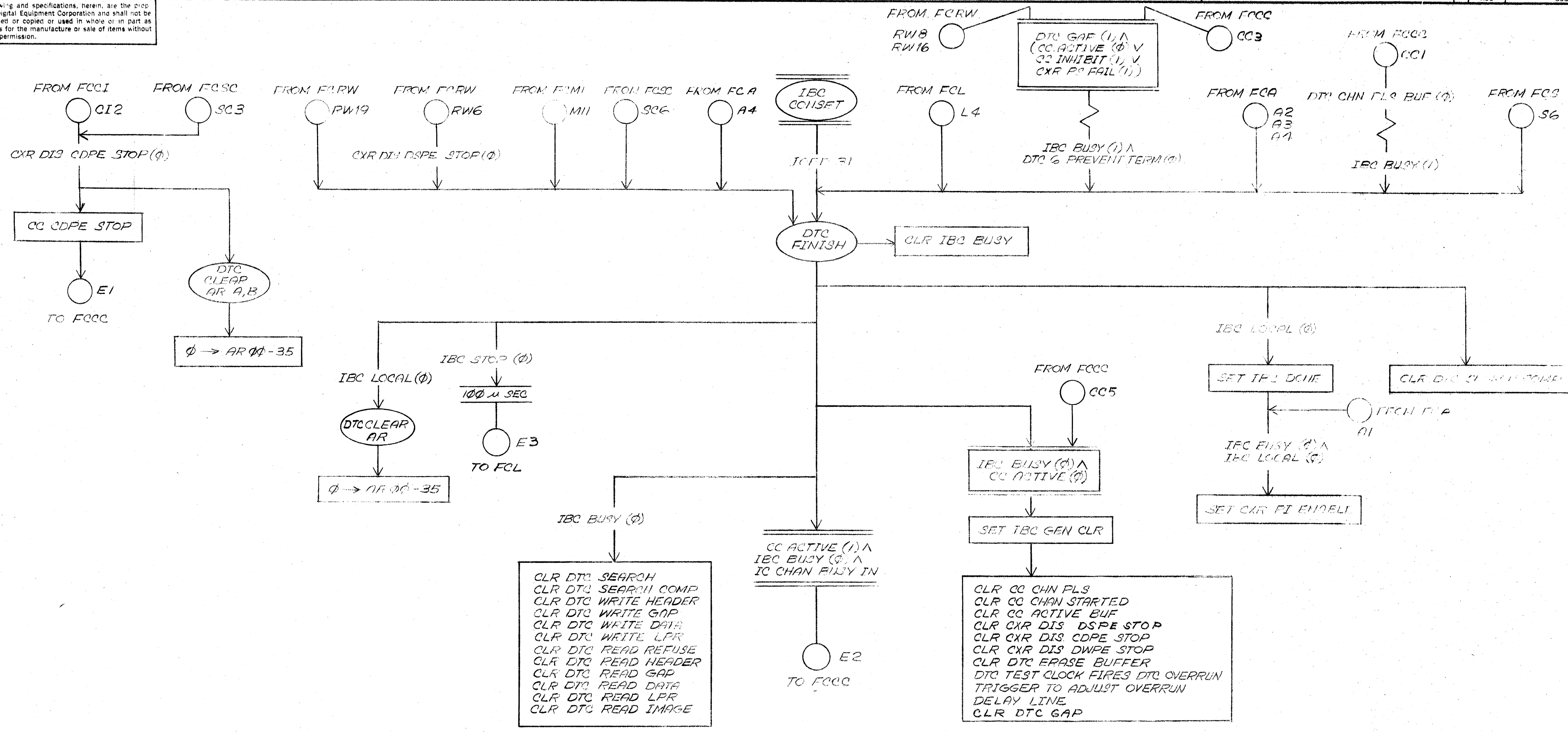
FOR INFORMATION ONLY
DO NOT BUILD TO THIS DRAWING

REVISIONS			
CHK	CHANGE NO.	REV	
[illegible]	RPIO-00004	A	T Cassidy 1-12-70
			GREEN JR.
	RPIO-00036	B	John C Green Jr 16JAN 70
			M. Davis 2-4-72
			B. WALSH
			B. Walsh 9-9-72

FIRST USED ON OPTION/MODEL	QTY.	DESCRIPTION	PART NO. / ITEM NO.
PDP/Ø	1000 DAY	PARTS LIST	

UNLESS OTHERWISE SPECIFIED	DRN. [illegible]	DATE 7/2/69	digital EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES TOLERANCES DECIMALS ± .005 FRACTIONS ± 1/64 ANGLES ± 0°30' FINAL SURFACE QUALITY ✓ REMOVE BURRS AND BREAK SHARP CORNERS	CHK'D. T. Hastings	DATE 11 NOV 69	TITLE FLOW CHART CHANNEL INTERFACE
	ENG. [illegible]	DATE [illegible]	
	PROJ. ENG. [illegible]	DATE [illegible]	
MATERIAL	PROD. [illegible]	DATE [illegible]	
	NEXT HIGHER ASSY G-AL-AA10-4		
FINISH	SCALE		SIZE D CODE FD NUMBER RPIØ-Ø-FCC1 REV B
	SHEET OF		DIST.

DEC FORM NO. DRD 102A



FOR REFERENCE ONLY
DO NOT BUILD TO THIS DRAWING

FIRST USED ON OPTION/MODEL	QTY	DESCRIPTION	PART NO.	ITEM NO.
PDP10		PARTS LIST		

UNLESS OTHERWISE SPECIFIED
DIMENSIONS IN INCHES
TOLERANCES
DECIMALS ±.005 FRACTIONS ±1/64 ANGLES ±0 30'
FINAL SURFACE QUALITY
REMOVE BURRS AND BREAK SHARP CORNERS

MATERIAL

FINISH

DRN / CHK'D / ENG / PROJ ENG / PROD / DATE

digital EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS

TITLE: FLOW CHART END

SIZE: D | NUMBER: KDP10-Ø-FCE | REV: B

SCALE | SHEET

RDS DATA OUT

SET DTC READ BIT

RDS EXT DATA WINC. ØnS

DTC READ CLOCK PULSE

FROM FCSC SØ5 FROM FCRW RW4 FROM FCRW RW5 FROM FCRW RW17

RDS DATA OUT

1 → DTC READ BIT

DSBC SEL UNIT SEC PLS

DTC READ IMAGE (Ø)

40 n SEC

DTC PAR CONT (1)
DTC READ BIT (1)
DTC READ ENABLE

(DTC READ HEADER (1) V
DTC READ DATA (1)) ∧
RDS READ BIT (1)

DTC LPR 36(Ø) 40 n SEC → SET DTC LPR 36

DTC LPR 36 (1) 40 n SEC → CLR DTC LPR 36

DTC PARITY(Ø) 40 n SEC → SET DTC PARITY

DTC PARITY (1) 40 n SES → CLR DTC PARITY

DTC READ ENABLE ∧
DTC PAR CONT (Ø) V
DTC READ IMAGE (1)

DTC SHIFT BIT CNT

FROM FCM2 M25

SHIFT WDC BIT SHFTCT REGISTER

DTC READ ENABLE ∧
DTC PAR CONT (Ø) V
DTC READ IMAGE (1)

DTC SHIFT SR

SHIFT SR LEFT ONE BIT WHILE SHIFTING DTC READ BIT INTO SR5

100 n SEC → DTC DATA STROBE → CLR DTC READ BIT

25Ø n SEC → DTC SET READ

FROM FCM? M21

DTC READ CLOCK DLY

DTC DATA CLOCK DLY

CLR WDC BYTE CT +1
CLR WDC WD CT +1A
CLR WDC WD CT +1B

WDC BIT SHFTCT ØØ(1) ∧
WDC BIT SHFT CT Ø1(Ø)

WHEN SHIFT REGISTER IS FILLED WITH SIX BITS WDC BIT SHFTCT EQ ØØ GOES TRUE

WDC BIT SHFTCT EQ ØØ

SET WDC BYTE CT+1
SET WDC WD CT +1A
SET WDC WD CT +1B

SHIFT WDC BYTE SHFT CT REGISTER

WDC BYTE SHFTCT ØØ(1) ∧
WDC BYTE SHFTCT Ø1(Ø)

WDC BYTE SHFT CT EQ ØØ

INCR WDC DATA CT ØØ-Ø6

WDC DATA CT ØØ-06 (Ø)

WDC WORD COUNT EQ ØØ

~WDC BIT SHFT CT EQ ØØ → CLR DTC SUPR BUF LD

(DTC READ LPR(1) V
DTC READ DATA(1) V
DTC READ HEADER(1) V
DTC READ IMAGE(1)) ∧
DTC PAR CONT (Ø) ∧
WDC BIT SHFTCT EQ ØØ ∧
DTC SUPR BUF LD (Ø)

DTC SHIFT LPR → ROTATE LPR LEFT SIX BITS

DTC LD BUFFER FM SR → JAM LOAD SR BUFFER FROM SR

400 n SEC

FROM FCM2 M22

DTC LD BUF FM SR DLY

IBC LOCAL (Ø) ∧
WDC BYTE SHFTCT EQ ØØ ∧
(DTC READ DATA (1) V
DTC READ IMAGE (1) V
DTC WRITE HEADER(1))

DTC SHFTCT Ø PLS → M16 TO FCCI

DTC READ DATA (1) V
DTC READ IMAGE (1)

DTC SHIFT AR

DTC READ ENABLE

SHIFT AR ØØ-36 LEFT SIX BITS WHILE SHIFTING SR BUF Ø-5 INTO AR 3Ø-35

DTC PAR CONT (1)
DTC PARITY (Ø)
DTC READ DATA (1)

SET CXR PARITY ER

SET CXR DISK WD PE

CXR DIS DWPE STOP (Ø) → M11 TO FCE

DTC LOAD LPR

EXCLUSIVE OR LPR 3Ø-35 WITH SR BUF ØØ-Ø5
IE SR BUF Ø-5 ⊕ LPR 3Ø-35 → LPR 3Ø-35

DTC PAR CONT (Ø) ∧
WDC BIT SHFTCT EQ ØØ ∧
WDC BYTE SHFTCT EQ ØØ ∧
DTC READ REFUSE (Ø) ∧
DTC READ GAP (Ø) ∧
DTC WRITE HEADER (Ø) ∧
DTC SUPR BUF LD (Ø) ∧
(DTC WRITE GAP (Ø) V
WDC DATA WD CT ØØ4(1))

40 n SEC → SET DTC 5 PAR CONT

M18 TO FCM2

M27 TO FCM2

M13 TO FCRW

M12 TO FCRW

M19 TO FCWH

DTC 7 READ HEADER (Ø)
∧ DTC 5 PAR CONT (Ø)

40 n SEC → CLR DTC 5 PARITY

DTC PAR CONT (1)

DTC CLEAR PAR PLS

WDC WORD COUNT EQ ØØ

DTC SET RD/WR LPR

DTC LOAD

CLR DTC PAR CONT

M14 TO FCRW

M15 TO FCRW

M26 TO FCRW

M17 TO FCSC

M11Ø TO FCWH

FOR INFORMATION ONLY
DO NOT BUILD TO THIS DRAWING

REVISIONS	CHK	CHANGE ID	REV.
		RP10-00004 T.Quilles 1-12-70 GREEN JR	A
		RP10-00008 John C. Haas 9a 1/JAN 70	B
		RP10-00036 M. Stein 2-7-72 B.WALSH	C

DEC FORM NO. DRD 102A

FIRST USED ON OPTION/MODEL	QTY.	DESCRIPTION	PART NO.	ITEM NO.
PDP1Ø		PARTS LIST		

UNLESS OTHERWISE SPECIFIED

UNLESS OTHERWISE SPECIFIED
DIMENSION IN INCHES
TOLERANCES
DECIMALS ± .005 FRACTIONS ± 1/64 ANGLES ± 0°30'
FINAL SURFACE QUALITY ✓
REMOVE BURRS AND BREAK SHARP CORNERS

MATERIAL

FINISH

DRN. W. Stephenson	DATE	5/28/69
CHK'D J. Longjo	DATE	11 NOV 69
ENG. [illegible]	DATE	11 NOV 69
PROJ. ENG. [illegible]	DATE	11 NOV 69
PROD. [illegible]	DATE	11-14-69

NEXT HIGHER ASSY
M-ML-RP1Ø-Ø

SCALE

SHEET / OF /

digital EQUIPMENT CORPORATION MAYNARD MASSACHUSETTS

TITLE

DATA TRANSFER CONTROL MICRO 1

SIZE	CODE	NUMBER	REV.
D	FD	RP1Ø-Ø-FCM1	C

DIST.

FROM FC9C SC7

DTC SEARCH COMP (Ø)

CLR DTC READ GAP

RW1 TO FC5C

RW2 TO FC5C

RW3 TO FCDC

WDC CLEAR DATA WD CT

Ø→WDC DATA WD CT ØØ-ØØ

FROM FCDC DC1

DTC HEAD SET(1)

DTC READ ALL EN ∧ IBC LOCAL (Ø)

SET DTC READ ALL

SET DTC SET READ

DTC READ GAP(1) ∧ ∼DTC BETA GAP ∧ DTC SEARCH COMP(1) ∧ DTC READ BIT(1) ∧ DTC READ CLOCK PULSE

SET DTC READ DATA

RW4 TO FCM1

FROM FC9C SC8

DTC READ DATA COM

SET DTC READ GAP

CLR DTC READ HEADER

DTC CLEAR LPR

Ø→LPR ØØ-35 DTC LPR 36

SET DTC SUPR BUF LD
CLR DTC READ GAP
SET DTC READ IMAGE V DATA

WDC CLEAR DATA WD CT

Ø→WDC DATA WD CT ØØ-Ø6

FROM FCM1 M13

DTC 7 READ DATA (1)

SET DTC 7 READ LPR

CLR DTC 7 READ DATA

CLR DTC 7 READ IMAGE V DATA

RW5 TO FCM1

FROM FCM1 M26

WDC DATA WD CT Ø6 (1)

CLR DTC 7 READ LPR

600 n SEC

CXR SEC PARITY ERROR

SET CXR DISK SEC FE
SET CXR PARITY ERROR

RW6 TO FCE

FROM FCWH WH7

SET DTC 5 GAP

RW7 TO FC5C

RW8 TO FCE

CLR DTC SET READ

RW9 TO FCDC

NOTE:
1. CXR SEC PARITY ERROR IS TRUE IF ANY LPR BIT IS CLEARED.

FROM FC9C SC1Ø

3 μ SEC

DTC WRITE DATA COM ∧ DTC SEARCH COMP(1)

SET DTC WRITE GAP

SET DTC WRITEENABLE
SET DTC SET WRITE
SET DTC SET ERASE

RW21 TO FCDC

SET DTC ERASE BUFFER

RW1Ø TO FCM2

DTC CLEAR LPR

CLR LPR ØØ-35
DTC LPR 36

FROM FCM1 M14

DTC WRITE GAP(1) ∧ WDC DATA WD CTØ4 (1)

WDC CLEAR DATA WD CT

Ø→WDC DATA WD CT ØØ-Ø6

CLR DTC WRITE GAP

SET DTC WRITE DATA

SET DTC SUBR BUF LD

WDC CLEAR DATA WD CT

Ø→WDC DATA WD CT ØØ-Ø6

RW11 TO FCM2

FROM FCM1 M12

DTC WRITE DATA (1)

CLR DTC WRITE DATA

SET DTC WRITE LPR

RW12 TO FCM2

(DTC WRITE HDR V DATA) ∧ ∼CC CDPE STOP ∧ CC CHN PLS (1)

SET DTC PREVENT TERM

FROM FCM1 M15

WDC DATA WD CT 5 (1)

CLR DTC WRITE LPR

CLR DTC SET WRITE
CLR DTC WRITE ENABLE

RW13 TO FCDC

CLR DTC SET ERASE

20 μ SEC

CLR DTC ERASE BUFFER

RW14 TO FCDC

FROM FC9C SC1

DTC READ IMAGE COM ∧ DTC HEAD SET(1)

SET DTC ST RD IM TIMEOUT

SET DTC READ IMAGE SS

75 M SEC

CLR DTC READ IMAGE SS

DTC SECTOR (Ø)

SET DTC NO SECTOR

SET DTC GAP

RW15 TO FC5C

RW16 TO FCE

DAR SEL INDEX

DTC READ IMAGE SS

SET DTC SECTOR

MPY SECTOR PULSE X

DTC SEC PLS X

DAR SEL DRIVE X ∧ DTC NO SECTOR (1)

DTC READ IMAGE INDEX

DTC READ IMAGE INDEX

SET DTC READ IMAGE

SET DTC READ IMAGE V DATA
SET DTC SET READ

RW17 TO FCM1

RW18 TO FCDC

DTC READ IMAGE INDEX

CLR DTC READ IMAGE

CLR DTC READ IMAGE V DATA
CLR DTC SET READ

RW19 TO FCE

RW2Ø TO FCDC

FOR INFORMATION ONLY
DO NOT BUILD TO THIS DRAWING

REVISIONS			
SYM	CHANGE NO.	REV.	
XX	RP10-00004	A	T. Quibell 13-JAN-70
			GREEN JR. 16 JAN 70
	RP10-00036	B	M. Gosse 2-3-72
			B. WALSH
			02-7-72

FIRST USED ON OPTION/MODEL	QTY.	DESCRIPTION	PART NO.	ITEM NO.
PDP1Ø		PARTS LIST		

UNLESS OTHERWISE SPECIFIED
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES
TOLERANCES
DECIMALS ± .005 FRACTIONS ± 1/64 ANGLES ± 0°30'
FINAL SURFACE QUALITY √
REMOVE BURRS AND BREAK SHARP CORNERS
MATERIAL
FINISH

DRN. W. Stephenson	DATE 7/2/69	digital EQUIPMENT CORPORATION MAYNARD MASSACHUSETTS
CHK'D. T. Quibell	DATE 11 NOV 69	
ENG. [illegible]	DATE 11 NOV 69	TITLE FLOW CHART READ/ WRITE
PROJ. ENG. [illegible]	DATE 11 NOV 69	
PROD. A. M. Gosse	DATE 11-14-69	
NEXT HIGHER ASSY A-ML-RP1Ø-Ø		SIZE D CODE FD NUMBER RP1Ø-Ø-FCRW REV. B
SCALE		
SHEET 1 OF 1		DIST.

FROM FCRW RW2 · FROM FCSC SC2 · FROM FCRW RW7 · FROM FCRW RW15

DSBS SEL UNIT SEC PLS

DTC SELECTED SECTOR → CLR DTC 6, CLOCK Ø,1 · CLR DTC PREV TERM

DTC READ IMAGE (Ø)

DTC CLEAR SHFTCT → CLR WDC BIT SHFTCT ØØ-Ø2 · CLR WDC BYTE SHFTCT ØØ-Ø2

WDC CLEAR DATA WD CT → CLR WDC DATA WD CT ØØ-Ø6

IBC BUSY (1)

1Ø μ SEC

DTC SECTOR DLY

RDS SYNC FIELD EN → DTC SEARCH SYNC

35Ø μ SEC

DTC 2 SEARCH (1) ∨ DTC 2 SEARCH COMP (1)

SET DTC 7 READ REFUSE → SET DTC SET READ

DTC CLEAR LPR → CLR LPR ØØ-35, DTC LPR 36

SC4 TO FCDC

~ DTC 7 SEARCH SYNC

DTC 7 READ HEADER (Ø) ∧ DTC READ BIT (1) ∧ DTC READ CLOCK PULSE DTC READ REFUSE (1)

SET DTC 7 READ HEADER → SET DTC SUPR BUF LD · CLR DTC READ REFUSE

SC5 TO FCM1 — HERE 36 BITS OF HEADER ARE ASSEMBLED IN THE LPR AND THE PARITY BIT IN DTC PARITY

FROM FCCC CC2

CC 2 STRTSCH → ENABLE CC1 DEVICE PULSE · ENABLE CC1 WR CON WD REQ · ENABLE CC1 WRITE EVEN PAR · ENABLE IC NO SUCH MEM · ENABLE IC WR CON WD COMP · ENABLE IC DATA PARITY ERROR · ENABLE IC CON PAR ERROR (TRANSMITTERS / RECEIVERS); IF(CC WRITE B): ENABLE DTC 2 DATA RCVR EN A,B,C; IF(CXR WRITE EVEN PAR (1)): SET CC WRITE EVEN PAR; CLR CC ACTIVE BUF

TO FCRW SC1 · FROM FCL L3 · SC12 TO FCWH

DTC READ DATA COM V DTC WRITE DATA COM

SET DTC 5 SEARCH

SC2 TO FCSC

INITIAL ADDRESS PULSE TO CHANNEL

IBC BUSY (1)

CC1 DATA STROBE A, B

IF SAWRITE IS ASSERTED THE AR WILL BE LOADED SIMULTANEOUSLY WITH IC CHANNEL PULSE

IC CHANNEL PULSE

THIS PULSE OCCURS WITH CHANNEL PULSE IF THE CHANNEL HAS DETECTED A PARITY ERROR IN THE DATA FROM MEMORY

IC DATA PARITY ERROR

CC STRTSCH → SET CXR CHAN DATA PE

SC3 TO FCE

CC STRTSCH → SET CC2 CHN PLS

SET DTC CHN PLS BUF

CC1 INT DEV PLS

CC STRTSCH → CC1 DEVICE PULSE

DTC READ IMAGEV DATA → CLRCC CHN PLS

5Ø h SEC

DTC SEARCH COMP (Ø)

DTC CLEAR AR A, B → CLR AR Ø-35

FROM FCRW RW1

DSBC SEL INDEX

DTC SEARCH (1)

CXR SEARCH ER Ø (Ø) → SET CXR SEARCH ER Ø

CXR SEARCH ER Ø (1) ∧ CXR SEARCH ER 1 (Ø) → SET CXR SEARCH ER 1

CXR SEARCH ER 1 (1) ∧ CXR SEARCH ER 2 (Ø) → SET CXR SEARCH ER 2

SC6 TO FCE

FROM FCM1 M17

DTC PAR CONT (Ø)

DTC READ HEADER (1)

DTC SRCH COMP STRB → SET DTC SUPR BUF LD

6ØØ h SEC

~ DTC SRCH COMP STRB

RDS SYNC FIELD EN → DTC BETA GAP H

SC8 TO FCRW

HCDE SURFSEC COMP ∧ HCDE CYL COMP ∧ DTC PARITY (1) ∧ DTC READ HEADER (1)

CLR DTC SEARCH · SET DTC SEARCH COMP

SC9 TO FCCI

SC11 TO FCDC

44 μ SEC → ~ DTC RPØ1

88 μ SEC → DTC RPØ1

~ DTC GAP H

SC7 TO FCRW

HCDE HDR READ ER

SET CXR SEARCH ER 2

SC6 TO FCE

CLR DTC 5 PARITY

DTC WRITE DATA COM → CLR DTC READ HEADER

SCIØ TO FCRW

DTC CLEAR SHFTCT → CLR WDC BIT SHFTCT ØØ-Ø2 · CLR WDC BYTE SHFTCT ØØ-Ø2

FOR INFORMATION ONLY
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REVISIONS	CHK	CHANGE NO.	REV.
T. Quinn 1-13-70	RPP	RPIØ-00004	A
GREEN JR.			
John C Green Jr. 16 JAN 70		RPIØ-00009	B
[illegible] 3-11-71			
IVES			
[illegible] 11-7-72		RPIØ-00036	C
M Quinn 2-3-72			
B. WALSH			
S Walsh 9-9-72			

DEC FORM NO. DRD 102A

FIRST USED ON OPTION/MODEL	QTY.	DESCRIPTION	PART NO.	ITEM NO.
PDPIØ		PARTS LIST		

UNLESS OTHERWISE SPECIFIED
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES
TOLERANCES DECIMALS ± .005 FRACTIONS ± 1/64 ANGLES ± 0°30'
FINAL SURFACE QUALITY REMOVE BURRS AND BREAK SHARP CORNERS

MATERIAL

FINISH

DRN.	C.W. Stephenson	DATE 7 DEC 69
CHK'D	T Longo	DATE 11 NOV 69
ENG.	John C Green Jr.	DATE 11 NOV 69
PROJ. ENG.	John C Green Jr.	DATE 11 NOV 69
PROD.	D. M. Ives	DATE 11-14-69

NEXT HIGHER ASSY: A-ML-RPIØ-Ø

SCALE

SHEET / OF /

digital EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS

TITLE: FLOW CHART SEARCH

SIZE D · CODE FD · NUMBER RPIØ-Ø-FCSC · REV. C

DIST.

SIZE D · CODE FD · NUMBER RPIØ-Ø-FCSC · REV. C

NOTE:
THE RPIØ HAS MANY ASYNCHRONOUS TIMING CHAINS AND SYNCHROIZERS, SOME ARE STARTED BY EXTERNAL TIMING PULSES, LEVELS FROM THE RPØ1,Ø2 AND DFIØ. THEY ARE COLLECTED HERE FOR REFERENCE.

DSBC SEL UNIT SEC PLS

DTC SET ERASE

DSBC SEL UNIT RD ONLY ∧ DTC OP CODE ØØ(Ø) ∧ DTC OP CODE Ø2 (1)

CC ILLEGAL WRITE

IBC BUSY (1)

SET CXR ILLEGAL WRITE

IBC BUSY (1)

A3 TO FCE

MPX ATTN X

SET DTC ATTN X

SET DTC ATTN X (1)

A1 TO FCE

~ DSBC SEL UNIT READY

~ DSBC SEL UNIT ON LINE

DSBC SEL FILE UNSAFE

CXR DISK NOT RDY

IBC BUSY (1)

A2 TO FCE

IC CON PAR ERROR

CC STRTCH

SET CXR CHAN CONT PE
SET CXR PARITY ER

IC WR CON WD COMP

CC STRTCH

SET CXR CW XFER COMP

W505 AT MØ2 DETECTS POWER SUPPLY OUT OF TOLERANCE

SET CXR PS FAIL

DISABE DSBC CONTROL GND

IC NO SUCH MEM

CC STRTSCH

SET CXR NON EX MEM

IC IOB RESET ∨ IBC LOCAL (Ø)

IC IOB RESET ∧ ~ IC CROBAR

IC CROBAR

CC CHANNEL RESET

IBC POWER CLEAR

A5 TO FCL

DISABLE CC SAWRITE OUT
DISABLE CC CHANNEL BUSY OUT
DISABLE CC WRITE EVEN PAR
DISABLE CC DEVICE PULSE
DISABLE CC WR CON WD REQ
DISABLE CC CHANNEL RESET
DISABLE CC CHAN START OUT
CLR CC ACTIVE BUF
CLR CC ACTIVE
CLR DTC ATTN ØØ-Ø7
SET IBC DONE
CLR IBC BUSY

IBC DATA O SET

DSBC SEL UNIT SEC PLS

DTC SELECTED SECTOR

Ø → DTC PREV TERM

Ø → DTC CLOCK ØØ
Ø → DTC CLOCK Ø1
Ø → WDC WD CNT ØØ-Ø6

DRIVE'S COMPOSITE SECTOR/INDEX PULSE

MPX SEC PLS X

SCC X CHANGE COUNT

SCC X 33

SCC X CLR PLS

-SCC X 33

SCC X +1 PLS

SET DTC SEC PLS

1-2 μ SEC

CLR DTC SEC PLS

SET SCC X 35

500 μ SEC

CLR SCC X 33

250 n3

~ IBC DOD

SCB FM SC REG JAM

C(SC X) → SCB BUFF

Ø → SC X Ø-4

C(SC X) + 1 → SC X

IC IOB DATAI ∧ IBC DEVICE SELECTED *

IC IOB CONI ∧ IBC DEVICE SELECTED *

GATE STATUS TO IC IOS' LINES

~ IC IOB DATAI

SCB FM SC REG JAM

C(SC X) → SCB BUFF

* NOTE: IBC DEVICE SELECTED REQUIRES IBC LOCAL (Ø)

~ IC CROBAR ∧ CXR PS FAIL (Ø)

DSBC CONTROL GND

+10 VOLTS & -15 VOLTS OK

DSBC SEQUENCE OUT

DSBC SEL UNIT EOC

IBC BUSY (1)

A4 TO FCE

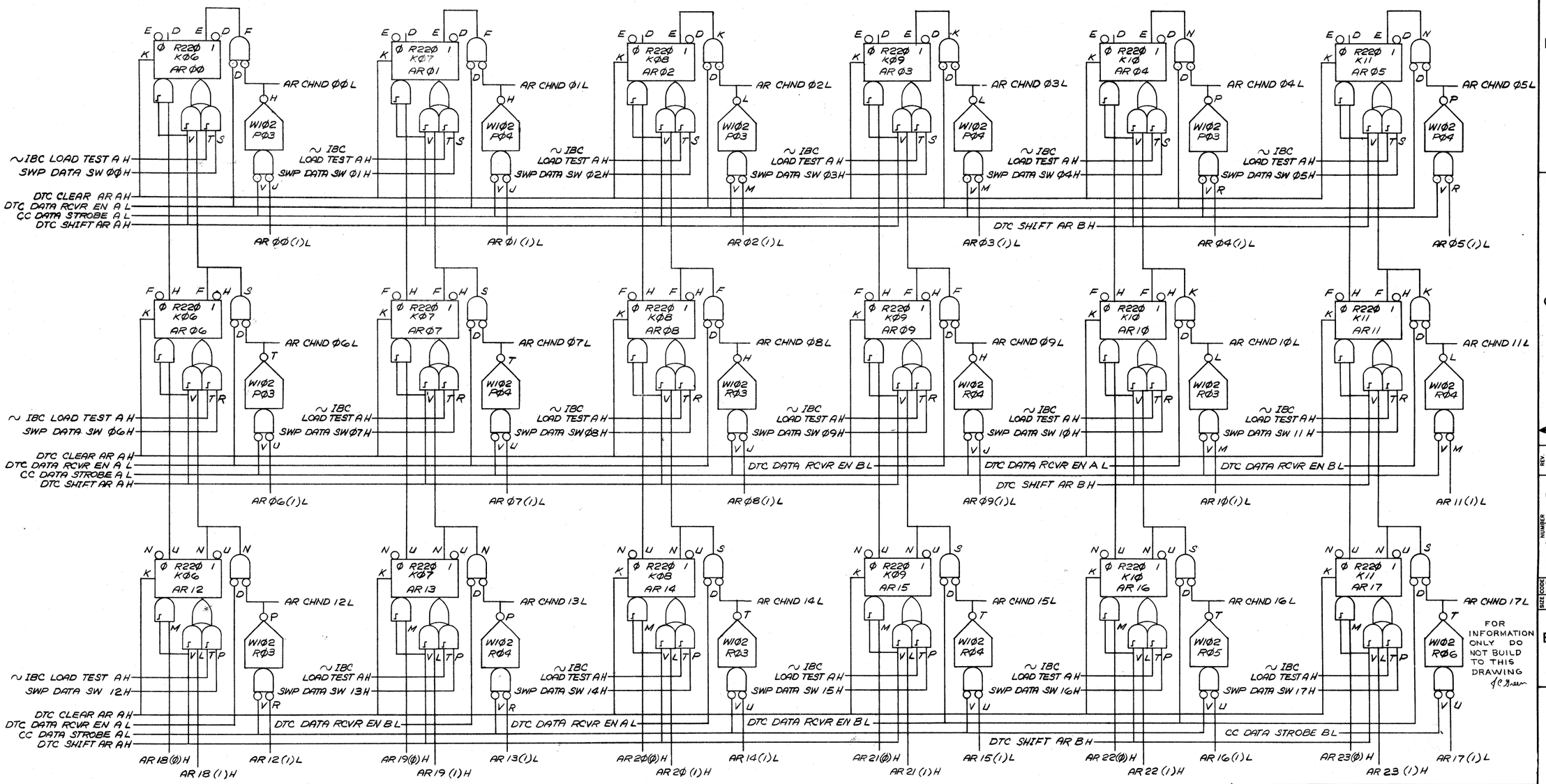
FOR INFORMATION ONLY
DO NOT BUILD TO THIS DRAWING

REVISIONS			
CHK	CHANGE NO.	REV	
	RPIC-00036	A	
M. Glenn	2-4-72		
B. WALSH			
[illegible]	2-9-72		

DEC FORM NO CHD 102A

FIRST USED ON OPTION/MODEL: PDP1Ø	QTY.	DESCRIPTION	PART NO. / ITEM NO.
UNLESS OTHERWISE SPECIFIED		PARTS LIST	
UNLESS OTHERWISE SPECIFIED: DIMENSION IN INCHES; TOLERANCES: DECIMALS ± .005, FRACTIONS ± 1/64, ANGLES ± 0°30'; FINAL SURFACE QUALITY √; REMOVE BURRS AND BREAK SHARP CORNERS	DRN. W. Stephenson	DATE 10/30/68	digital EQUIPMENT CORPORATION MAYNARD MASSACHUSETTS
	CHK'D. [illegible]	DATE 11 NOV 69	TITLE: FLOW CHART ASYNCHRONOUS & SECTOR COUNTERS
	ENG. [illegible]	DATE [illegible]	
	PROJ. ENG. [illegible]	DATE [illegible]	
	PROD. [illegible]	DATE 11-14-69	
MATERIAL	NEXT HIGHER ASSY A-ML-RPIØ-Ø		SIZE D; CODE FD; NUMBER RPIØ-Ø-FCA; REV A
FINISH	SCALE		
	SHEET / OF /		DIST.

SIZE D; CODE FD; NUMBER RPIØ-Ø-FCA; REV A



REVISIONS	CHANGE NO.	CHK

FIRST USED ON OPTION/MODEL: PDP1Ø

UNLESS OTHERWISE SPECIFIED
UNLESS OTHERWISE SPECIFIED
DIMENSION IN INCHES
TOLERANCES
DECIMALS ± .005 FRACTIONS ± 1/64 ANGLES ± 0°30'
FINAL SURFACE QUALITY
REMOVE BURRS AND BREAK SHARP CORNERS

MATERIAL

FINISH

DRN. W. Stephenson	DATE	11/11/68
CHK'D. J. Longo	DATE	11 NOV 69
ENG. John C. Green Jr.	DATE	11 NOV 69
PROJ. ENG. John C. Green Jr.	DATE	11 NOV 69
PROD. G. Stevenson	DATE	11-16-69

NEXT HIGHER ASSY: A-ML-RP1Ø-Ø

SCALE

SHEET 1 OF 2

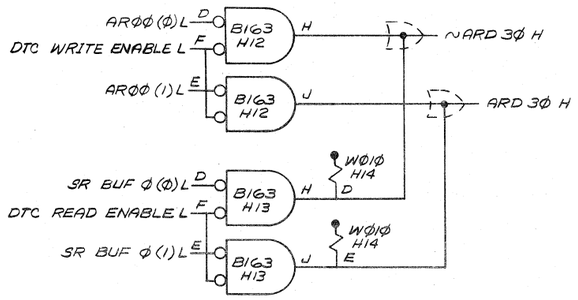
digital EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS

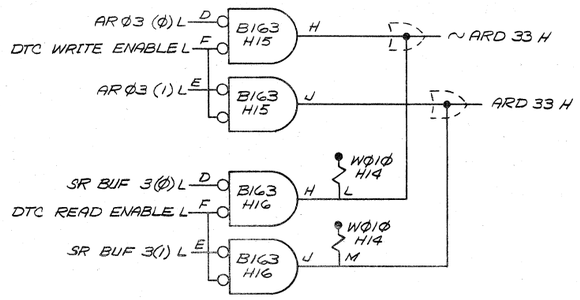
TITLE: ASSEMBLY REGISTER

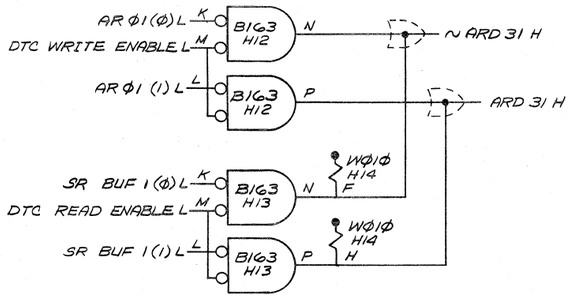
SIZE	CODE	NUMBER	REV.
D	BS	RP1Ø-Ø-AR	

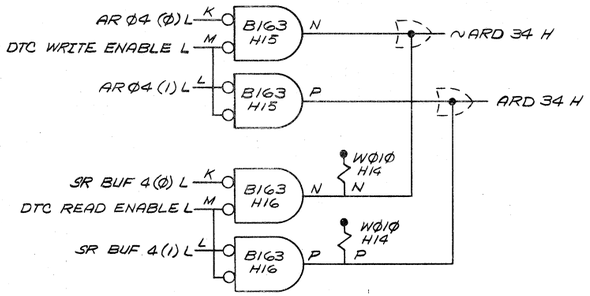
DIST.

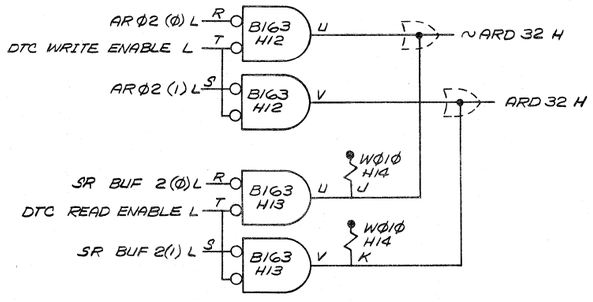
DEC FORM NO. DRD 102A

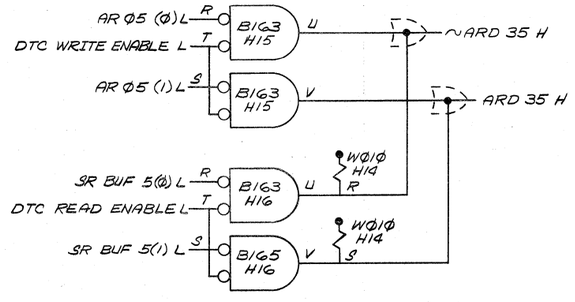












FOR INFORMATION ONLY
DO NOT BUILD TO THIS DRAWING

REVISIONS	REV.	CHK	CHANGE NO.

FIRST USED ON OPTION/MODEL	QTY.	DESCRIPTION	PART NO.	ITEM NO.
PDPIØ		PARTS LIST		

UNLESS OTHERWISE SPECIFIED
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES
TOLERANCES
DECIMALS ± .005 FRACTIONS ± 1/64 ANGLES ± 0°30'
FINAL SURFACE QUALITY √
REMOVE BURRS AND BREAK SHARP CORNERS
MATERIAL
FINISH

DRN.	W. Stephenson	DATE 11/21/68
CHK'D.	T. Livingston	DATE 11 NOV 69
ENG.	John C. Rosen Jr.	DATE 11 NOV 69
PROJ. ENG.	John C. Rosen Jr.	DATE 11 NOV 69
PROD.	G. McArthur	DATE 11-16-69

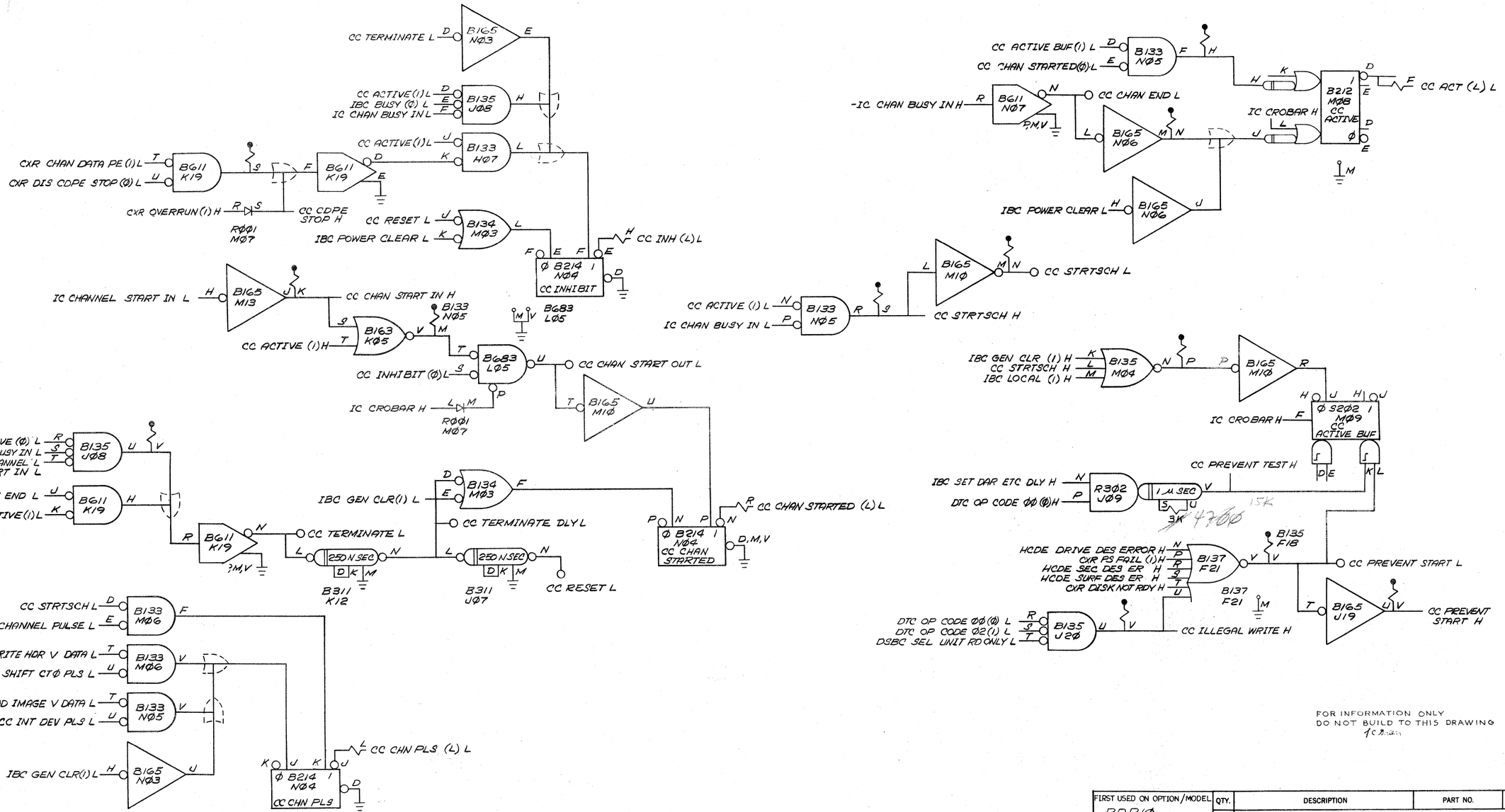
NEXT HIGHER ASSY A-ML-RPIØ-Ø
SCALE
SHEET / OF /

digital EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS

TITLE
ASSEMBLY
REGISTER
DATA GATE

SIZE D | CODE BS | NUMBER RPIØ-Ø-ARD | REV
DIST.

DEC FORM NO. DRD 102A



FOR INFORMATION ONLY
DO NOT BUILD TO THIS DRAWING
TC Brown

FIRST USED ON OPTION/MODEL	QTY.	DESCRIPTION	PART NO.	ITEM NO.
PDPIØ		PARTS LIST		

UNLESS OTHERWISE SPECIFIED

UNLESS OTHERWISE SPECIFIED
DIMENSION IN INCHES
TOLERANCES
DECIMALS ± .005 FRACTIONS ± 1/64 ANGLES ± 0°30'
FINAL SURFACE QUALITY ✓
REMOVE BURRS AND BREAK SHARP CORNERS

MATERIAL

FINISH

DRN. W. Stephenson	DATE 1/23/69	digital EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS
CHK'D T. Longo	DATE 11 NOV 69	
ENG. John C. Brown	DATE 11 NOV 69	
PROJ. ENG. John C. Brown	DATE 11 NOV 69	
PROD. [illegible]	DATE 11-14-69	
NEXT HIGHER ASSY A-ML-RPIØ-Ø		
SCALE		
SHEET 2 OF 2		

TITLE: CHANNEL CONTROL

SIZE	CODE	NUMBER	REV.
D	BS	RPIØ-Ø-CC	

DIST.

REVISIONS | REV. | CHANGE NO. | CHK.

DEC FORM NO. DRD 102A

IBC SET DAR ETC A H
IBC INITIAL CLEAR A H
IOBD Ø6 H
IOBD Ø7 H
IOBD Ø8 H
IOBD Ø9 H
IOBD 1Ø H
IOBD 11 H
IOBD 12 H
IOBD 13 H
Ø S2Ø3 I C25 DAR CYL 128
Ø S2Ø3 I C25 DAR CYL 64
Ø S2Ø3 I C25 DAR CYL 32
Ø S2Ø3 I C26 DAR CYL 16
Ø S2Ø3 I C26 DAR CYL Ø8
Ø S2Ø3 I C26 DAR CYL Ø4
Ø S2Ø3 I C27 DAR CYL Ø2
Ø S2Ø3 I C27 DAR CYL Ø1
B163 A11
B163 A12
B163 A13
∿ SWP CYL 128 L
∿ SWP CYL 64 L
∿ SWP CYL 32 L
∿ SWP CYL 16 L
∿ SWP CYL Ø8 L
∿ SWP CYL Ø4 L
∿ SWP CYL Ø2 L
∿ SWP CYL Ø1 L
IBC LOCAL (I) L
SWP CYL 128 H
SWP CYL 64 H
SWP CYL 32 L
SWP CYL 16 H
SWP CYL Ø8 H
SWP CYL Ø4 H
SWP CYL Ø2 H
SWP CYL Ø1 H
B165 AØ6
B165 AØ5
SWP SURF ØØ L
SWP SURF Ø1 L
SWP SURF Ø2 L
SWP SURF Ø3 L
SWP SURF Ø4 L
B163 A16
IBC LOCAL (I) L
IBC SET DAR ETC B H
IBC INITIAL CLEAR B H
IOBD 14 H
IOBD 15 H
IOBD 16 H
IOBD 17 H
IOBD 18 H
Ø S2Ø3 I C27 DAR SURF ØØ
Ø S2Ø3 I C28 DAR SURF Ø1
Ø S2Ø3 I C28 DAR SURF Ø2
Ø S2Ø3 I C28 DAR SURF Ø3
Ø S2Ø3 I D26 DAR SURF Ø4
IBC INITIAL CLEAR C H
S2Ø3 D26 DAR CYL 256
IOBD 19 H
SWP CYL 256 L
IBC LOCAL (I) L
B163 A16
B133 E15
B165 BØ6
∿ SWP CYL 256 H
SWP SEC ØØ L
SWP SEC Ø1 L
SWP SEC Ø2 L
SWP SEC Ø3 L
SWP SEC Ø4 L
B163 A17
IBC LOCAL (I) L
IBC SET DAR ETC B H
IBC INITIAL CLEAR C H
IOBD 19 H
IOBD 20 H
IOBD 21 H
IOBD 22 H
IOBD 23 H
Ø S2Ø3 I D26 DAR SEC ØØ
Ø S2Ø3 I D26 DAR SEC Ø1
Ø S2Ø3 I D27 DAR SEC Ø2
Ø S2Ø3 I D27 DAR SEC Ø3
Ø S2Ø3 I D27 DAR SEC Ø4

FOR INFORMATION ONLY
DO NOT BUILD TO THIS DRAWING

REVISIONS				
CHK	CHANGE NO.	REV		
	RPIØ-ØØØ17	A	Tom Swift 5-28-70	
			IVES	6/1/70
	RPIØ-ØØØ32	B		
			WALSH	6/15/70

FIRST USED ON OPTION/MODEL	PDPIØ		
UNLESS OTHERWISE SPECIFIED			
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES TOLERANCES DECIMALS ± .005 FRACTIONS ± 1/64 ANGLES ± 0°30' FINAL SURFACE QUALITY REMOVE BURRS AND BREAK SHARP CORNERS	DRN. W. Stephenson	DATE 12/3/69	digital EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS
	CHK'D. J. Scruggs	DATE 11 NOV 69	TITLE DATA ADDRESS REGISTER
	ENG. John C. Ives Jr.	DATE 11 NOV 69	
	PROJ. ENG. John C. Ives Jr.	DATE 11 NOV 69	
	PROD. W. M. Walsh	DATE 11-16-69	
MATERIAL	NEXT HIGHER ASSY A-ML-RPIØ-Ø		SIZE D CODE BS NUMBER RPIØ-Ø-DAR REV. B
FINISH	SCALE		
	SHEET 1 OF 2		DIST.

DEC FORM NO. DRD 102A

DTC BUS LINE STROBE L
DTC SET CYL (1) L
DAR CYL 128(1)L
B135 H22
DTC SET WRITE H
B133 L23
DTC SET READ H
DTC BUS LINE STROBE L
DTC SET CYL (1) L
DAR CYL 64(1) L
B135 H22
DTC BUS LINE STROBE H
B134 K21
DTC SEEK START (1) L
B165 J22
B165 J22
DAR CYL 32(1)L
DTC SET CYL (1) L
B163 F29
DTC BUS LINE STROBE H
B134 K21
DAR CYL 16(1) L
B163 F29
DTC CLEAR HD REG (1)L
B165 J22
DTC SET HD REG(1)L
DAR SURF 0 (1)L
B133 J21
DTC SET ERASE H
B133 L23
DTC BUS LINE STROBE L
DTC SET CYL (1) L
DAR CYL 8(1)L
B135 H22
DTC BUS LINE STROBE L
DTC SET HD REG(1)L
DAR SURF 1 (1)L
B135 H23
DTC SET READ L
DTC SET ERASE L
B134 K21
B135 H22
B133 L23
DTC BUS LINE STROBE L
DTC SET HD REG(1)L
DAR SURF 2 (1)L
B135 H23
DTC BUS LINE STROBE L
DTC SET CYL (1) L
DAR CYL 4(1) L
B135 H23
DTC RESTORE L
B165 J22
DAR SURF 3(1) L
DTC SET HD REG(1)L
B163 F29
B135 H23
B134 K21
DTC BUS LINE STROBE H
DAR SURF 4 (1)L
B163 F29
DTC BUS LINE STROBE H
B134 K22
DAR CYL 2(1)L
DTC SET CYL (1)L
B163 F29
DAR CYL 1 (1)L
B163 F29
DTC SET HD ADV(1) L
B165 J22
B165 M23
B165 M23
B165 M23
B165 M23
B165 M24
B165 M24
W692 N14
W692 N15
W692 N20
B135 N26
DTC BUS LINE STROBE L
DTC SET CYL(1) L
DAR CYL 256(1) L
B135 N26
W852 P21
DSBC UB 0 L [1]
DSBC UB 1 L [3]
DSBC UB 2 L [4]
DSBC UB 3 L [7]
DSBC UB 4 L [8]
DSBC UB 5 L [11]
DSBC UB 6 L [12]
DSBC UB 7 L [14]
W852 P22
DSBC BUS 256 L [15]

FOR INFORMATION ONLY
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REVISIONS	REV.	CHK	DRN
CHANGE NO. RP10-00008	A		T. Quillen 2-13-70
GREEN			D. C. Ives 2/16/70
RP10-00022	B.	FV	T. Quillen 6/2/70
IVES			D. C. Ives 6/24/70

FIRST USED ON OPTION/MODEL PDP10	QTY.	DESCRIPTION	PART NO. / ITEM NO.
		PARTS LIST	

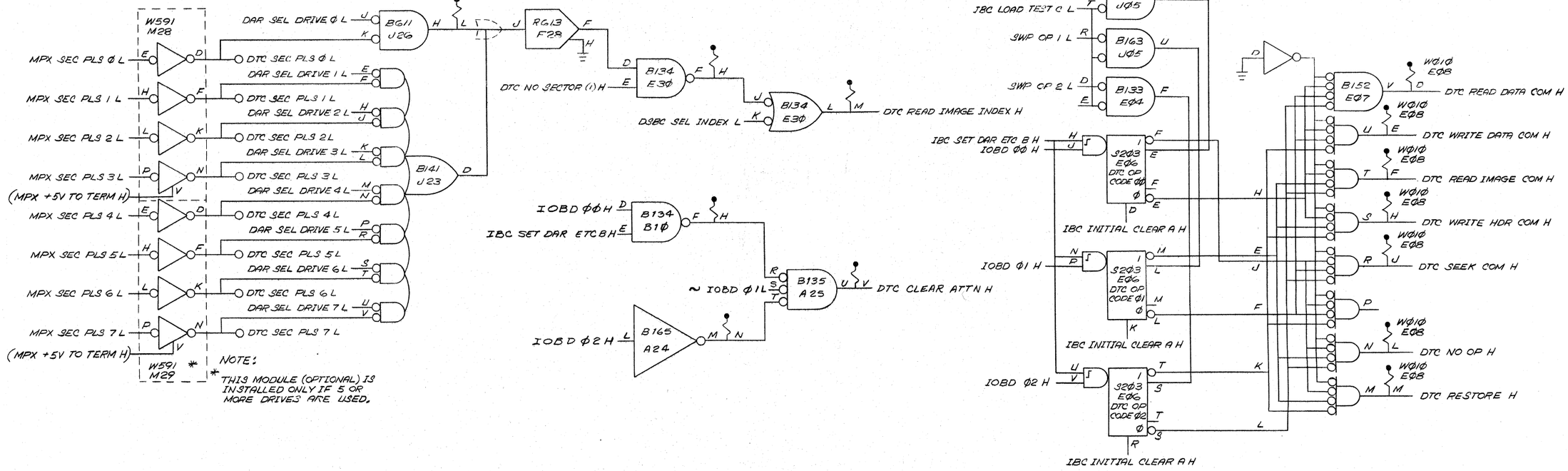
UNLESS OTHERWISE SPECIFIED
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES
TOLERANCES
DECIMALS ± .005 FRACTIONS ± 1/64 ANGLES ± 0°30'
FINAL SURFACE QUALITY √
REMOVE BURRS AND BREAK SHARP CORNERS
MATERIAL
FINISH

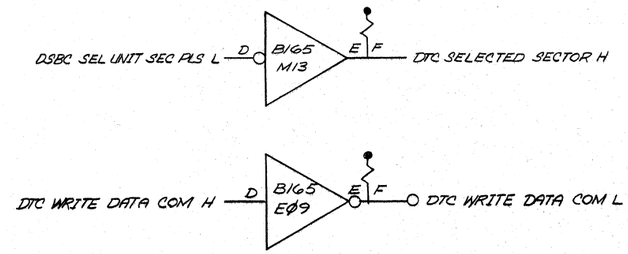
DRN. W. Stephenson	DATE 12/19/68	digital EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS
CHK'D T. Quillen	DATE 11 NOV 69	
ENG. John C. Green Jr.	DATE 11 NOV 69	
PROJ. ENG. John C. Green Jr.	DATE 11 NOV 69	
PROD. A. McMahon	DATE 11-16-69	
NEXT HIGHER ASSY A-ML-RP10-0		
SCALE		
SHEET 1 OF 2		

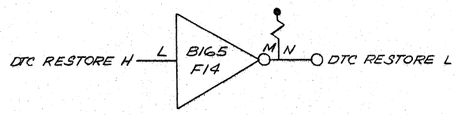
TITLE
DISK SIGNAL BUS CONNECTORS

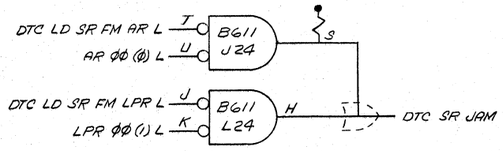
SIZE	CODE	NUMBER	REV.
D	BS	RP10-0-DSBC	B
DIST.			

DEC FORM NO. DRD 102A









FOR INFORMATION ONLY
DO NOT BUILD TO THIS DRAWING

REVISIONS			
CHK	CHANGE NO.		REV.
	RPIO-00006		A
	GREEN		
	RPIO-00008		B
	GREEN		
FV	RPIO-00009		C
	GREEN		
FV	RPIO-00013		D
	RPIO-00017		E
FV	RPIO-00327		F
	RPIO-000[illegible]		H
	RPIO-00029		J
	RPIO-00031		K
	RPIO-00033		L
	RPIO-00035		M

FIRST USED ON OPTION/MODEL	QTY.	DESCRIPTION	PART NO.	ITEM NO.
PDP1Ø		PARTS LIST		

UNLESS OTHERWISE SPECIFIED

UNLESS OTHERWISE SPECIFIED
DIMENSION IN INCHES
TOLERANCES
DECIMALS ± .005 FRACTIONS ± 1/64 ANGLES ± 0°30'
FINAL SURFACE QUALITY ✓
REMOVE BURRS AND BREAK SHARP CORNERS

MATERIAL

FINISH

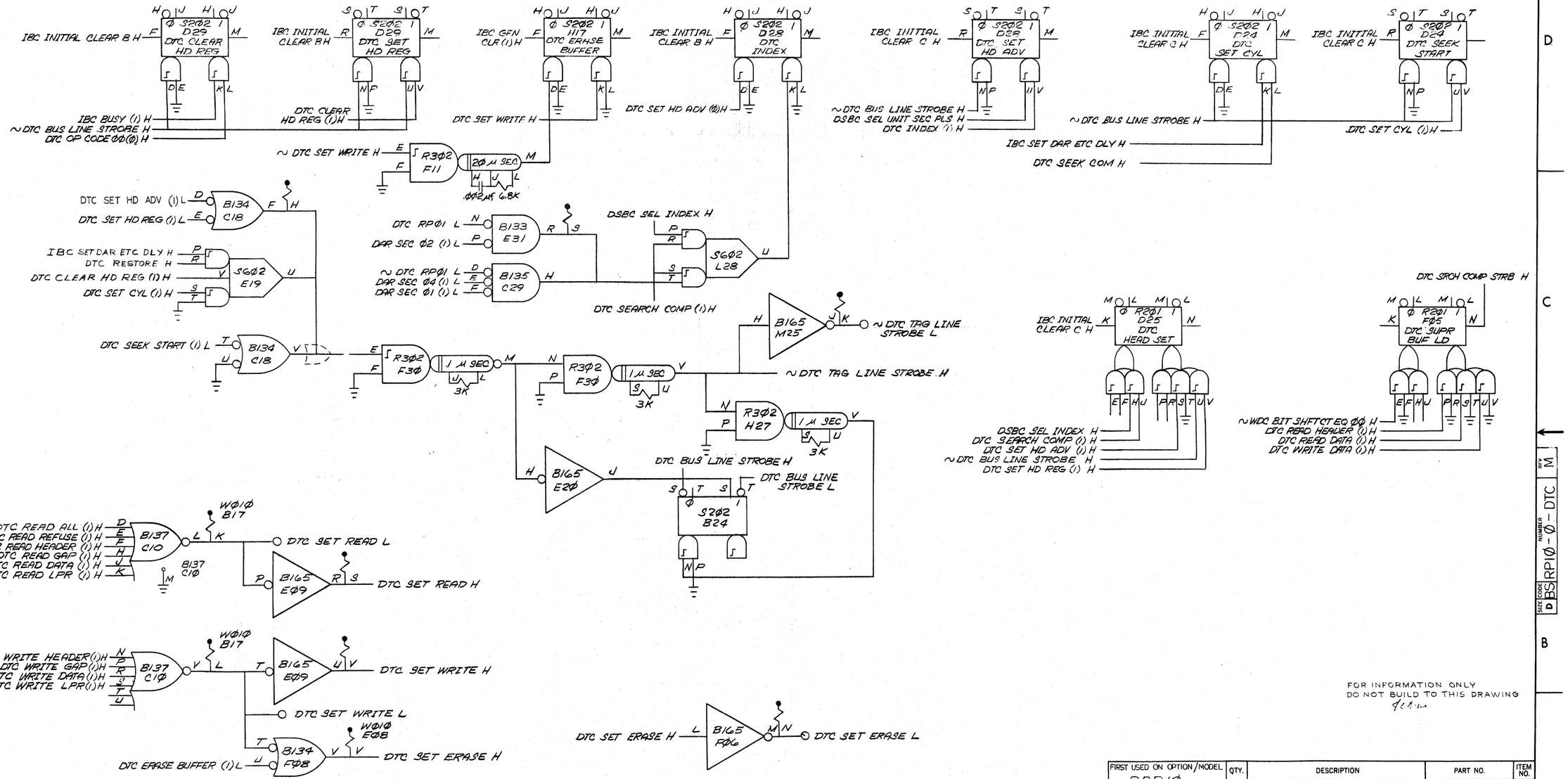
DRN. W. Stephenson DATE 9/26/69
CHK'D. [signature] DATE 11/10/69
ENG. [signature] DATE 11/10/69
PROJ. ENG. [signature] DATE 11/10/69
PROD. [signature] DATE 11-10-69
NEXT HIGHER ASSY A-ML-RPIØ-F
SCALE
SHEET 1 OF 7

digital EQUIPMENT CORPORATION
MAYNARD, MASSACHUSETTS

TITLE
DATA TRANSFER CONTROL

SIZE D | CODE BS | NUMBER RPIØ - Ø - DTC | REV. M
DIST.

DEC FORM NO. DRD 102A



FOR INFORMATION ONLY
DO NOT BUILD TO THIS DRAWING

REVISIONS	CHANGE NO.	CHK	REV.

FIRST USED ON OPTION/MODEL	QTY.	DESCRIPTION	PART NO.	ITEM NO.
PDP1Ø		PARTS LIST		

UNLESS OTHERWISE SPECIFIED
UNLESS OTHERWISE SPECIFIED
DIMENSION IN INCHES
TOLERANCES
DECIMALS ± .005 FRACTIONS ± 1/64 ANGLES ± 0°30'
FINAL SURFACE QUALITY √
REMOVE BURRS AND BREAK SHARP CORNERS

MATERIAL

FINISH

DRN.	DATE	6/30/69
CHK'D.	DATE	11NOV69
ENG.	DATE	11NOV69
PROJ. ENG.	DATE	11NOV69
PROD.	DATE	11-16-69

NEXT HIGHER ASSY: A-ML-RPIØ-Ø

SCALE

SHEET 3 OF 7

digital EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS

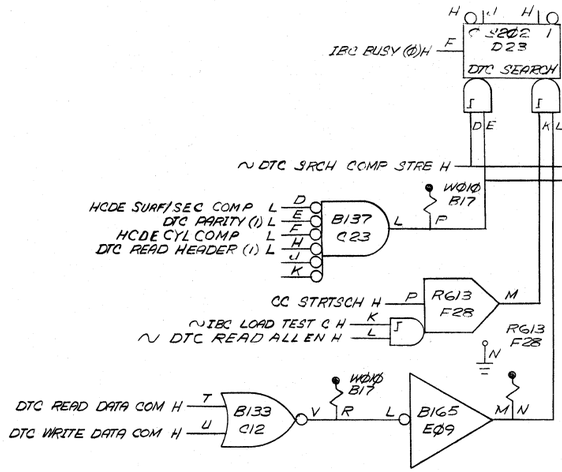
TITLE: DATA TRANSFER CONTROL

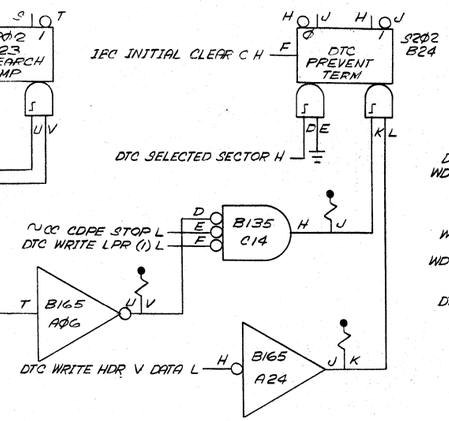
SIZE	CODE	NUMBER	REV
D	BS	RPIØ-Ø-DTC	M

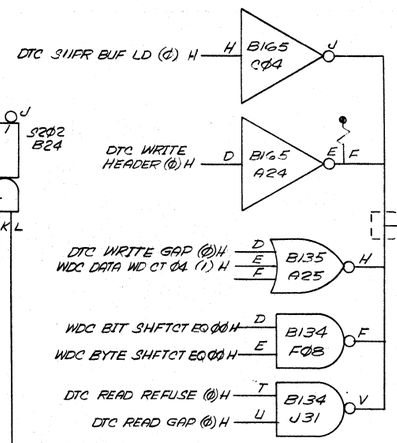
DIST.

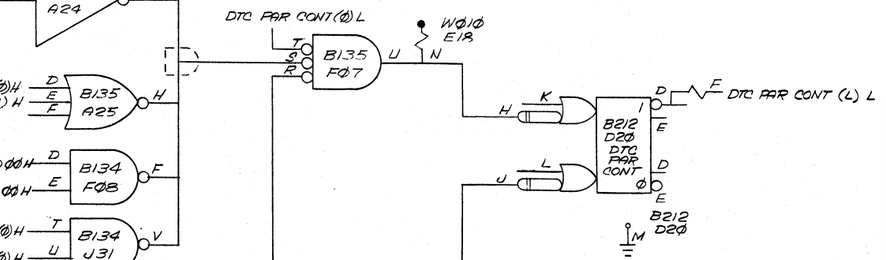
DEC FORM NO. DRD 102A

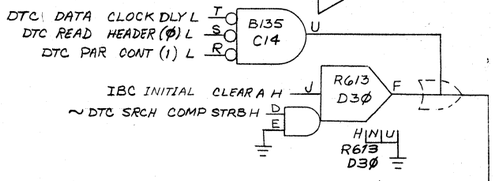
SIZE D BS NUMBER RPIØ-Ø-DTC REV M

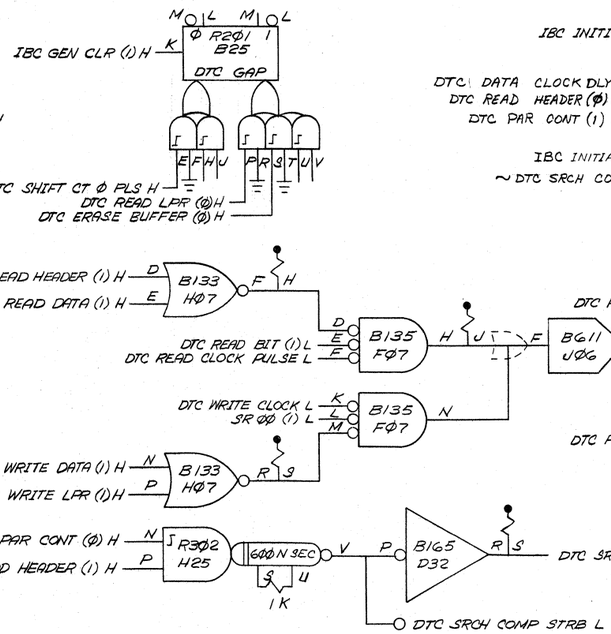


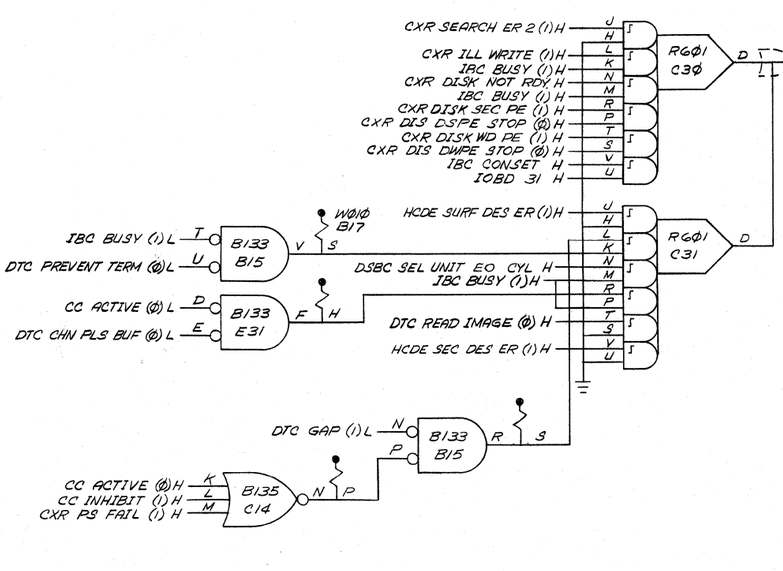












FOR INFORMATION ONLY
DO NOT BUILD TO THIS DRAWING

REVISIONS	CHANGE NO.	CHK	REV.

FIRST USED ON OPTION/MODEL	QTY.	DESCRIPTION	PART NO.	ITEM NO.
PDP1Ø		PARTS LIST		

UNLESS OTHERWISE SPECIFIED

UNLESS OTHERWISE SPECIFIED
DIMENSION IN INCHES
TOLERANCES
DECIMALS ± .005 FRACTIONS ± 1/64 ANGLES ± 0°30'
FINAL SURFACE QUALITY √
REMOVE BURRS AND BREAK SHARP CORNERS

MATERIAL

FINISH

DRN. W. Stephenson	DATE 7/10/69	digital EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS
CHK'D. J. Longo	DATE 11NOV69	
ENG. John C. Hall	DATE 11NOV69	
PROJ. ENG. John C. Hall	DATE 11NOV69	
PROD. J. McAllister	DATE 11-16-69	

NEXT HIGHER ASSY
A9-ML-RPIØ-Ø

SCALE

SHEET 5 OF 7

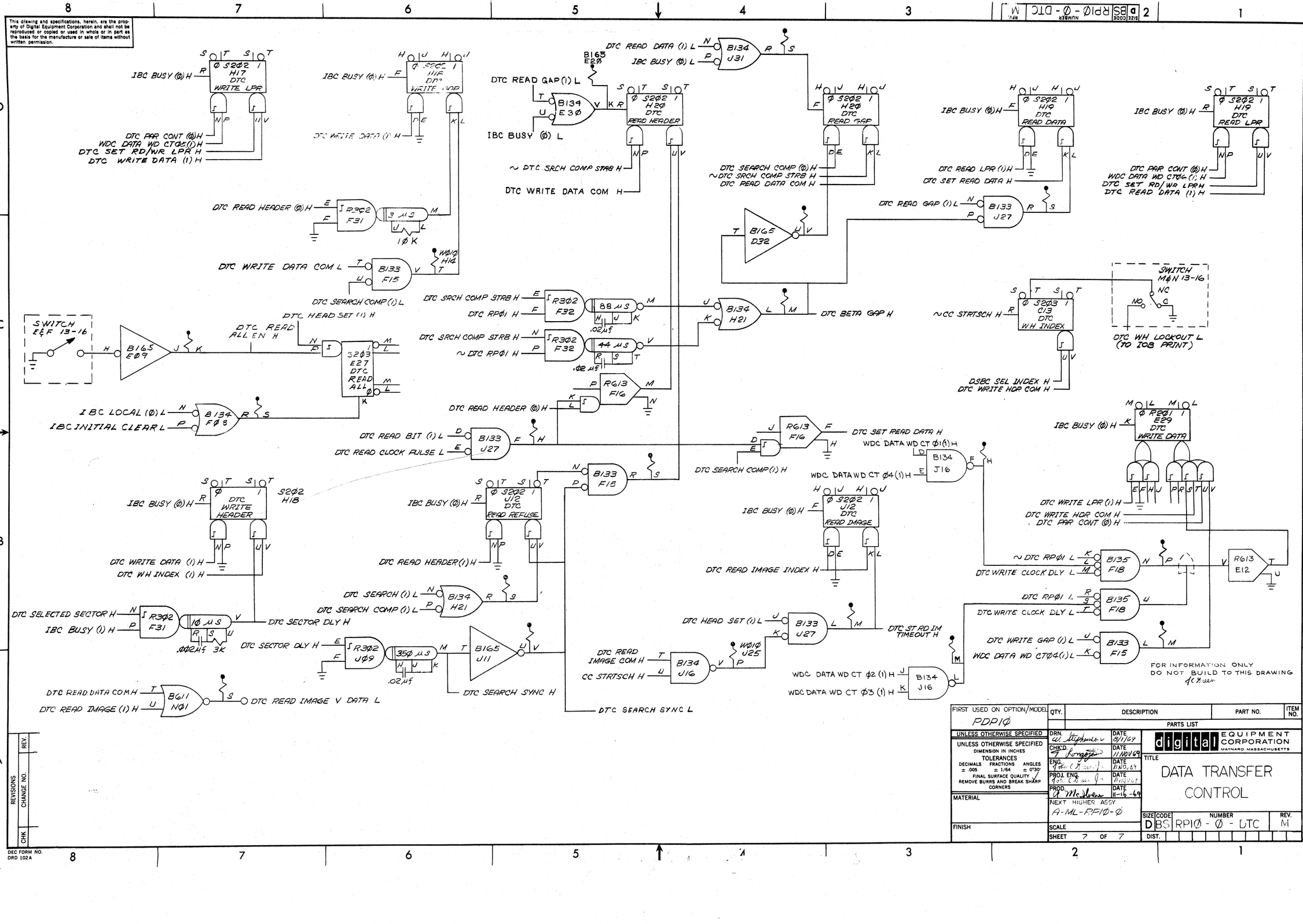
TITLE
DATA TRANSFER CONTROL

SIZE	CODE	NUMBER	REV
D	BS	RPIØ-Ø - DTC	M

DIST.

DEC FORM NO
DRD 102A

SIZE D CODE BS NUMBER RPIØ-Ø - DTC REV M



REVISIONS	REV.	CHANGE NO.	CHK

FIRST USED ON OPTION/MODEL	QTY.	DESCRIPTION	PART NO.	ITEM NO.
PDPIØ		PARTS LIST		

UNLESS OTHERWISE SPECIFIED

UNLESS OTHERWISE SPECIFIED
DIMENSION IN INCHES
TOLERANCES
DECIMALS ± .005 FRACTIONS ± 1/64 ANGLES ± 0°30'
FINAL SURFACE QUALITY
REMOVE BURRS AND BREAK SHARP CORNERS

MATERIAL

FINISH

DRN.	W. Stephenson	DATE 8/11/69
CHK'D.	T. Forgette	DATE 11/10/69
ENG.	[illegible]	DATE 11/10/69
PROJ. ENG.	[illegible]	DATE 11/10/69
PROD.	A. McAleer	DATE 11-16-69

NEXT HIGHER ASSY
A-ML-RPIØ-Ø

SCALE

SHEET 7 OF 7

digital EQUIPMENT CORPORATION
MAYNARD, MASSACHUSETTS

TITLE
DATA TRANSFER CONTROL

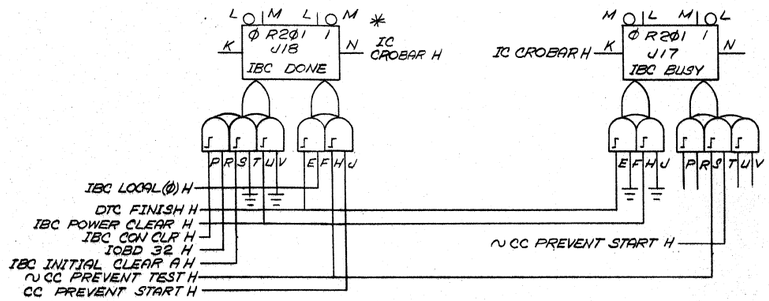
SIZE	CODE	NUMBER	REV.
D	BS	RPIØ - Ø - DTC	M

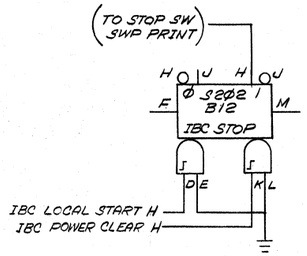
DIST.

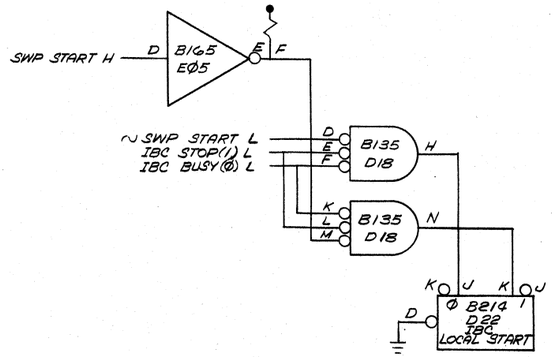
DEC FORM NO. DRD 102A

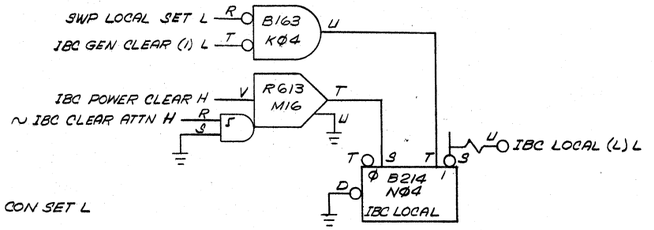
SIZE D | CODE BS | NUMBER RPIØ - Ø - DTC | REV. M

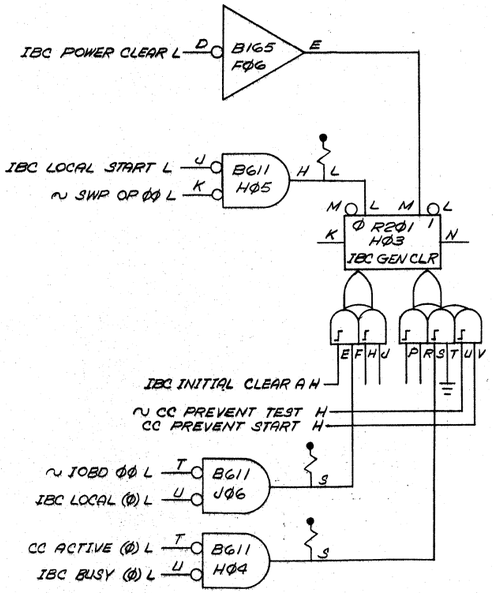
NOTE:
* = THE STATES OF THIS FLIP-FLOP ARE REDEFINED.

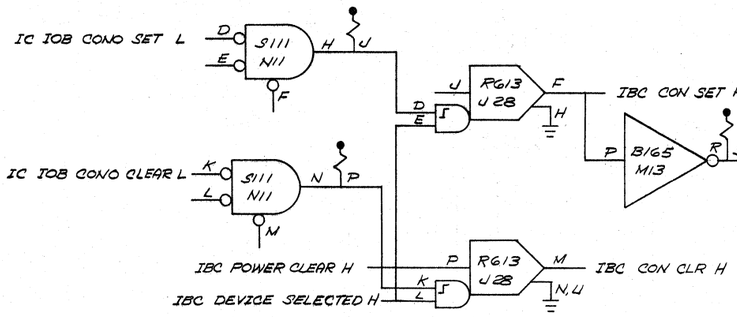


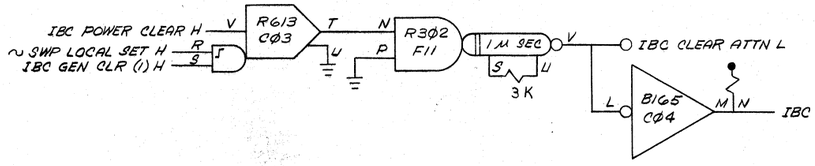


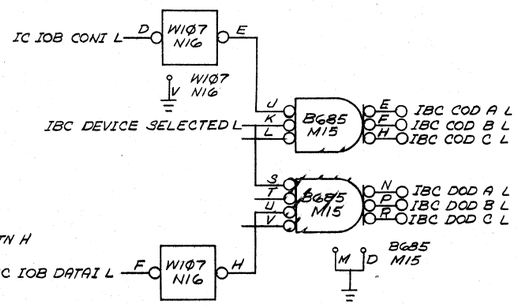


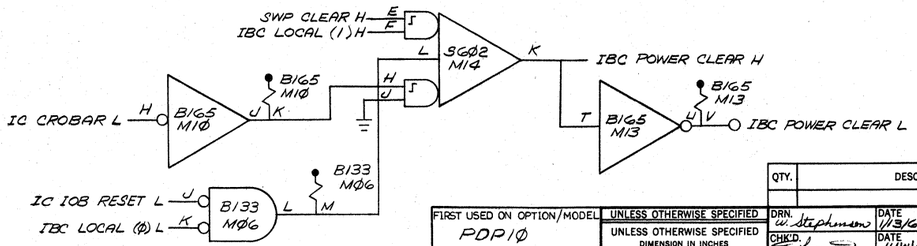












FOR INFORMATION ONLY
DO NOT BUILD TO THIS DRAWING
JC Green

QTY.	DESCRIPTION	PART NO.	ITEM NO.

PARTS LIST

REVISIONS	CHANGE NO.	CHK
REV.		

FIRST USED ON OPTION/MODEL: PDP1Ø

UNLESS OTHERWISE SPECIFIED
UNLESS OTHERWISE SPECIFIED
DIMENSION IN INCHES
TOLERANCES
DECIMALS ± .005 FRACTIONS ± 1/64 ANGLES ± 0°30'
FINAL SURFACE QUALITY
REMOVE BURRS AND BREAK SHARP CORNERS
MATERIAL
FINISH

DRN.	W. Stephenson	DATE 11/3/69
CHK'D.	T. Sorgato	DATE 11/Nov/69
ENG.	John C. Green Jr.	DATE 11Nov69
PROJ. ENG.	John C. Green Jr.	DATE 11Nov69
PROD.	R. McMahon	DATE 11-16-69

NEXT HIGHER ASSY
A-ML-RPIØ-Ø
SCALE
SHEET 1 OF 2

digital EQUIPMENT CORPORATION
MAYNARD, MASSACHUSETTS

TITLE
I/O BUS CONTROL

SIZE D | CODE BS | NUMBER RPIØ-Ø-IBC | REV
DIST.

DEC FORM NO. DRD 102A

- IC CROBAR
IC CROBAR
SEE NOTE

NOTE : THIS SIGNAL IS DERIVED IN THE POWER CONTROL AND IS AT GROUND WHEN POWER IS OFF AND FOR APPROXIMATELY FOUR SECS. AFTER POWER UP.

CABLE 1A
W851 P13 — W851 P17
IOB Ø0 H
IOB Ø1 H
IOB Ø2 H
IOB Ø3 H
IOB Ø4 H
IOB Ø5 H
IOB Ø6 H
IOB Ø7 H
IOB Ø8 H
IN OUT

CABLE 1C
W851 P14 — W851 P18
IOB 18 H
IOB 19 H
IOB 2Ø H
IOB 21 H
IOB 22 H
IOB 23 H
IOB 24 H
IOB 25 H
IOB 26 H
IN OUT

CABLE 2A
W851 P15 — W851 P19
IC IOB RESET L
(IOB DR SPLIT)
IC IOS Ø3 (Ø) H
IC IOS Ø3 (1) H
IC IOS Ø4 (Ø) H
IC IOS Ø4 (1) H
IC IOS Ø5 (Ø) H
IN OUT

CABLE 2C
W851 P16 — W851 P2Ø
IC IOB DATAO CLEAR L
IC IOB DATAO SET L
IC IOB CONO CLEAR L
IC IOB CONO SET L
IC IOB DATAI L
IC IOB CONI L
(IOB RDI PULSE)
(IOB RDI DATA)
IN OUT

CABLE 1B
W851 R13 — W851 R17
IOB Ø9 H
IOB 1Ø H
IOB 11 H
IOB 12 H
IOB 13 H
IOB 14 H
IOB 15 H
IOB 16 H
IOB 17 H
IN OUT

CABLE 1D
W851 R14 — W851 R18
IOB 27 H
IOB 28 H
IOB 29 H
IOB 3Ø H
IOB 31 H
IOB 32 H
IOB 33 H
IOB 34 H
IOB 35 H
IN OUT

CABLE 2B
W851 R15 — W851 R19
IC IOS Ø5 (1) H
IC IOS Ø6 (Ø) H
IC IOS Ø6 (1) H
IC IOS Ø7 (Ø) H
IC IOS Ø7 (1) H
IC IOS Ø8 (Ø) H
IC IOS Ø8 (1) H
IC IOS Ø9 (Ø) H
IC IOS Ø9 (1) H
IN OUT

CABLE 2D
W851 R16 — W851 R2Ø
CXR IOB PI1 H
CXR IOB PI2 H
CXR IOB PI3 H
CXR IOB PI4 H
CXR IOB PI5 H
CXR IOB PI6 H
CXR IOB PI7 H
IN OUT

FOR INFORMATION ONLY
DO NOT BUILD TO THIS DRAWING

REVISIONS	REV.	CHANGE NO.	CHK

DEC FORM NO. DRD 102A

FIRST USED ON OPTION/MODEL	QTY.	DESCRIPTION	PART NO.	ITEM NO.
PDPIØ		PARTS LIST		

UNLESS OTHERWISE SPECIFIED
UNLESS OTHERWISE SPECIFIED
DIMENSION IN INCHES
TOLERANCES
DECIMALS ± .005 FRACTIONS ± 1/64 ANGLES ± 0°30'
FINAL SURFACE QUALITY √
REMOVE BURRS AND BREAK SHARP CORNERS
MATERIAL
FINISH

DRN. W. Stephenson DATE 11/3/69
CHK'D. J. Longo DATE 11NOV69
ENG. Tom C. Kelly DATE 11NOV69
PROJ. ENG. Tom C. Kelly DATE 11NOV69
PROD. G. McLaine DATE 11-16-69
NEXT HIGHER ASSY A-ML-RPIØ-Ø
SCALE
SHEET 1 OF 2

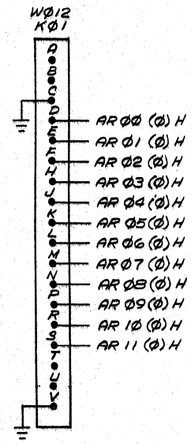
digital EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS

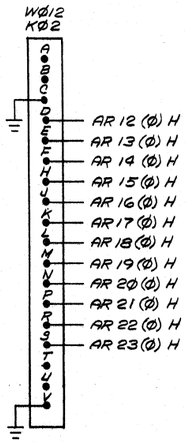
TITLE
INTERCONNECTING CABLES

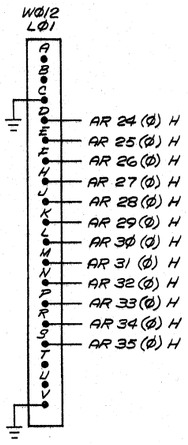
SIZE	CODE	NUMBER	REV.
D	IC	RPIØ-Ø-IC	-

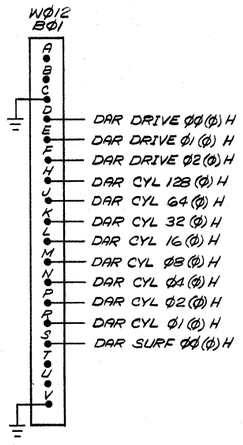
DIST.

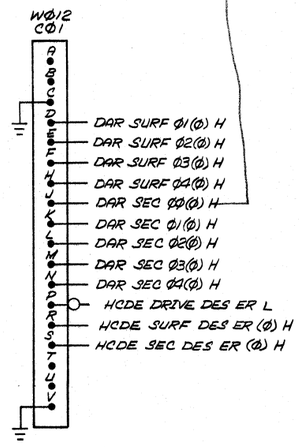
CYL 256

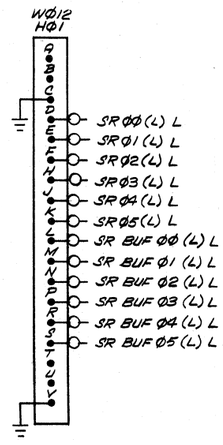


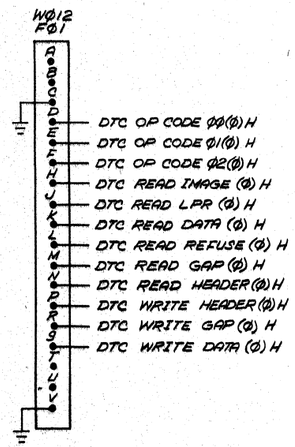


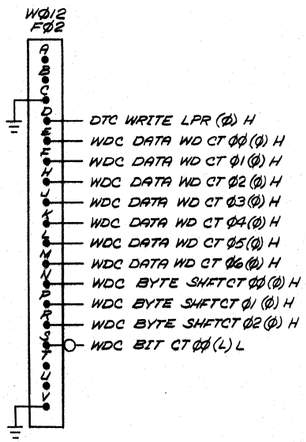


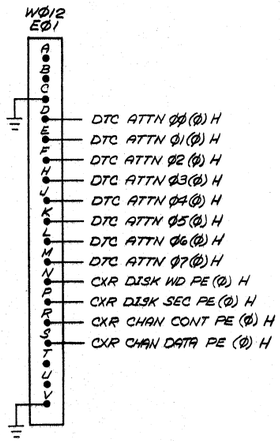


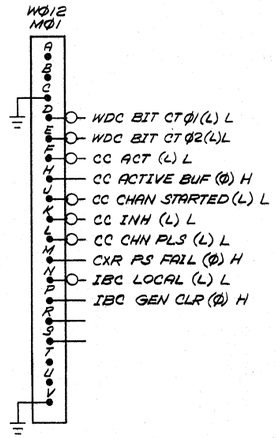


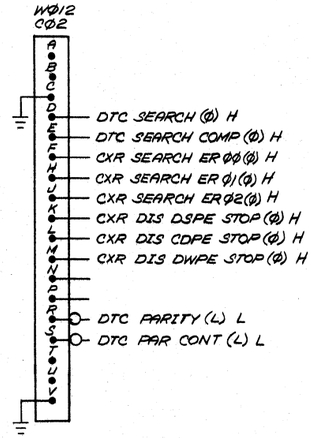


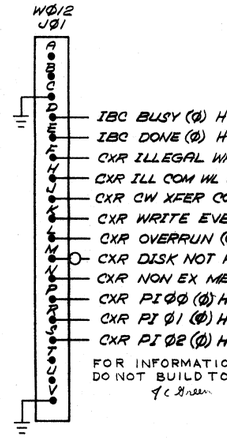












FOR INFORMATION ONLY
DO NOT BUILD TO THIS DRAWING
T.C. Green

FIRST USED ON OPTION/MODEL	QTY.	DESCRIPTION	PART NO.	ITEM NO.
PDP1Ø		PARTS LIST		

UNLESS OTHERWISE SPECIFIED

UNLESS OTHERWISE SPECIFIED
DIMENSION IN INCHES
TOLERANCES
DECIMALS ± .005 | FRACTIONS ± 1/64 | ANGLES ± 0°30'
FINAL SURFACE QUALITY √
REMOVE BURRS AND BREAK SHARP CORNERS

MATERIAL

FINISH

DRN. W. Stephenson	DATE 2/9/69	digital EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS
CHK'D. T. Longajo	DATE 11NOV69	
ENG. T. C. Green	DATE 11NOV69	TITLE: INDICATORS
PROJ. ENG. T. C. Green	DATE 11NOV69	
PROD. D. McAllister	DATE 11-16-69	
NEXT HIGHER ASSY: A-ML-RPIØ-Ø		SIZE D | CODE IC | NUMBER RPIØ-Ø-INDC | REV.
SCALE		
SHEET / OF /		DIST.

REVISIONS | REV. | CHANGE NO. | CHK

DEC FORM NO. ORD 102A

IOB ØØ H — D — W1Ø7 K16 — E — IOBD ØØ H

IOB Ø6 H — D — W1Ø7 K17 — E — IOBD Ø6 H

IOB 12 H — D — W1Ø7 K18 — E — IOBD 12 H

IOB 18 H — D — W1Ø7 L16 — E — IOBD 18 H

IOB 24 H — D — W1Ø7 L17 — E — IOBD 24 H

IOB 3Ø H — D — W1Ø7 L18 — E — IOBD 3Ø H

IOB Ø1 H — F — W1Ø7 K16 — H — IOBD Ø1 H

IOB Ø7 H — F — W1Ø7 K17 — H — IOBD Ø7 H

IOB 13 H — F — W1Ø7 K18 — H — IOBD 13 H

IOB 19 H — F — W1Ø7 L16 — H — IOBD 19 H

IOB 25 H — F — W1Ø7 L17 — H — IOBD 25 H

IOB 31 H — F — W1Ø7 L18 — H — IOBD 31 H

IOB Ø2 H — J — W1Ø7 K16 — K — IOBD Ø2 H

IOB Ø8 H — J — W1Ø7 K17 — K — IOBD Ø8 H

IOB 14 H — J — W1Ø7 K18 — K — IOBD 14 H

IOB 2Ø H — J — W1Ø7 L16 — K — IOBD 2Ø H

IOB 26 H — J — W1Ø7 L17 — K — IOBD 26 H

IOB 32 H — J — W1Ø7 L18 — K — IOBD 32 H

IOB Ø3 H — L — W1Ø7 K16 — M — IOBD Ø3 H

IOB Ø9 H — L — W1Ø7 K17 — M — IOBD Ø9 H

IOB 15 H — L — W1Ø7 K18 — M — IOBD 15 H

IOB 21 H — L — W1Ø7 L16 — M — IOBD 21 H

IOB 27 H — L — W1Ø7 L17 — M — IOBD 27 H

IOB 33 H — L — W1Ø7 L18 — M — IOBD 33 H

IOB Ø4 H — N — W1Ø7 K16 — P — IOBD Ø4 H

IOB 1Ø H — N — W1Ø7 K17 — P — IOBD 1Ø H

IOB 16 H — N — W1Ø7 K18 — P — IOBD 16 H

IOB 22 H — N — W1Ø7 L16 — P — IOBD 22 H

IOB 28 H — N — W1Ø7 L17 — P — IOBD 28 H

IOB 34 H — N — W1Ø7 L18 — P — IOBD 34 H

IOB Ø5 H — R — W1Ø7 K16 — S — IOBD Ø5 H

IOB 11 H — R — W1Ø7 K17 — S — IOBD 11 H

IOB 17 H — R — W1Ø7 K18 — S — IOBD 17 H

IOB 23 H — R — W1Ø7 L16 — S — IOBD 23 H

IOB 29 H — R — W1Ø7 L17 — S — IOBD 29 H

IOB 35 H — R — W1Ø7 L18 — S — IOBD 35 H

W1Ø7 K16 (V)

IOBD 27 H — D — B165 JØ2 — E F — IOBD 27 L

IOBD 28 H — H — B165 JØ2 — U K — IOBD 28 L

W1Ø7 K17 (V)

IOBD 29 H — L — B165 JØ2 — M N — IOBD 29 L

IOBD 3Ø H — P — B165 JØ2 — R S — IOBD 3Ø L

W1Ø7 K18 (V)

W1Ø7 L16 (V)

IOBD 31 H — T — B165 JØ2 — U V — IOBD 31 L

IOBD 32 H — P — B165 FØ6 — R S — IOBD 32 L

W1Ø7 L17 (V)

IOBD 33 H — T — B165 FØ6 — U V — IOBD 33 L

IOBD 34 H — D — B165 M1Ø — E F — IOBD 34 L

W1Ø7 L18 (V)

FOR INFORMATION ONLY
DO NOT BUILD TO THIS
9C8xxx DRAWING

QTY.	DESCRIPTION	PART NO.	ITEM NO.
	PARTS LIST		

FIRST USED ON OPTION/MODEL: PDPIØ

UNLESS OTHERWISE SPECIFIED
UNLESS OTHERWISE SPECIFIED
DIMENSION IN INCHES
TOLERANCES
DECIMALS ± .005 FRACTIONS ± 1/64 ANGLES ± 0°30'
FINAL SURFACE QUALITY
REMOVE BURRS AND BREAK SHARP CORNERS

MATERIAL

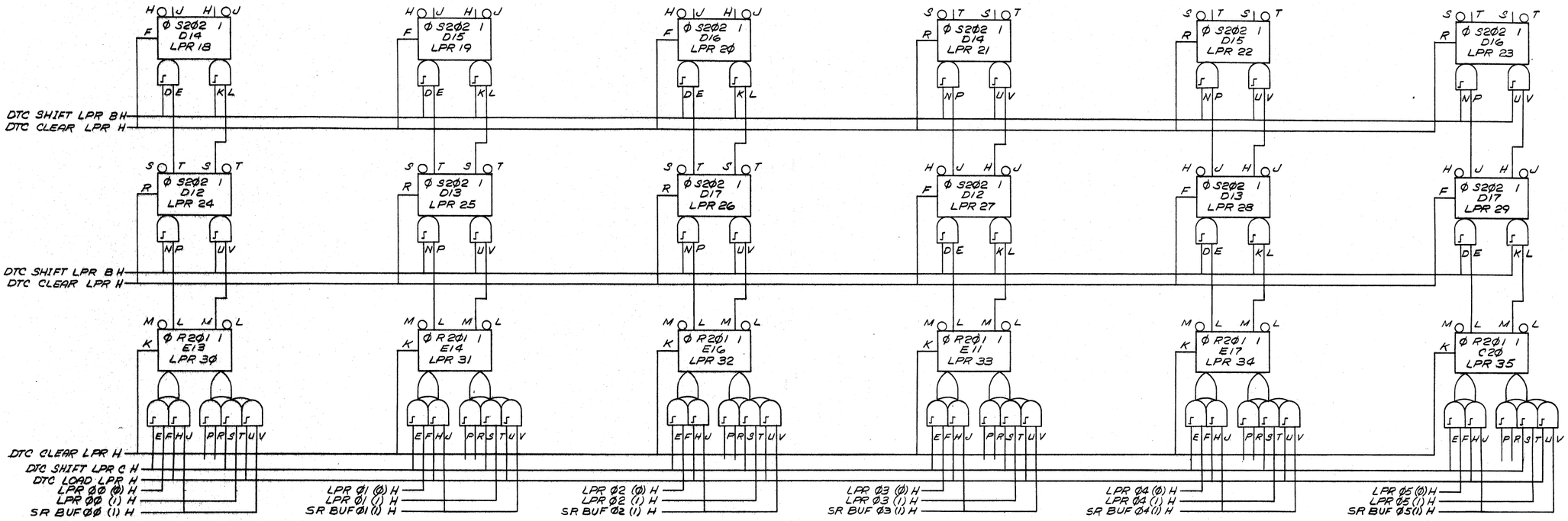
FINISH

DRN. W. Stephenson	DATE 10/26/69	digital EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS
CHK'D. J. Longo	DATE 11NOV69	
ENG. John C. Greenly Jr	DATE 11NOV69	TITLE: IOB DATA RECEIVER
PROJ. ENG. John C. Greenly Jr	DATE 11NOV69	
PROD. W. M. Slater	DATE 11-6-69	
NEXT HIGHER ASSY: A-ML-RPIØ-Ø		SIZE D | CODE BS | NUMBER RPIØ-Ø-IOBD | REV.
SCALE		DIST.
SHEET / OF /		

REVISIONS | REV. | CHANGE NO. | CHK

DEC FORM NO. DRD 102A

SIZE D | CODE BS | NUMBER RPIØ-Ø-IOBD | REV.



FOR INFORMATION ONLY
DO NOT BUILD TO THIS DRAWING
J C Green

REVISIONS	CHANGE NO.	REV
CHK		

DEC FORM NO. DRD 102A

FIRST USED ON OPTION/MODEL	QTY.	DESCRIPTION	PART NO.	ITEM NO.
PDP1Ø		PARTS LIST		

UNLESS OTHERWISE SPECIFIED

UNLESS OTHERWISE SPECIFIED
DIMENSION IN INCHES
TOLERANCES
DECIMALS ± .005 FRACTIONS ± 1/64 ANGLES ± 0°30'
FINAL SURFACE QUALITY √
REMOVE BURRS AND BREAK SHARP CORNERS

MATERIAL

FINISH

DRN. W. Stephenson	DATE 11/12/68	digital EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS
CHK'D T Sargeo	DATE 11NOV69	
ENG. John C Green	DATE 11NOV69	TITLE LONGITUDINAL PARITY REGISTER
PROJ. ENG. John C Green	DATE 11NOV69	
PROD. J McKeen	DATE 11-14-69	

NEXT HIGHER ASSY A-ML-RP1Ø-Ø

SCALE

SHEET 2 OF 2

SIZE	CODE	NUMBER	REV.
D	BS	RP1Ø-Ø-LPR	

DIST.

SIZE D CODE BS NUMBER RP1Ø-Ø-LPR REV

RDS ERR B (Ø) H
RDS ERR A (Ø) H
G589 S23
B410 S22 VCO SLOW
DTC RPØ1 ENABLE L
G590 T23
B410 T22 VCO FAST
DTC RRØ2 ENABLE L
RDS DIVIDE 2 (1) H
RDS DIVIDE 2 (Ø) H
B136 S25
B611 S30
B611 T27
B212 S28 RDS DIVIDE Ø 2
B311 S29
75 NSEC
B611 S26
B611 T31
B133 T28
S27K
B312 S24
B133 T28
RDS CLOCK OUT H
B212 S28 RDS DATA WIND Ø
DTC SET READ L
B212 T32 RDS CLK WIND Ø
B611 T30
RDS DATA WIND (1) H
S32V
RDS EXT DATA WIND L
B135 S32
RDS DATA OUT H
RDS DATA IN L
RDS CLOCK WIND (Ø) L
RDS CLOCK OUT H
B212 T24 RDS ERR A
B212 T24 RDS ERR B
RDS ERR A (1) L
RDS ERR B (1) L
B133 T28
B611 S26
RDS DIVIDE 2 (1) H
B611 T27
RDS CLOCK DLY H
DTC RPØ2 ENABLE L
B611 S30
RDS CLOCK OUT H
B611 T30
RDS DATA IN L
B133 T28
B611 T30
B312 T26
DTC RPØ1 ENABLE L
B611 T27
B312 T25
RDS CLOCK DLY H
DTC BETA GAP H
DTC SEARCH SYNC H
B611 S15
RDS SYNC FIELD ENB L
~DTC RPØ1 ENABLE H
B136 S25
B312 T29
B611 T30
RDS DATA IN L
MPX RAW DATA H
~DTC RPØ2 ENABLE H
B136 S25
B133 T28

NOTE: * SEE SHEET 2 FOR CALIBRATION PROCEDURE

REVISIONS					
REV.	CHANGE NO.	CHK			
A	RP10-00009		REVISED & REDRAWN	T. Quinn	13 FEB 70
B	RP10-00012			J. GREEN	4.2.70
C	RP10-00013	FV		J. GREEN	4/8/70
D	RP10-00027	FV		J. GREEN	5/4/70
E	RP10-00031			IVES	11/3/71
F	RP10-00033			B. WALSH	11/3/71

FIRST USED ON OPTION/MODEL	QTY.	DESCRIPTION	PART NO.	ITEM NO.
PDP-10		PARTS LIST		

UNLESS OTHERWISE SPECIFIED
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES
TOLERANCES DECIMALS ± .005 FRACTIONS ± 1/64 ANGLES ± 0°30'
FINAL SURFACE QUALITY REMOVE BURRS AND BREAK SHARP CORNERS
MATERIAL
FINISH

DRN.	STEPHENSON	DATE 4/10/69
CHK'D.	LONGJO	DATE 11-11-69
ENG.	GREEN JR.	DATE 11-11-69
PROJ. ENG.	GREEN JR.	DATE 11-11-69
PROD.	MC ILVENE	DATE 11-16-69
NEXT HIGHER ASSY	A-ML-RPIØ-Ø	
SCALE		
SHEET	1 OF 2	

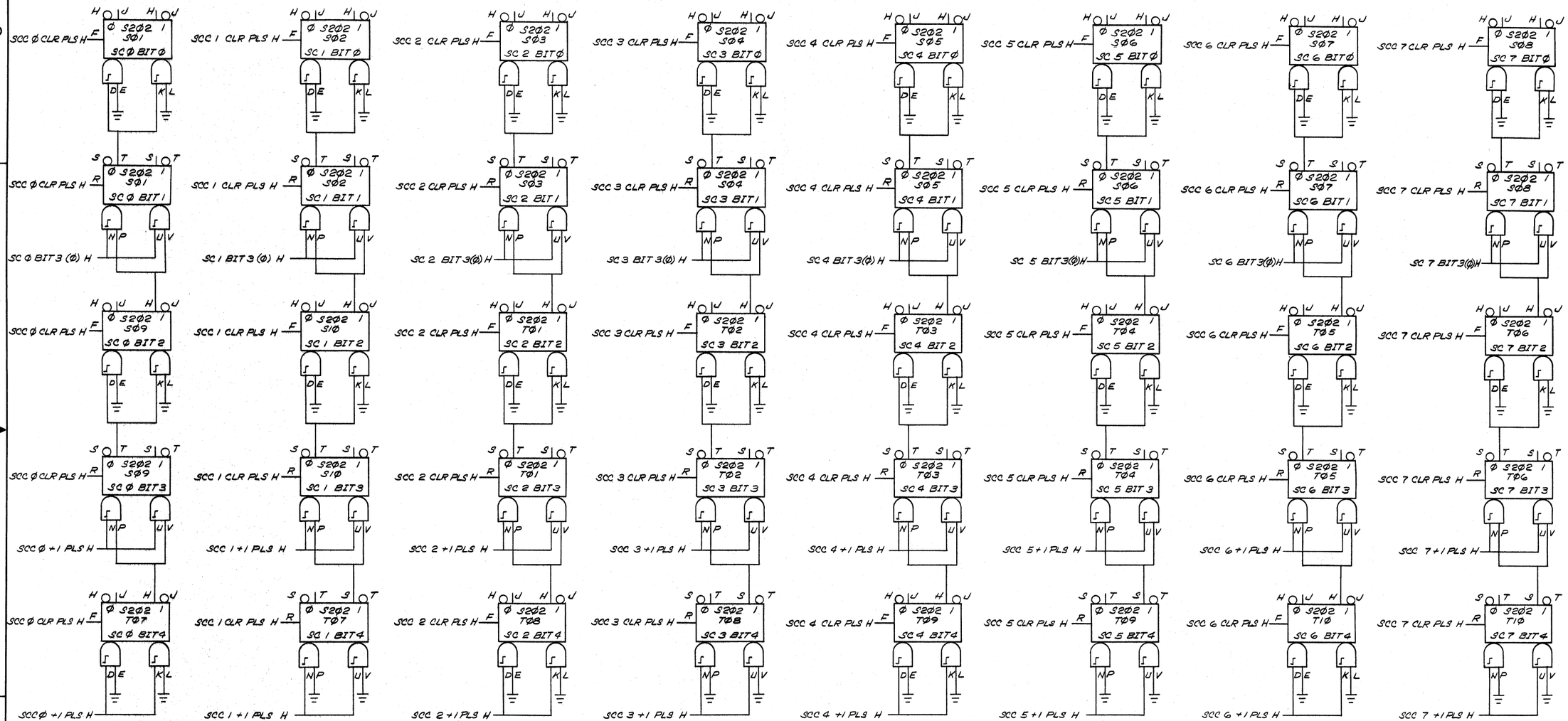
digital EQUIPMENT CORPORATION MAYNARD MASSACHUSETTS

TITLE: READ DATA SEPARATOR

SIZE	CODE	NUMBER	REV.
D	BS	RPIØ-Ø-RDS	F

DIST.

DEC FORM NO DRD 102A



FOR INFORMATION ONLY
DO NOT BUILD TO THIS DRAWING

REVISIONS		
REV.	CHANGE NO.	CHK

FIRST USED ON OPTION/MODEL	QTY.	DESCRIPTION	PART NO.	ITEM NO.
PDP1Ø		PARTS LIST		

UNLESS OTHERWISE SPECIFIED

UNLESS OTHERWISE SPECIFIED
DIMENSION IN INCHES
TOLERANCES
DECIMALS ± .005 FRACTIONS ± 1/64 ANGLES ± 0°30'
FINAL SURFACE QUALITY
REMOVE BURRS AND BREAK SHARP CORNERS

MATERIAL

FINISH

DRN. W. Stephenson	DATE	5/7/69
CHK'D. T. Forgette	DATE	11NOV69
ENG. John C. Rivers	DATE	11NOV69
PROJ. ENG. John C. Rivers	DATE	11NOV69
PROD. W. McLaren	DATE	11-16-69

NEXT HIGHER ASSY
A-ML-RPIØ-Ø

SCALE

SHEET 1 OF 1

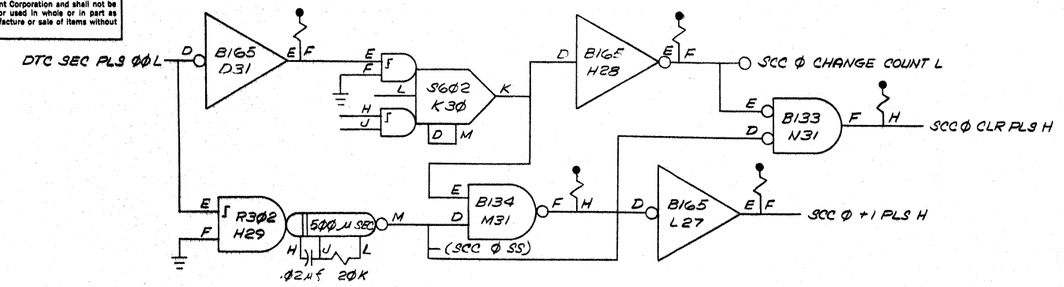
digital EQUIPMENT CORPORATION
MAYNARD, MASSACHUSETTS

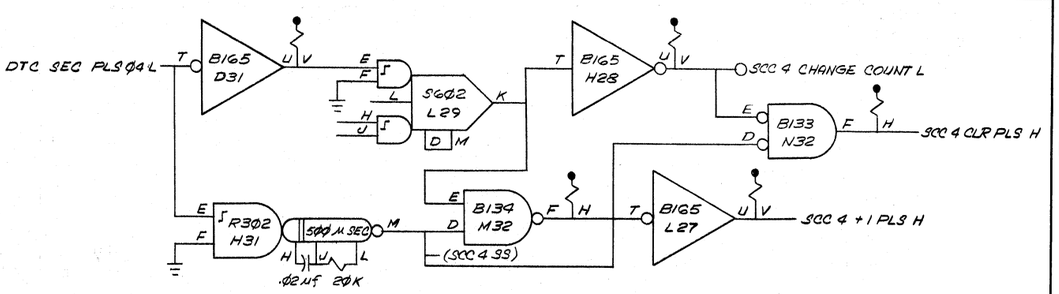
TITLE
SECTOR COUNTER

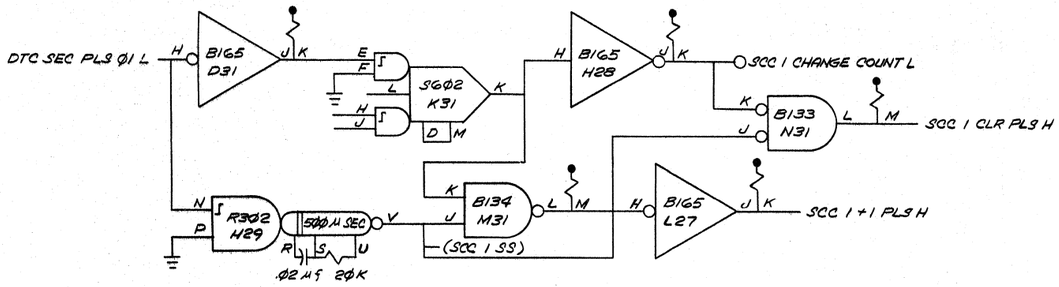
SIZE	CODE	NUMBER	REV.
D	BS	RPIØ-Ø-SC	

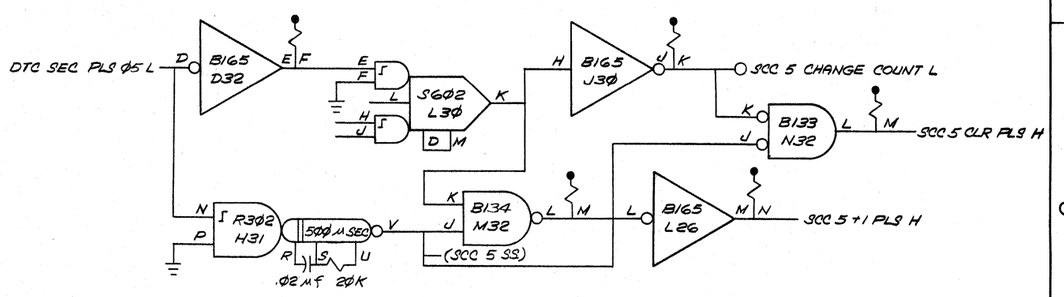
DIST.

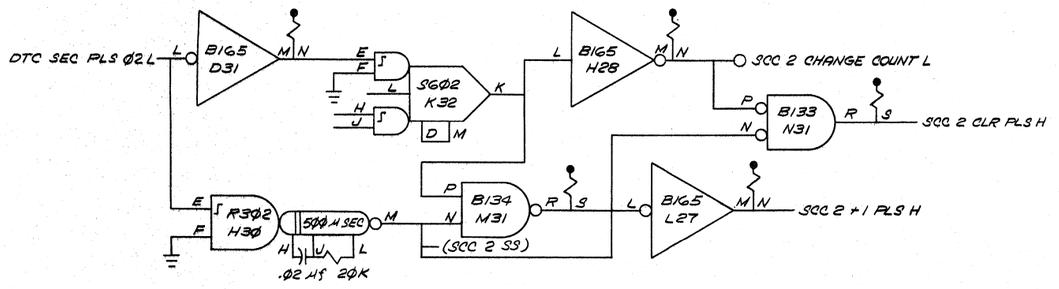
DEC FORM NO.
DRD 102A

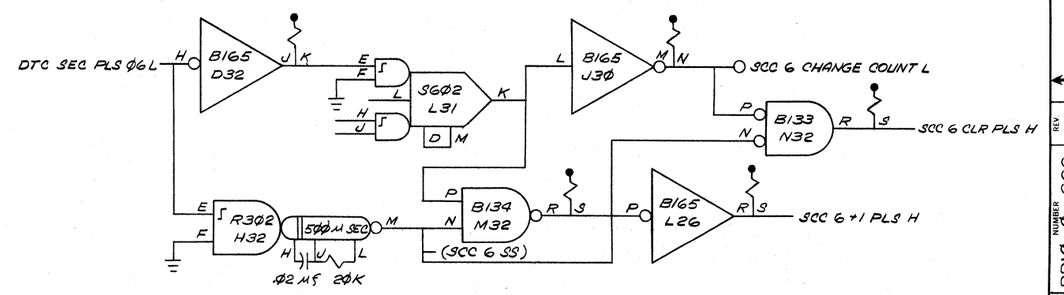


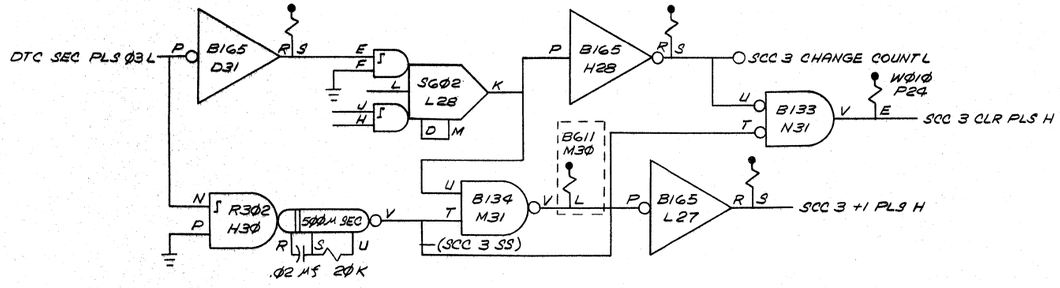


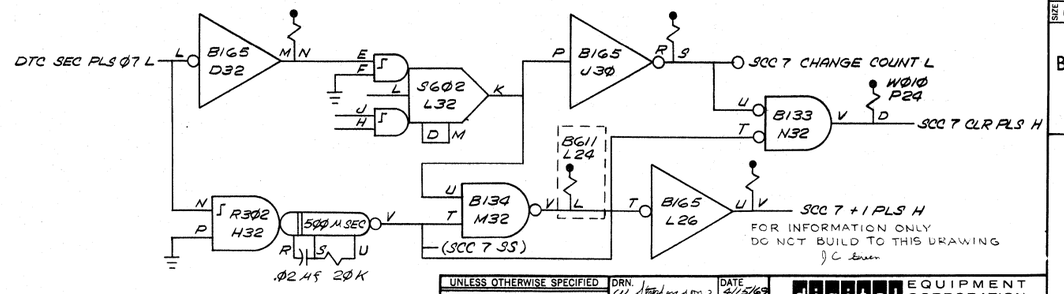












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J C Emerson

REVISIONS	REV.	CHANGE NO.	CHK

UNLESS OTHERWISE SPECIFIED		
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES TOLERANCES DECIMALS ± .005 FRACTIONS ± 1/64 ANGLES ± 0°30' FINAL SURFACE QUALITY √ REMOVE BURRS AND BREAK SHARP CORNERS	DRN. W. Stephenson	DATE 4/15/69
	CHK'D. T. Longo	DATE 11NOV69
	ENG. John C. Emerson	DATE 11NOV69
	PROJ. ENG. John C. Emerson	DATE 11NOV69
MATERIAL	PROD. J. M. Shaw	DATE 11-16-69
	NEXT HIGHER ASSY A-ML-RP1Ø-Ø	
FINISH	SCALE	
	SHEET 1 OF 1	

digital EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS

TITLE: SECTOR COUNTER CONTROL PULSES

SIZE	CODE	NUMBER	REV.
D	BS	RP1Ø-Ø-SCC	

DIST.

FIRST USED ON OPTION/MODEL: PDP1Ø

DEC FORM NO. DRD 102A

AR Ø5(1) L
DTC LD BUF FM AR L
AR Ø5(Ø) L
B163 HØ9 U
B163 HØ9 V
AR Ø4(1) L
AR Ø4(Ø) L
B163 HØ8 U
B163 HØ8 V
AR Ø3(1) L
AR Ø3(Ø) L
B163 HØ8 N
B163 HØ8 P
AR Ø2(1) L
AR Ø2(Ø) L
B163 HØ8 H
B163 HØ8 J
AR Ø1(1) L
AR Ø1(Ø) L
B163 HØ9 N
B163 HØ9 P
AR ØØ(1) L
AR ØØ(Ø) L
B163 HØ9 H
B163 HØ9 J
SR BUF Ø5(L) L
SR BUF Ø4(L) L
SR BUF Ø3(L) L
SR BUF Ø2(L) L
SR BUF Ø1(L) L
SR BUF ØØ(L) L
Ø B214 I F12 SR BUF Ø5
Ø B214 I F12 SR BUF Ø4
Ø B214 I F12 SR BUF Ø3
Ø B214 I F12 SR BUF Ø2
Ø B214 I F13 SR BUF Ø1
Ø B214 I F13 SR BUF ØØ
B214 E12
B214 F13
SR 5(1) L
DTC LD BUFFER FM SR L
SR 5(Ø) L
SR 4(1) L
SR 4(Ø) L
SR 3(1) L
SR 3(Ø) L
SR 2(1) L
SR 2(Ø) L
SR 1(1) L
SR 1(Ø) L
SR Ø(1) L
SR Ø(Ø) L
B163 H11
B163 H1Ø
LPR Ø5(Ø) L
DTC LOAD SR FM LPR L
LPR Ø4(Ø) L
LPR Ø3(Ø) L
LPR Ø2(Ø) L
LPR Ø1(Ø) L
LPR ØØ(Ø) L
B163 F1Ø
WØ1Ø E2Ø
AR Ø5(1) L
DTC LD SR FM AR L
AR Ø4(1) L
AR Ø3(1) L
AR Ø2(1) L
AR Ø1(1) L
AR ØØ(1) L
B163 J13
DTC READ BIT(1) L
DTC SHIFT SR L
B163 J14
WØ1Ø L12
DTC READ BIT(Ø) L
B163 J15
DTC WRITE ENABLE L
B611 J06
B212 L13 SR5
B212 L13 SR4
B212 L14 SR3
B212 L14 SR2
B212 L15 SR1
B212 L15 SRØ
SR 5(L) L
SR 4(L) L
SR 3(L) L
SR 2(L) L
SR 1(L) L
SR Ø(L) L
B212 L13
B212 L14
B212 L15
DTC SHIFT SR L
DTC SR JAM H

FOR INFORMATION ONLY
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REVISIONS	REV.	CHANGE NO.	CHK

FIRST USED ON OPTION/MODEL: PDPIØ	QTY.	DESCRIPTION	PART NO. / ITEM NO.
		PARTS LIST	
UNLESS OTHERWISE SPECIFIED	DRN. W. Stephenson	DATE 12/5/68	digital EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES TOLERANCES DECIMALS ± .005 FRACTIONS ± 1/64 ANGLES ± 0°30' FINAL SURFACE QUALITY REMOVE BURRS AND BREAK SHARP CORNERS	CHK'D. E. Lorraine	DATE 11 NOV 69	TITLE: SHIFT REGISTER
MATERIAL	ENG. John C Bassett	DATE 11 NOV 69	
	PROJ. ENG. John C Bassett	DATE 11 NOV 69	
FINISH	PROD. G. McLaren	DATE 11-26-69	
	NEXT HIGHER ASSY: A-ML-RPIØ-Ø		SIZE D | CODE BS | NUMBER RPIØ-Ø-SR | REV
	SCALE		DIST.
	SHEET 1 OF 1		

DEC FORM NO. DRD 102A

G704 TERMINATORS

LOC	D	E	F	H	J	K	L	M	N	P	R	S	T	U	V
C09	IOBD 27	IOBD 28	IOBD 29	IOBD 30	IOBD 31	IOBD 32	IOBD 33	IOBD 34	DSBC SEL INDEX L	IBC SET DAR ETC DLY L	DTC READ DATA (1) H		DTC RP Ø1	IBC LOCAL (Ø) H	GND
D05	IBC CLEAR ATTN H	WDC BIT SHIFTT EQ Ø L		DTC READ IMAGE (Ø) H	DTC READ GAP (Ø) H	IBC POWER CLEAR L	DTC READ HEADER (1) H	DTC READ LPR (1) H	IBC CONSET H	DAR SEC ØØ (Ø) H	DAR SEC Ø1 (Ø) H	DAR SEC Ø2 (Ø) H	DAR SEC Ø3 (Ø) H	DTC WRITE LPR (1) H	
E28	DAR CYL 32 (Ø) H	DAR CYL 16 (Ø) H	DAR CYL Ø2 (Ø) H	DAR CYL Ø1 (Ø) H	CC ACTIVE (1) H	CC PREVENT TEST L	DTC READ ENABLE L	DTC WRITE HDR COM H	WDC CLEAR DATA NDCT H		IBC CONCLR H	IBC SET DAR ETC B	DTC WRITE GAP(1) H	DTC READ HEADER(Ø) H	
K26	DAR CYL 128 (Ø) H	DAR CYL 64 (Ø) H	DAR CYL Ø8 (Ø) H	DAR CYL Ø4 (Ø) H	DTC READ DATA (Ø) H	DTC SEARCH (Ø) H	IBC DEVICE SELECTED H	IBC DEVICE SELECTED L	IBC POWER CLEAR H	DTC RESTORE H	DTC WRITE DATA(1) H	IBC LOAD TEST B L	IBC LOAD TEST A L		
K29	DAR DRIVE ØØ (Ø) H	DAR DRIVE Ø1 (Ø) H	DAR DRIVE Ø2 (Ø) H	CXR CHAN CONT PE (Ø) H	CXR DISK WD PE (Ø) H	DTC BUS LINE STROBE L	HCDE SEC DES ER (Ø) H	HCDE SURF DES ER (Ø) H	DTC ATTN ØØ (Ø) H	DTC ATTN Ø1 (Ø) H	DTC ATTN Ø2 (Ø) H	DTC ATTN Ø3 (Ø) H	DTC ATTN Ø4 (Ø) H	DTC ATTN Ø5 (Ø) H	
N19	DTC ATTN Ø6 (Ø) H	DTC ATTN Ø7 (Ø) H	DAR SEL DRIVE ØØ H	DAR SEL DRIVE Ø1 H	DAR SEL DRIVE Ø2 H	DAR SEL DRIVE Ø3 H	DAR SEL DRIVE Ø4 H	DAR SEL DRIVE Ø5 H	DAR SEL DRIVE Ø6 H	DAR SEL DRIVE Ø7 H	DTC PAR CONT (Ø) H	DTC PAR CONT (1) H	DTC PARITY (Ø) H	IBC LOCAL START H	
M11	CXR WRITE EVEN PAR (Ø) H		DTC WRITE DATA (Ø) H		IBC BUSY (1) H	IBC BUSY (Ø) H	CXR NON EX MEM (Ø) H	CXR OVERRUN (Ø) H	CXR PI ØØ (Ø) H	CXR PI Ø1 (Ø) H	CXR PI Ø2 (Ø) H	CXR SEARCH ER (Ø) H	CC ACTIVE (Ø) H		GND

G700 TERMINATORS

LOC	D	E	F	H	J	K	L	M	N	P	R	S	T	U	V
E10		DTC WRITE CLOCK L	GND	J06D	GND	DTC READ CLOCK DLY L	GND	DTC LD BUFFER FM SR L	GND	H04D	GND		DTC LDSR FM AR L	GND	CC DATA STROBE B L
K13	DTC SHIFT BIT CNT L	DTC LD BUF FM AR L	GND	DTC LDSR FM LPR L	GND	DTC CLOCK/PAR CONT L	GND	DTC SHIFT SR L	GND		GND	CC CHAN END L	K19D	GND	N0IN
P08	CC TERMINATE L	CC INT DEV PLS L	GND	CC DATA STROBE A L	GND		GND	N01D	GND	CC CHAN START OUT L	GND			GND	
S27	S26N	S26D	GND	T30N	GND	T30D	GND	T27D	GND	S30D	GND	S30N	T27N	GND	RDS DATA IN L
S16	S15D	SCB FM SC REG JAM L	GND	IC CHAN BUSY IN L	GND		GND	CC SAWRITE OUT L	GND	CC CHANNEL BUSY OUT L	GND	IC SAWRITE IN L	IC CHANNEL START IN L	GND	

NOTE:
PIN "C" IS GROUNDED ON ALL G70Ø AND G704 TERMINATOR MODULES.

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REVISIONS			
CHK	CHANGE NO.		REV.
FV	RP10-00009	T. Scully 3.14.70	A
		GREEN D.C. June 3/24/70	
FV	RP10-00013	D. IVES 5/1/70	B
		J. GREEN D.C. June 5/4/70	
DMB	RP10-00016	D. IVES 5/11/70	C
DMB	RP10-00017	IVES D.C. June 5/14/70	D
FV	RP10-00027	IVES D.C. June 6/1/70	E
		IVES D.C. June 1/29/71	

FIRST USED ON OPTION/MODEL	QTY.	DESCRIPTION	PART NO. / ITEM NO.
PDP10		PARTS LIST	
UNLESS OTHERWISE SPECIFIED	DRN. W. Stephenson	DATE 7/22/69	digital EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES TOLERANCES DECIMALS ± .005 FRACTIONS ± 1/64 ANGLES ± 0°30' FINAL SURFACE QUALITY √ REMOVE BURRS AND BREAK SHARP CORNERS	CHK'D. T. Scully	DATE 11 NOV 69	TITLE PULSE & LEVEL TERMINATIONS
	ENG. [illegible]	DATE 11 NOV 69	
	PROJ. ENG. [illegible]	DATE 11 NOV 69	
	PROD. J. McGlinn	DATE 11-16-69	
MATERIAL	NEXT HIGHER ASSY A-ML-RP10-Ø		SIZE D CODE CL NUMBER RP10-Ø-TERM REV. E
FINISH	SCALE		
	SHEET / OF /		DIST.

DEC FORM NO. DRD 102A

DIGITAL EQUIPMENT CORPORATION
MAYNARD, MASSACHUSETTS

ENGINEERING SPECIFICATION

DATE 10/22/70

TITLE DISK PACK DRIVE SYSTEM MANUFACTURING TEST PROCEDURES AND SPECIFICATIONS

REVISIONS

REV	DESCRIPTION	CHG NO	ORIG	DATE	APPD BY	DATE
A	CHANGE PER ECO	00034	B. WALSH	10/71	B. Walsh	11/24/71
B	CHANGE PER ECO	00037	B. WALSH	2/72	B. Walsh	3/9/72

ENG	APPD	SIZE	CODE	NUMBER	REV
D. Ives	David C. Ives	A	SP	RP10-0-MTP	B

DEC FORM NO. DRA 107

ENGINEERING SPECIFICATION — digital — CONTINUATION SHEET

TITLE DISK PACK DRIVE SYSTEM MANUFACTURING TEST PROCEDURES AND SPECIFICATIONS

A. Diagnostic and Reliability Testing

Requirements: RP10, RP02's and/or RP03's DF10, KA10 with 16 k of memory minimum and RP02P's.

1. Run the diagnostics MAINDEC-10-DCRPA and MAINDEC-10-DCRPB, making at least one pass per drive. No errors are acceptable. All eight (8) ports on the RP10 should be tested, even though the system is comprised of less than eight drives.

2. Load the Reliability Test MAINDEC-10— DCRPC

   a. Type in a listing of all flagged tracks on the packs to be used.

   b. Format the disk pack on each of the drives, using the program FORMAT. The program should run to completion without any header verify errors.

   c. Run C TEST (compatability) to verify that all surfaces of each pack which have been written by each drive are capable of being read by every other drive in the system.

   d. Run MANUAL to insure that all drive switch functions are operating properly.

   e. Run a system reliability test, using ACCEPT or the following sequence.

```
DDT END/0            EX ATEST
DDT END+1/0          EX BTEST      DDTEND+10/0  JRST.-4
DDT END+2/0          EX MTEST
DDT END+3/0          EX PTEST
DDT END+4/0          EX FRTEST
DDT END+5/0          EX OTEST
DDT END+6/0          EX RTEST
DDT END+7/0          EX DTEST
DDT END+8/0          EX RTEST
DDTEND+9/0           EX PNTOT
```

This test should be run for a sufficient amount of time to allow each drive to transfer a minimum of 1×10^{10} bits READ.

	SIZE	CODE	NUMBER	REV
	A	SP	RP10-0-MTP	B

DEC FORM NO 16-1022 DRA 108

RP10 MANUFACTURING TEST MARGINS

		+10V		-15V	
MOUNTING PANEL		LOW	HIGH	LOW	HIGH
A		2.5	17.5	-18	-12
B		2.5	17.5	-18	-12
C		2.5	17.5	-18	-12
D		2.5	17.5	-18	-12
E		2.5	17.5	-18	-12
F		2.5	17.5	-18	-12
H		2.5	17.5	-18	-12
J		*6	17.5	-18	-12
K		2.5	17.5	-18	-12
L		2.5	17.5	-18	-12
M		*6	17.5	-18	-12
N		**6.5	14	-18	-12
P		**6.5	14	-18	-12
R		**6.5	14	-18	-12
S		***7.5	15	-18	-12
T		***7.5	15	-18	-12

* B685 Limits +10V margins of this panel

** W591 and W692 Limits +10V margins of this panel

*** B410 Limits +10V margins of this panel

DF1Ø MANUFACTURING TEST MARGINS

		+10V		-15V	
	Mounting Panel	Low	High	Low	High
	A	2.5	17.5	-18	-12
	B	2.5	17.5	-18	-12
***	C	3.0	17.5	-18	-12
*	D	3.5	17.5	-18	-12
	E	2.5	17.5	-18	-12
**	F	6.0	17.5	-18	-12
**	H	6.0	17.5	-18	-12
	J	2.5	17.5	-18	-12
	K	2.5	17.5	-18	-12
	L	2.5	17.5	-18	-12

* The B130 modules limit the +10V margins of panel D.

** The B685 modules limit the +10V margins of panels F and H.

*** The R3Ø3 module for NON-EXMEM limits +10V margins for this panel.

At this point, SEARCH COMPLETE should be setting reliably. Position the drive to several other cylinders and try to read other surfaces and sectors.

Check the timing of these delays:

DTC5 SRCH COMP STRB 6ØØ ns ± 2Ø%

H27M 6ØØ ns ± 2Ø%

DTC7 SECTOR DLY 1Øµsec ± 2Ø%

To read data properly, the following signals must also work properly:

DTC7 READ GAP Flop

DTC7 READ DATA Flop

DTC2 SHIFT AR A, B, C

WDC WORD COUNT EQ ØØ

DTC7 READ LPR Flop

7. Position the drive to cylinder Ø. Set up the maintenance panel to have the drive write data on CYLINDER Ø, SURFACE Ø, SECTOR Ø. Move the drive's READ-WRITE/READ ONLY switch to the READ-WRITE position. Press CLEAR, STOP, START. NOTE: Several cylinders of headers will probably be destroyed before the write data command works. If one is destroyed, move to another one. Check the timing of these delays:

F11M 2Ø µsec ± 2Ø%

F31M 3 µsec ± 2Ø%

Verify proper operation of these additional signals:

DTC7 WRITE GAP Flop

DTC7 WRITE DATA Flop

DTC6 CLOCK Ø, 1 Flops

DTC6 WRITE CLOCK

DTC6 CLOCK ∧~ PAR CONT

DTC3 SET ERASE

DTC3 ERASE BUFFER Flop

DTC2 LD SR FM LPR

DTC2 LD SR FM AR

DTC6 WRITE CLOCK Flop

DTC6 CRYSTAL CLOCK

8. If the controller is an RP10-C, setup the maintenance panel to do a read verify operation (OPCODE 2) and verify for proper operation, the same signals as in step 6 of this specification and in addition verify the following:

DSBC BUS 256 L
Check the delay at A23V to be 8.5 µs ±20%

DIGITAL EQUIPMENT CORPORATION
MAYNARD, MASSACHUSETTS

ENGINEERING SPECIFICATION

DATE 10/16/70

TITLE RP10 CONTROLLER AND RP02 RP03 DISK PACK DRIVE MANUFACTURING TEST PROCEDURE

REVISIONS

REV	DESCRIPTION	CHG NO	ORIG	DATE	APPD BY	DATE
A	CHANGE PER ECO	00034	B. WALSH	10/71	B. Walsh	11/24/71

ENG	APPD	SIZE	CODE	NUMBER	REV
David C. Ives	David C. Ives	A	SP	RP10-0-CDPD	A

DEC FORM NO. DRA 107

ENGINEERING SPECIFICATION digital CONTINUATION SHEET

TITLE RP10 CONTROLLER AND RP02, RP03 DISK PACK DRIVE MANUFACTURING TEST PROCEDURE

A. OFFLINE TEST

REQUIREMENTS: RP10, RP02, or RP03, RP02P, Tektronix 453 oscilloscope or equivalent.

1. Check voltages at several places on all mounting panels. Connect RP02/3 Unit Bus Cable to Drive 0 connector. Connect RP02/3 Signal Bus Connector. Jumper W990's on DTC6 and HCDE as required. If a mixed system of RP02/3's the RP03's must be the first units on the signal bus.
2. Check DTC4 TEST CLOCK frequency (30kc-375kc). Set up delay line F04V=1.5μsec (DTC4).
   Set up the following delays to the values specified:

   F32M 88 μsec
   F32V 44 μsec
   J09M 350 μsec
   S17V See note on DTC6
   H25M 500 nsec.

3. Set up the maintenance panel to recalibrate the drive. Press, in sequence, CLEAR, STOP, START. Verify that CLEAR clears all lights except GEN CLR, SHIFT REGISTER, and BUFFER. Verify proper operation of the following signals:

   IBC POWER CLEAR

   IBC LOCAL START

   IBC INITIAL CLEAR, A, B, C

   IBC SET DAR ETC A, B

   IBC SET DAR ETC DLY

   IBC LOAD TEST A, B, C

   DTC1 RESTORE

   DTC3 BUS LINE STROBE

   DTC3 TAG LINE STROBE

   DSBC UB 6

	SIZE	CODE	NUMBER	REV
	A	SP	RP10-0-CDPD	A

DEC FORM NO 16-1022 DRA 108

TEST 2

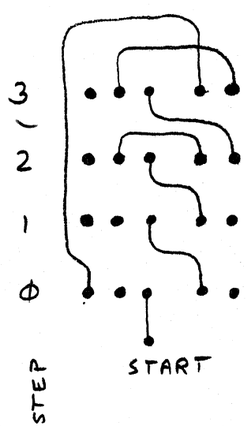
This test checks the PI lines.

It should loop if successful, or get stuck on 2 or 3 if there are problems.

Change the right half of the command in step:

	Ø	2	3
to	ØØ6	ØØ2	175
	ØØ5	ØØ4	173
	ØØ4	Ø1Ø	167
	ØØ3	Ø2Ø	157
	ØØ2	Ø4Ø	137
	ØØ1	1ØØ	Ø77
	ØØØ	ØØØ	177

AND REPEAT

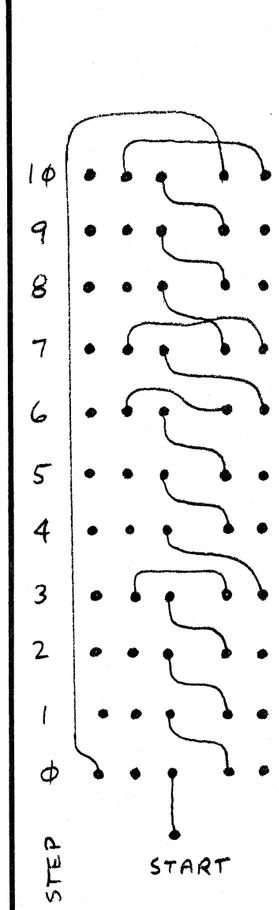


	CLEAR	OP CODE	DEVICE SEL			DATA SWITCHES					
	0	1	2	5	0	0	0	0	1	7	6
	0	1	2	5	0	0	0	0	0	0	1
	0	2	2	5	0	0	0	0	0	2	0
	1	5	2	5	0	0	0	0	0	0	7
	ONE BIT	OCTAL	OCTAL	OCTAL	ONE BIT	OCTAL	OCTAL	OCTAL	OCTAL	OCTAL	OCTAL

TEST 3

This test will check the channel interface and several status bits.

It will halt on error or will loop if there is no error.



	CLEAR	OP CODE	DEVICE SEL			DATA SWITCHES					
	0	3	2	5	0	0	5	4	0	0	0
	0	4	2	5	0	0	0	0	0	0	0
	0	0	4	0	0	0	0	0	0	0	0
	0	3	2	5	0	0	5	4	0	0	0
	0	3	2	5	0	0	0	0	0	1	0
	0	4	2	5	0	0	0	0	0	0	0
	0	0	4	0	0	1	0	0	0	0	0
	0	3	2	5	0	0	0	0	0	1	0
	0	4	2	5	0	0	0	0	0	0	0
	0	0	2	0	0	0	0	0	7	7	6
	1	0	4	0	0	7	0	0	0	0	0
	ONE BIT	OCTAL	OCTAL	OCTAL	ONE BIT	OCTAL	OCTAL	OCTAL	OCTAL	OCTAL	OCTAL

FIELD SERVICE REP. RESPONSIBLE_____

INITIAL

1. Run DCRPA pass per drive _____

2. Run DCRPB pass per drive _____

3. Run FORMAT _____

4. Run CTEST _____

5. Run MANUAL _____

6. RP1Ø

   a. Vibration (AIR) run DCRPA _____
   b. Vibration (MODULES & CAB.) run DCRPB _____
   c. Do RP1Ø Local Mode Switch check _____
   d. Check all RP1Ø ports _____
   e. Margins
      DCRPA _____

7. DF1Ø

   a. Vibration (AIR) run DCRPB _____
   b. Vibration (MODULES & CAB.) DCRPB _____
   c. Do DF1Ø Local Mode Switch check _____
   d. Margins
      DCRPB _____

8. Run Reliability Test DCRPC using EX ACCEPT or the following tests.

   DDTEND/ Ø EX ATEST
   DDTEND+1/Ø EX BTEST
   DDTEND+2/Ø EX MTEST
   DDTEND+3/Ø EX PTEST
   DDTEND+4/Ø EX FRTEST
   DDTEND+5/Ø EX OTEST
   DDTEND+6/Ø EX RTEST
   DDTEND+7/Ø EX DTEST
   DDTEND+8/Ø EX RTEST
   DDTEND+9/Ø EX PNTOT
   DDTEND+1Ø/Ø JRST. -4

   a. Read Error Rate _____
   b. Write Error Rate _____
   c. Positioner Seek Error Rate _____

9. Run PTIME (Send to Customer) Abbr. Version _____

b. Program the sequence:

```
↑ Z
EP Ø 312
EP 312 Ø
EO Ø 312
EZ Ø 312
ED Ø 312
EM Ø 312
EN Ø 312
↑ C
```

Total running time is about 3 hours.

c. The positioner test should produce seek times which are less than or equal to:

	HIGH	LOW	AVERAGE
RPØ2-AS/BS	6Øms	1Øms	32ms
RPØ2-A/B	8Øms	2Øms	5Øms

d. If more than one random data error occurs during one pass of this program, there is a strong possibility of a pack, head, or preamplifier problem in the drive. A longer run is suggested to establish an error pattern (same surface, etc.) if one exists.

e. If the drive successfully passes the data transfer testing in par. b, the following test should be run for a minimum of 12 hours to verify the overall reliability of the drive.

```
↑ Z
EN Ø 312
↑ C
```

No positioning errors and no more than 1 random data error should occur during each 2 hours of running time.

APPENDIX I

1. Formatting a pack

The 8I tester program AUTOCAT-8I-CW5A-D will operate only with a specially formatted pack. The format program (F) should be run with location 7Ø47 set to 4ØØØ. To format a pack for PDP-9, PDP-1Ø, or PDP-15 operation, set location 7Ø47 to ØØØ1. Normal acceptance procedure on the RP1Ø controller will initially format all packs so this should not cause any inconvenience. Also, a special pack should be set aside for tester use only.

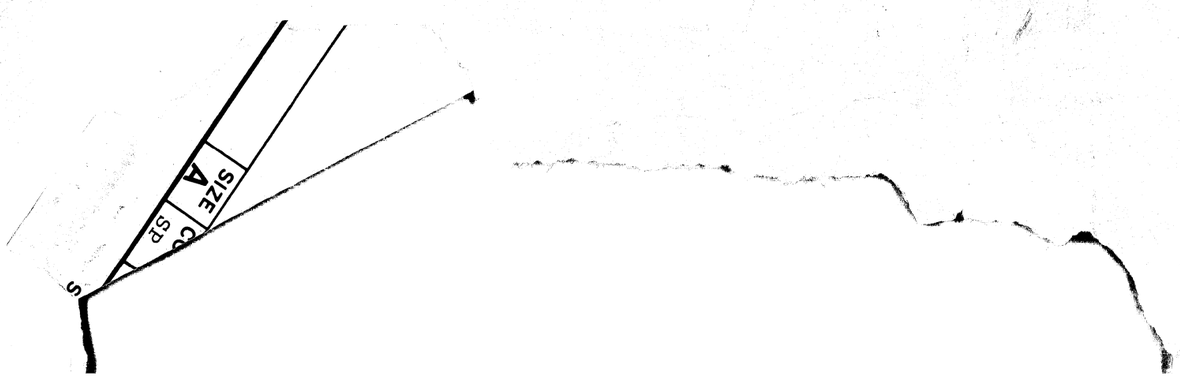
2. Testing the BAI Ø86A interface

The interface can be tested with BAI diagnostic BAIØ86-3C. To run this diagnostic, pin B22 A1 should be connected to A26 N2. For running any DEC programs, pin B22 A1 should be connected to B19 U1.

3. RPØ1/RPØ2 operation with BAI interface

For RPØ1 operation, the switch on card A2 should be in the 63Ø position and the 63Ø VFO should be plugged in the proper slot. (See BAI prints and technical description). For RPØ2 operation, the switch should be in the 66Ø position and the 66Ø VFO should be plugged in.

For both the RPØ1 and RPØ2 the on-off switch above the 63Ø/66Ø switch on the A2 card should be off, signifying no automatic parity insertion.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