



(3B1) 6PB INT
 (1C1) 2793 INT
 (3A1) LTR DLY
 (3A1) DEV CLR
 (1B1) DCLK
 (3B1) 6PB DCLK
 (2D1) 6PB DCLK L
 (1C1) 2793 DREQ
 (2B1) SET TAPE PRG L

DCAM CS (1BB)
 ADMSTR L (1BA)
 6PB CS L (3DA)
 2793 CS L (1DA)
 6PB DACK L (2PA) (3DA)
 DT (1DB) (3CB)
 D6 (1DB) (3CB)
 D5 (1DB) (3CB)
 D4 (1DB) (3CB)
 D3 (1DB) (3CB)
 D2 (1DB) (3CB)
 D1 (1DB) (3CB)
 D0 (1DB) (3CB)
 RA7 (1CB)
 RA6 (1BB)
 RA5 (1BB)
 RA4 (1CB)
 A2 (3CB)
 A1 (1DB) (3BB)
 A0 (1DB) (3BB)
 PU1 (1AB)
 RA3 (1BB)
 RA2 (1CB)
 RA1 (1CB)
 RA0 (1CB)
 2793 MRL (1DA)
 FORLE TRK (1AB)
 DS4 L
 DS3 L
 DS2 L
 DS1 L
 DS1 L
 SIDE SELECT L
 LED 1 WR L (3AQ)
 LED 2 WR L (3AB)
 CLR PARITY ERR (1DB)
 I/P RD L (3AB)
 CLR 6PB L (3BA)
 SET TAPE DREQ L (2PA)
 10 RD L (1DB) (3DB)
 10 WR L (1DA) (3DB)
 ERROR (RED)
 XOR TEST (1CB)
 LIC (3DB)
 BLINK EN L (3AB)
 TAPE CLK (1DB)
 TAPE DACK L (1DA)
 ADC MUX (1BA)
 XDA CLK (1CB)
 XDB EN (1CB)
 DRAM WE L (1FB)
 XOR DACK L (1DB)
 MEM RD L (1BB)
 6MHz (3DB)
 RESET (3DB)
 YDA CLEAR L (2CB)

D

C

B

A

D

C

B

A

